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PROCESSORS

Pentium III
Pentium II
Pentium

Low-Power Pentium

▶ 486

386 EX/CX/SX

EXACT

Motorola

486 In-Circuit Emulators

Software debuggers lack hardware event triggers and full-speed trace, making it difficult to isolate and identify real-time conflicts. Logic analyzers don't provide run-time control of the target processor. The Microtek EA-486 emulator does all of the above and more, in a surprisingly small package.

PowerPack EA-486 Features

No other 486 emulator offers as many powerful features:

- 33 MHz, zero wait state operation
- Intel bondout technology for accurate control and execution tracing
- 3.3V & 5V support using the same probe
- DX2 support
- 8 complex trigger events
- 4-level sequential triggering
- Probe draws no power from target hardware beyond normal CPU power
- Full-featured Windows SLD source-level debugger
- 256 software breakpoints, 8 hardware breakpoints
- 256K frames of clock-cycle or bus-cycle trace
- 96-bit trace address, data, and I/O signals, not just execution addresses
- Fully qualified trace
- Hardware-timestamped trace that includes 8 external trace bits.
- 1 MB overlay memory is zero wait state up to 33 MHz
- 128 KB overlay mapping resolution, up to 16 overlay ranges
- SAST board for system diagnostic tests
- 115K connection to host, providing 600K/minute code downloads.
- Small probe and flexible cables that fit any target environment
- One-year hardware warranty, low-priced extended Gold Support plan

Intel486™ DX4 Support

Microtek's Intel486 in-circuit emulators are based on licensed Intel bondout devices. These bondout devices allow direct support for DX and DX2 processors. Emulation support for the DX4 is also possible, with the following provisions:

- 3.3V operation must be fully supported.
- The DX2 probe must be used, providing 2X core clock, not 3X.
- The bondout device must have an 8 KB cache.

AMD 486 Support

The Microtek 486 PowerPack series of emulators provides debug support for the AMD processors. This table describes supported processors in the AMD family.

Processor	Speed	Limitations
AMD 486DX	33 MHz	
AMD 486DX2	66 MHz	Debug does not support write-back cache.
AMD 486DX4	100 MHz	Debug does not support write-back cache. Debug is limited to 8 KB of cache. Debug is limited to 66 MHz.
AMD 486DX5	133 MHz	Debug does not support write-back cache. Debug is limited to 8 KB of cache. Debug is limited to 66 MHz.

Network Option

The network option allows the emulator to exist on a network as a shared resource. Networking provides the development team with fast convenient access to the emulator.

Open a new browser window to view the following PDF file:

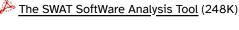
Network Option for PowerPack® Emulators (185K)



SWAT

The SWAT (SoftWare Analysis Tool) option lets you perform rigorous testing from the start of a project. By running the tests and saving the results during the development, a designer is able to pass along not only the design, but a great deal of the testing required for final integration. The result is a much smoother transition from the designer's workbench through system integration and into the manufacturing environment.

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