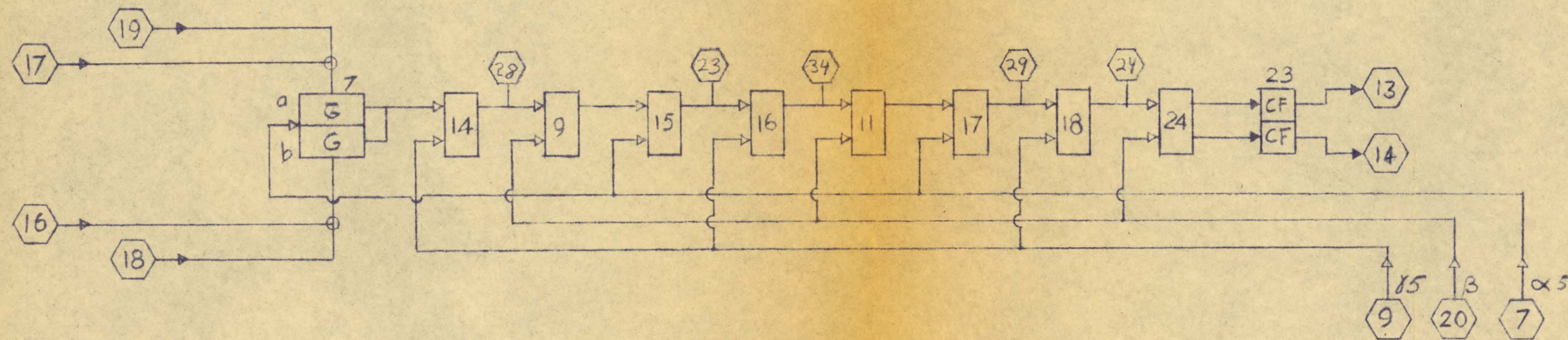
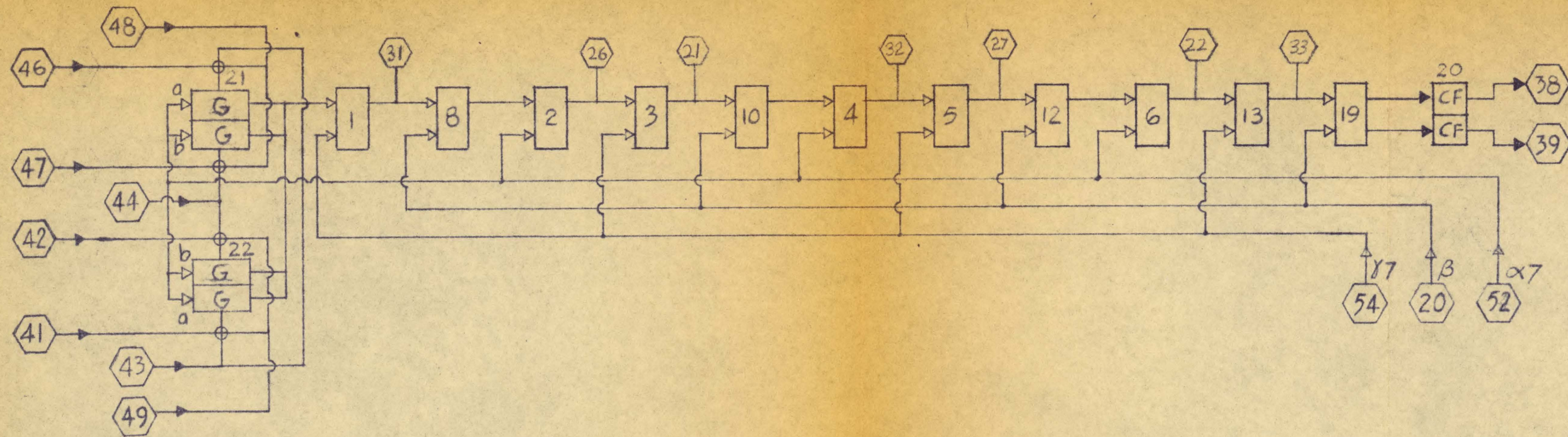
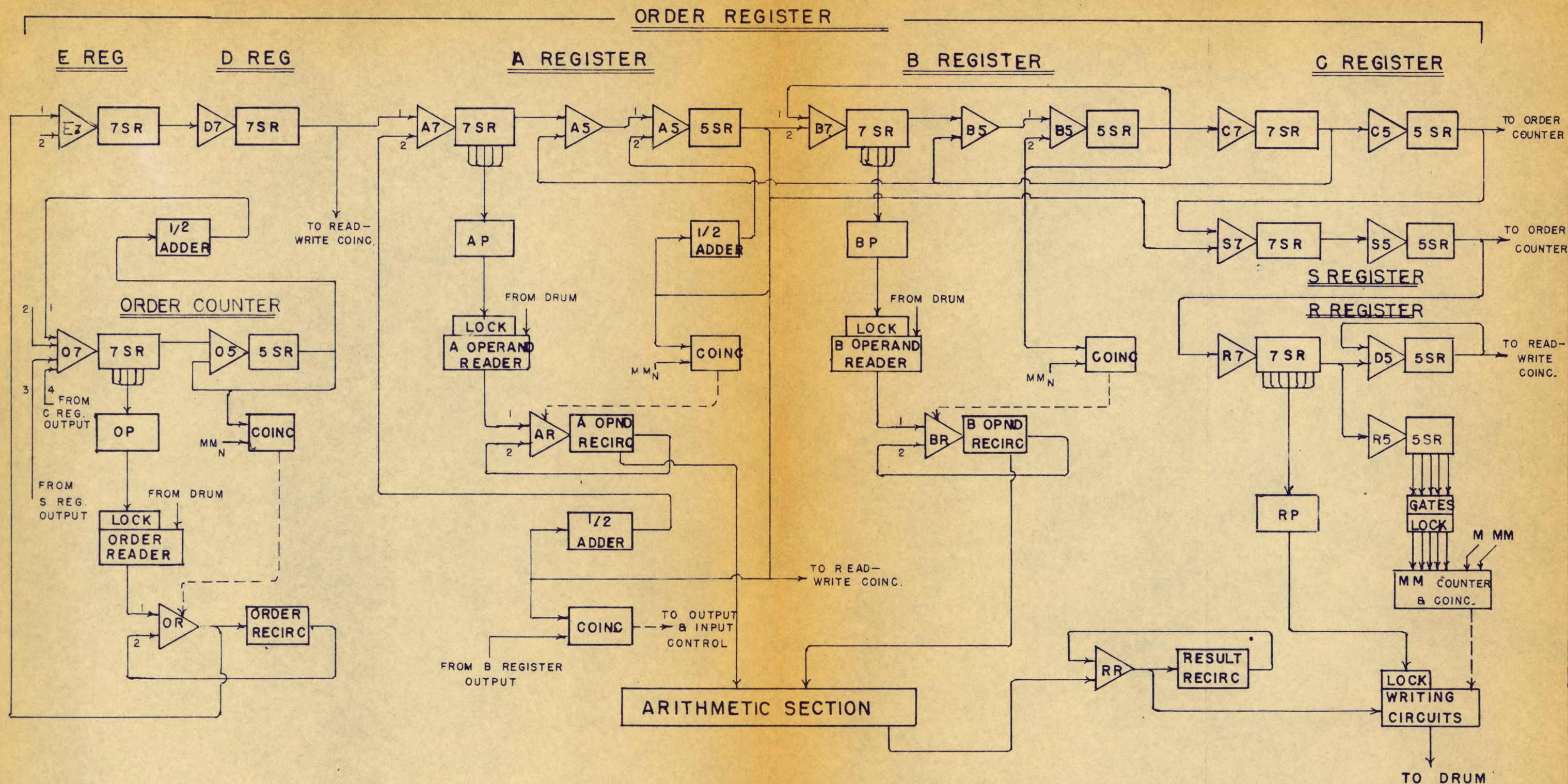




INPUTS

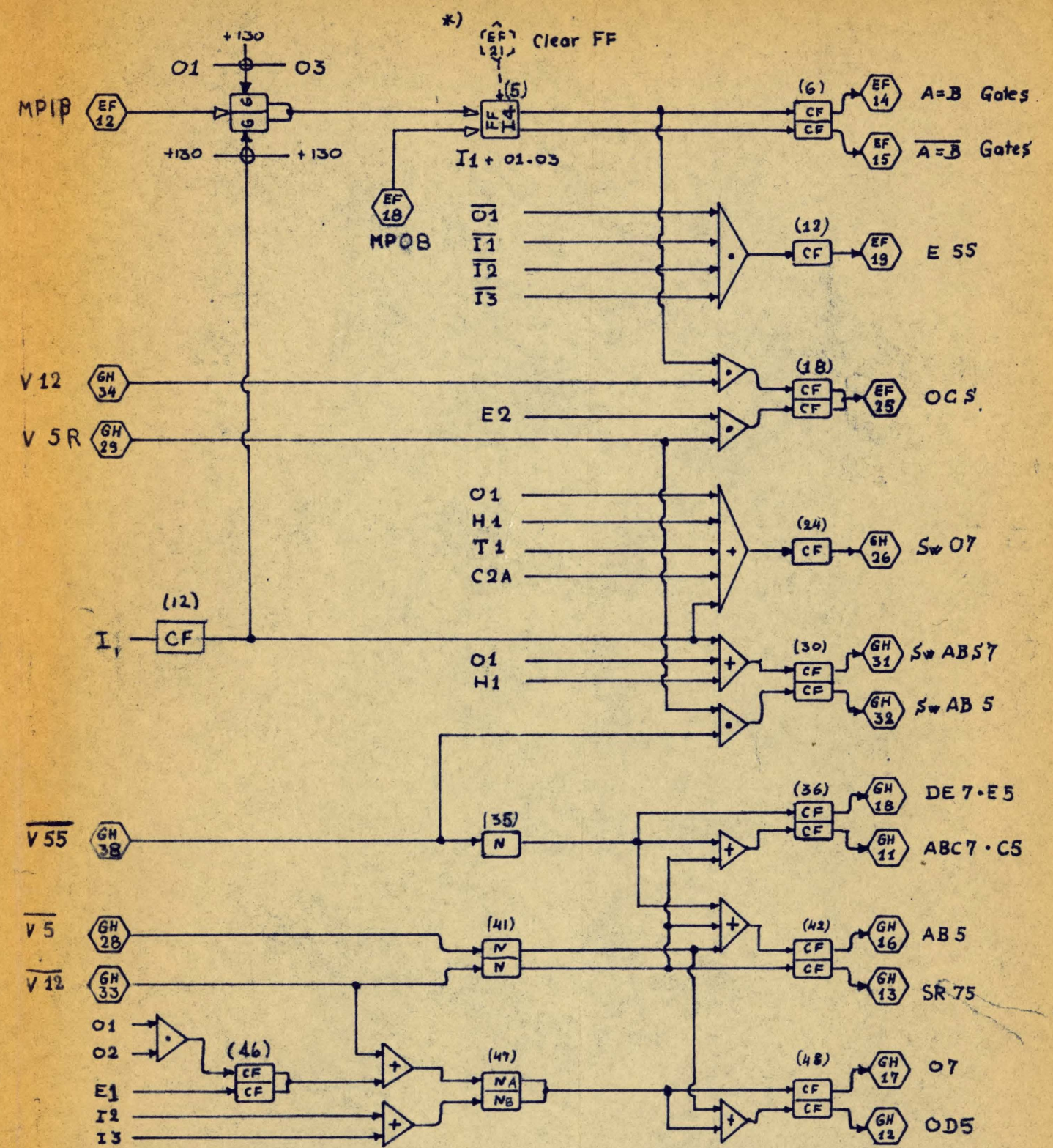
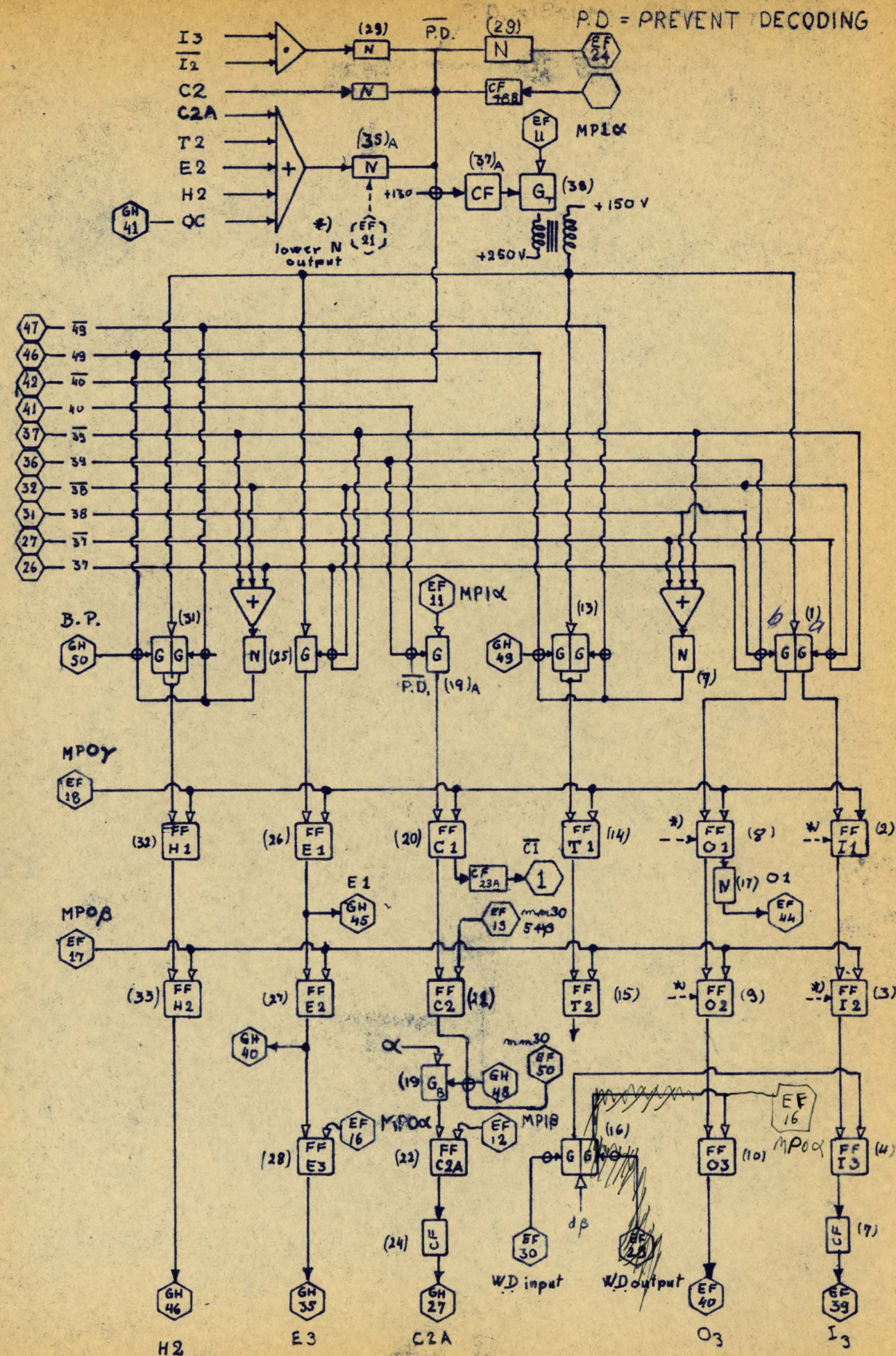
CONTROLS

Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN	
				Elec. Eng. Dept. - Computer Research	
				12 DIGIT SHIFT REGISTER	
Capacitors in _____				Dr.	Eng. JLA
Resistors in _____ 1/2 W, 10 %				Date	9-3-55
UNLESS OTHERWISE NOTED				Dwg No.	



— INFORMATION
 - - - - - CONTROL

Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec. Eng. Dept.— Computer Research		
				BLOCK DIAGRAM OF INFORMATION FLOW IN THE WISC		
Capacitors in _____ Resistors in _____ 1/2 W, 10%				UNLESS OTHERWISE NOTED		
				Dr. DLD	Eng. DSN	Date 9-2-55
				Dwg No. W-010L-01B		



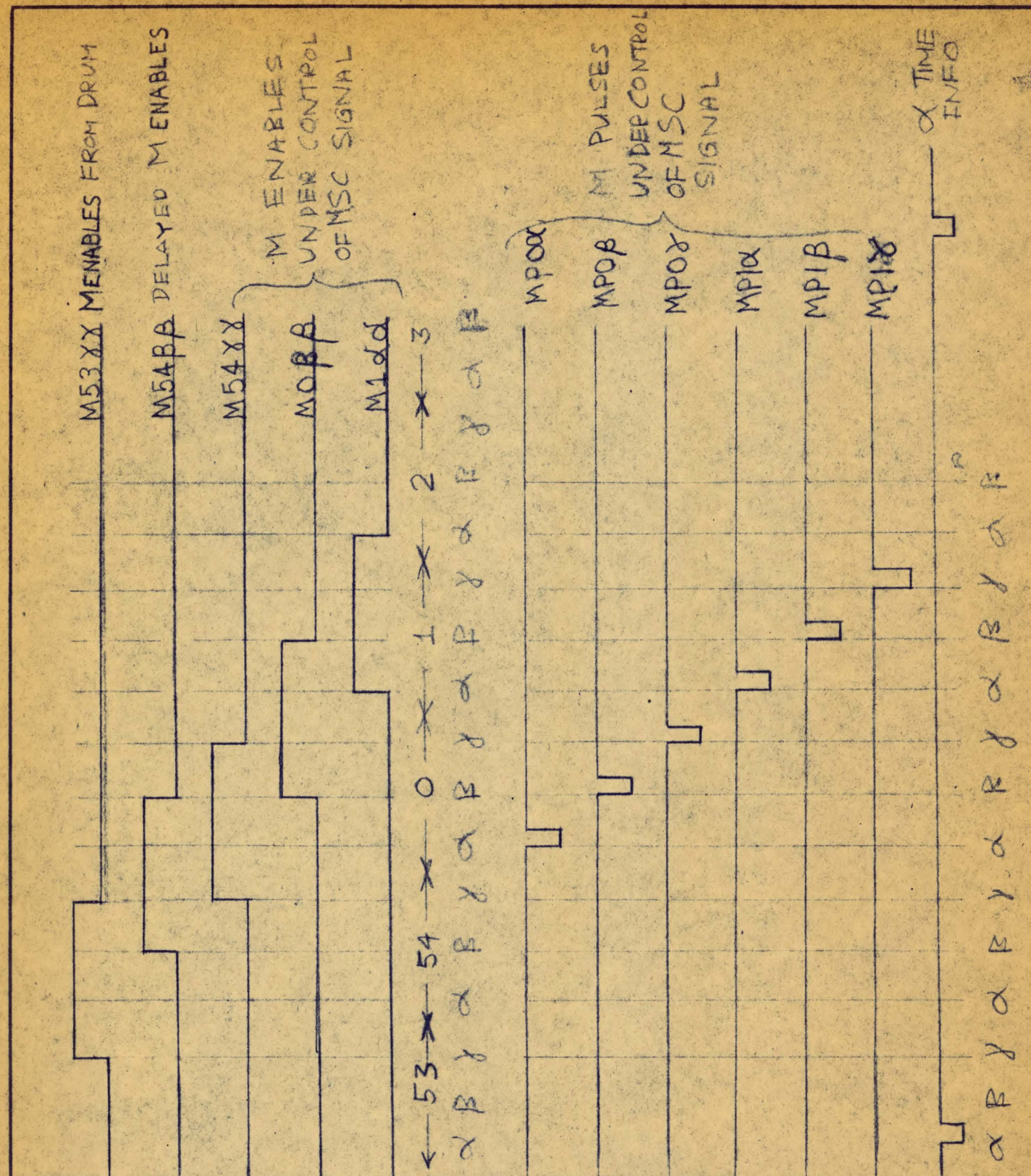
Issue No.	Revisions	By	Date
2	CORRECTIONS	AKS	6-18-56
3	CORRECTIONS	AKS	5-18-57
Capacitors in _____ } UNLESS OTHERWISE NOTED Resistors in _____ 1/2 W, 10%			

UNIVERSITY of WISCONSIN
Elec. Eng. Dept. - Computer Research

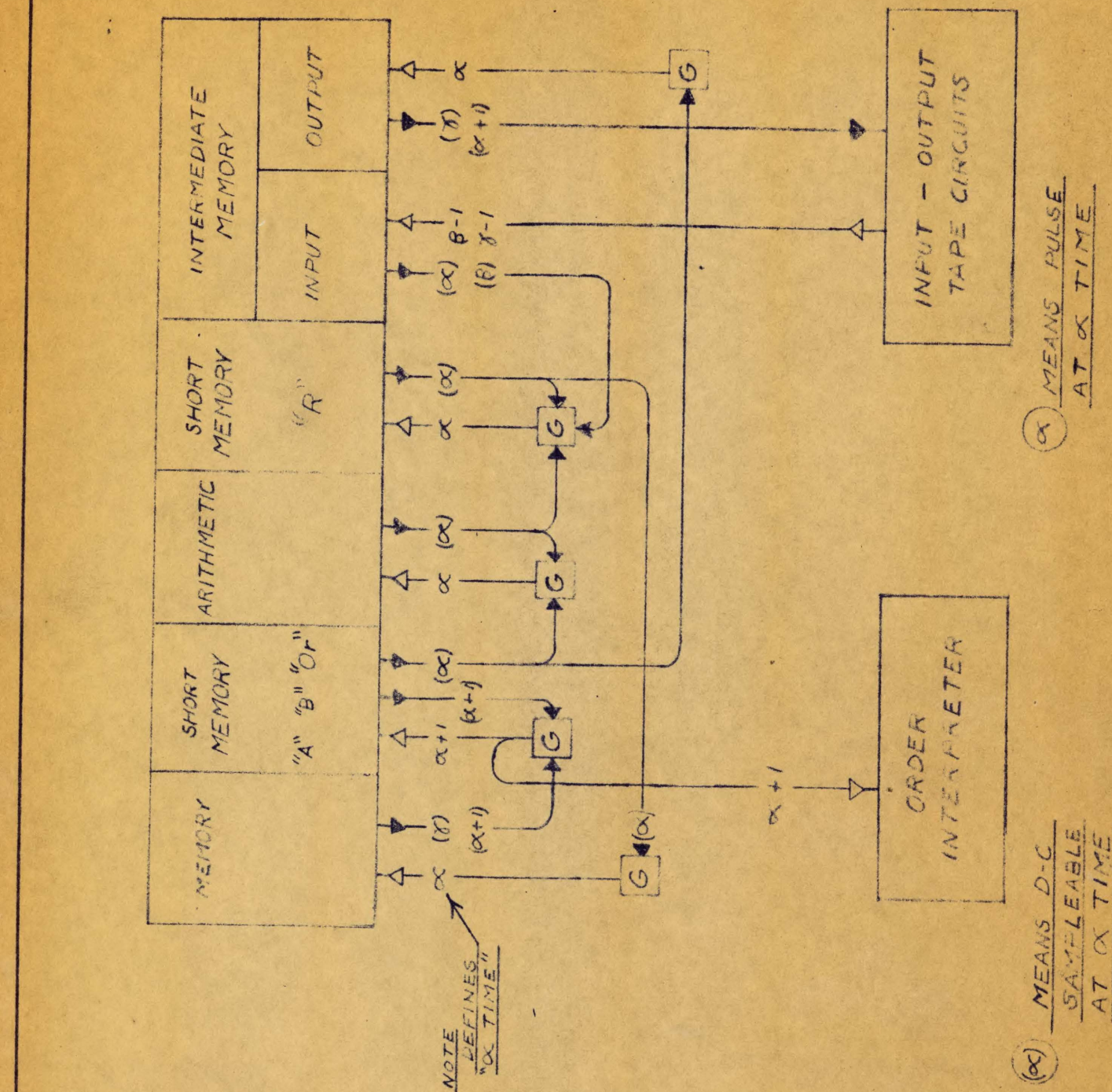
ORDER DECODER

Dr. P. B. Eng. D. S. N Date II-10-56

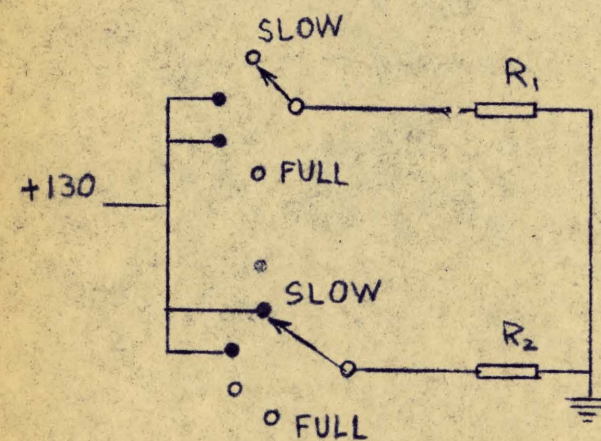
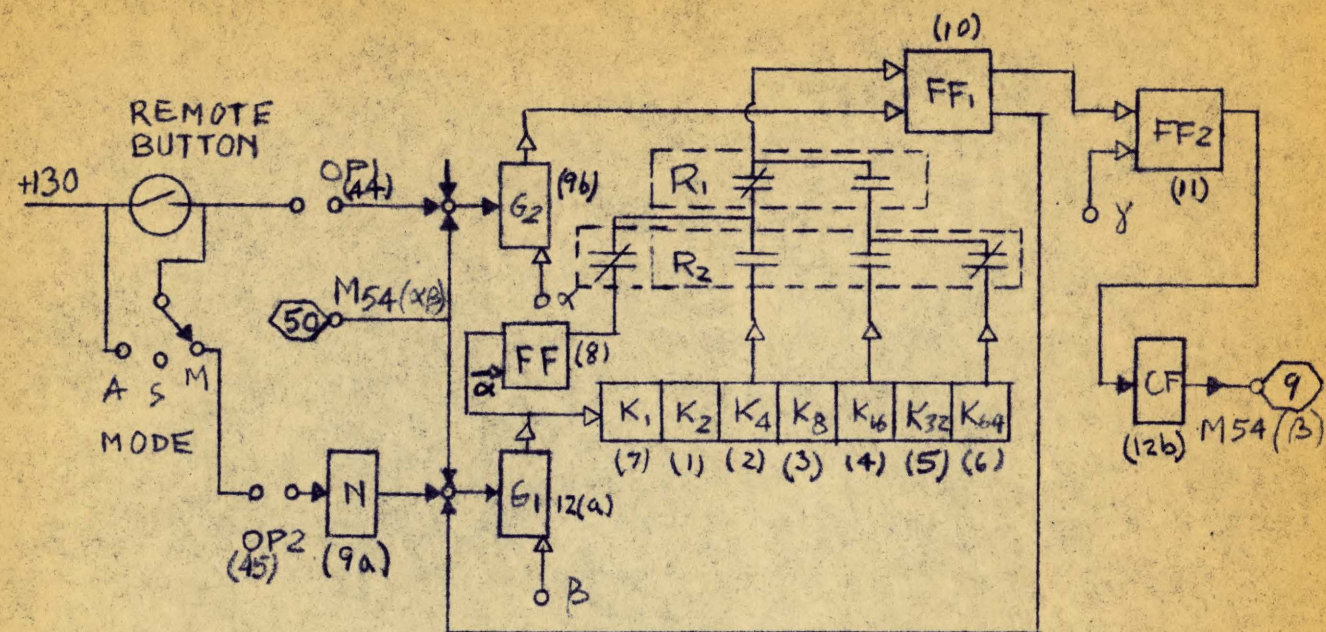
Dwg No. W-082L-088



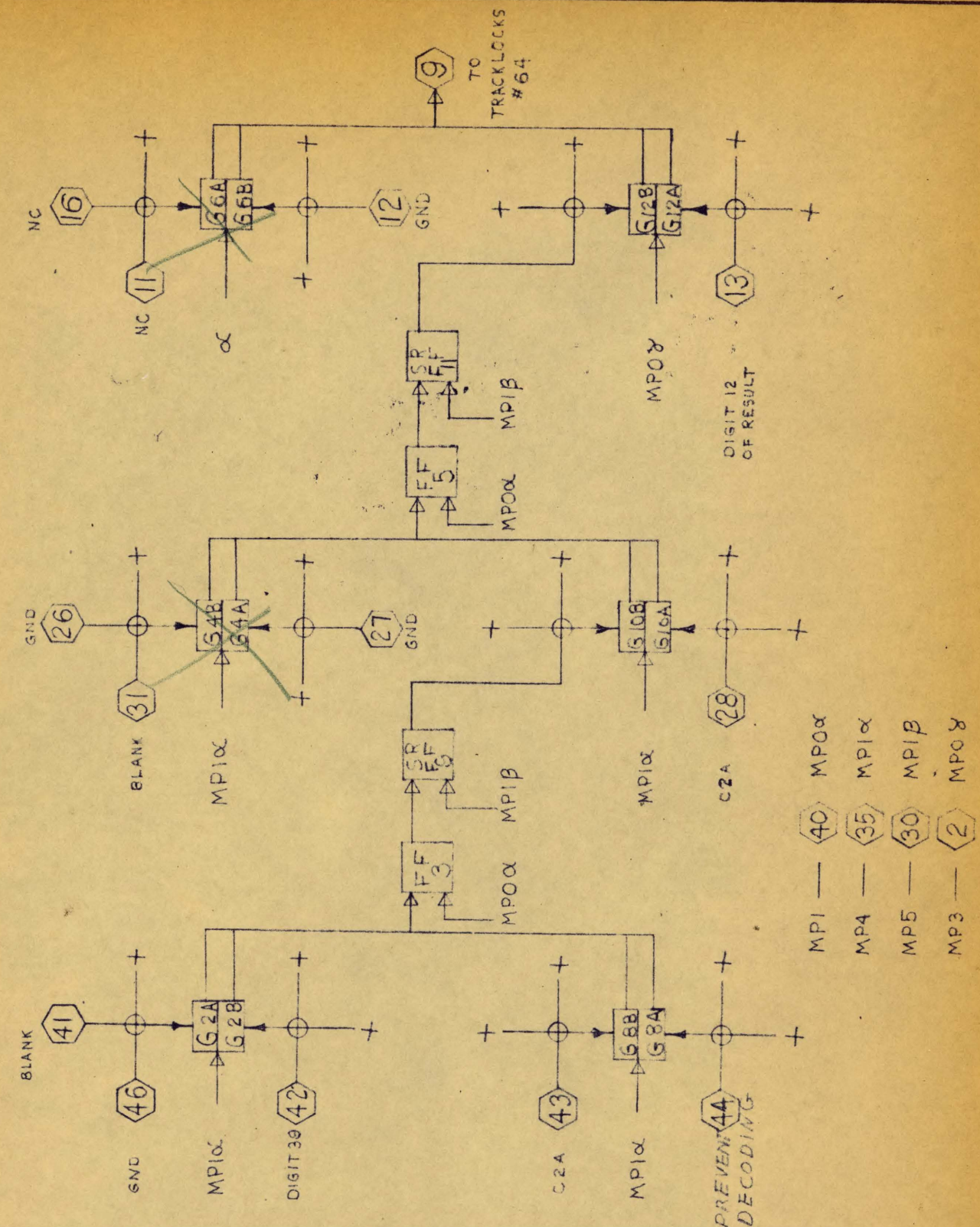
Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec. Eng. Dept.— Computer Research M ENABLES AND M PULSES		
Capacitors in _____ Resistors in _____ ½ W, 10 % } UNLESS OTHERWISE NOTED				Dr.	Eng.	Date
				Dwg No. W-080T-01A		



Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec.Eng.Dept.— Computer Research <i>PHASE RELATIONSHIPS</i> <i>BETWEEN DRUM SECTIONS</i>
Capacitors in _____ Resistors in _____ $\frac{1}{2}$ W, 10 %				Dr. <i>D</i> Eng. <i>CHD</i> Date <i>2/5/54</i> Dwg No. <i>W-010T-01A</i>



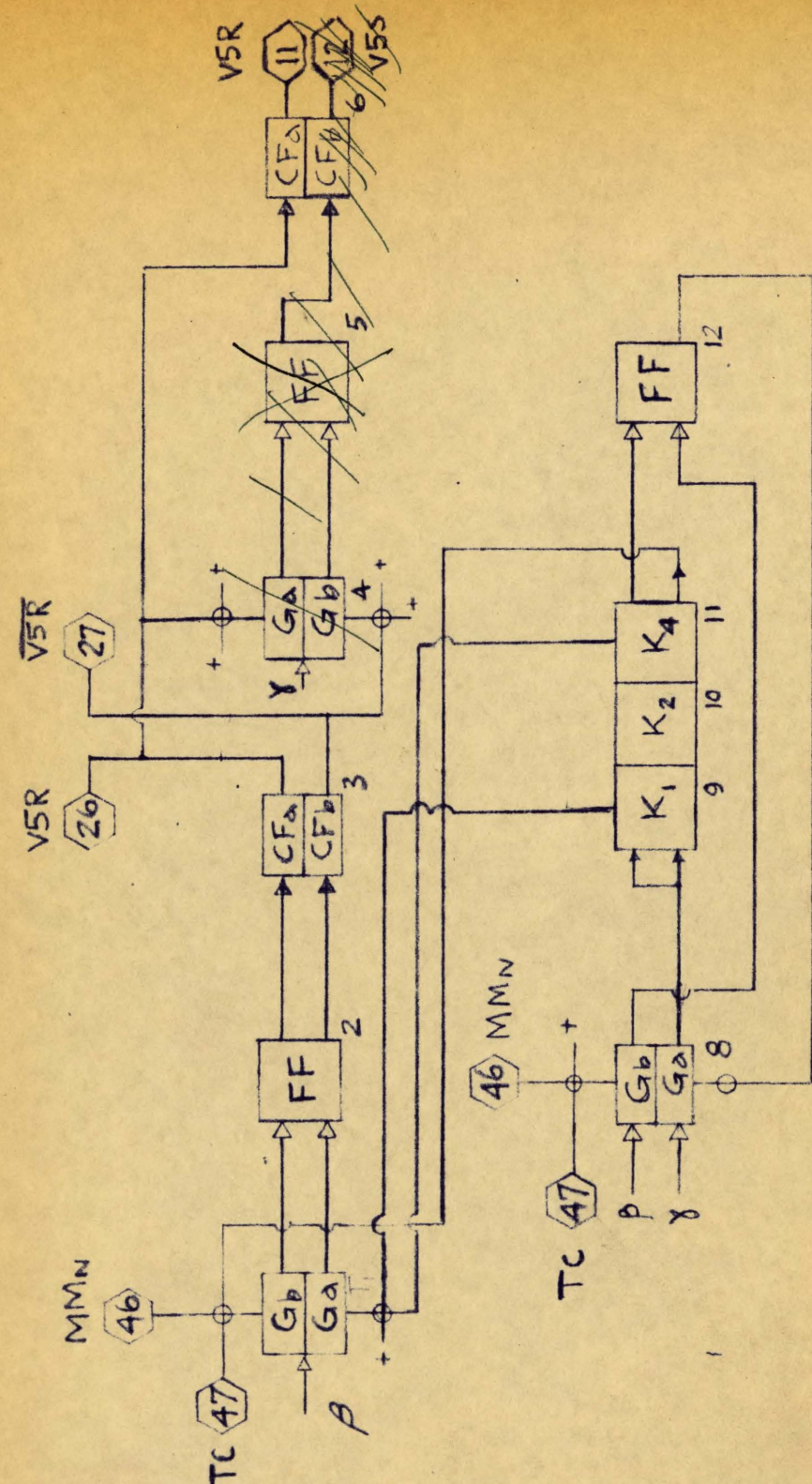
Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec.Eng.Dept.— Computer Research ONE PULSER
2	Pin & Tube Nos. INCLUDED	AKS	6/27/50	
Capacitors in _____ } UNLESS Resistors in _____ 1/2 W, 10% } OTHERWISE NOTED.				Dr. NDL Eng. JASK Date 11/11/55 Dwg No. W-135L-01A



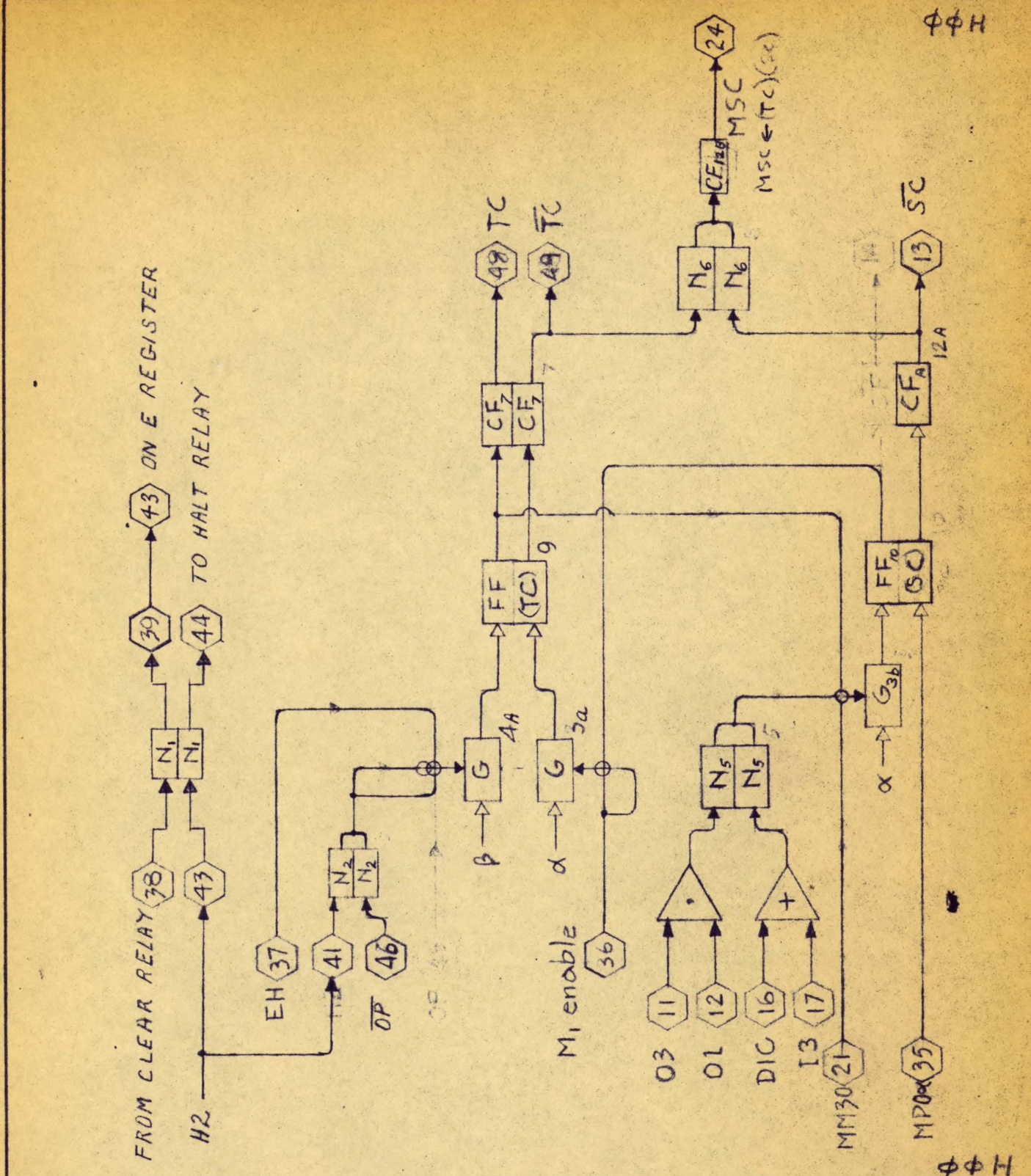
Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec.Eng.Dept.— Computer Research BLOCK WRITING CONTROL CHASSIS
NO 2	SEE ISSUE 1	DSN	8/17/55	
Capacitors in _____ } UNLESS Resistors in _____ 1/2 W, 10% } OTHERWISE NOTED				Dr. WMC Eng. DSN Date 5/17/54 Dwg No. W-082L-2A

Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec.Eng.Dept.— Computer Research		
2	SEE ISSUE 1	AKS	3/30/57	MANUAL CONTROLS & RELAYS		
Capacitors in _____ Resistors in _____ 1/2 W, 10%				Dr. NDL Dwg No. W-161L-03A	Eng. DSN	Date 11/7/55
} UNLESS OTHERWISE NOTED						

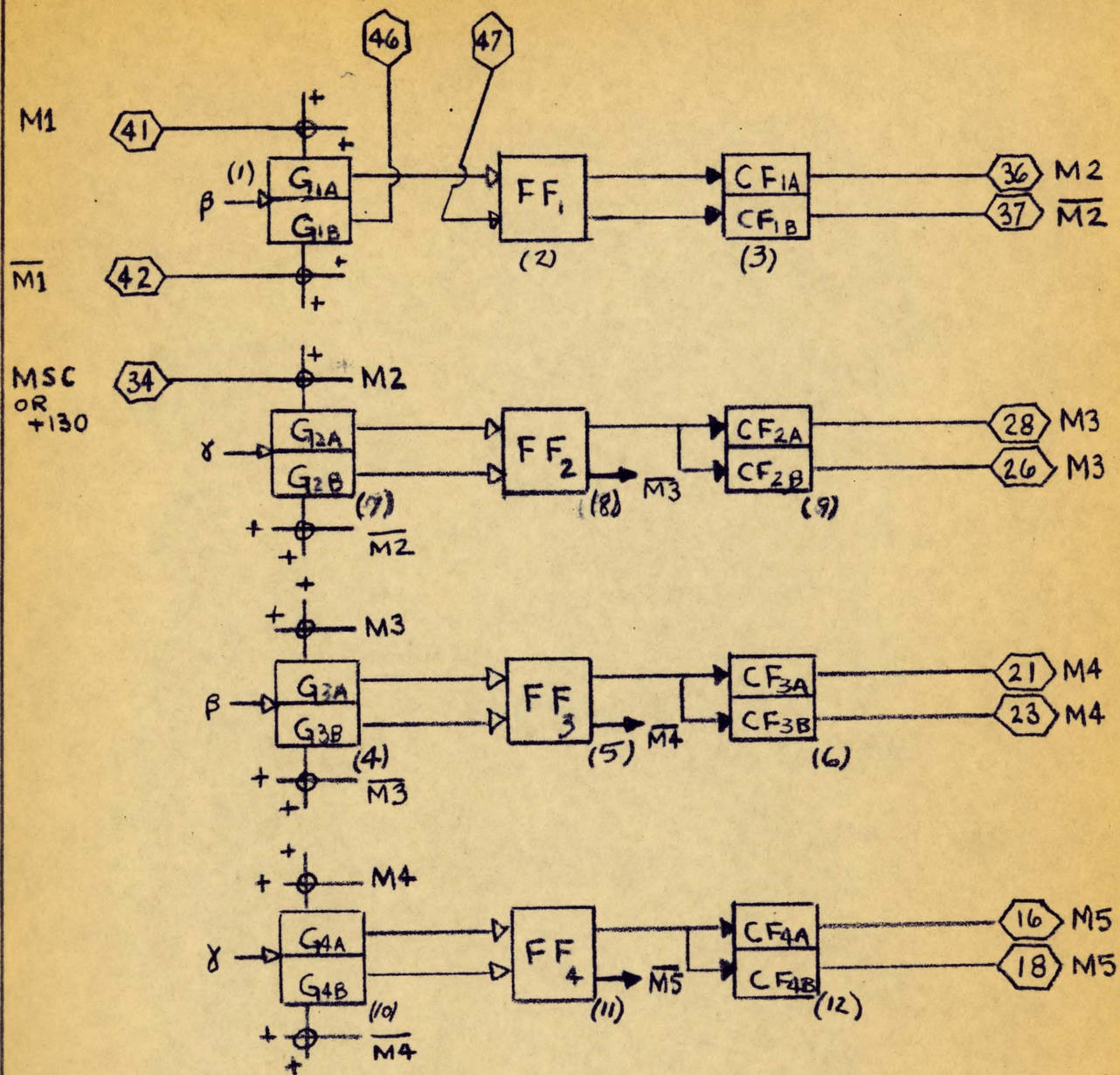
Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec. Eng. Dept. - Computer Research CONTROL CONSOLE AND RELAYS		
				Dr. NDL	Eng. DSN	Date 11-11-55
Capacitors in _____ Resistors in _____ $\frac{1}{2}$ w, 10%				} UNLESS OTHERWISE NOTED		
				Dwg No. W-161L-02A		



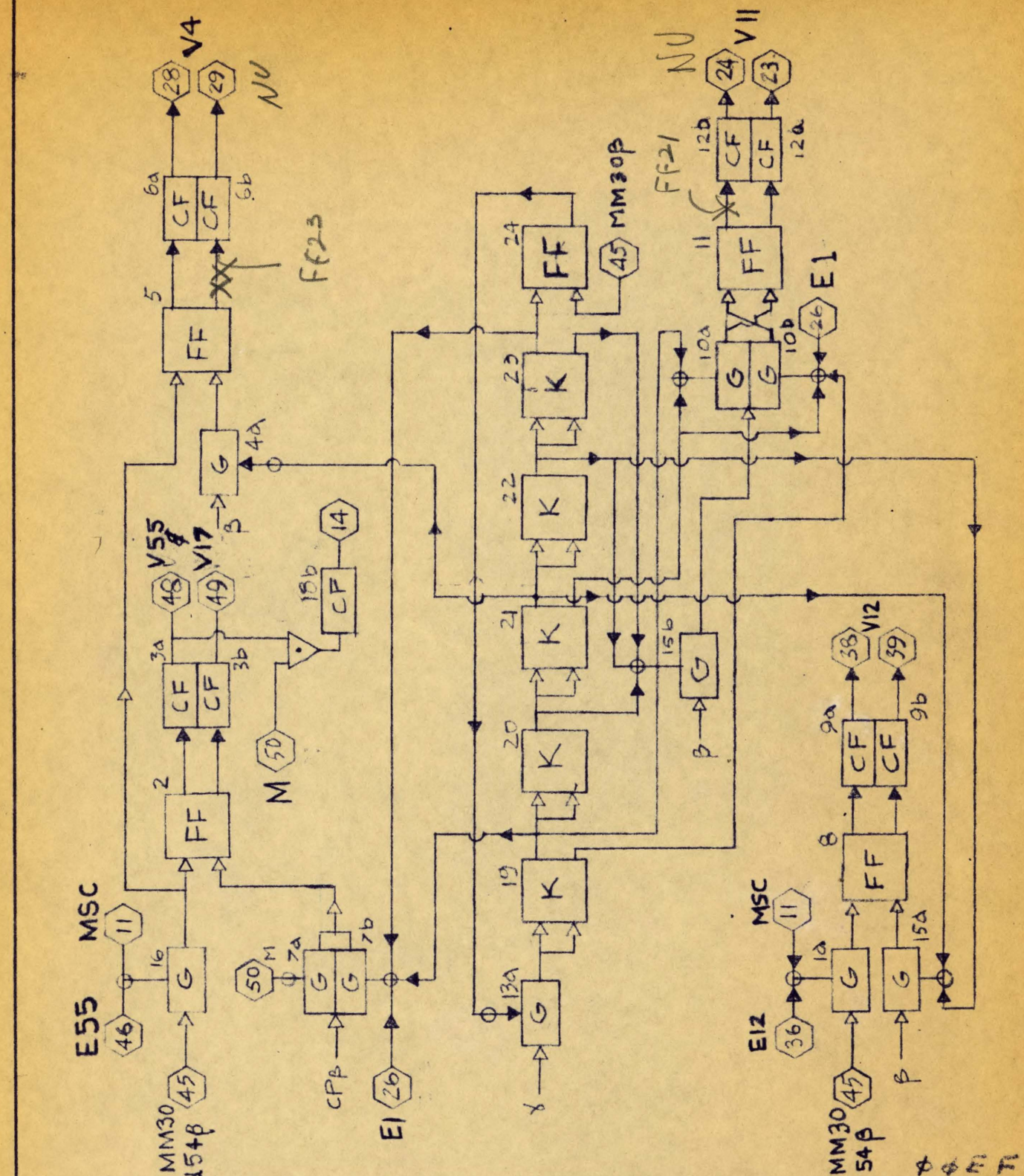
Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec.Eng.Dept.- Computer Research MMN TIME CHECK GENERATOR			
				Dr. NDL	Eng. DSN	Date	11/5/55
Capacitors in _____ Resistors in _____ 1/2 W, 10% } UNLESS OTHERWISE NOTED				Dwg No. W-084L-01A			



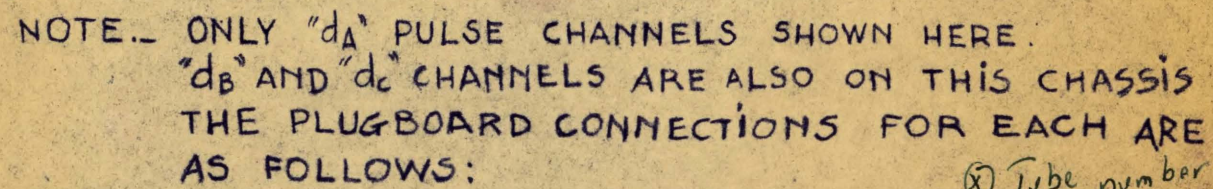
Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec.Eng.Dept.- Computer Research WISC START STOP CONTROL CHASSIS SSC			
2	SEE ISSUE #1	A.K.S.	3/30/57	Dr. 1128	Eng. DSN	Date	11-4-55
Capacitors in _____ Resistors in _____ 1/2 W, 10% } UNLESS OTHERWISE NOTED				Dwg No. W-084L-03A			



Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec. Eng. Dept. - Computer Research	
				ENABLE VOLTAGE DELAY UNIT EVD	
Capacitors in _____ Resistors in _____ 1/2 W, 10%				UNLESS OTHERWISE NOTED	
				Dr. AKS	Eng. DSN
				Date 6/26/56	
				Dwg No. W-043L-02A	



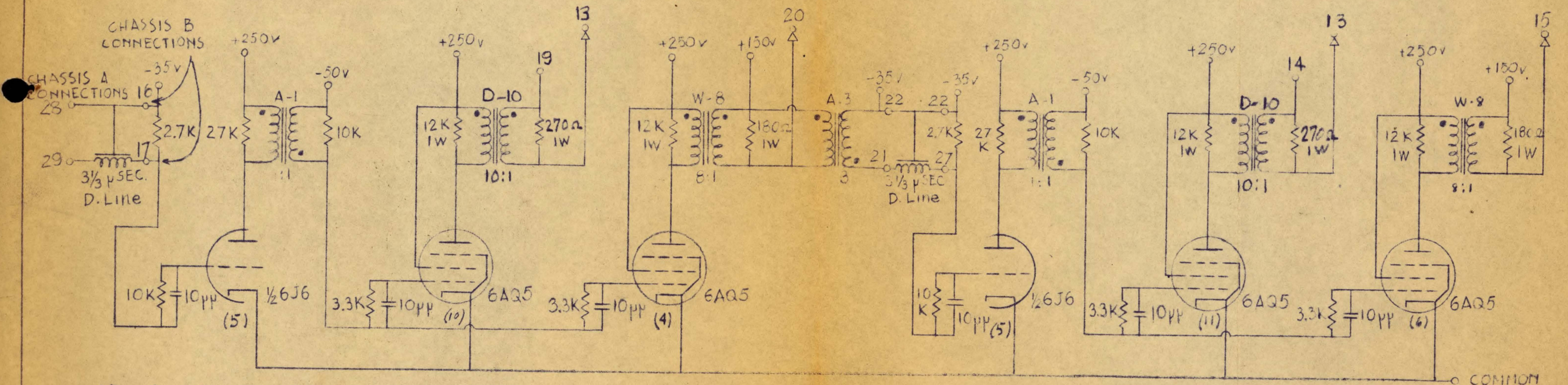
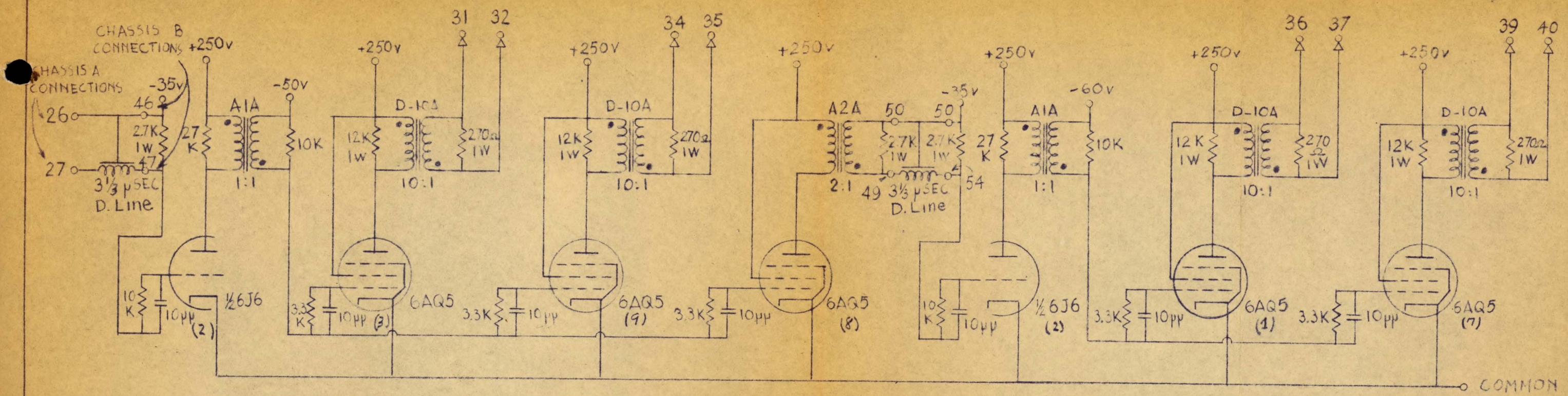
Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec. Eng. Dept. - Computer Research	
				V - VOLTAGE GENERATOR TVG	
Capacitors in _____ Resistors in _____ 1/2 W, 10%				UNLESS OTHERWISE NOTED	
				Dr. NDL	Eng. DSN
				Date 11-5-55	
				Dwg No. W-084L-02A	



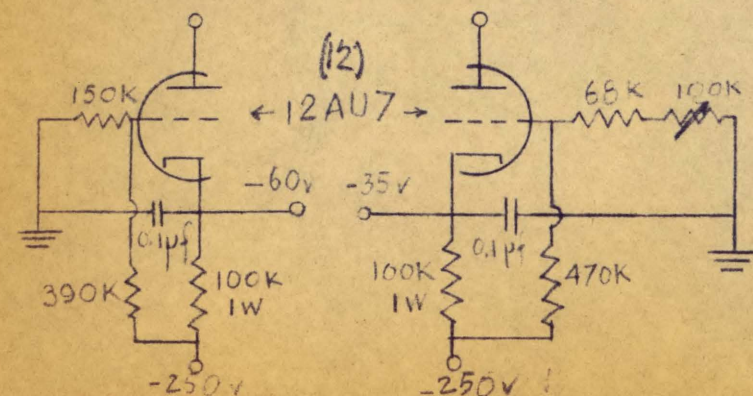
CHANNEL	TIMING PULSE INPUT		DIGIT PULSE OUTPUT		
	POS	NEG	#1	#2	#3
dA β	41	42	43 $\textcircled{1}$	44 $\textcircled{2}$	45 $\textcircled{7}$
dB	26	27	28 $\textcircled{3}$	29 $\textcircled{4}$	30 $\textcircled{8}$
dc	11	12	13 $\textcircled{5}$	14 $\textcircled{6}$	15 $\textcircled{9}$

[illegible]

Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec. Eng. Dept.— Computer Research		
				MINOR MEMORY ENABLE DELAY		
Capacitors in _____ } UNLESS Resistors in _____ 1/2 W, 10% } OTHERWISE NOTED				Dr. WMC	Eng.	Date 8/16/56
				Dwg No. W-084L-04A		

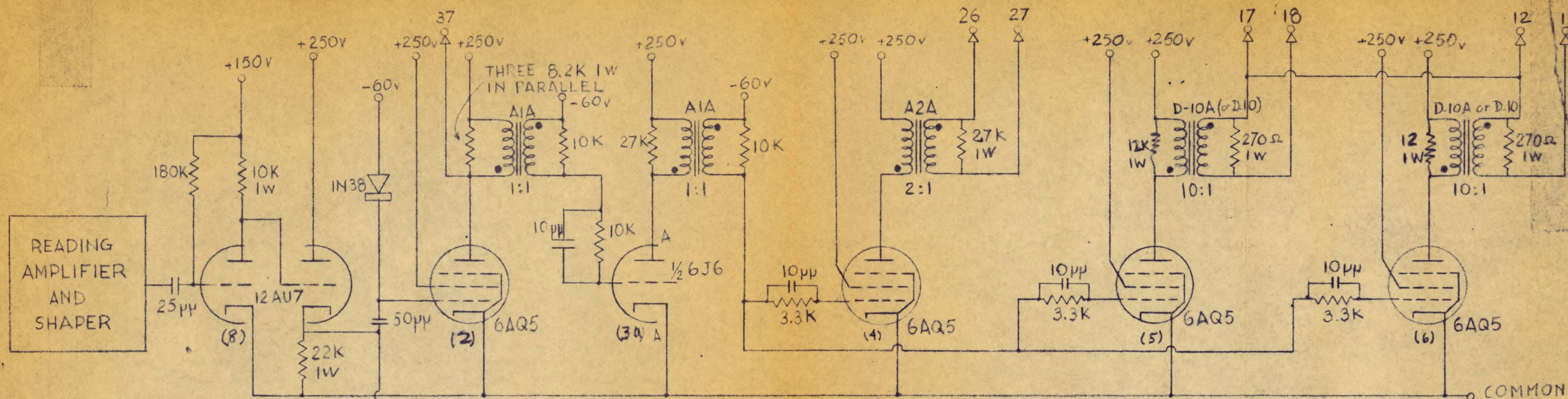


-35v & -60v BIAS SOURCES

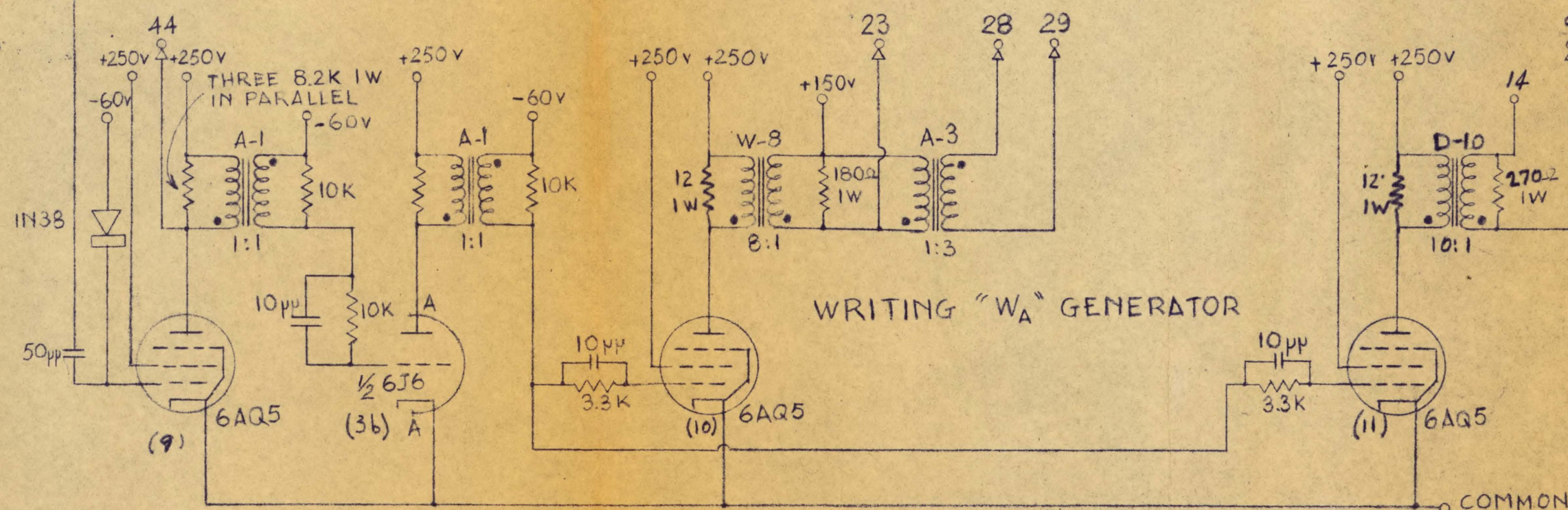


NOTE... 3 1/3 pSEC DELAY LINES ARE EXTERNALLY CONNECTED AS SHOWN

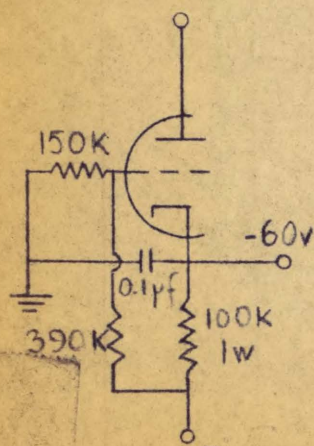
Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec.Eng.Dept.- Computer Research CLOCK PULSE GENERATOR CHASSIS B		
				Dr. NDL	Eng. AKS	Date 7-29-54
Capacitors in _____ Resistors in _____ 1/2 W, 10% } UNLESS OTHERWISE NOTED				Dwg No. W-1315-03B		



DIGIT "TA" GENERATOR



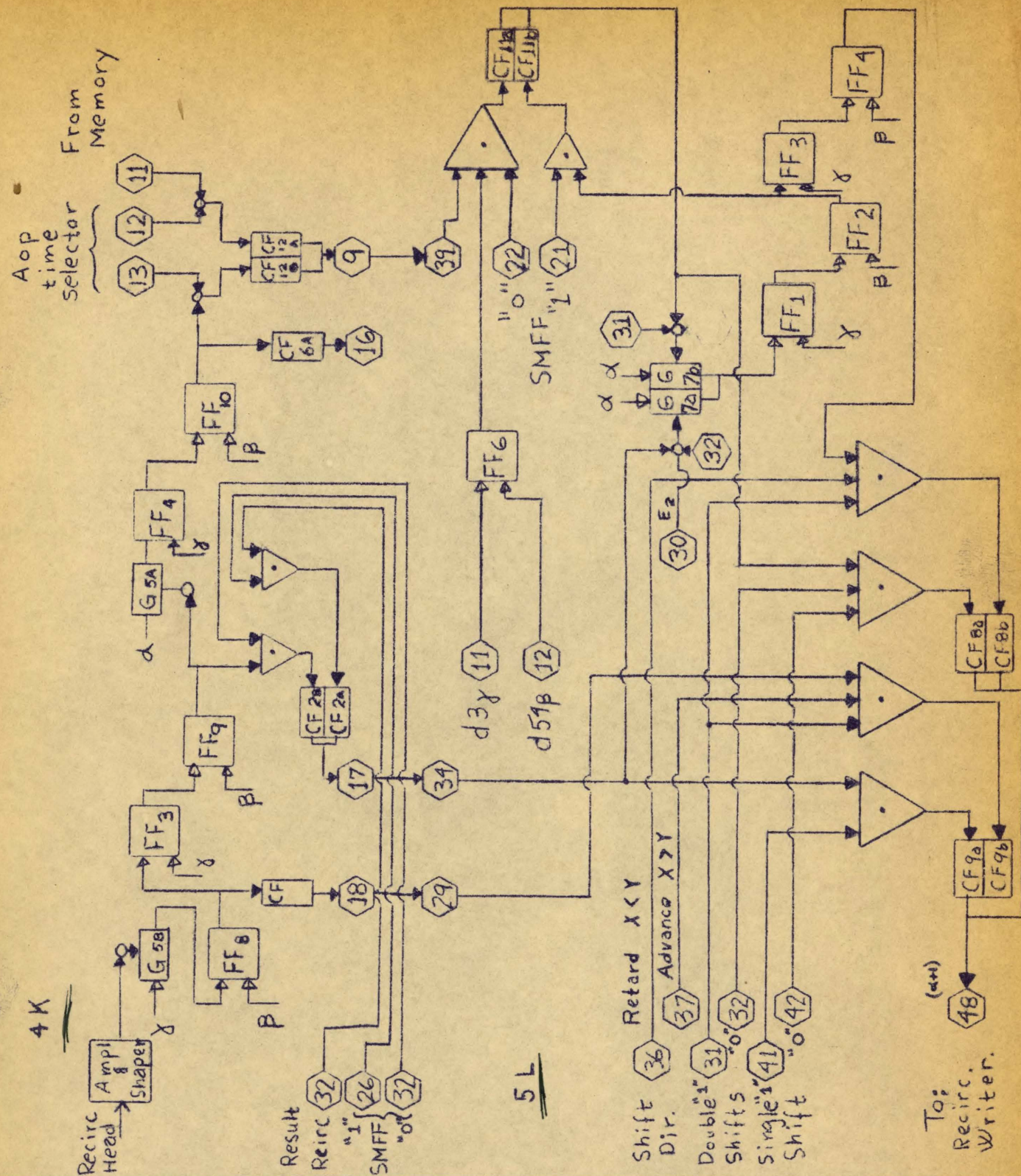
-60V BIAS SOURCE



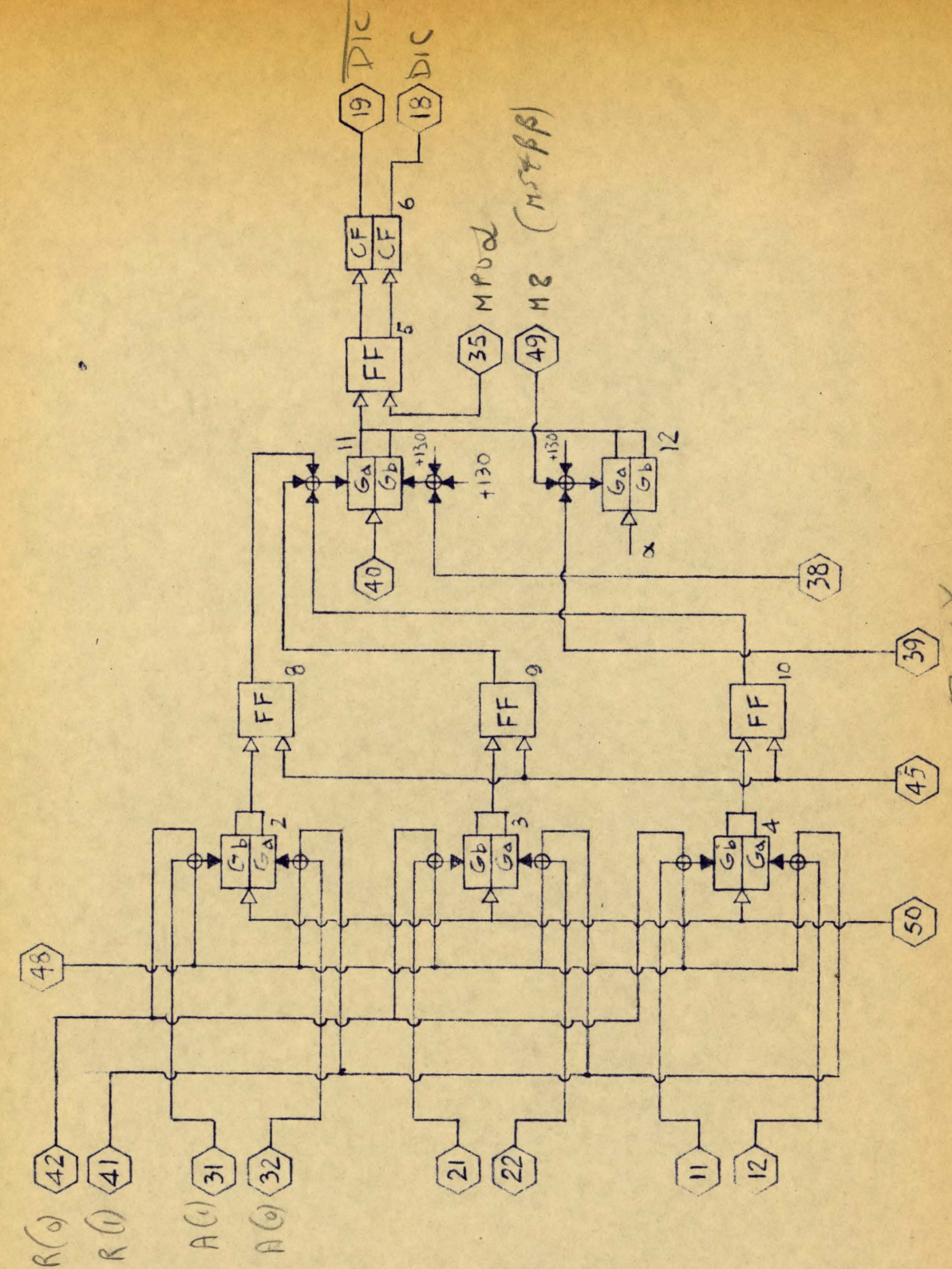
NOTE... DELAY LINES ARE EXTERNALLY CONNECTED AS FOLLOWS :

1 pSEC. SHORTED DELAY LINES : 37 + 38
44 + 45
3 1/3 pSEC. DELAY LINES : 26 + 27
28 + 29

Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec. Eng. Dept. - Computer Research	
Capacitors in μK Ω Resistors in μpF $\frac{1}{2}$ W, 10%				CLOCK PULSE GENERATOR CHASSIS A	
UNLESS OTHERWISE NOTED				Dr. N.D.L	Eng. A.K.S
				Date 7-27-54	
				Dwg No. W-1315-028	



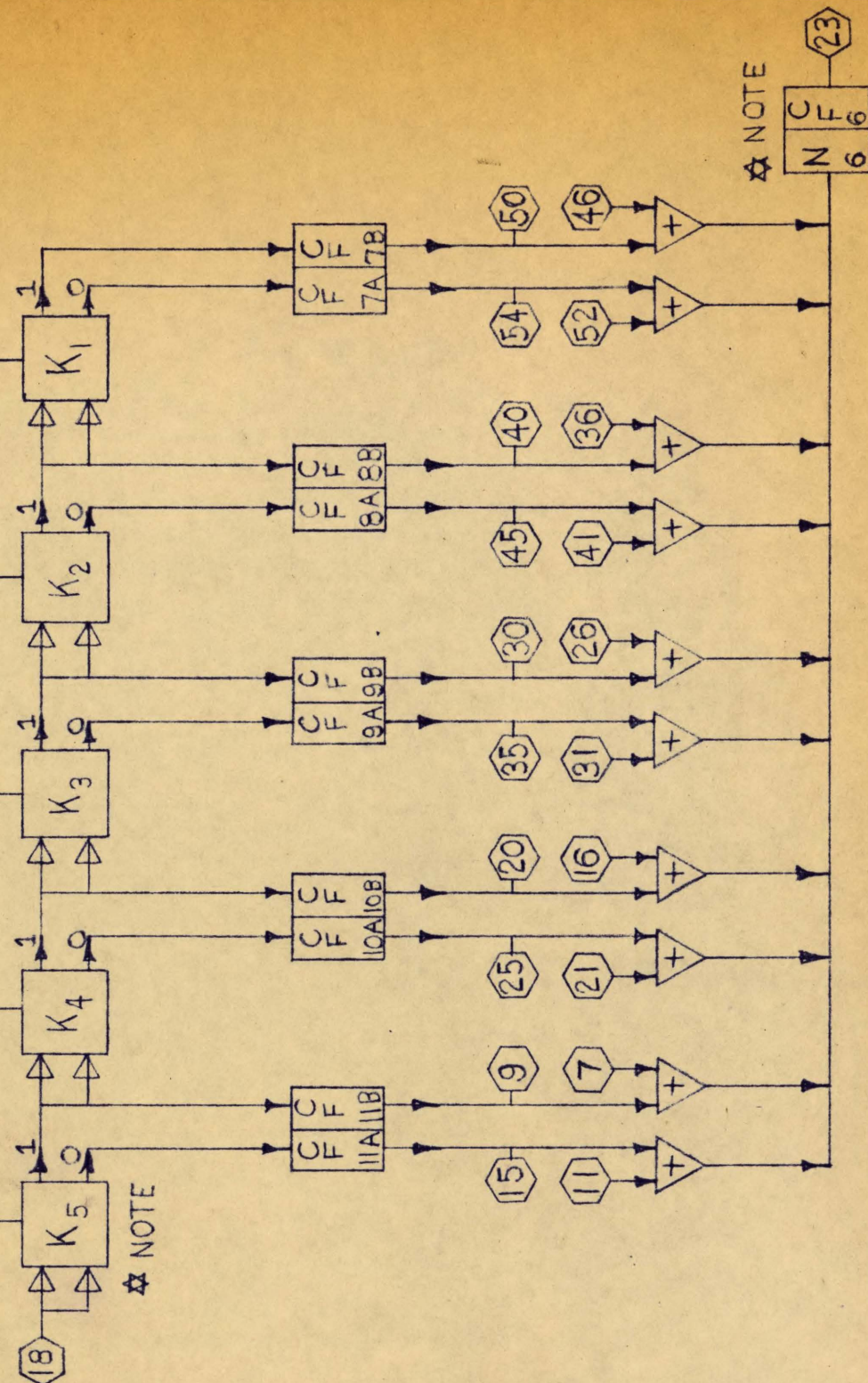
Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec. Eng. Dept.— Computer Research <i>ERS & RS</i> A Operand Recirculator.	
Capacitors in _____ Resistors in _____ 1/2 W, 10%				Dr. XPR	Eng. XPR
				Date	
				Dwg No. W-122L-01A	



Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec. Eng. Dept.— Computer Research READ - WRITE COINCIDENCE DETECTOR	
Capacitors in _____ Resistors in _____ 1/2 W, 10%				Dr. NDL	Eng. DSN
				Date 11/20/55	
				Dwg No. W-044L-03A	

★ NOTE
13

RESET TO ZERO



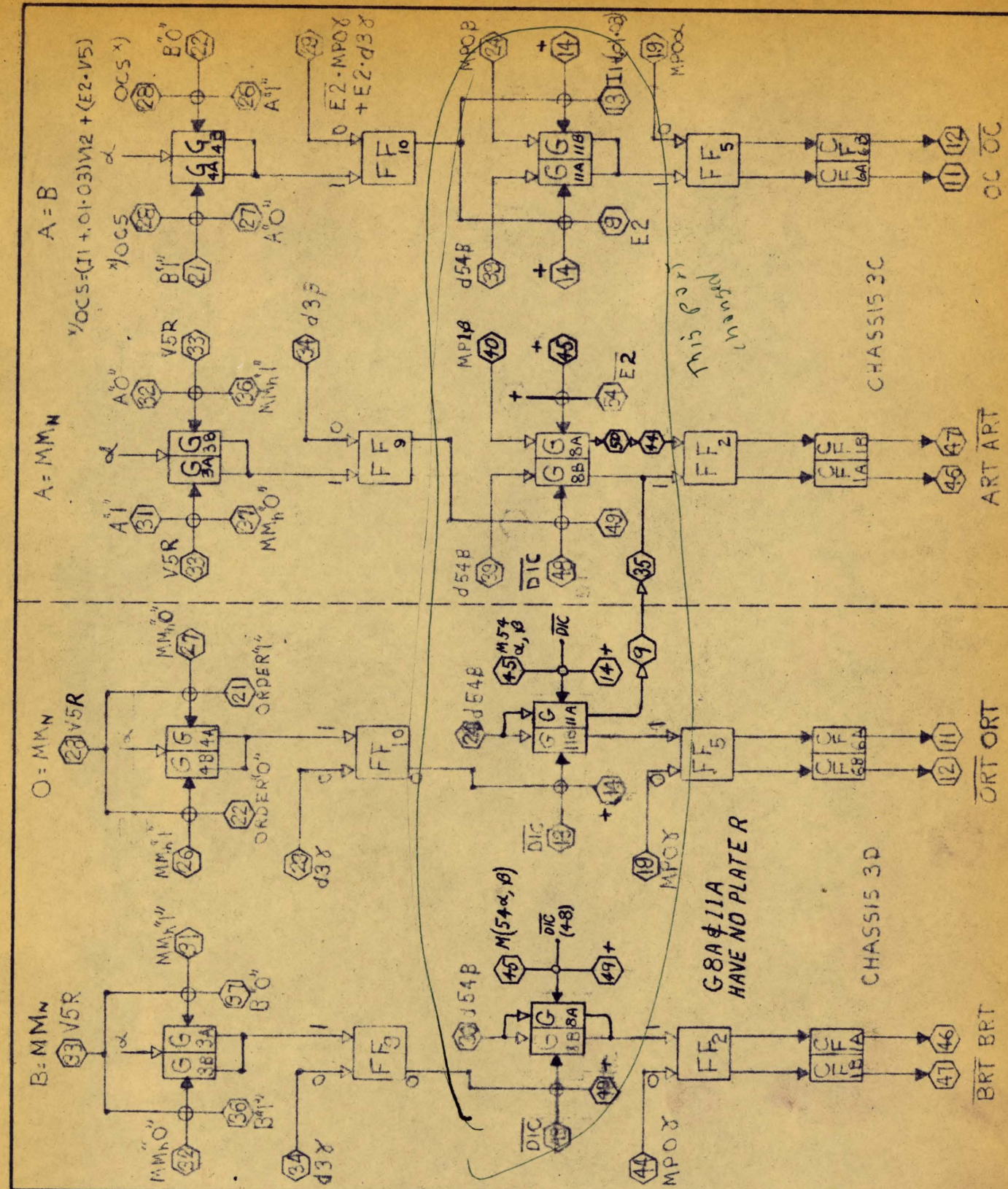
★ NOTE NON-STANDARD CIRCUITS SEE DWG. W-044S-01B

Issue No.	Revisions	By	Date

UNIVERSITY of WISCONSIN
Elec. Eng. Dept. - Computer Research
COUNTER AND COINCIDENCE
DETECTOR UNIT PKG 2

Dr. WMC Eng. AKS Date 7/19/56
Dwg No. W-044 L-01A

Capacitors in _____
Resistors in _____ 1/2 W, 10% } UNLESS OTHERWISE NOTED

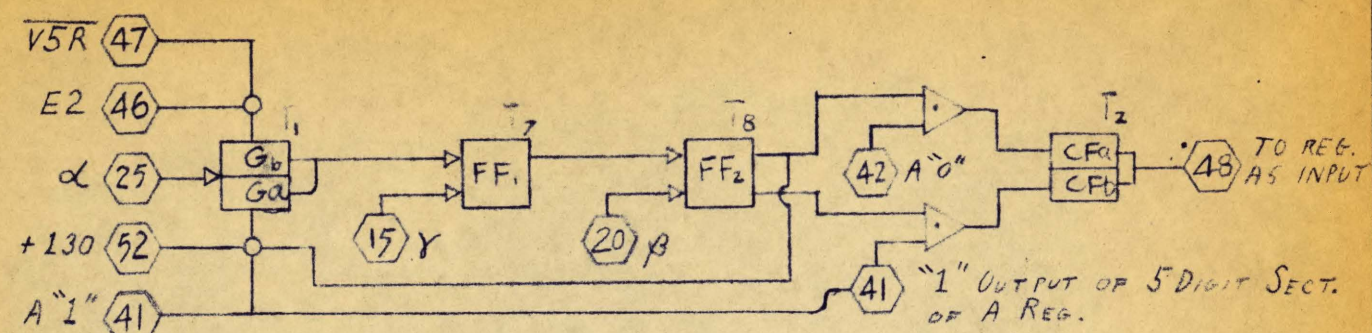


Issue No.	Revisions	By	Date
2	ADDITIONS	N.P.	5-18-57

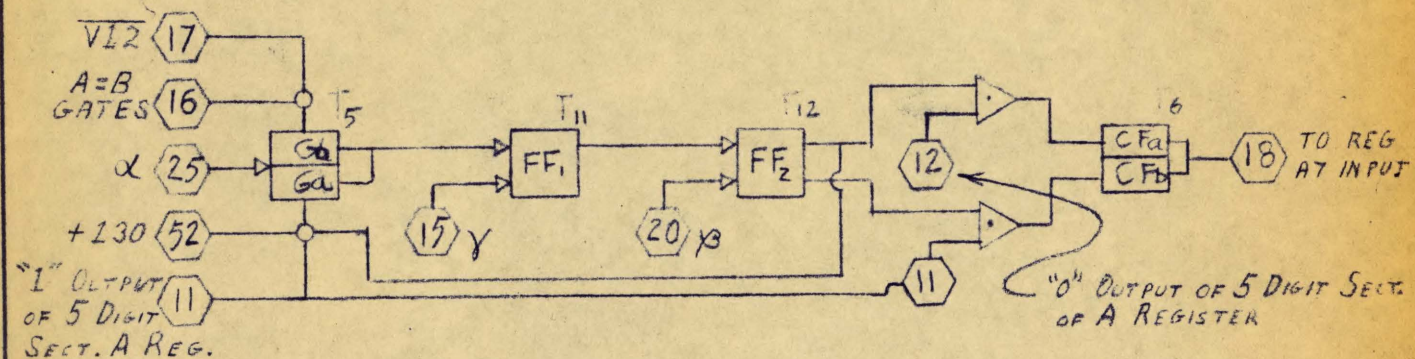
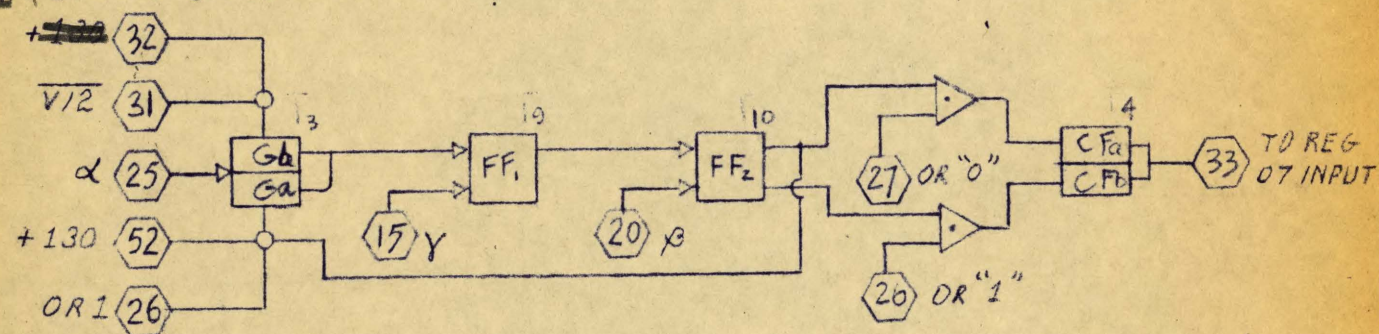
UNIVERSITY of WISCONSIN
Elec. Eng. Dept. - Computer Research
SERIAL COINCIDENCE DETECTORS

Dr. WMC Eng. A.K.S. Date 8/3/56
Dwg No. W-044 L-04A

Capacitors in _____
Resistors in _____ 1/2 W, 10% } UNLESS OTHERWISE NOTED

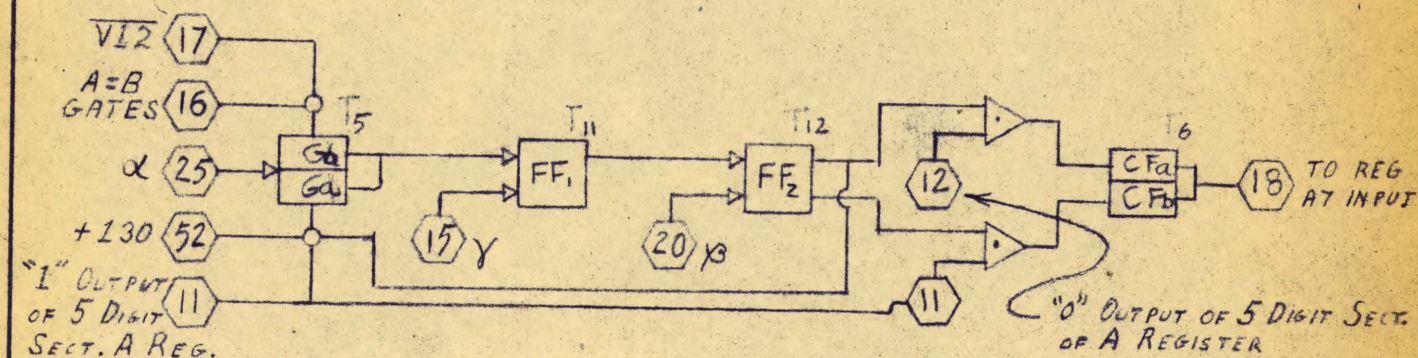
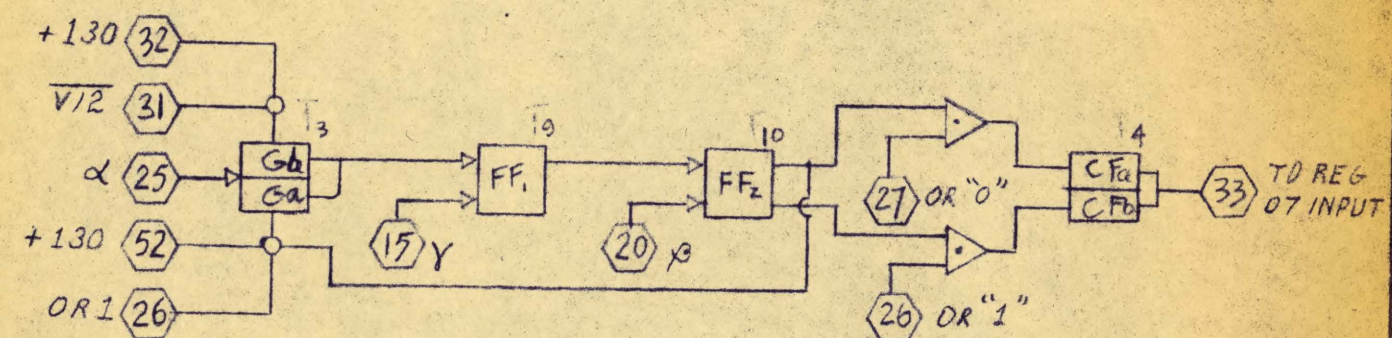
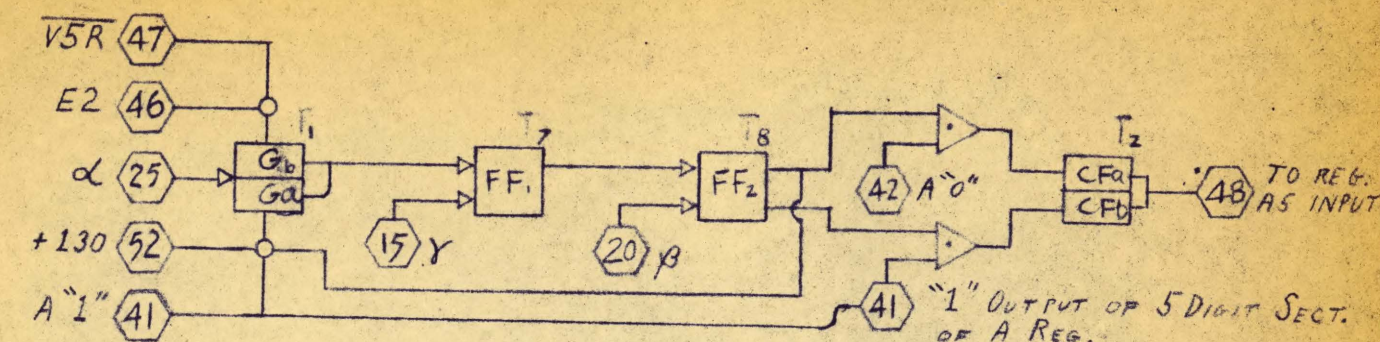


Block Order Counter



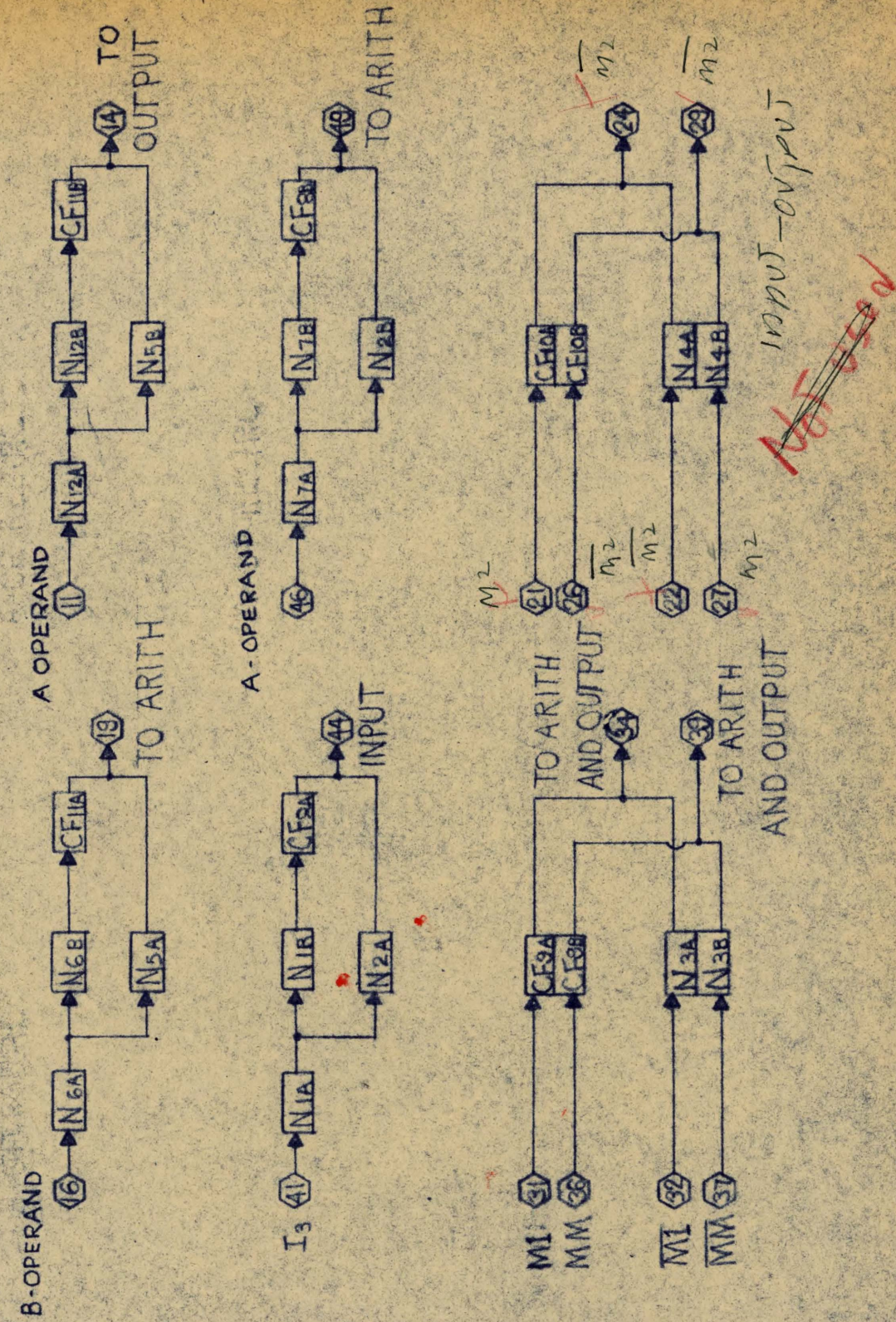
4E

Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec. Eng. Dept.— Computer Research		
2	SEE ISSUE #1	A.K.S.	3/30/57	T TRIPLE HALF ADDER 4E		
Capacitors in _____ Resistors in _____ 1/2 W, 10% }				Dr. NDL	Eng. DSM	Date 10-21-57
UNLESS OTHERWISE NOTED				Dwg No. W-046L-01A		



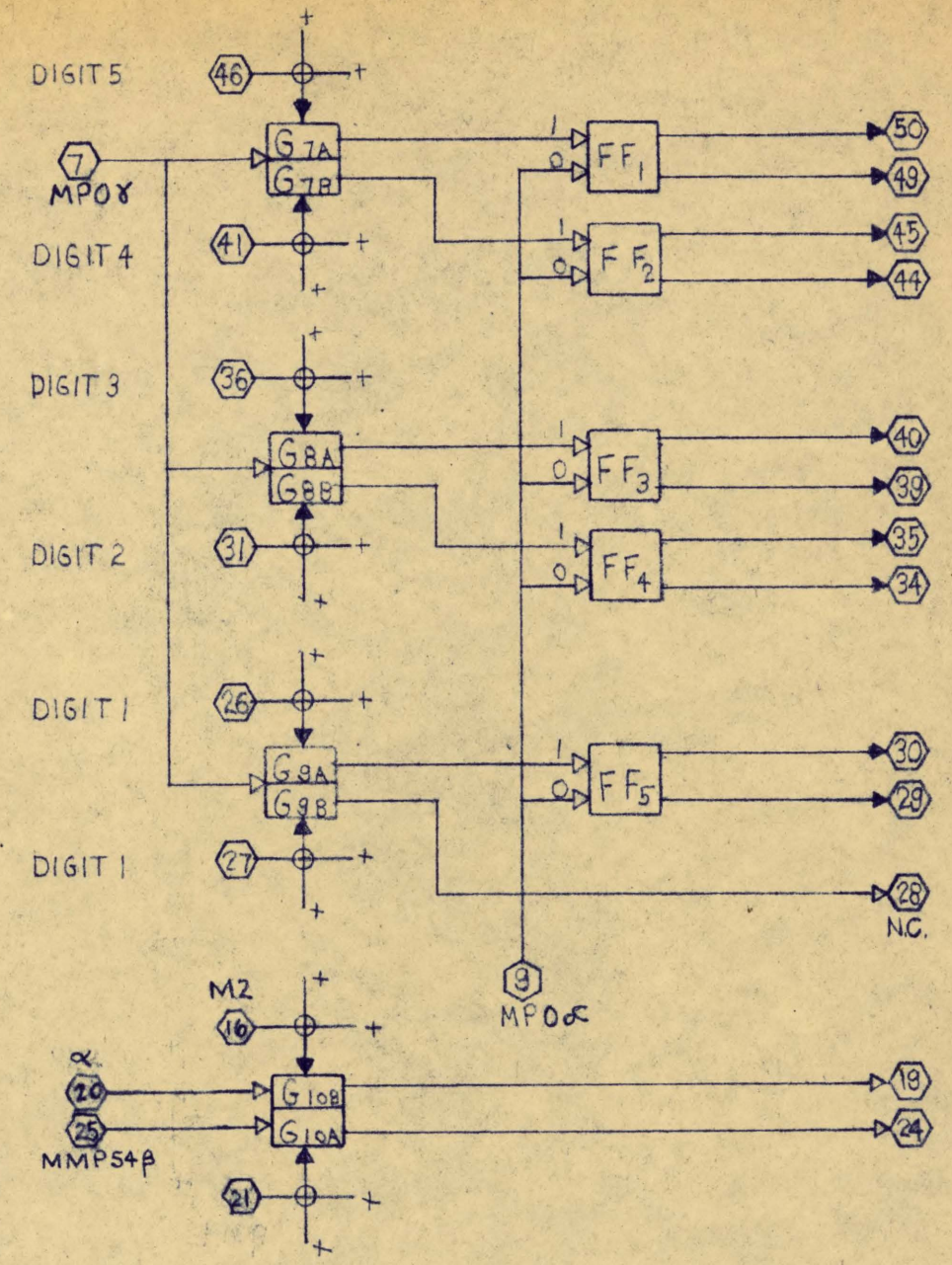
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Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec. Eng. Dept.— Computer Research		
2	SEE ISSUE #1	A.K.S.	3/30/57	T TRIPLE HALF ADDER 4E		
Capacitors in _____ Resistors in _____ 1/2 W, 10% }				Dr. NDL	Eng. DSM	Date
UNLESS OTHERWISE NOTED				Dwg No. W-046L-01A		

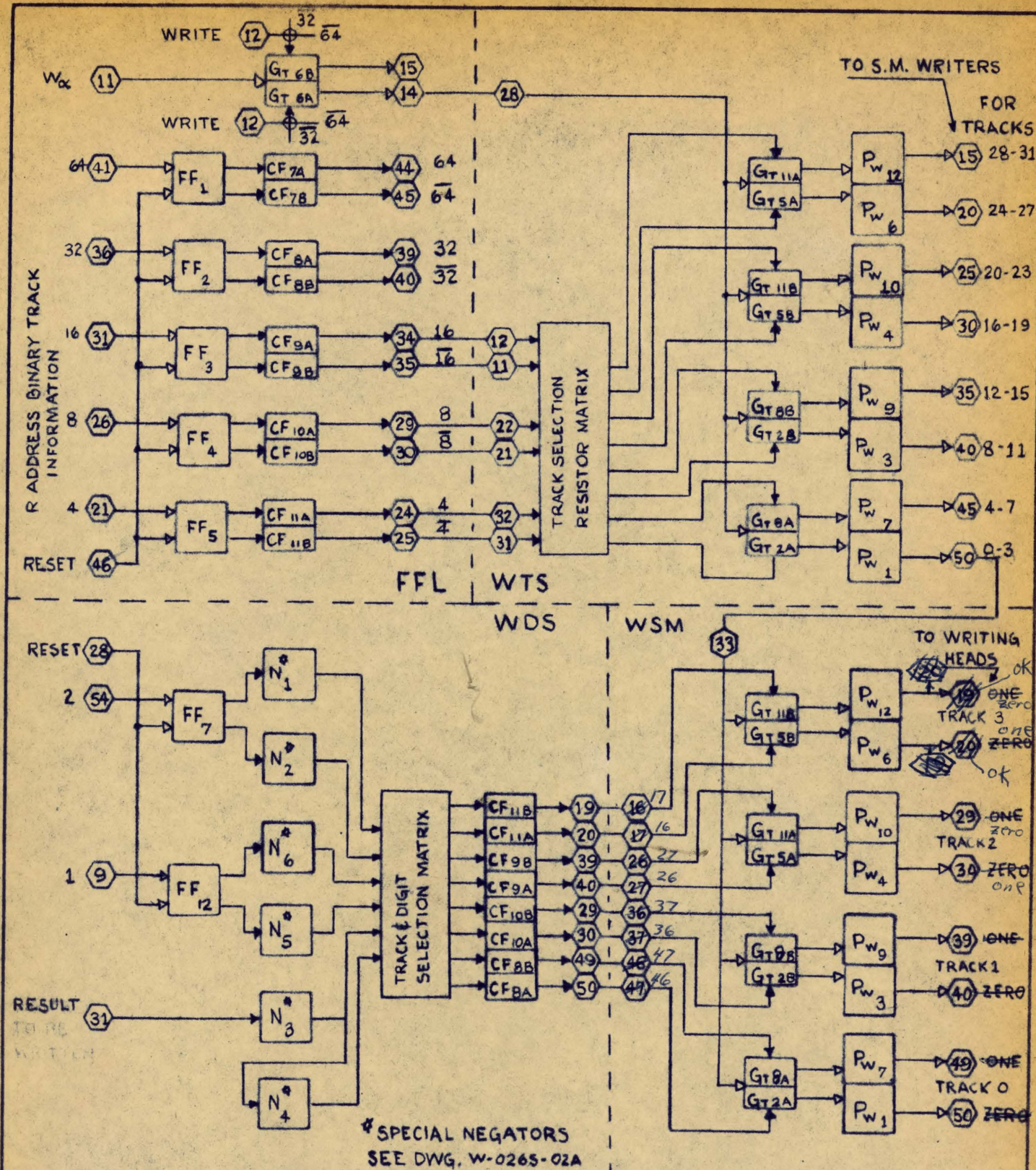


Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec.Eng.Dept.- Computer Research DRIVER UNIT
Capacitors in _____ Resistors in _____ 1/2 W, 10%				Dr. WMC Eng. Date 8/16/56 Dwg No. W048L-02A

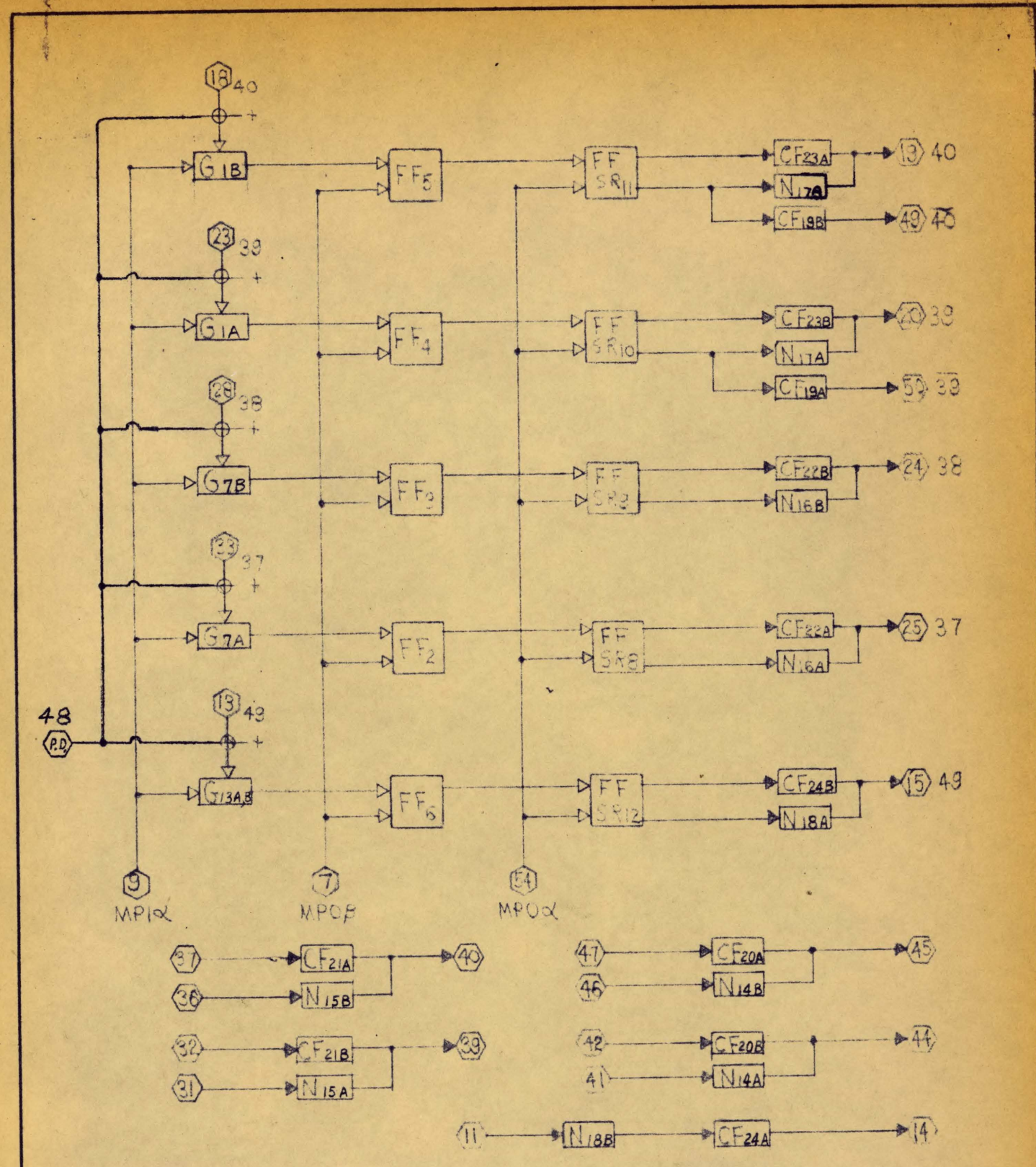
RESULT REGISTER INFORMATION



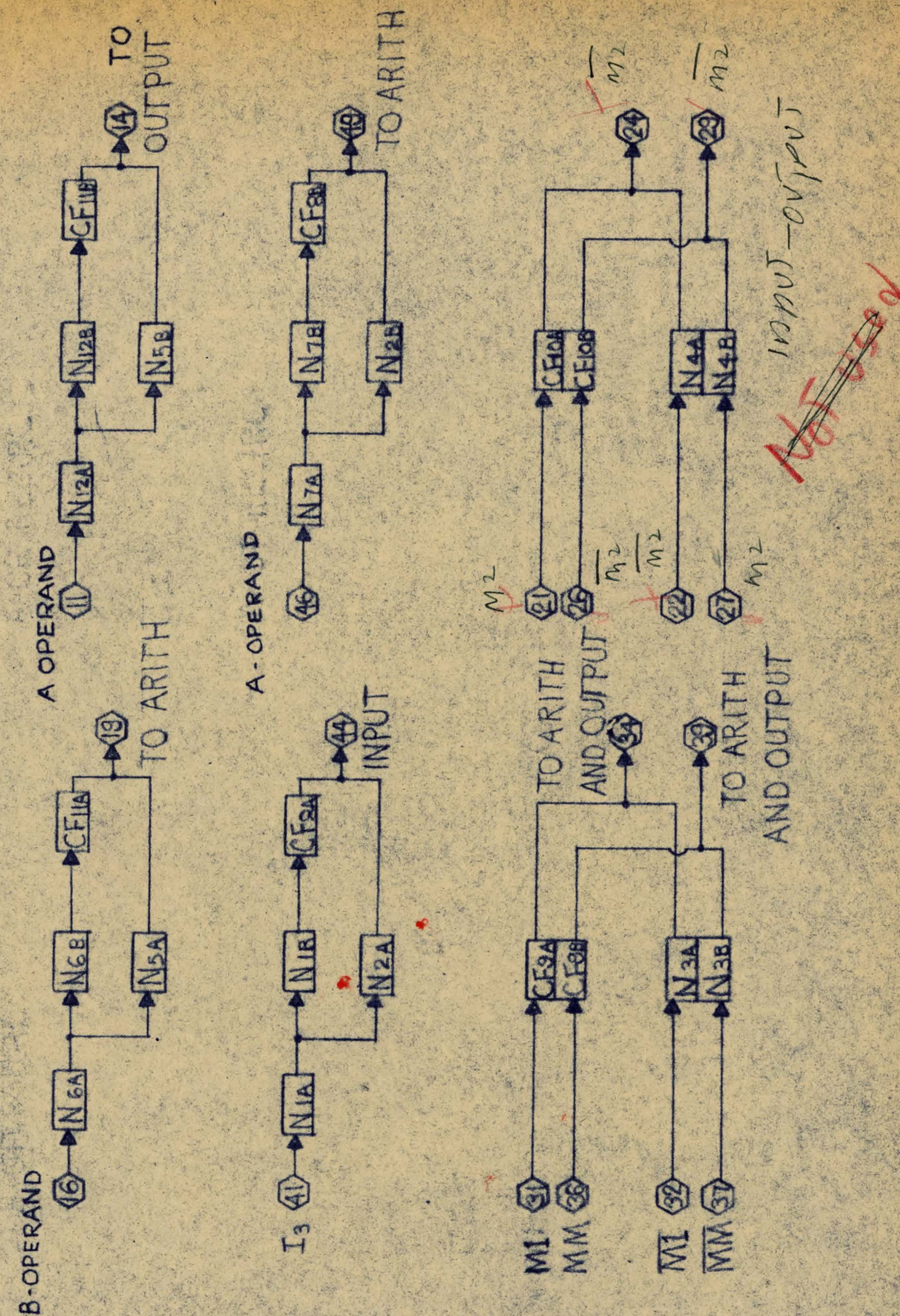
Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec.Eng.Dept.- Computer Research WRITING TIME LOCKS		
Capacitors in _____ Resistors in _____ 1/2 W, 10%				Dr. WMC Eng. Date 8/2/56	Dwg No. W-071L-07B	



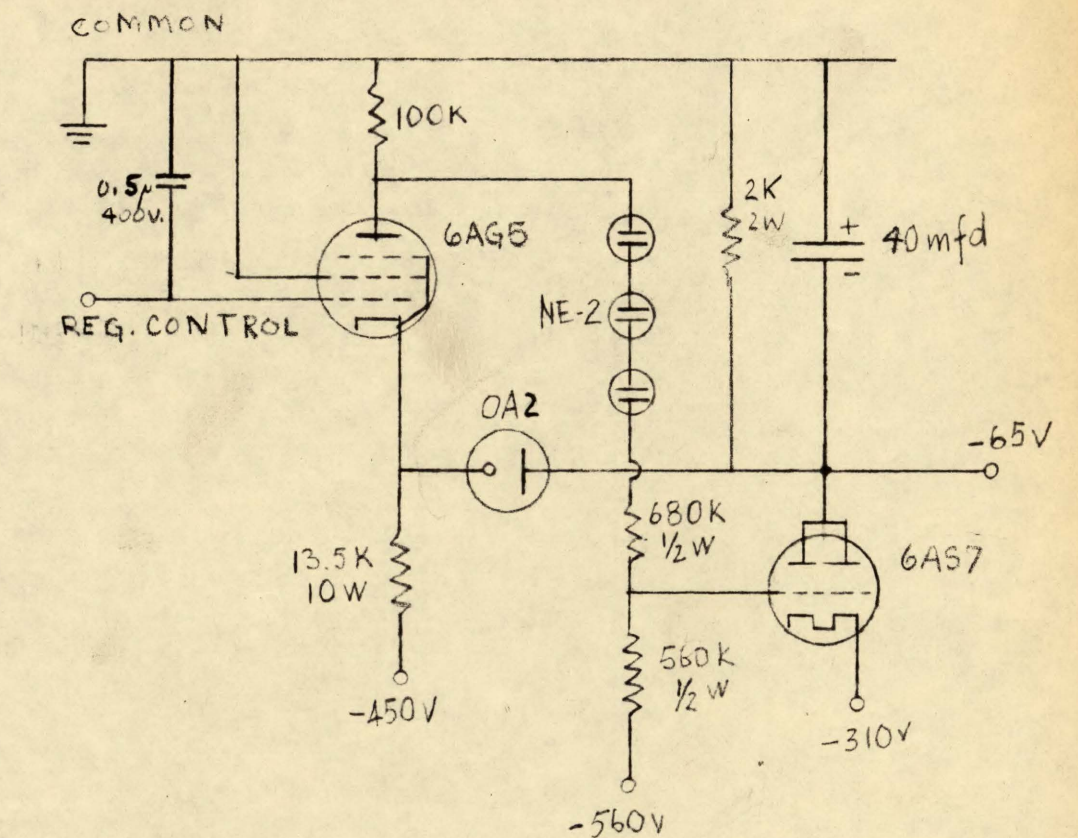
Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec. Eng. Dept.— Computer Research STANDARD MEMORY-WRITING TRACK SELECTION		
				Dr. AKS	Eng. AKS	Date 3/10/54
Capacitors in _____ Resistors in _____ 1/2 W, 10% }				UNLESS OTHERWISE NOTED Dwg No. W-071L-08A		



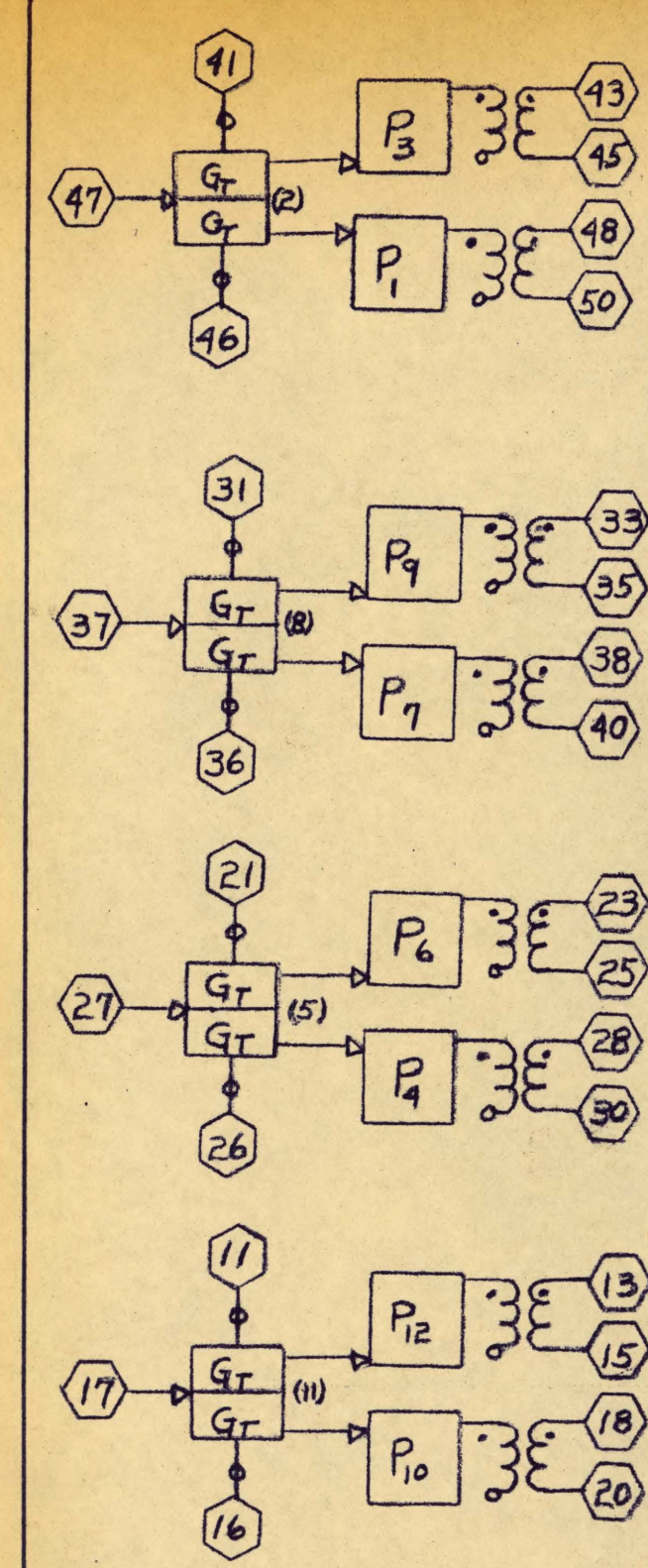
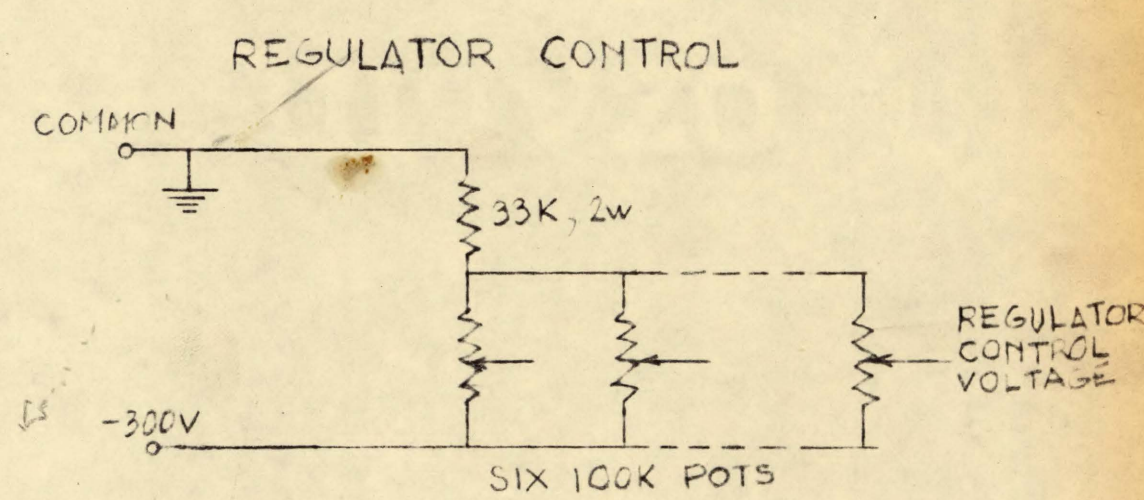
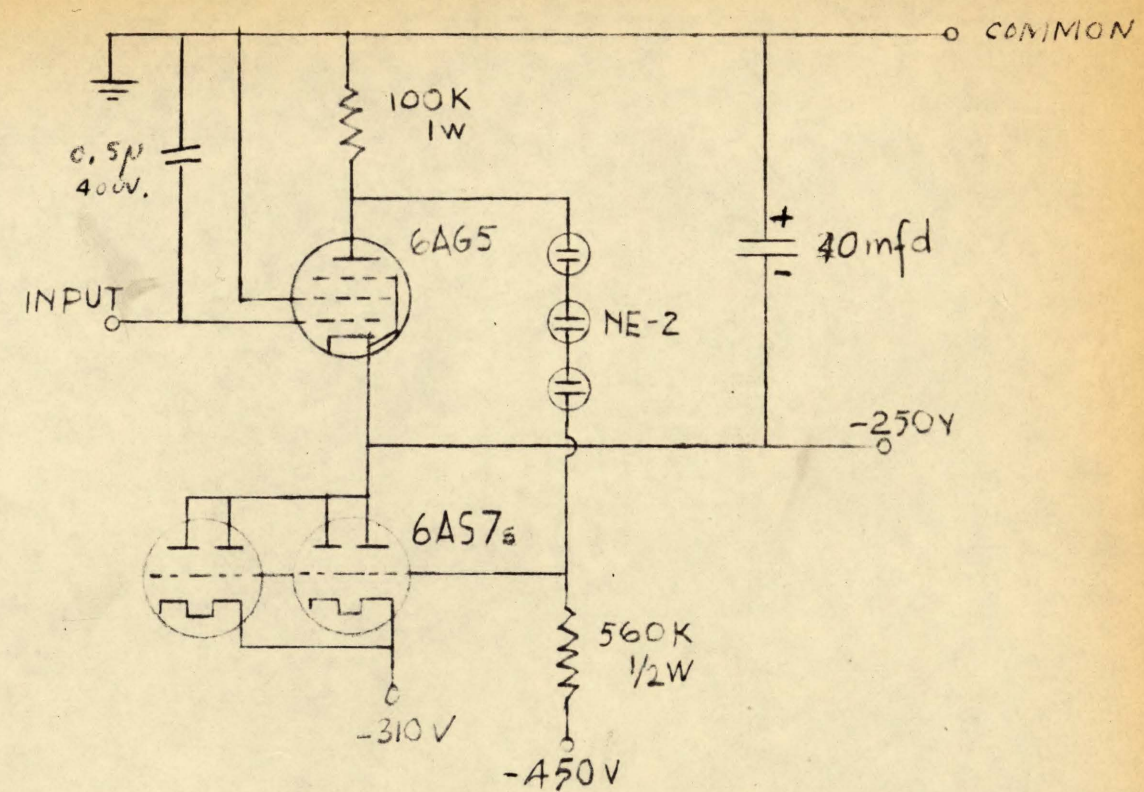
Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec. Eng. Dept.— Computer Research ORDER TYPE DELAY AND OUTPUT UNIT		
2	TUBE NUMBERS	RK	3-27-57	Dr. WMC	Eng.	Date 8/3/56
Capacitors in _____ Resistors in _____ 1/2 W, 10% }				UNLESS OTHERWISE NOTED Dwg No. W-082L-08B		



Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec.Eng.Dept.— Computer Research	
				DRIVER UNIT	
Capacitors in _____		Dr. WMC	Eng.	Date 8/16/56	
Resistors in _____ 1/2 W, 10%		Dwg No. W-048L-02A			



Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec.Eng.Dept.— Computer Research	
2	INPUT C, 2K LOAD, 0A2	AKS	3/24/57		
				-65 V REGULATOR	
Capacitors in _____		Dr. NDL	Eng. D.L.D.	Date 8/4/55	
Resistors in _____ 1/2 W, 10%		Dwg No. W-1735-02A			



LOC PIN	ΦC	IC	ID	4M
47	β	α	Y	β
41	MM(3)β	SR75	SR75	AAW
46	MM30	DE7·E5	DE7·E5	MOB
43	MMP3β	SRα75	SRα75	TO AA RECIRC.
48	MM30P54β	DEα7·Eα5	DEα7·Eα5	RESET SMFEL
37	α	α	Y	β
31	MOββ	OD5	OD5	BBW
36	M54Y8	O7	O7	RRW
33	MPIα	ODα5	ODY5	TO BB RECIRC.
38	MPOα	Oα7	OY7	TO RR RECIRC.
27	β	α	Y	α
21	M1αα	ABC7·C5	ABC7·C5	OW
26	M54Y8	AB5	AB5	AW
23	MPIβ	ABCα7·Cα5	ABCY7·CY5	TO O RECIRC.
28	MPDB	A,βα5	A,βY5	TO A RECIRC.
17	Y	α	β	α
11	MM3Y	V5R	MM54β	RW
16	MOββ	M·V55·α (19·ONTV6)	M538Y	BW
13	d3Y	RWTIME COINC. 50	d54β	TO R RECIRC.
18	MPOY	TO RR55MD 50	BLANK	TO B RECIRC.

NOTE: IN ALL CASES EXCEPT LOCATION 4M PINS 15, 20, 25, 30, 35, 40, 45, AND 50 ARE CONNECTED TO +150

Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec.Eng.Dept.— Computer Research -250V REGULATOR
Capacitors in _____ Resistors in _____ 1/2 W, 10%				Dr. NDL Dwg No. W-173S-01A
UNLESS OTHERWISE NOTED				Eng. DLD Date 7/3/55

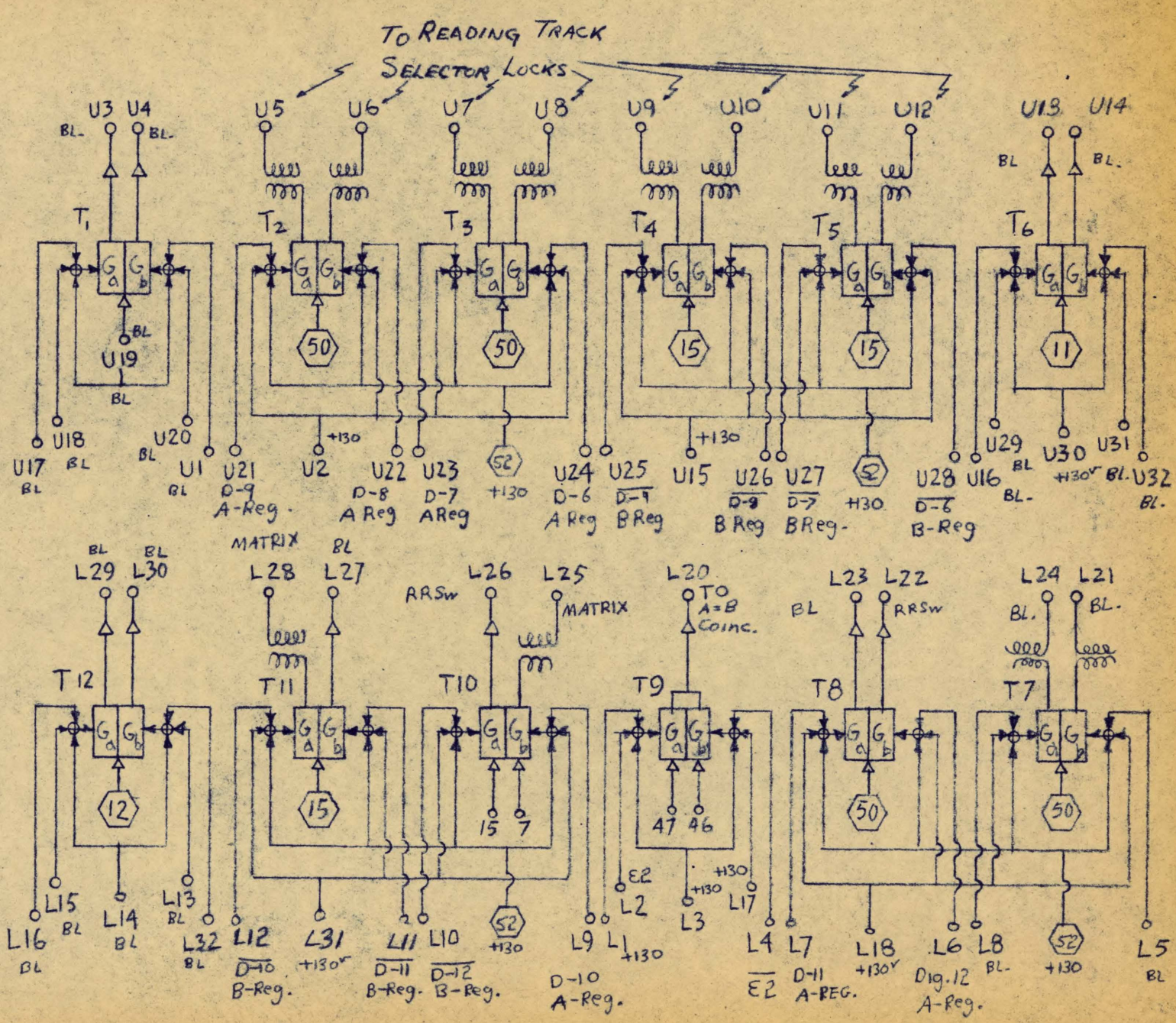
Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec.Eng.Dept.— Computer Research 8 CHANNEL POWER PULSERS
Capacitors in _____ Resistors in _____ 1/2 W, 10%				Dr. PMG Dwg No. W-045LW-01A
UNLESS OTHERWISE NOTED				Eng. AKS Date 3/20/57

Issue No.		Revisions		By		Date	
Capacitors in _____		Resistors in _____		UNLESS OTHERWISE NOTED			
Dr. NDL		Eng. DSN		Date 12/14/55			
Dwg No. W-047L-01A							

UNIVERSITY of WISCONSIN
Elec. Eng. Dept. - Computer Research

MULTIPLE GATES Chassis II

4D

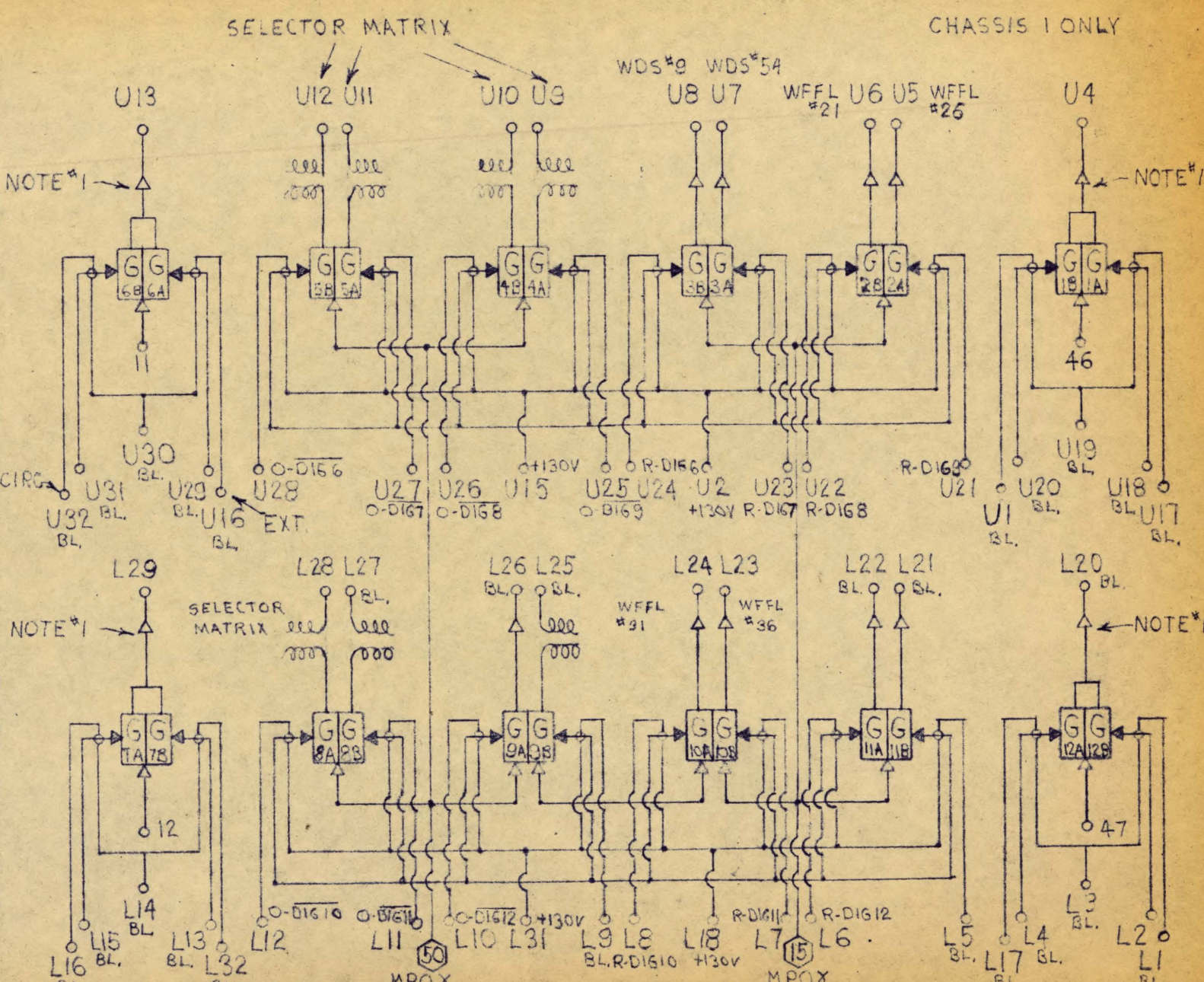


Issue No.		Revisions		By		Date	
Capacitors in _____		Resistors in _____		UNLESS OTHERWISE NOTED			
Dr. WMC		Eng. DSN		Date 8/10/56			
Dwg No. W-047L-02A							

UNIVERSITY of WISCONSIN
Elec. Eng. Dept. - Computer Research

MULTIPLE GATE CHASSIS I

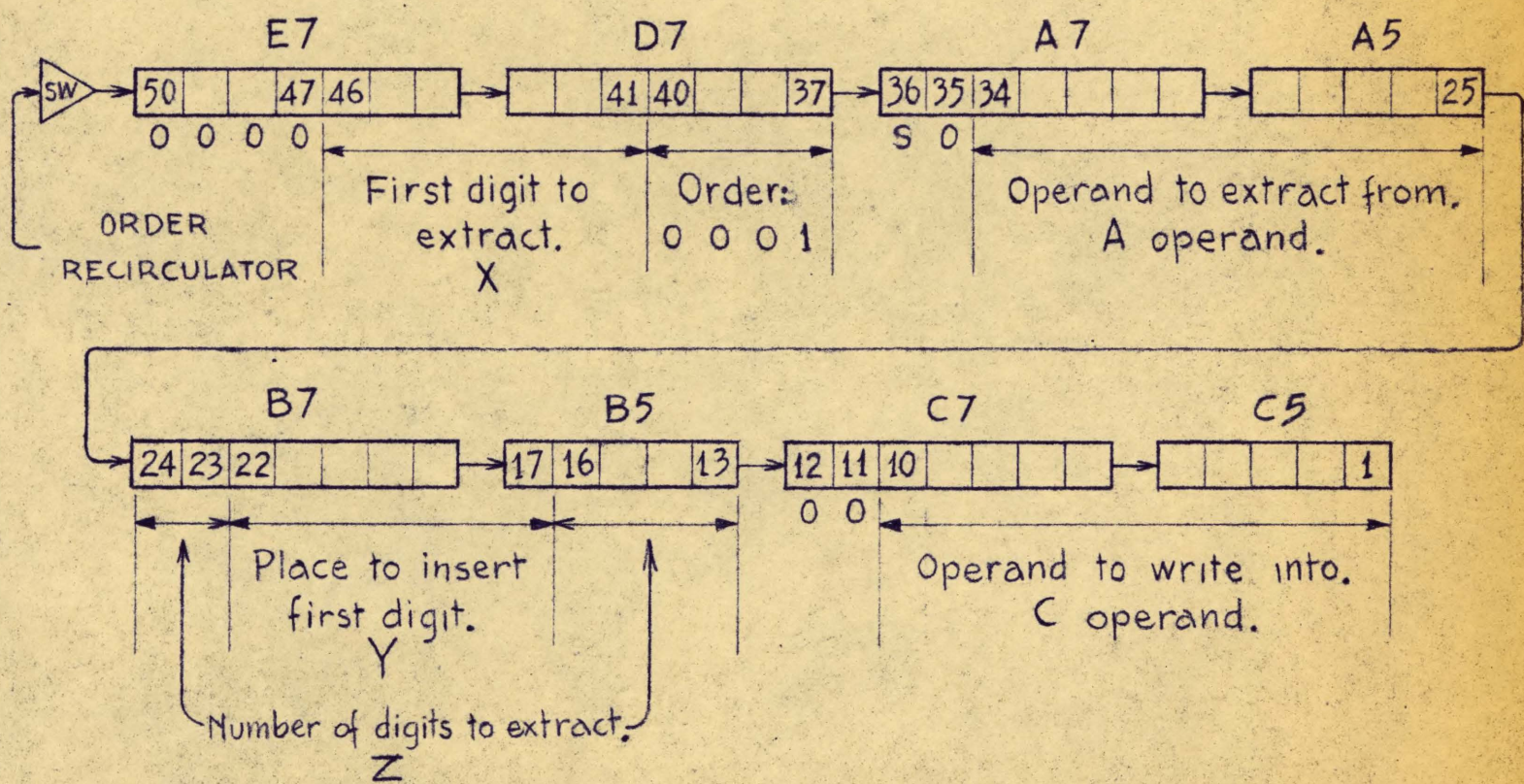
BLOCK DIAGRAM



NOTE #1 - NO PLATE RESISTOR IS USED ON THIS CHASSIS FOR THE INDICATED GATES. THE PLATE RESISTOR MUST BE SUPPLIED ON ANOTHER CHASSIS

NOTE #2 - PREFIXES "U" AND "L" ON TERMINAL NUMBERS INDICATE UPPER AND LOWER AMPHENOL CONNECTORS RESPECTIVELY. NO PREFIX INDICATES BANANA PLUG.

Fig. 2



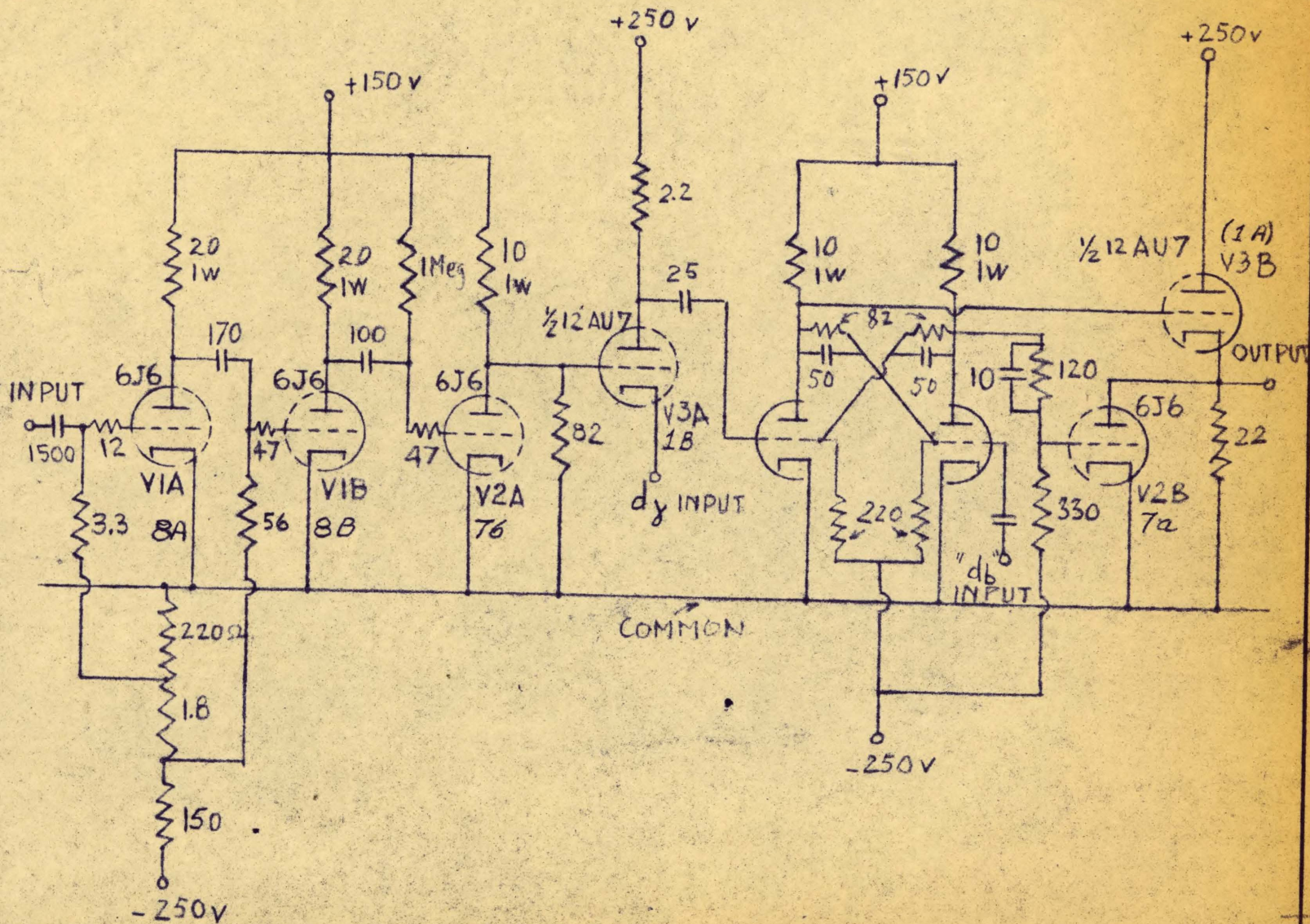
S = 0 Normal Operation
= 1 "Short Memory"

Issue No.	Revisions	By	Date
Capacitors in _____ } UNLESS OTHERWISE NOTED Resistors in _____ } $\frac{1}{2}$ W, 10%			

UNIVERSITY of WISCONSIN
 Elec. Eng. Dept. — Computer Research
 AN EXTRACT ORDER, SHIFTED
 INTO THE ORDER REGISTER

Dr. <i>W. D. Z.</i>	Eng. <i>X. D. X.</i>	Date <i>5/22/56</i>
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Dwg No. *W-0837-01A*

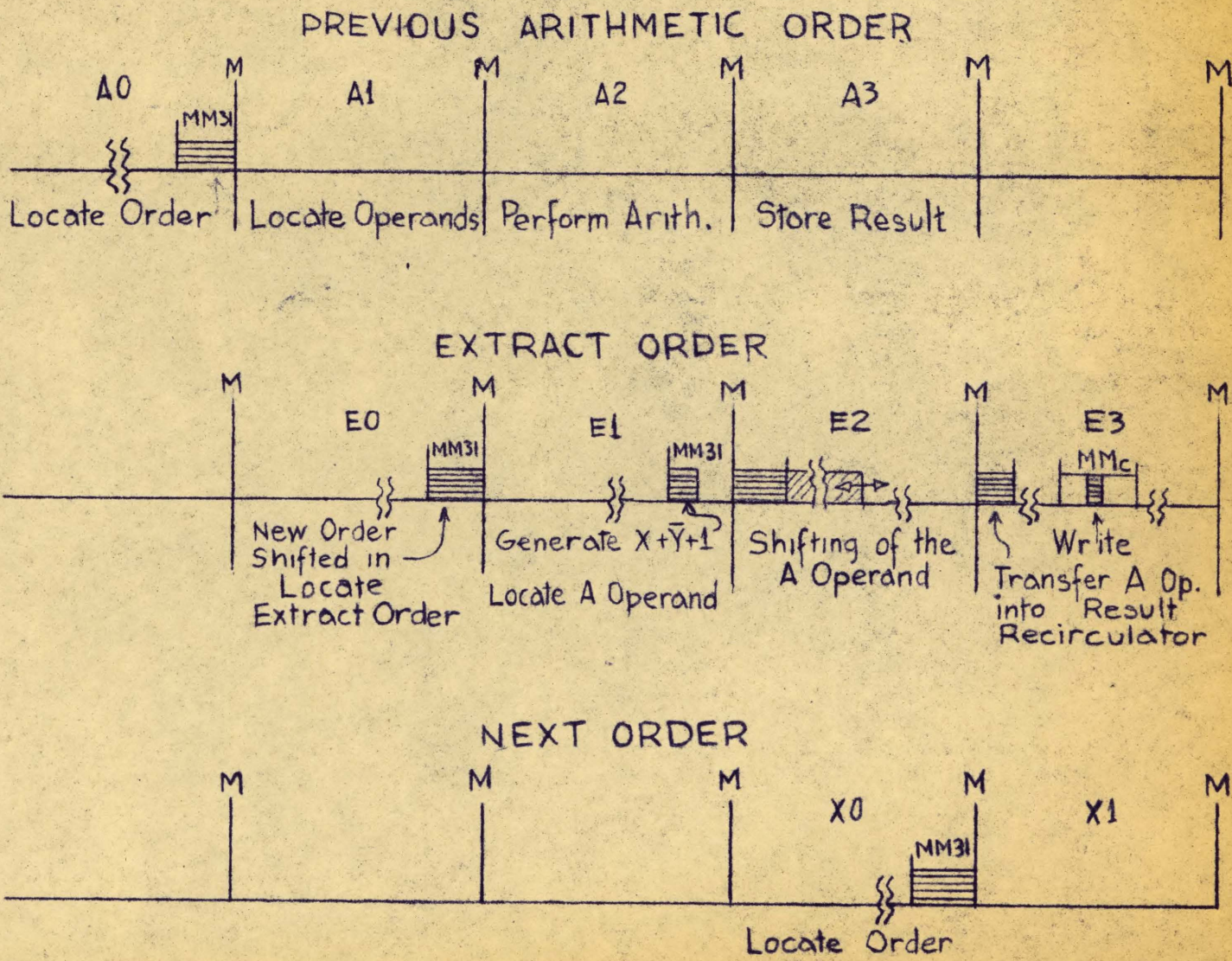


Issue No.	Revisions	By	Date

Capacitors in	Resistors in	UNLESS OTHERWISE NOTED
44K	1/2 W, 10%	

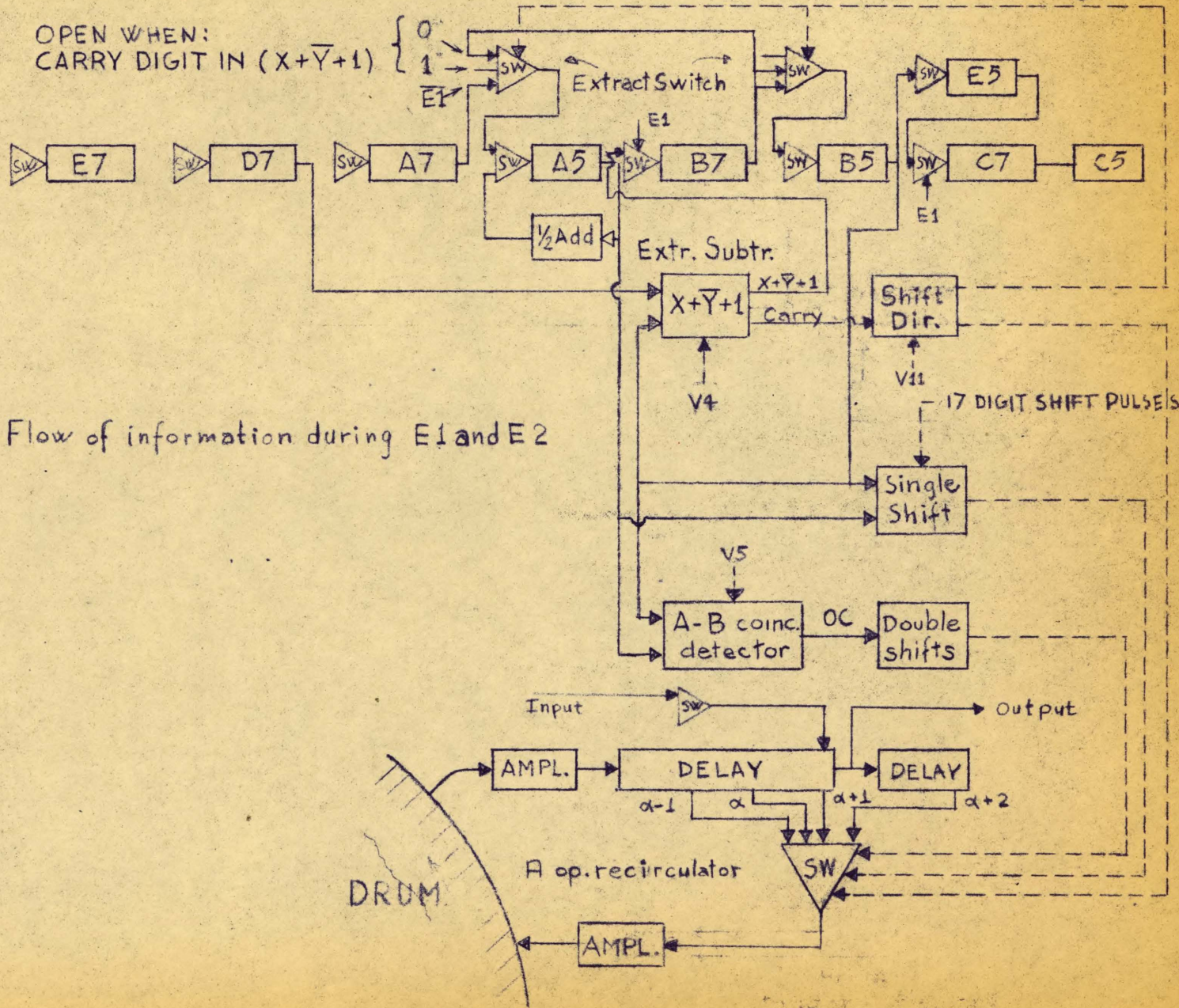
UNIVERSITY of WISCONSIN Elec. Eng. Dept.— Computer Research STANDARD MEMORY READING CHANNEL		
Dr. NDL	Eng. AKS	Date 9/3/54
Dwg No.	W-06715-01A	

Fig. 4



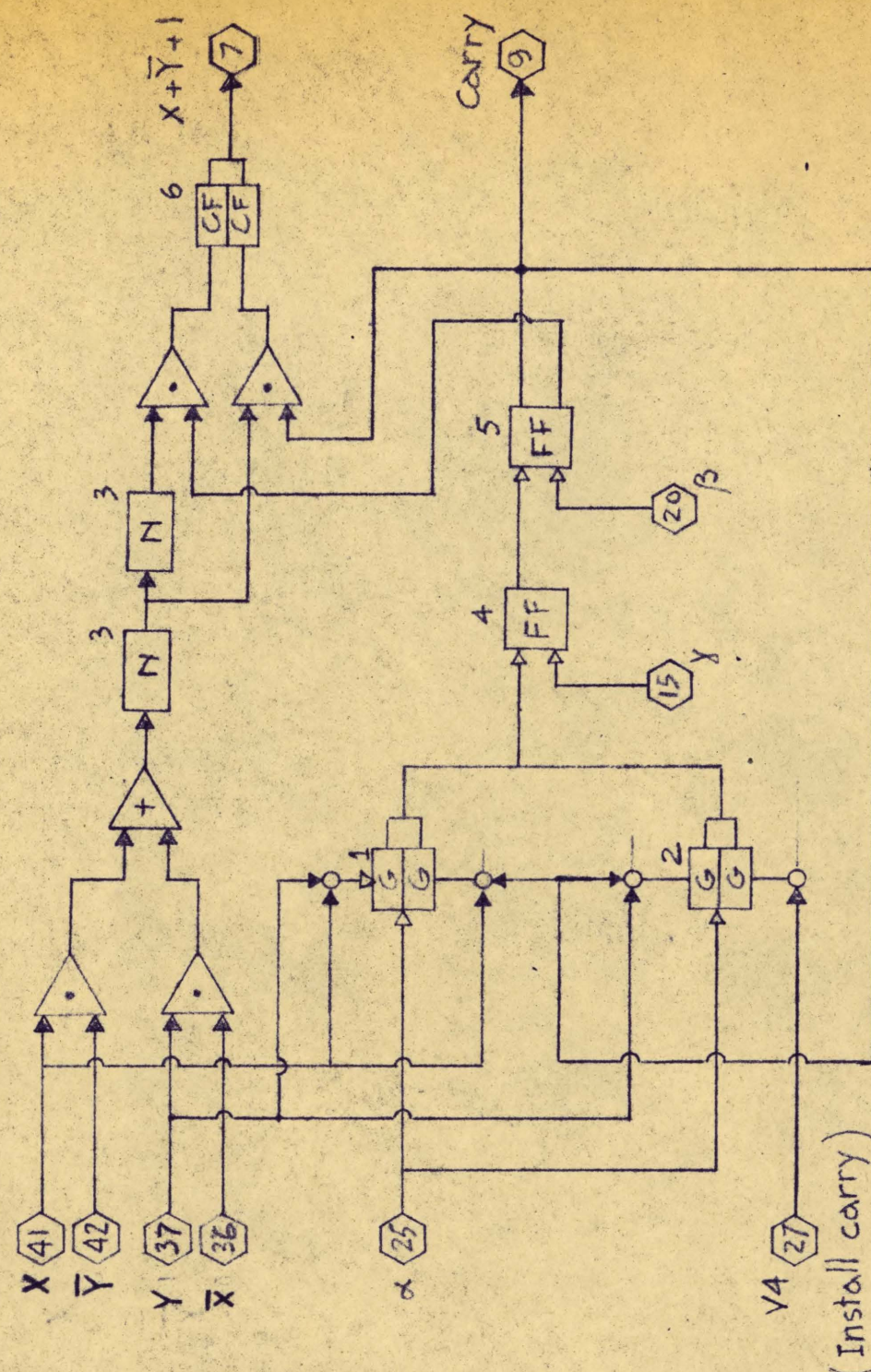
Issue No.	Revisions	By	Date
Capacitors in Resistors in	1/2 W, 10%	UNLESS OTHERWISE NOTED	
Dr. NDL	Eng. NDL	Date 5/12/56	Dwg No. W-0837-02A
UNIVERSITY of WISCONSIN Elec.Eng.Dept.- Computer Research Integral Synchronization			

Fig. 5



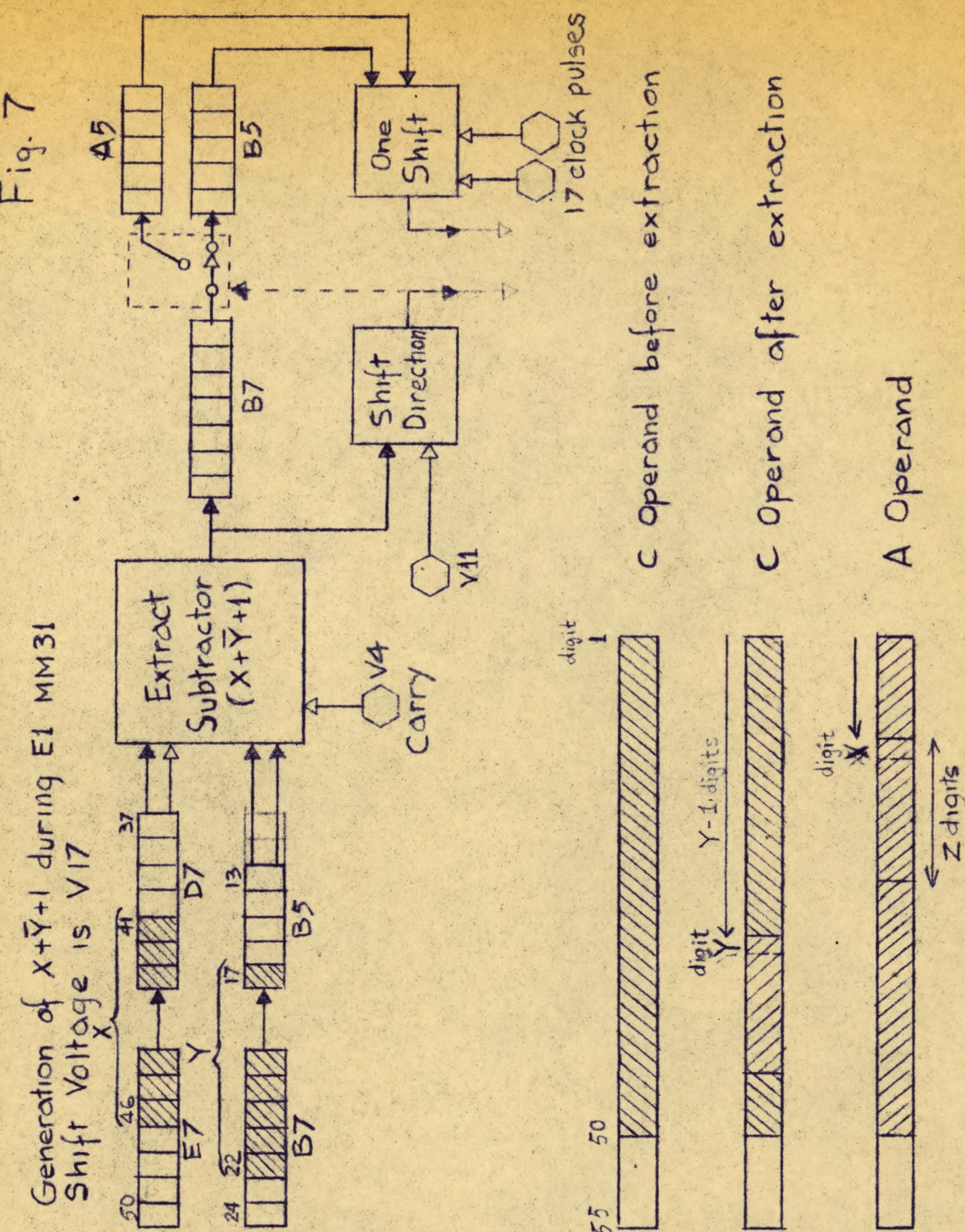
Issue No.	Revisions	By	Date
Capacitors in Resistors in	1/2 W, 10%	UNLESS OTHERWISE NOTED	
Dr. NDL	Eng. NDL	Date 12-22-55	Dwg No. W-0831-01A
UNIVERSITY of WISCONSIN Elec.Eng.Dept.- Computer Research BLOCK DIAGRAM OF THE SHIFTING OPERATION			

Fig. 6



Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec.Eng.Dept.— Computer Research EXTRACT SUBTRACTOR
2	INPUTS 4, 42, 97, 36	AKS	3/24/57	
Capacitors in _____ Resistors in _____ $\frac{1}{2}$ W, 10%				Dr. <i>NDL</i> Eng. <i>X.P.X.</i> Date <i>12/22/55</i> Dwg No. <i>W-083L-02A</i>

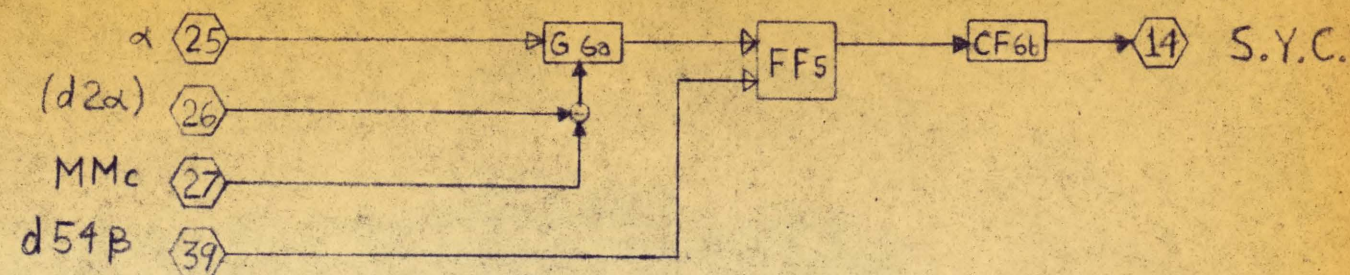
Fig. 7



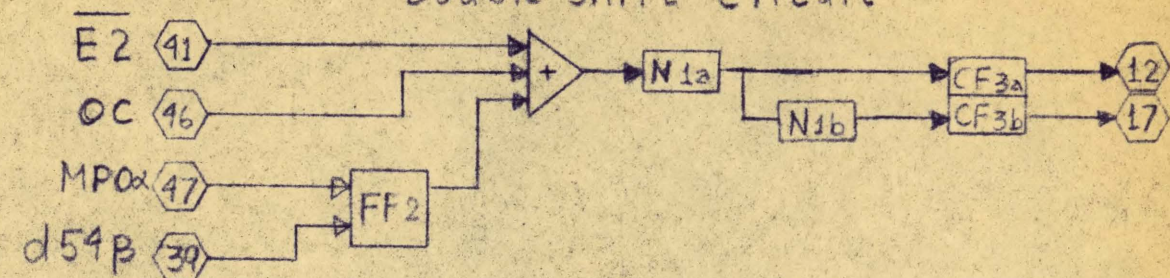
Note: digit 1 is the first to appear (from standard memory)

Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN	
				Elec. Eng. Dept.— Computer Research	
				Extract Subtraction	
Capacitors in _____ Resistors in _____ $\frac{1}{2}$ w, 10 %				} UNLESS OTHERWISE NOTED	Dr. NDL Eng. X.P.X Date 5/22/56
				Dwg No.	W-083T-03A

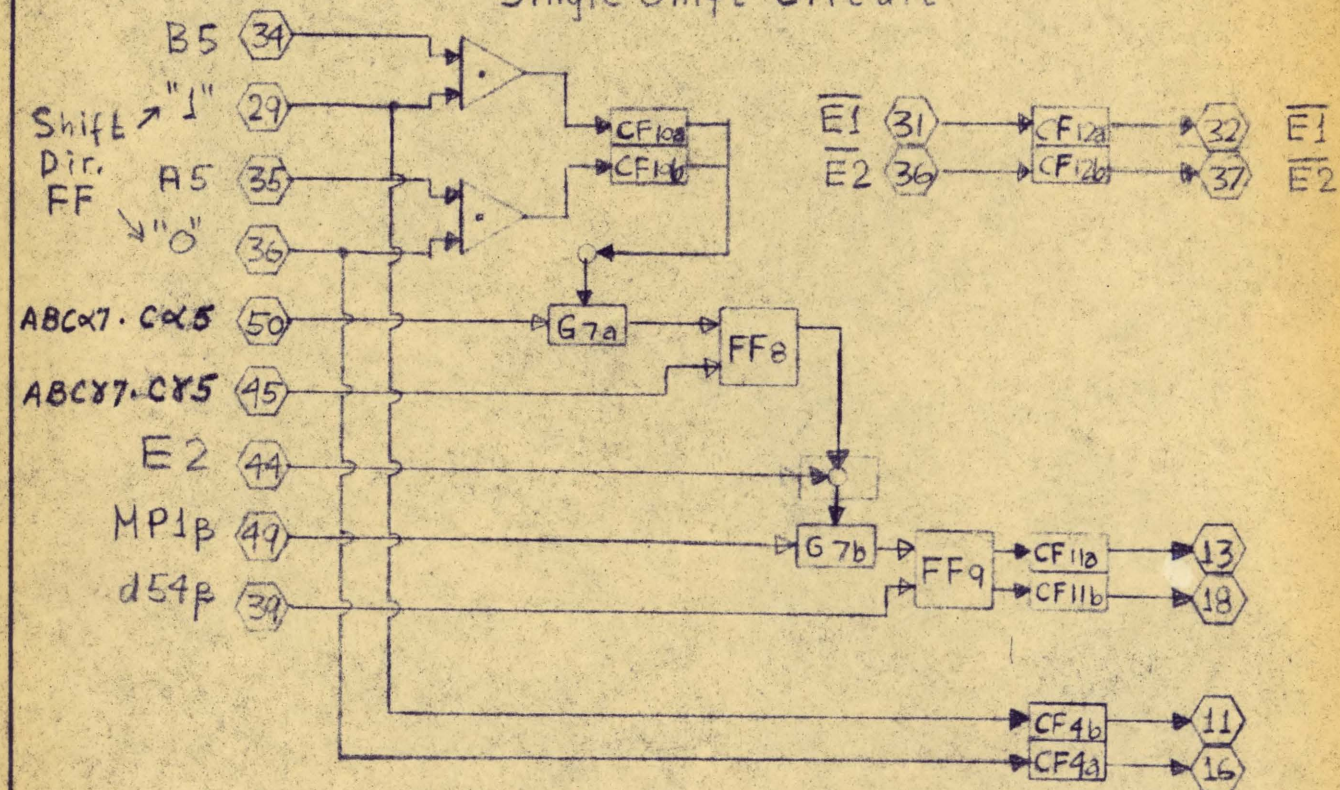
"Start Y Counter" Circuit



Double Shift Circuit

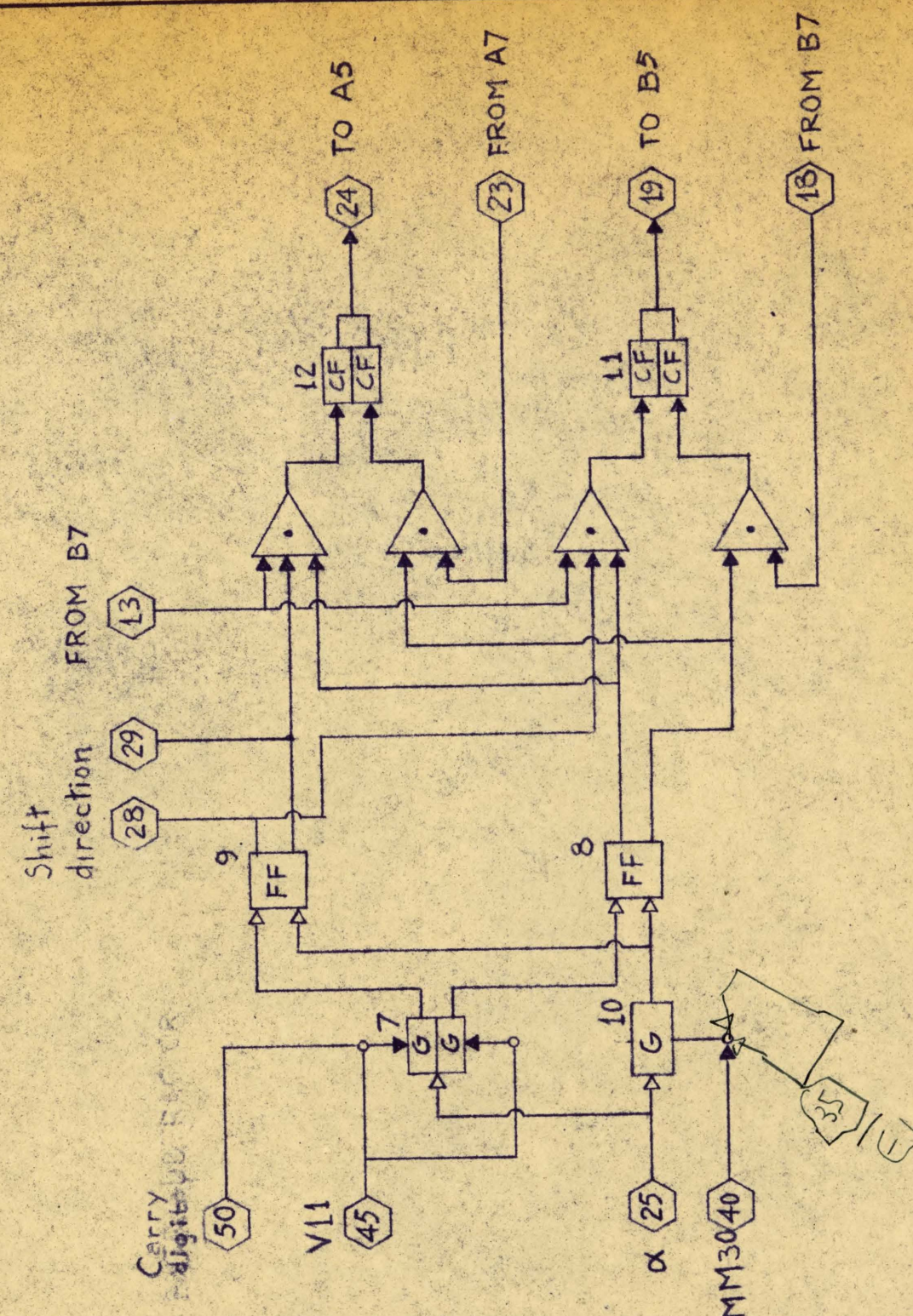


Single Shift Circuit



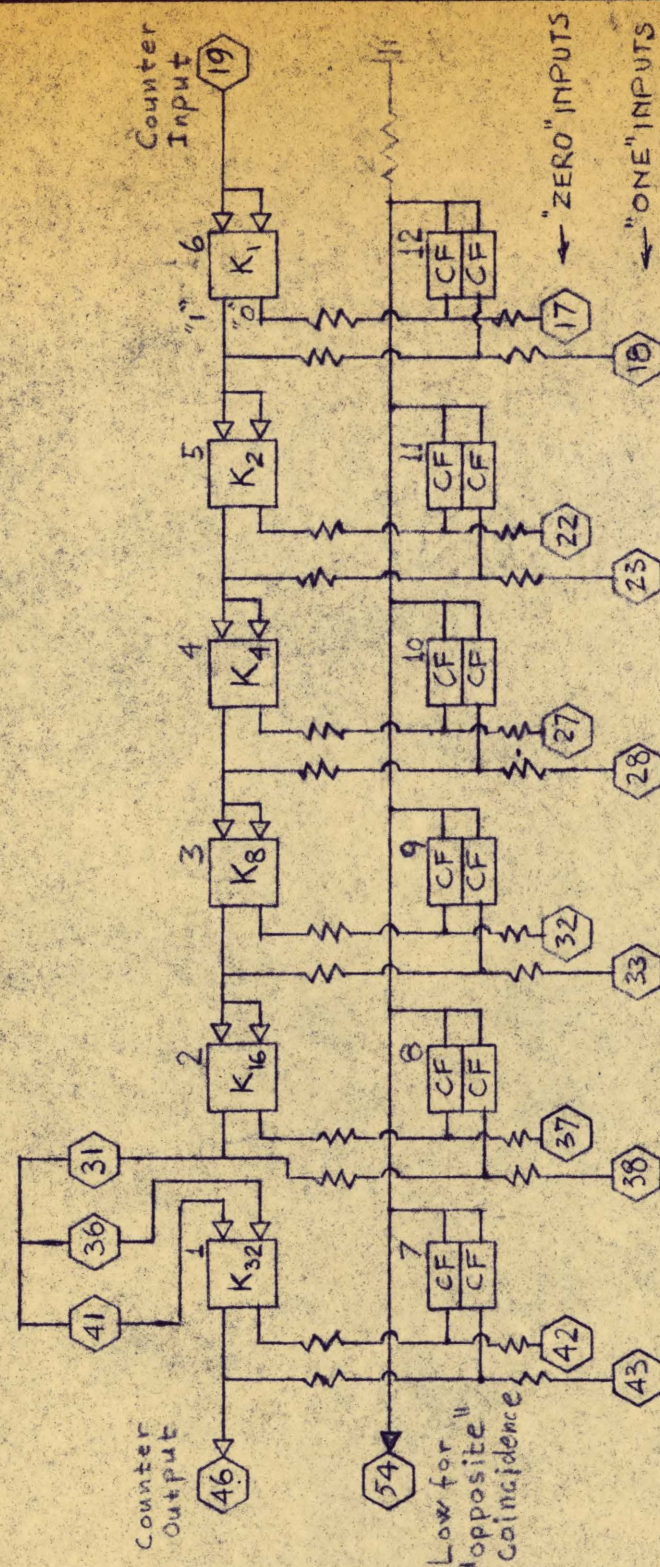
Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec.Eng.Dept.- Computer Research	
2	ABCx7 etc.	AYF	3/25/57	AUXILIARY EXTRACT CONTROLS	
Capacitors in _____ Resistors in _____ 1/2 W, 10% } UNLESS OTHERWISE NOTED				Dr. X P X	Eng. X P X
				Date 5/22/56	
				Dwg No. W-083L-06A	

Fig. 9



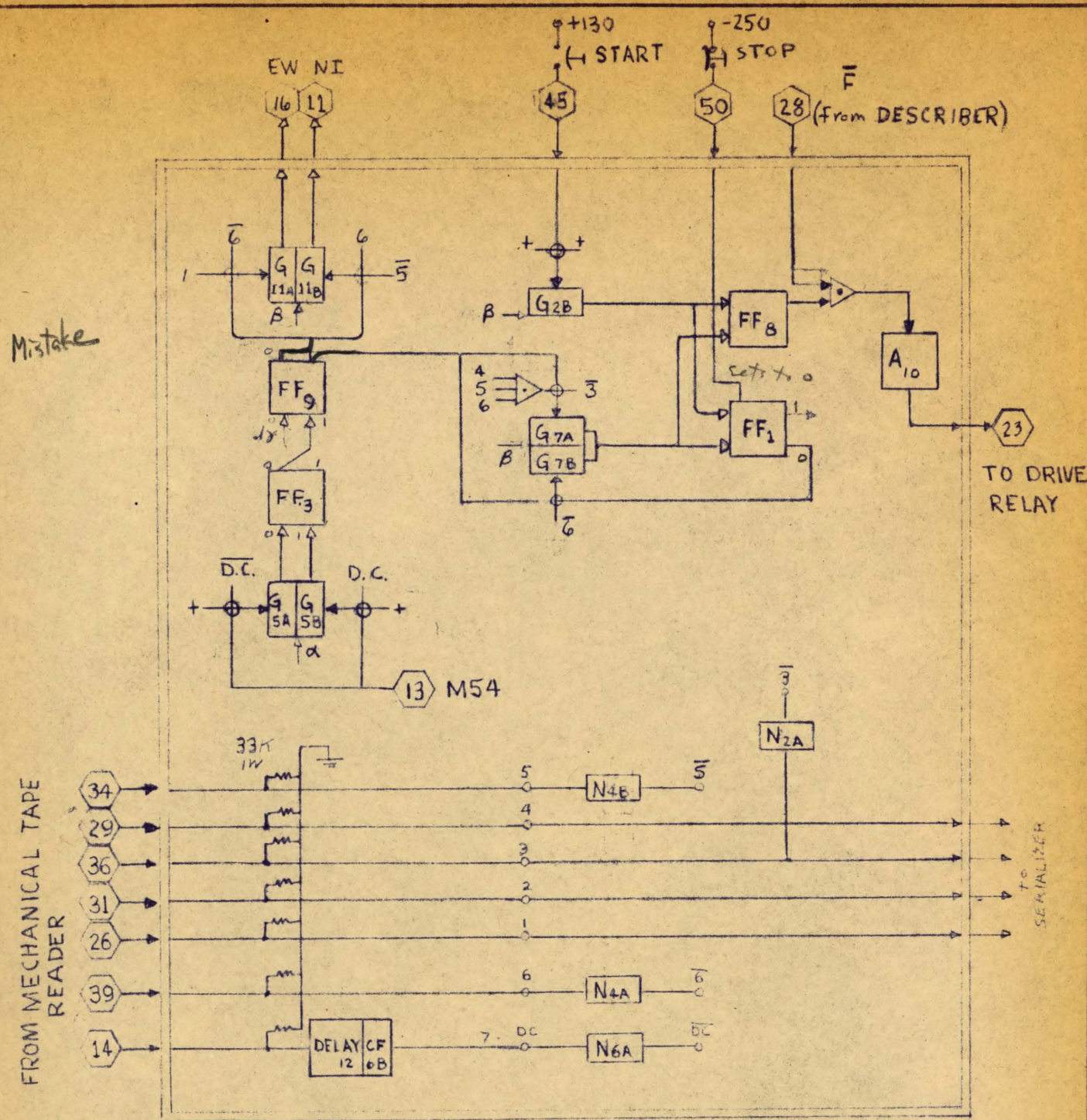
Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec.Eng.Dept.- Computer Research	
				EXTRACT SWITCH	
Capacitors in _____ Resistors in _____ 1/2 W, 10% } UNLESS OTHERWISE NOTED				Dr. NDL	Eng. X P X
				Date 12/22/55	
				Dwg No. W-083L-03A	

Fig. 12



	Y COUNTER COINCIDENCE COUNT	Z COUNTER COINCIDENCE COUNT
5	17	1
6	18	2
7	19	3
8	20	4
9	21	11
10	22	12
11	23	13
12	24	14
13	25	15
14	26	16
15	27	17
16	28	18
17	29	19
18	30	20
19	31	21
20	32	22
21	33	23
22	34	24
23	35	25
24	36	26
25	37	27
26	38	28
27	39	29
28	40	30
29	41	31
30	42	32
31	43	33
32	44	34
33	45	35
34	46	36
35	47	37
36	48	38
37	49	39
38	50	40
39	51	41
40	52	42
41	53	43
42	54	44
43	55	45
44	56	46
45	57	47
46	58	48
47	59	49
48	60	50
49	61	51
50	62	52
51	63	53
52	64	54
53	65	55
54	66	56
55	67	57
56	68	58
57	69	59
58	70	60
59	71	61
60	72	62
61	73	63
62	74	64
63	75	65
64	76	66
65	77	67
66	78	68
67	79	69
68	80	70
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72	84	74
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95	107	97
96	108	98
97	109	99
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417	429	419

Mistake

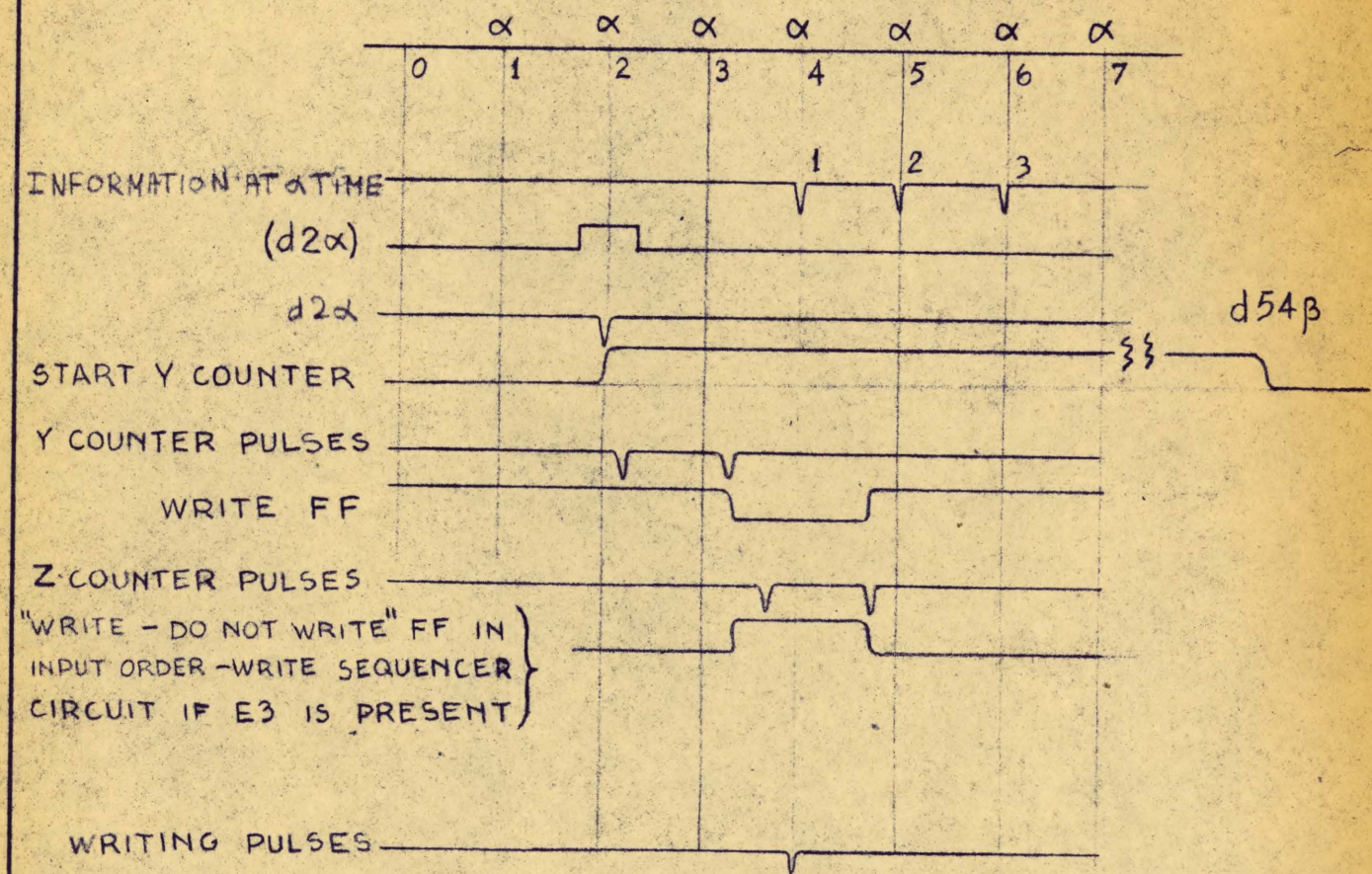


FROM MECHANICAL TAPE
READER

TO DRIVE
RELAY

TO
SERIALIZER

Fig. 15

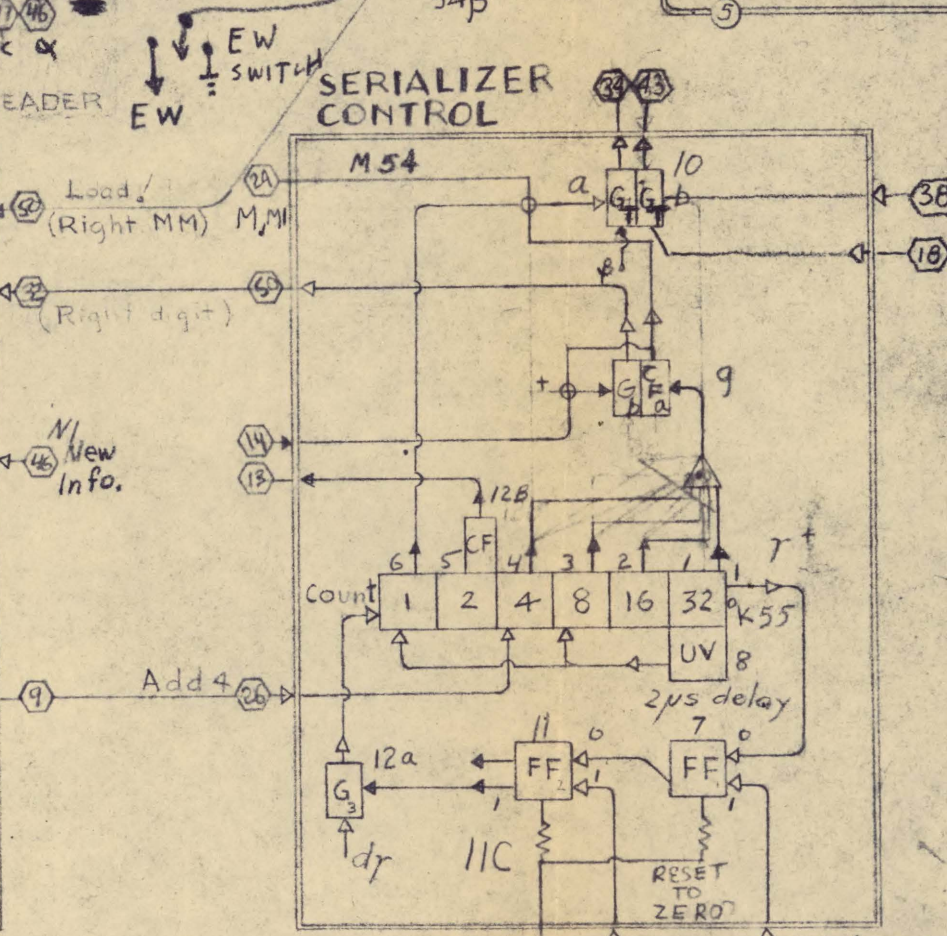
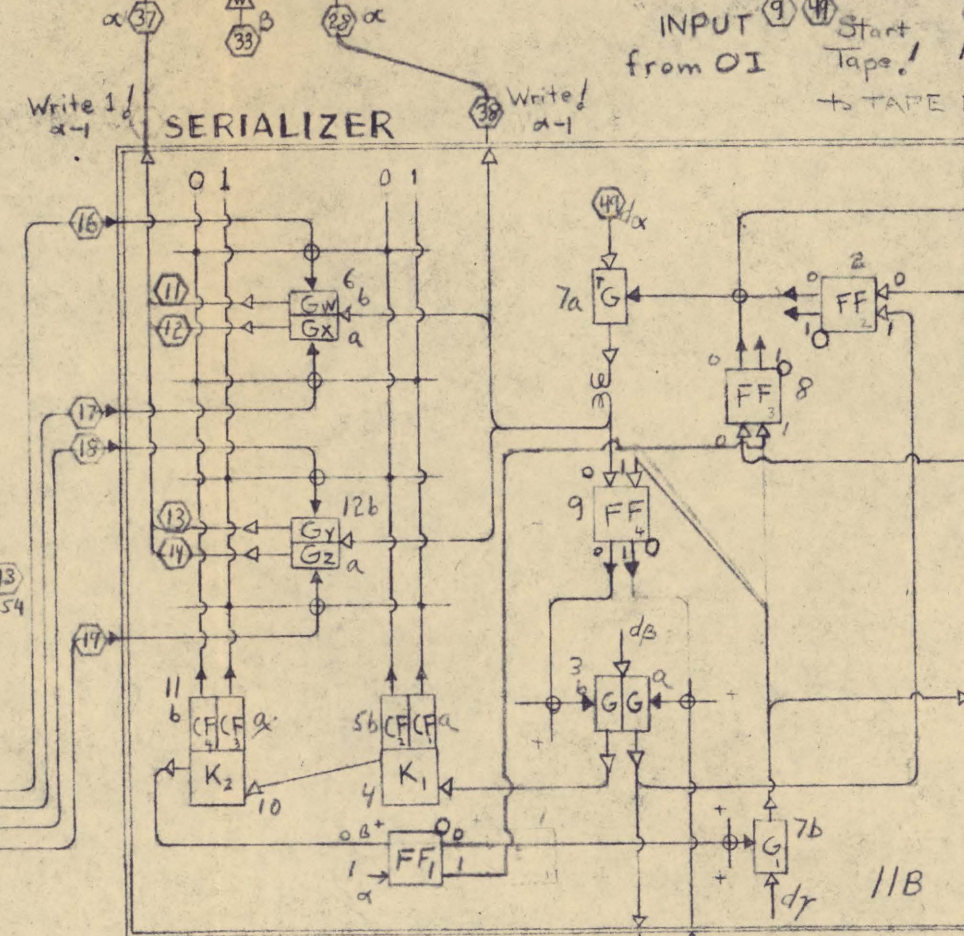
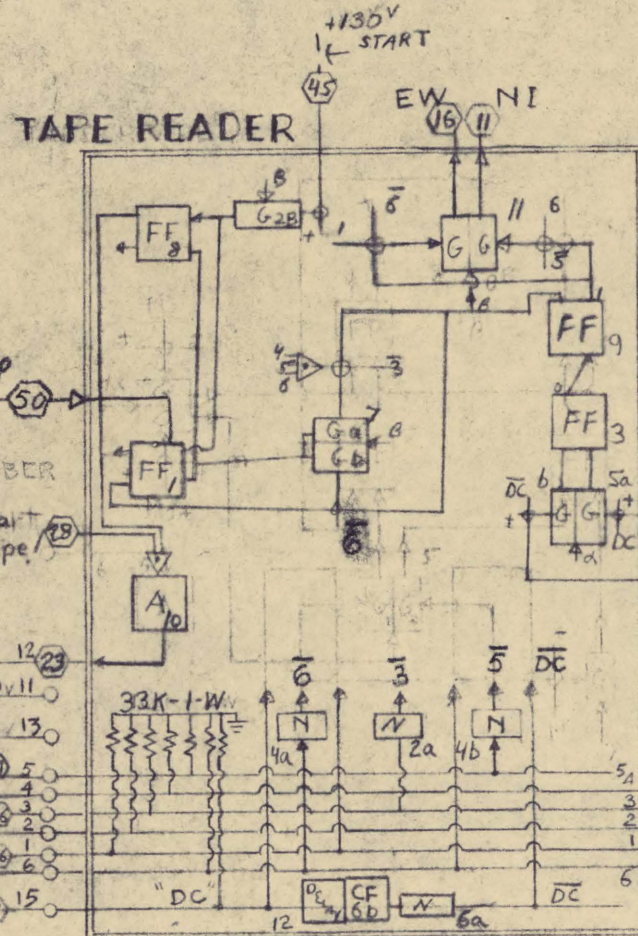
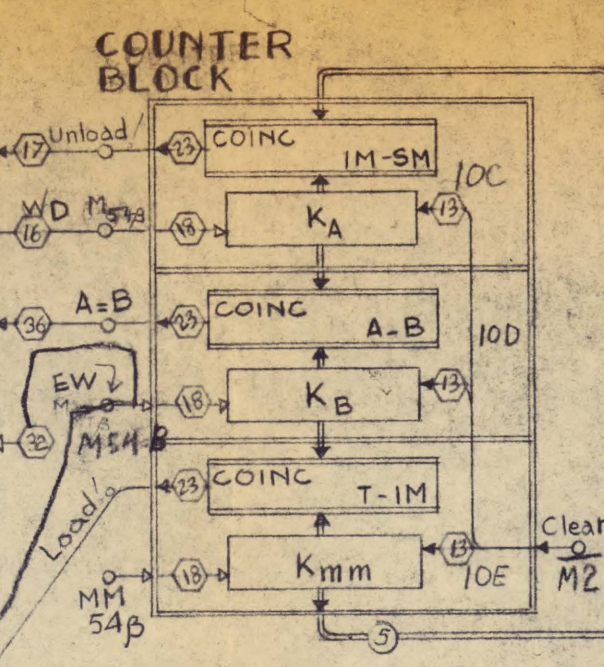
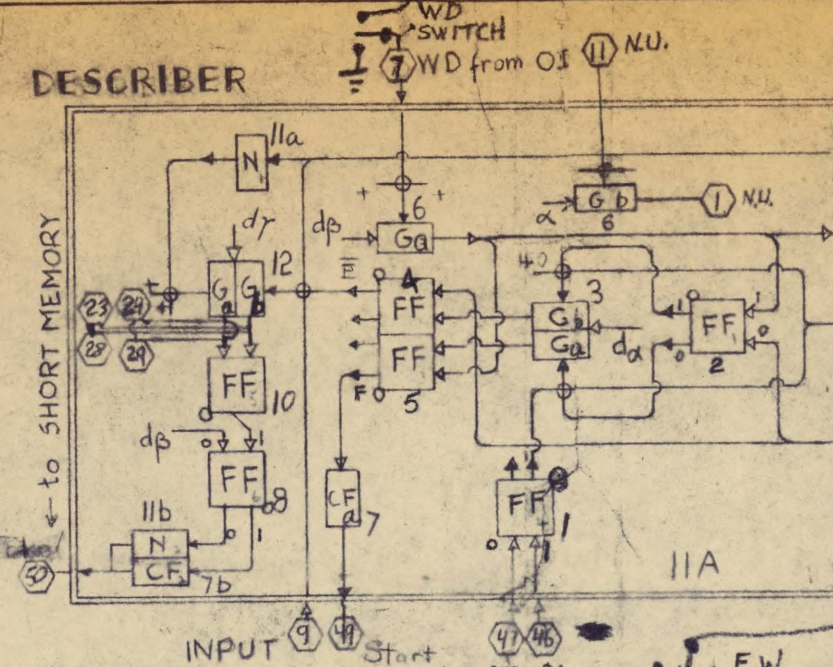
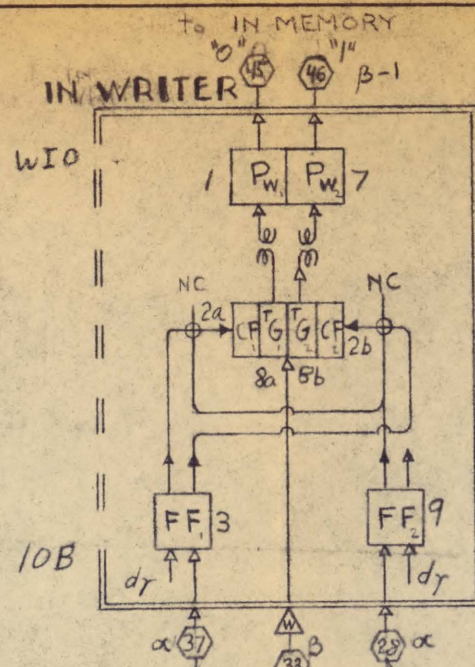
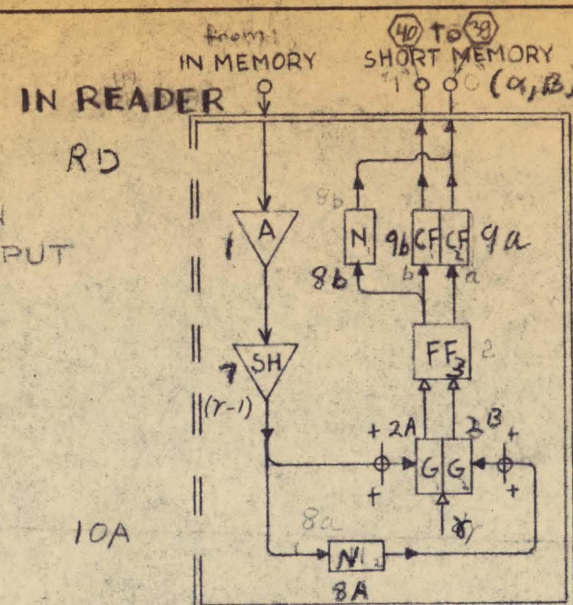


TIMING OF COINCIDENCE CIRCUIT VOLTAGES
FOR THE CASE OF $X=Y=1$

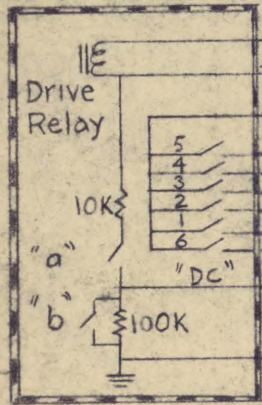
Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec. Eng. Dept. - Computer Research INPUT TAPE READER AND STOP-START CONTROLS		
	CORRECT 7/18/56			Dr. DLD	Eng. DSN AKS	Date 3-1-55
Capacitors in _____ Resistors in _____ 1/2 W, 10%			UNLESS OTHERWISE NOTED			
Dwg No. W-145 L-02 A			CHECKED BY 7/19/56 HJR			

Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec. Eng. Dept. - Computer Research COINCIDENCE CIRCUIT TIMING		
				Dr. DLD	Eng. XPS	Date 5/22/56
Capacitors in _____ Resistors in _____ 1/2 W, 10%			UNLESS OTHERWISE NOTED			
Dwg No. W-083T-04A						

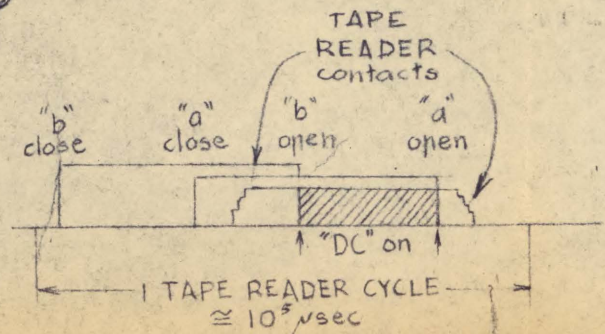
NOTE: dashed lines represent chassis shared with corresponding OUTPUT unit.



MECHANICAL TAPE READER



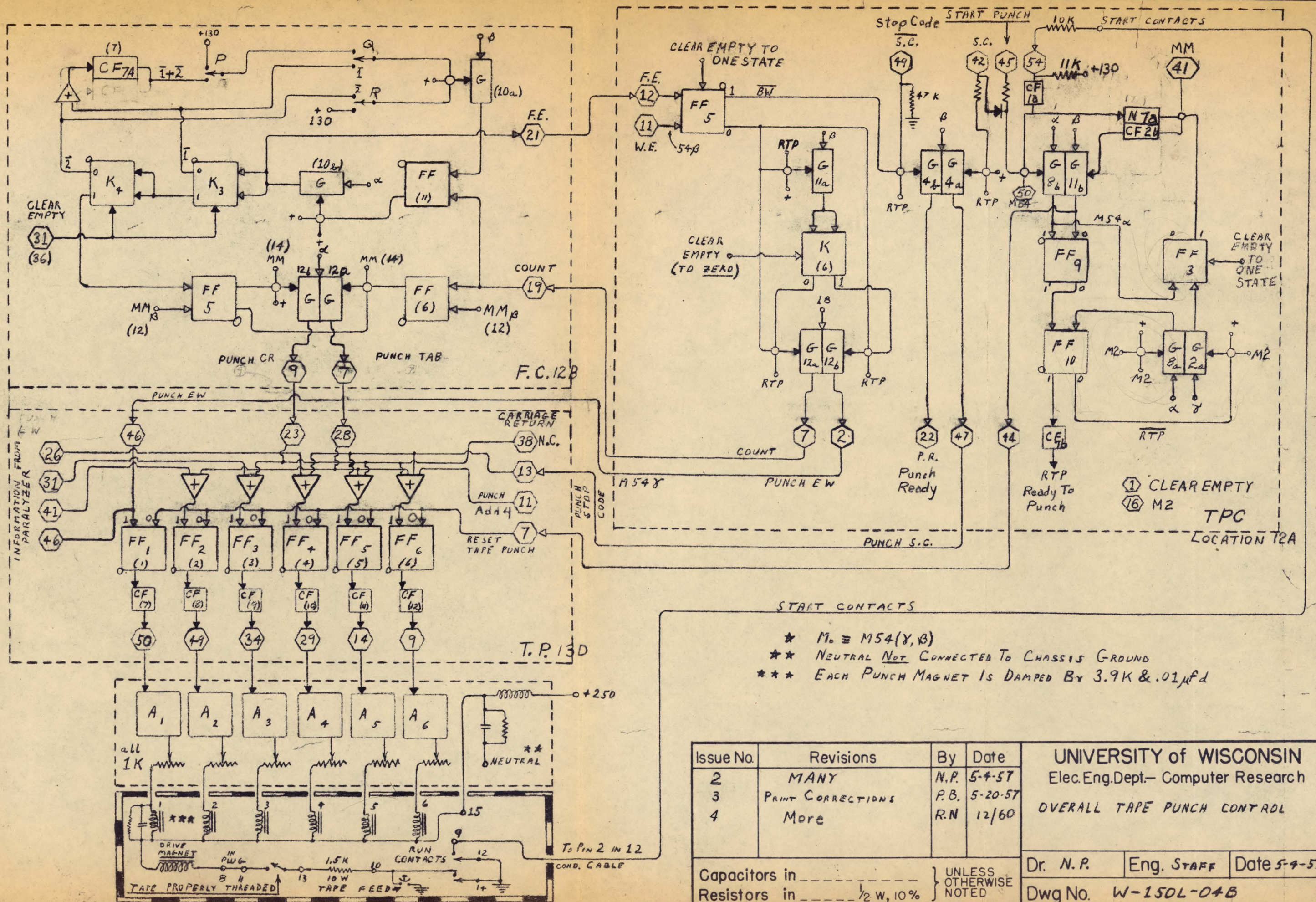
Pin #'s on Connecting Plug



"a" & "b" are Microswitches
en. by cam on Tape Reader Shaft
See TIMING diagram

Issue No.	Revisions	By	Date
2	Revised - o.k. More	15W R.N	7/20/58 12/60
Capacitors in _____ Resistors in _____ 1/2 W, 10% } UNLESS OTHERWISE NOTED			

UNIVERSITY of WISCONSIN Elec. Eng. Dept. - Computer Research		
INPUT OVERALL		
Dr. NDL	Eng. CHD	Date 11-5-54
Dwg No. W-140L-02 B		



Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec. Eng. Dept. - Computer Research	
2	MANY	N.P.	5-4-57	OVERALL TAPE PUNCH CONTROL	
3	PRINT CORRECTIONS	P.B.	5-20-57		
4	MORE	R.N.	12/60		
Capacitors in _____ Resistors in _____				UNLESS OTHERWISE NOTED	
				Dr. N.P.	Eng. STAFF
				Date 5-4-57	
				Dwg No. W-150L-04B	

DASHED LINES INDICATE CHASSIS SHARED WITH CORRESPONDING INPUT UNIT.

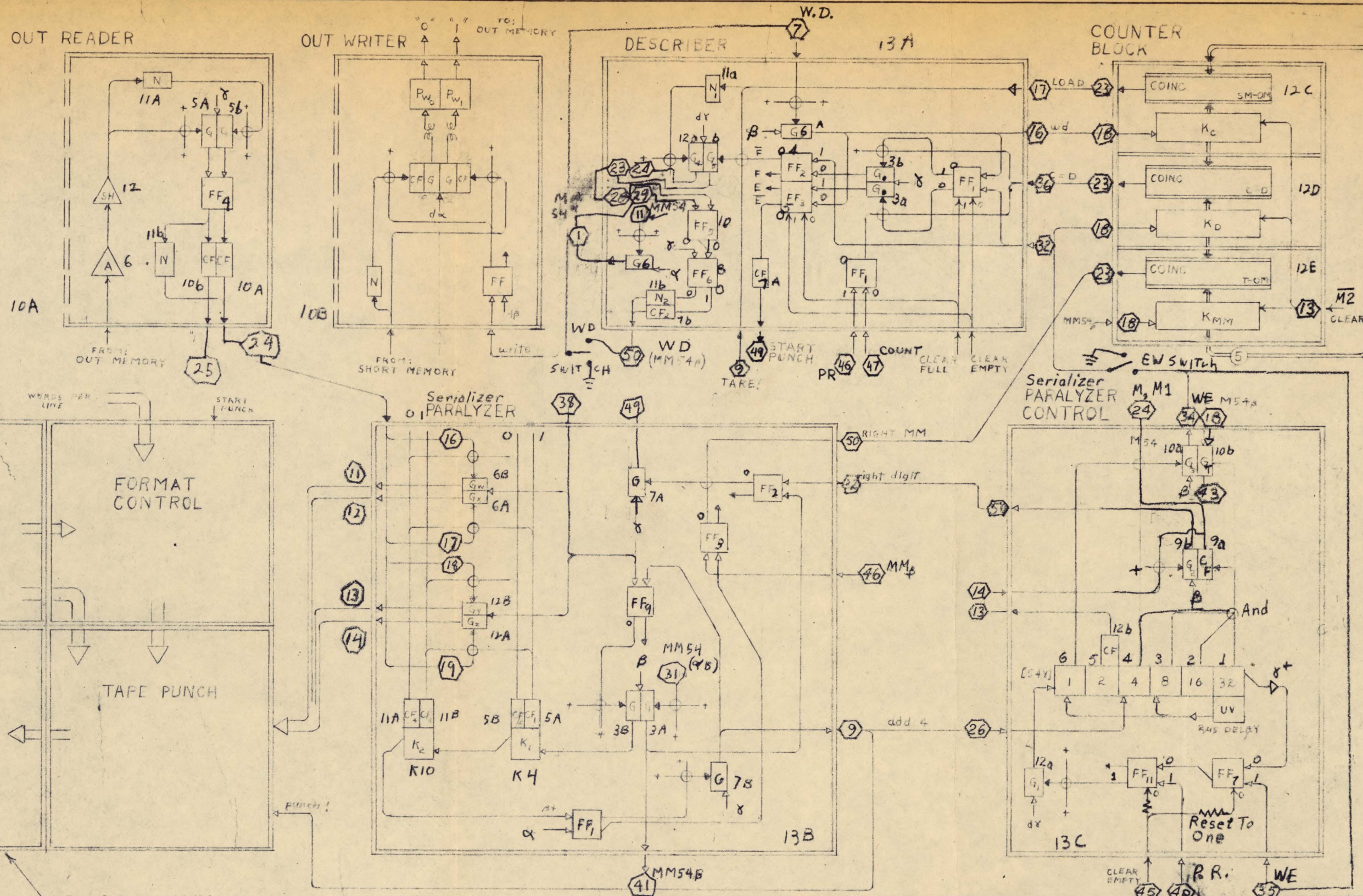
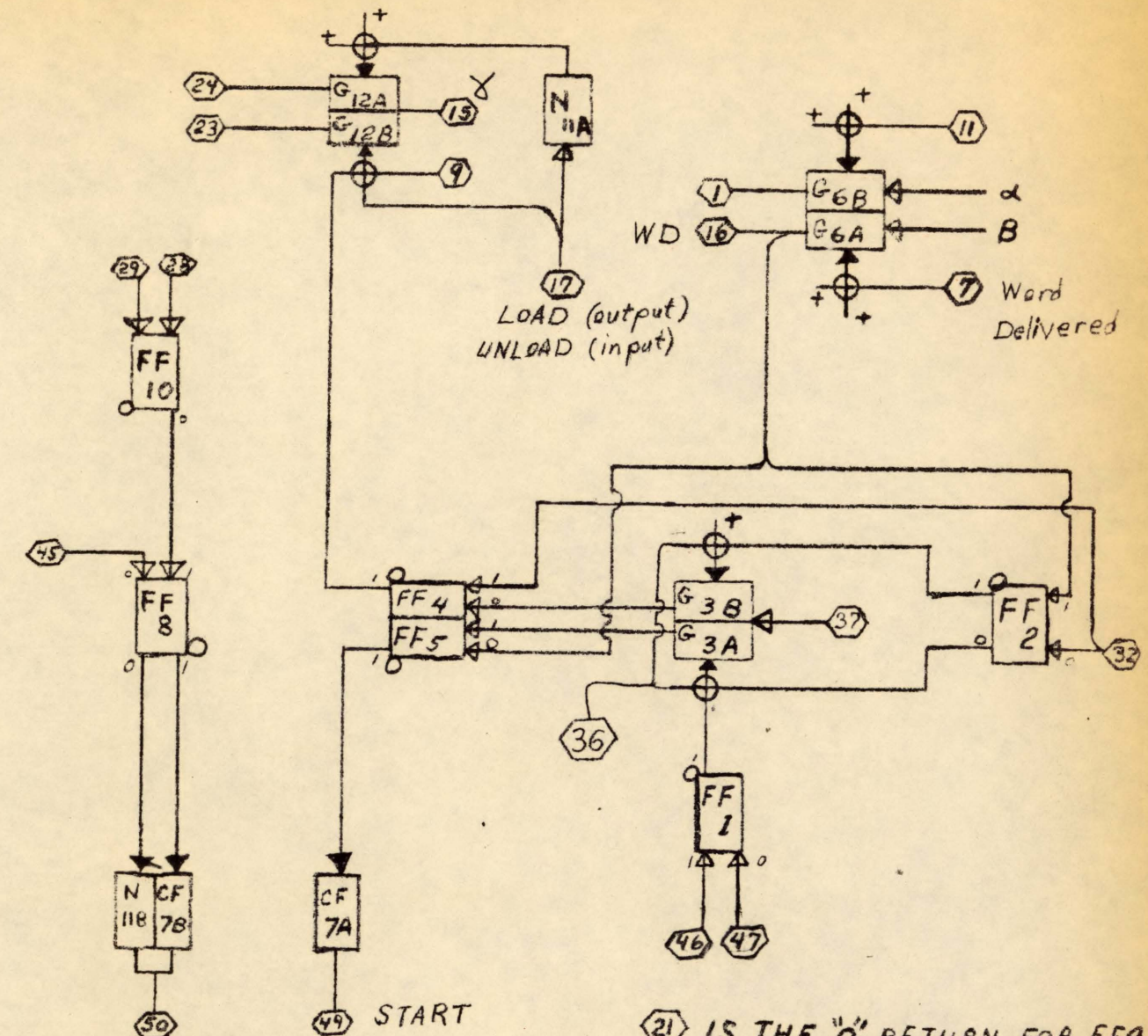
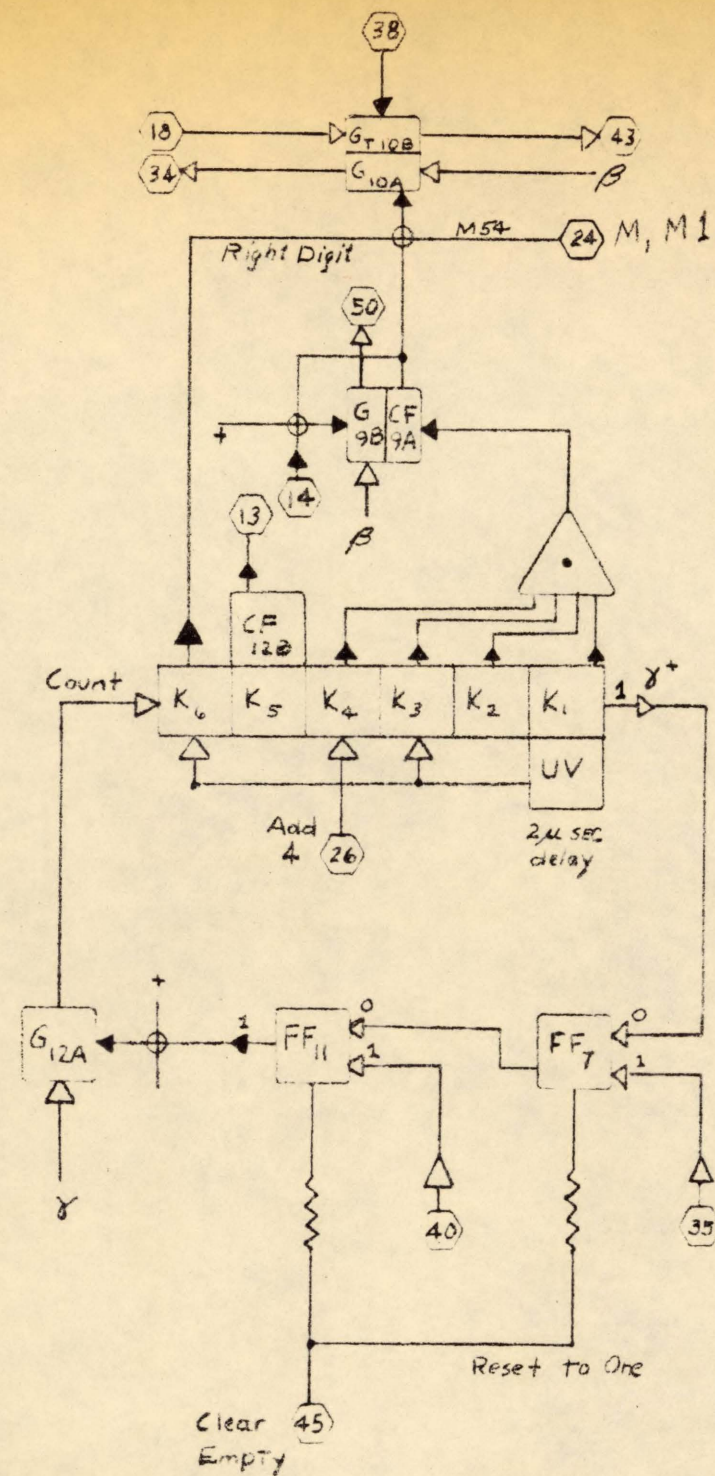


Figure 13.

Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec. Eng. Dept. - Computer Research	
2 3	plenty	DEE BLW	2/17/60 12/60	OVERALL OUTPUT	
Capacitors in _____ Resistors in _____ 1/2 W, 10%				Dr. L.D.D.	Eng. C.H.D. D.L.D.
UNLESS OTHERWISE NOTED				Dwg No.	Date 3-22-55
				W-150L-05B	



Clear Empty sets
FF4 & FF5 to "1"
and FF2 to "0"

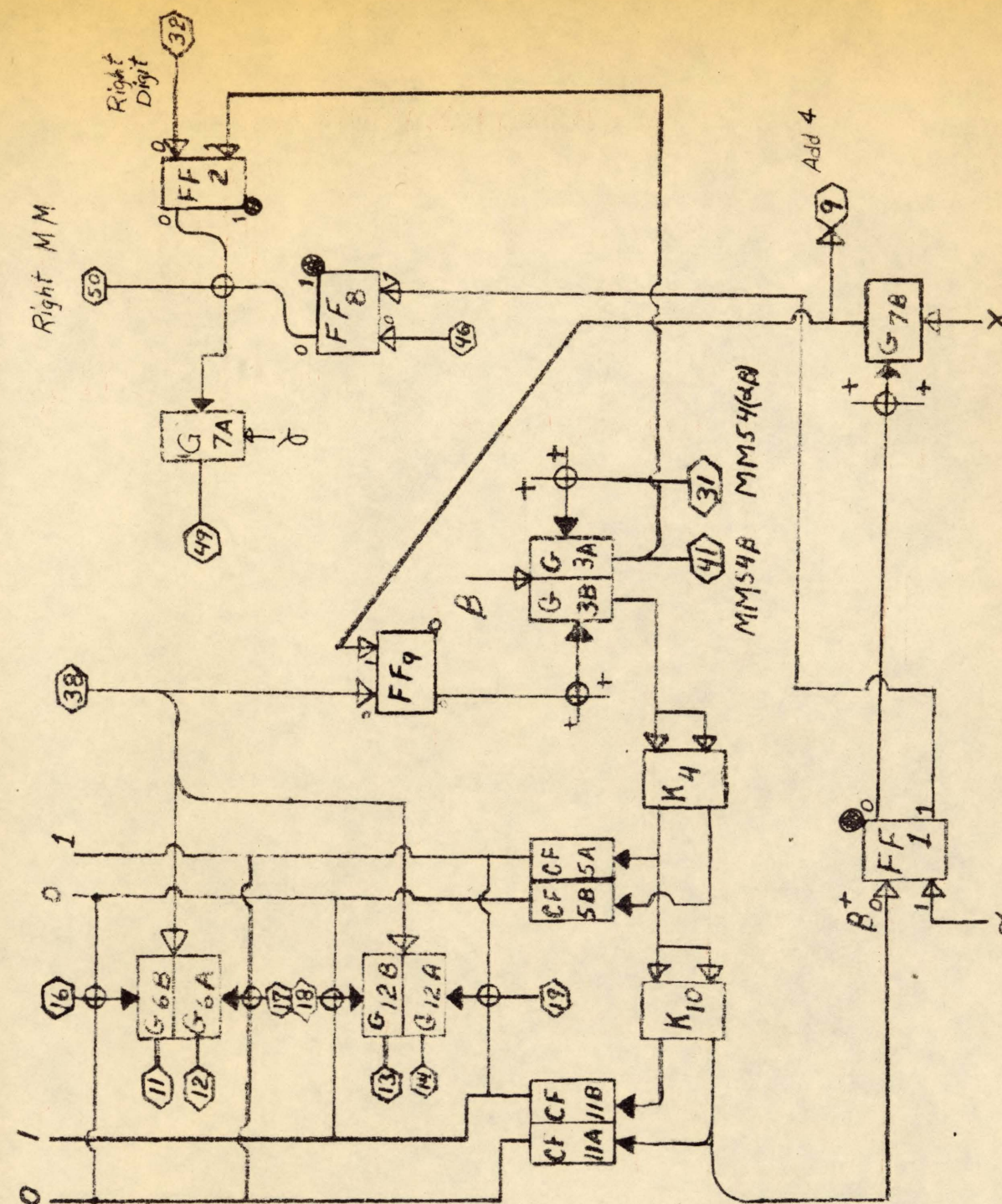
(21) IS THE "0" RETURN FOR FF9,
FF5, AND "1" RETURN FOR FF2
(26) IS THE "1" RETURN FOR FF4,
FF5, AND "0" RETURN FOR FF2

Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec. Eng. Dept.— Computer Research		
				SERIALIZER PARALYZER CONTROL		
Capacitors in _____ Resistors in _____ 1/2 W, 10% }				Dr. R. R.	Eng. A. K. S.	Date 8-6-60
UNLESS OTHERWISE NOTED				Dwg No. W142L 02A		

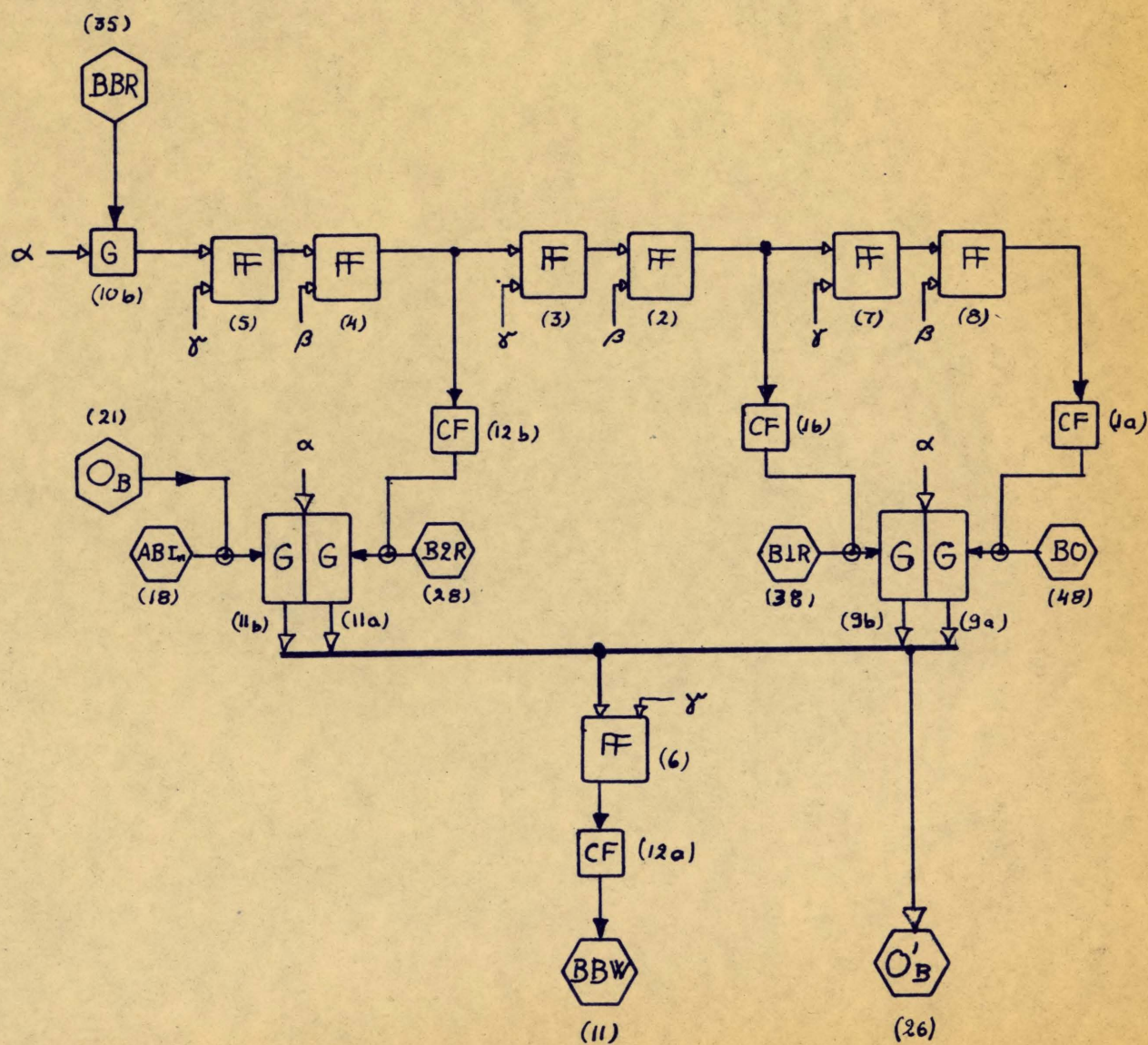
Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec. Eng. Dept.— Computer Research <i>DESCRIBER</i>		
Capacitors in _____ Resistors in _____ 1/2 W, 10% } UNLESS OTHERWISE NOTED				Dr. CMS	Eng. AKS	Date JULY 60
				Dwg No. W-144L-01A		

40	39	38	37	
0	0	1	0	Multiply
0	0	1	1	Divide
1	0	0	0	Add
1	0	0	1	Add Absolute
1	0	1	0	Subtract
1	0	1	1	Subtract Absolute
1	1	0	0	Transfer on Zero
1	1	0	1	Transfer on Zero Absolute
1	1	1	0	Transfer on Negative
1	1	1	1	Transfer on Negative Absolute

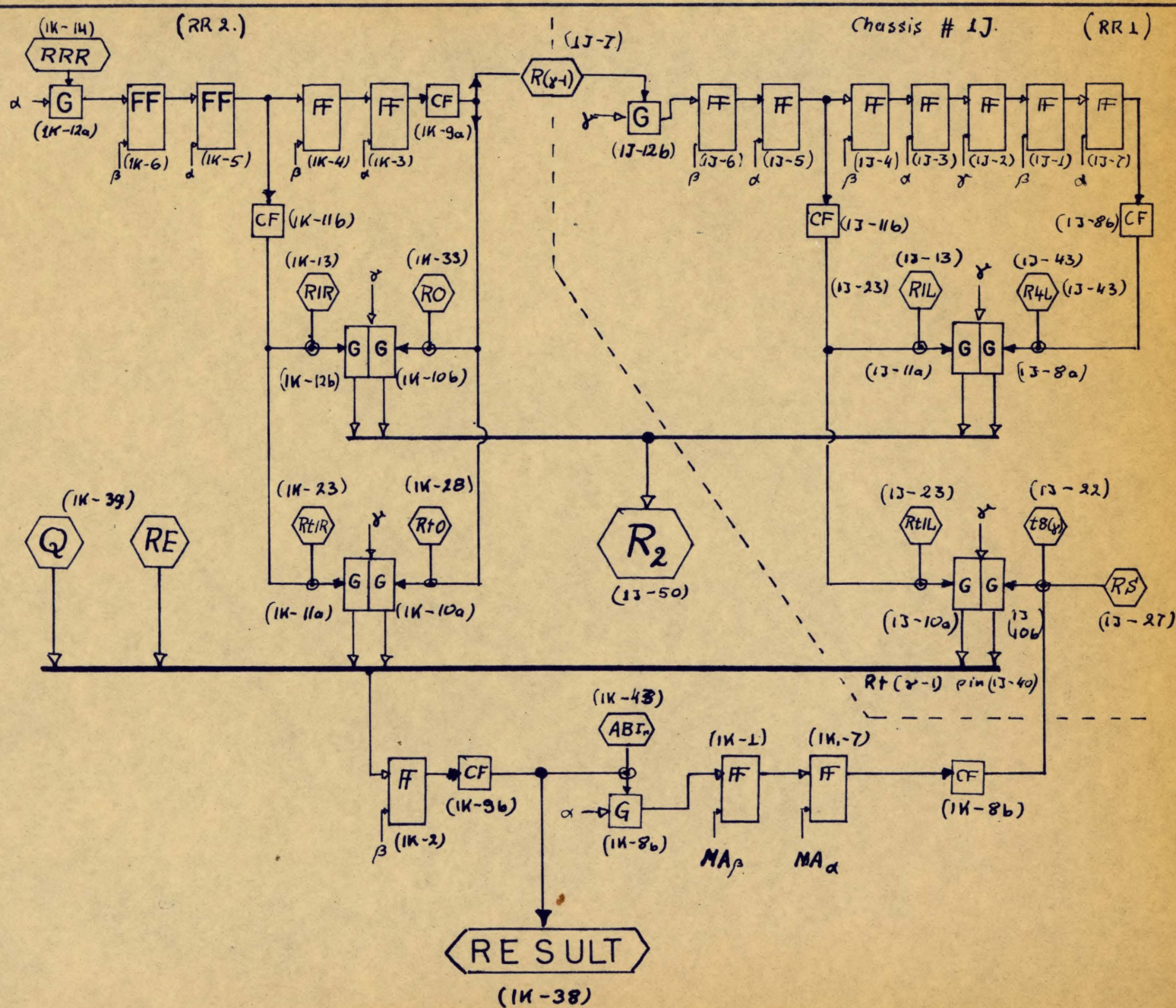
Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec. Eng. Dept. - Computer Research ARITHMETIC ORDER TYPE CODE			
				Dr. JLA	Eng. JLA	Date	5/2/55
Capacitors in _____ Resistors in _____ 1/2 W, 10%				UNLESS OTHERWISE NOTED Dwg No. W-100-04A			



Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec. Eng. Dept. - Computer Research SERIALIZER PARALYZER			
				Dr. CM.S	Eng. AKS	Date	7-26-60
Capacitors in _____ Resistors in _____ 1/2 W, 10%				UNLESS OTHERWISE NOTED Dwg No. W-141L-02A			



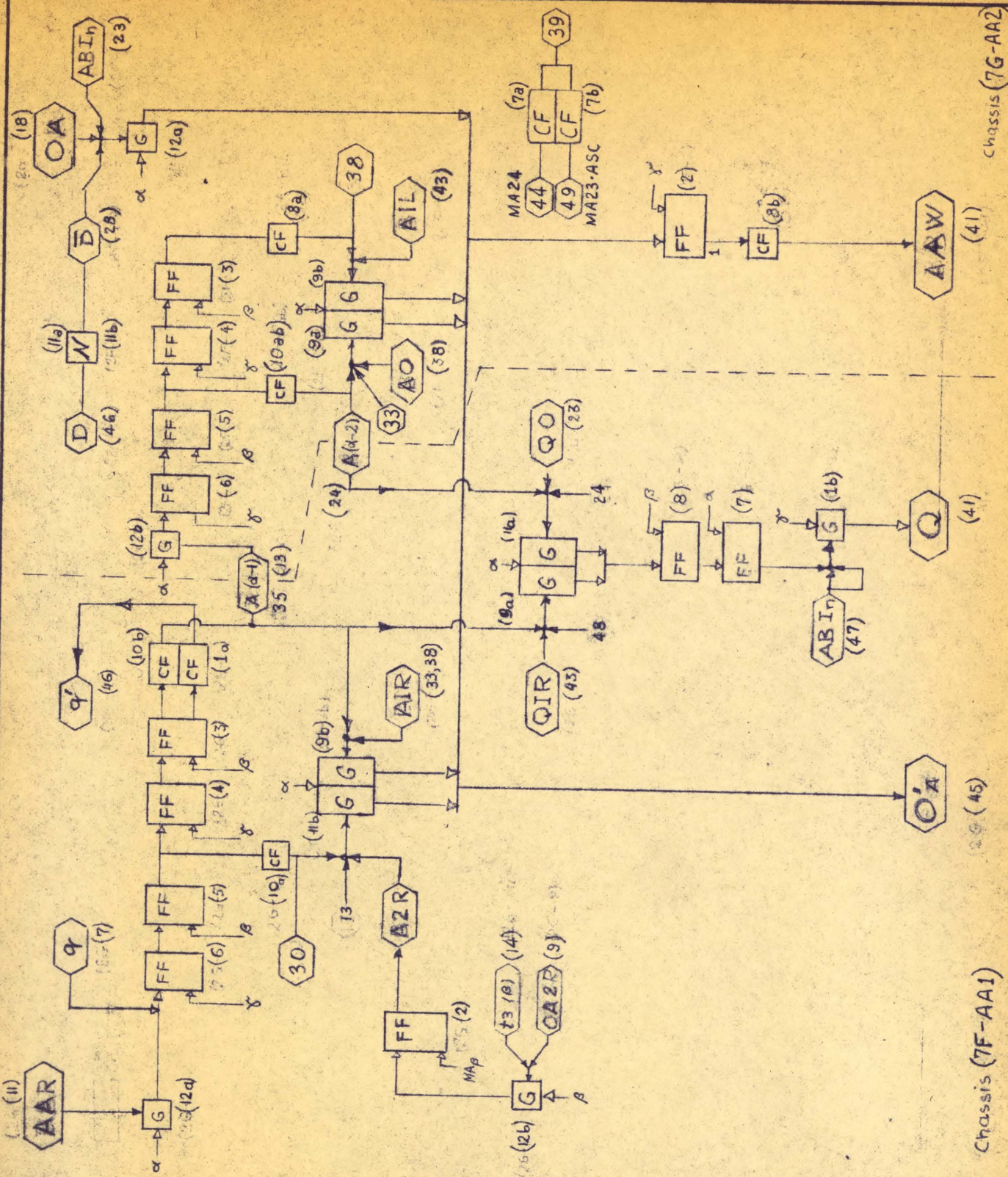
Issue No.	Revisions	By	Date
UNIVERSITY of WISCONSIN Elec. Eng. Dept.— Computer Research "B" Operand Recirculator Register			
Capacitors in _____ Resistors in _____	1/2 W. 10%	UNLESS OTHERWISE NOTED	
Dr. P. B.	Eng. J. L. A	Date <u>IV. 9. 55.</u>	
Dwg No.	W-105-01A		



Issue No.	Revisions	By	Date

Capacitors in _____	} UNLESS OTHERWISE NOTED
Resistors in _____ 1/2 W, 10 %	

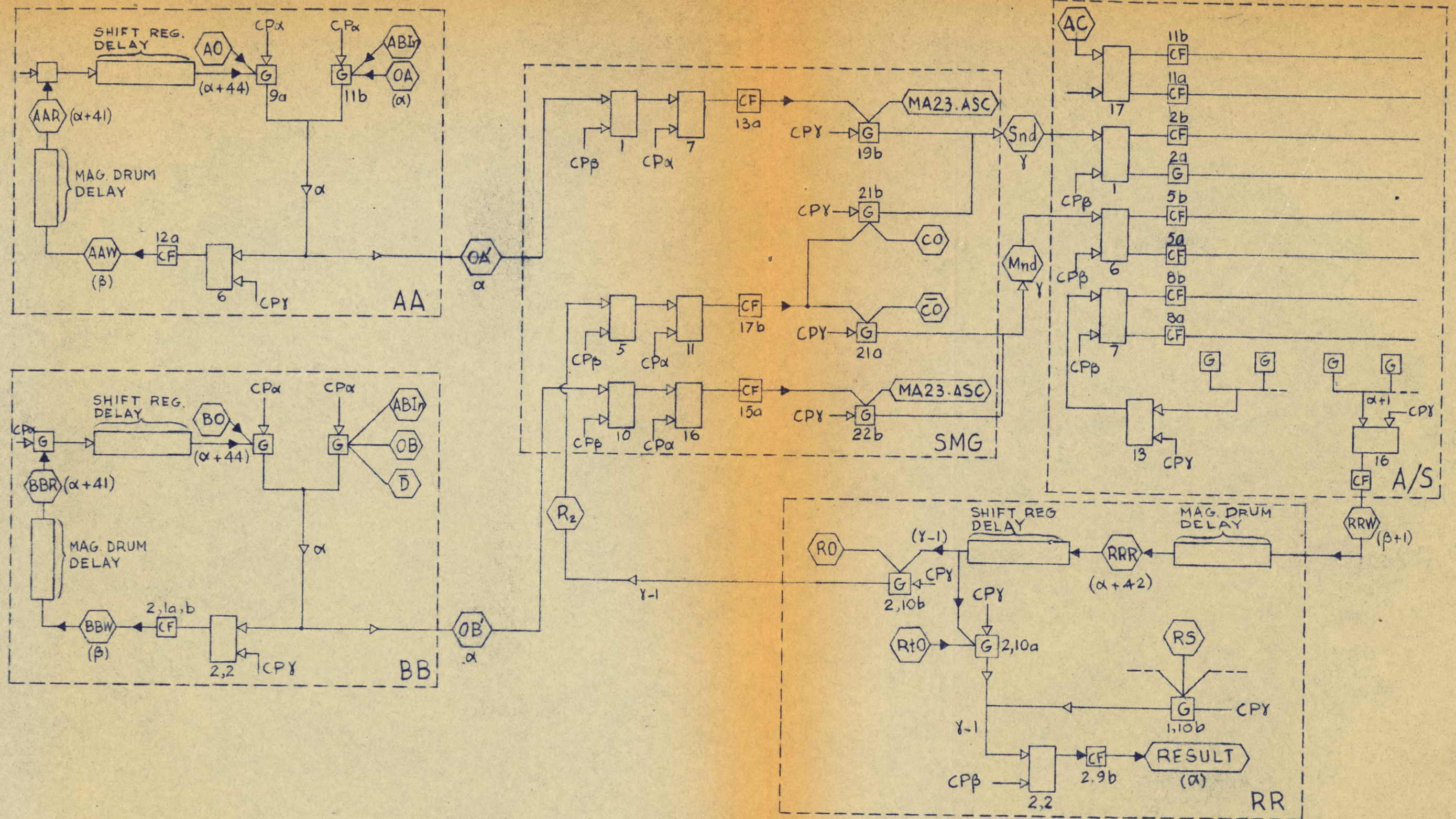
UNIVERSITY of WISCONSIN Elec. Eng. Dept.— Computer Research Result Recirculator <i>Register</i>		
Dr. P.B.	Eng. JLA	Date <u>IV 1.55.</u>
Dwg No. <u>W-106-Q1A</u> <u>A</u>		



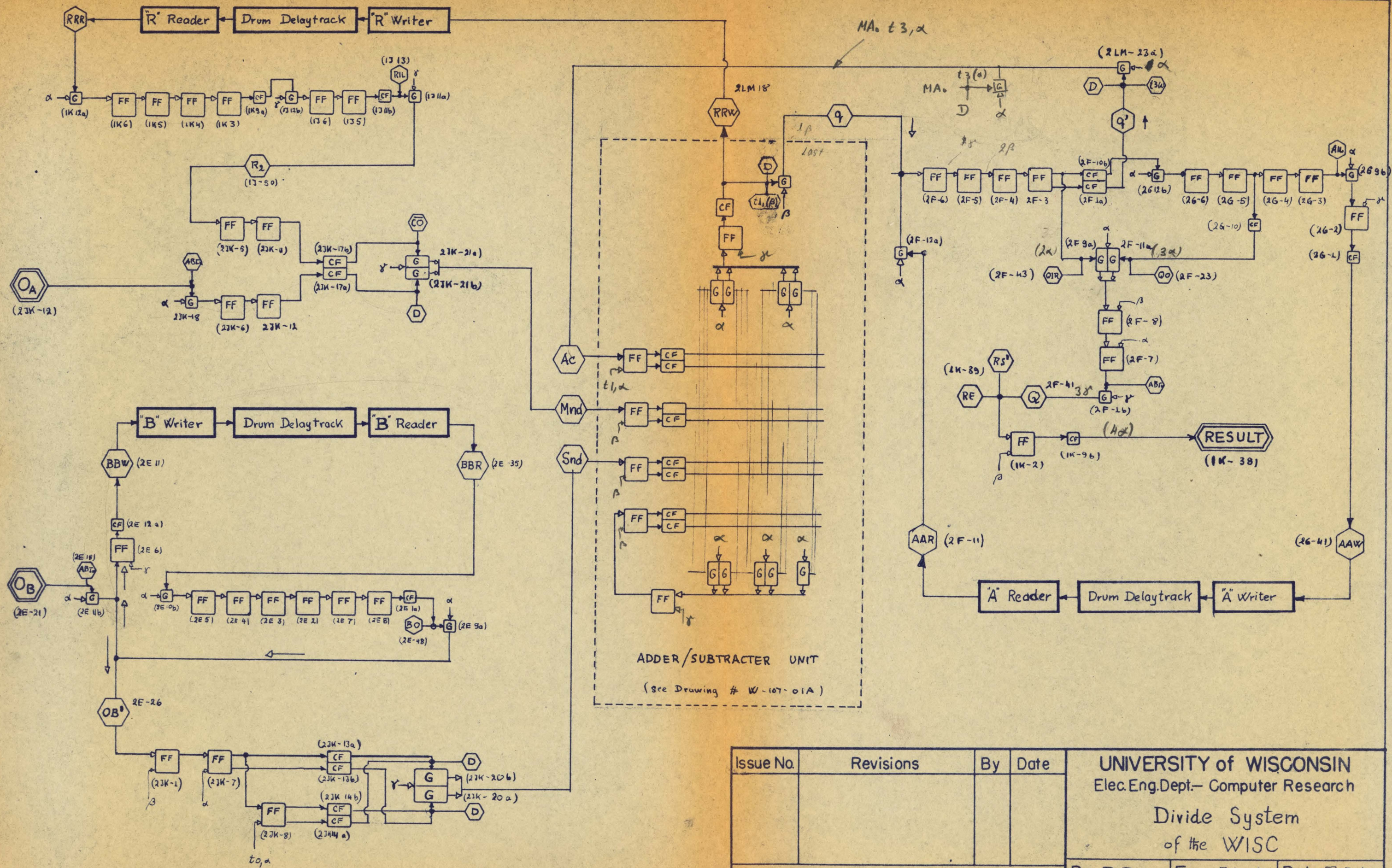
40	39	38	37	
0	0	1	0	Multiply
0	0	1	1	Divide
1	0	0	0	Add
1	0	0	1	Add Absolute
1	0	1	0	Subtract
1	0	1	1	Subtract Absolute
1	1	0	0	Transfer on Zero
1	1	0	1	Transfer on Zero Absolute
1	1	1	0	Transfer on Negative
1	1	1	1	Transfer on Negative Absolute

Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec.Eng.Dept.— Computer Research "A" Operand Recirculator
Capacitors in _____ Resistors in _____ 1/2 W, 10%				Dr. P.B Eng. JLA Date II-4-55 Dwg No. W-104-01A

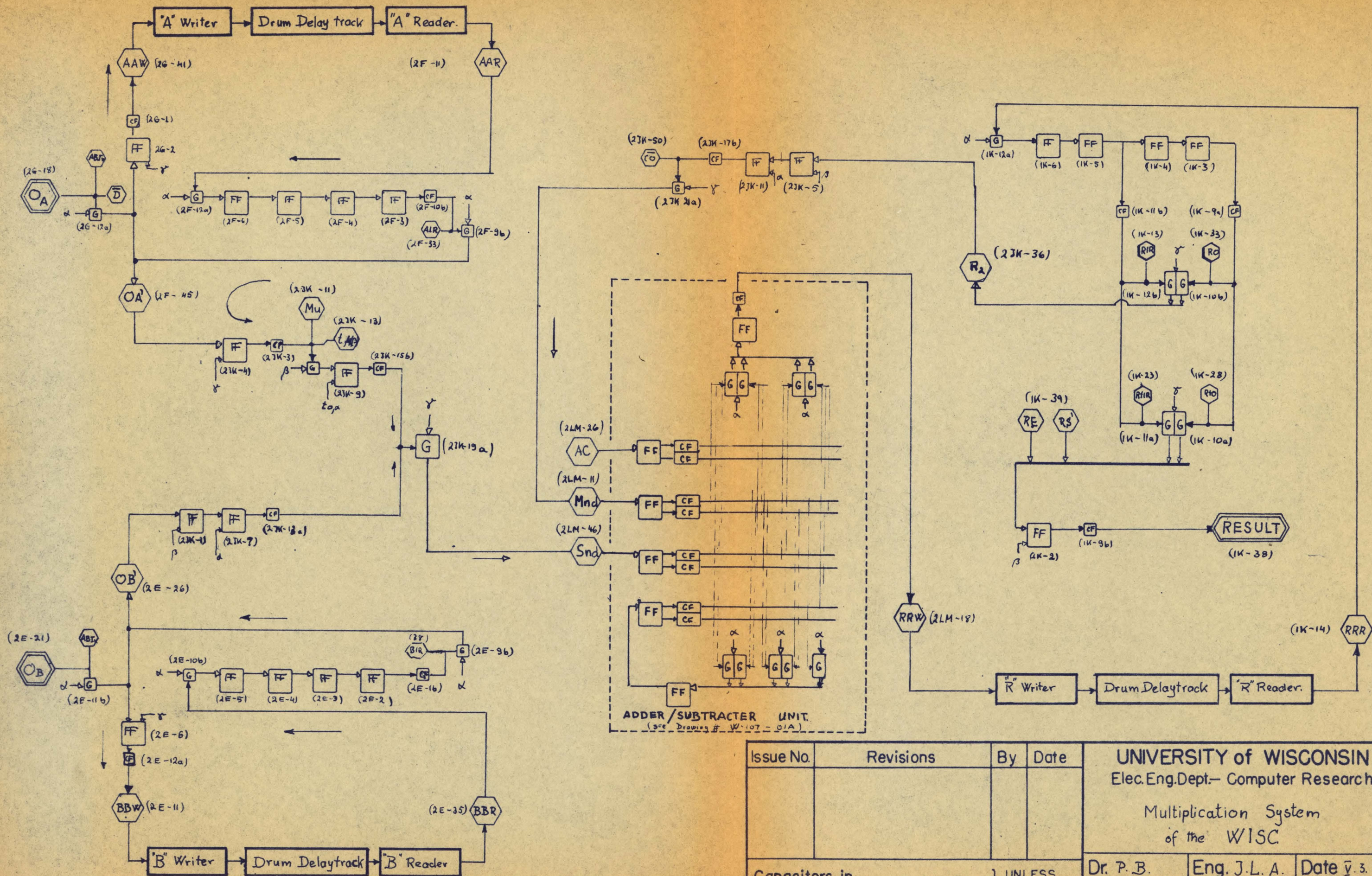
Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec.Eng.Dept.— Computer Research ARITHMETIC ORDER TYPE CODE
Capacitors in _____ Resistors in _____ 1/2 W, 10%				Dr. JLA Eng. JLA Date 5/2/55 Dwg No. W-100-04A



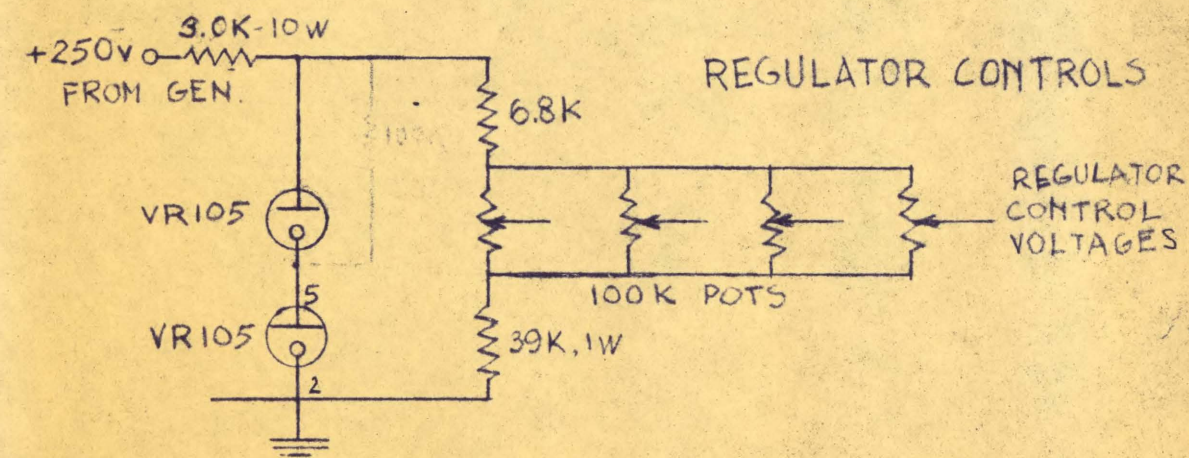
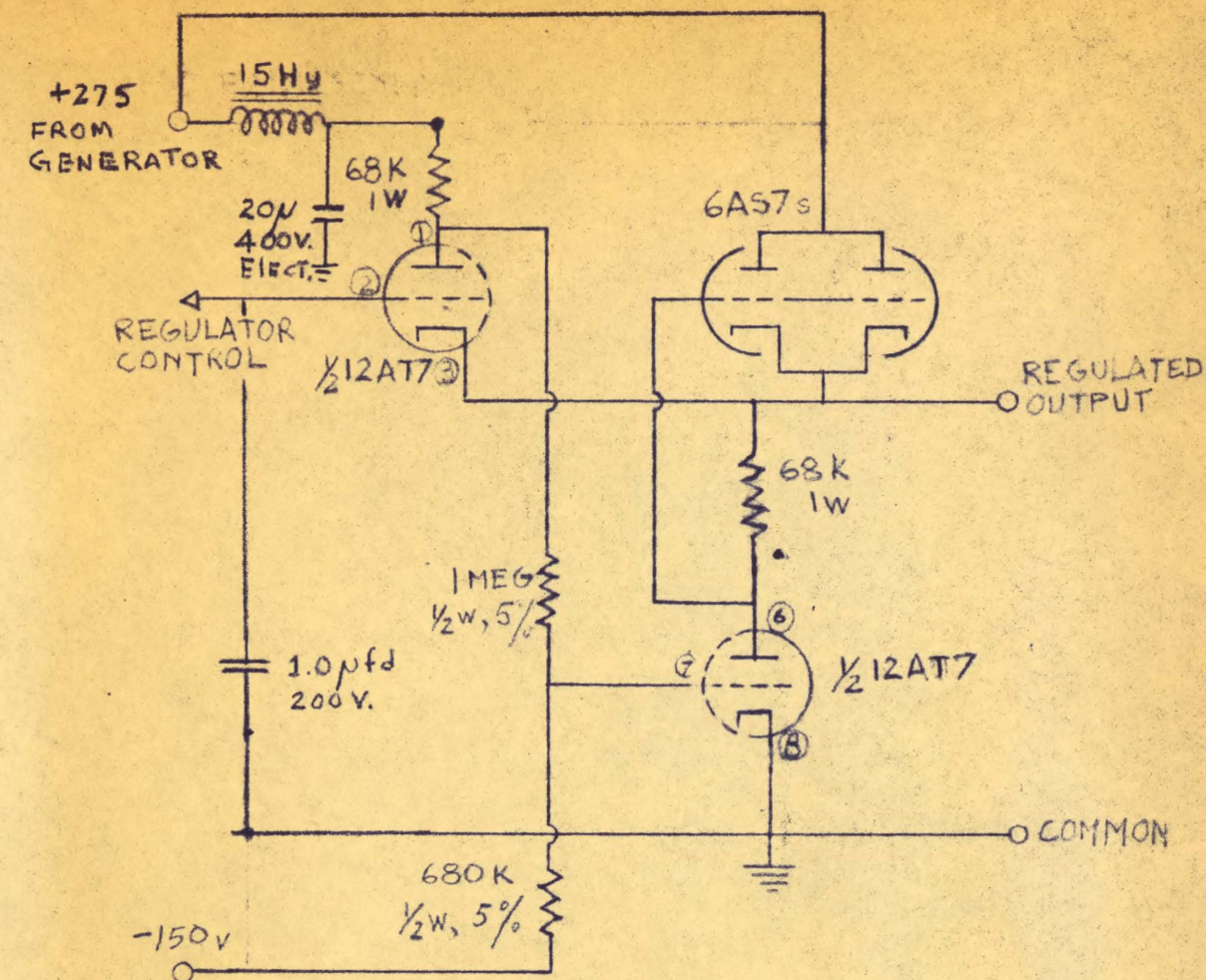
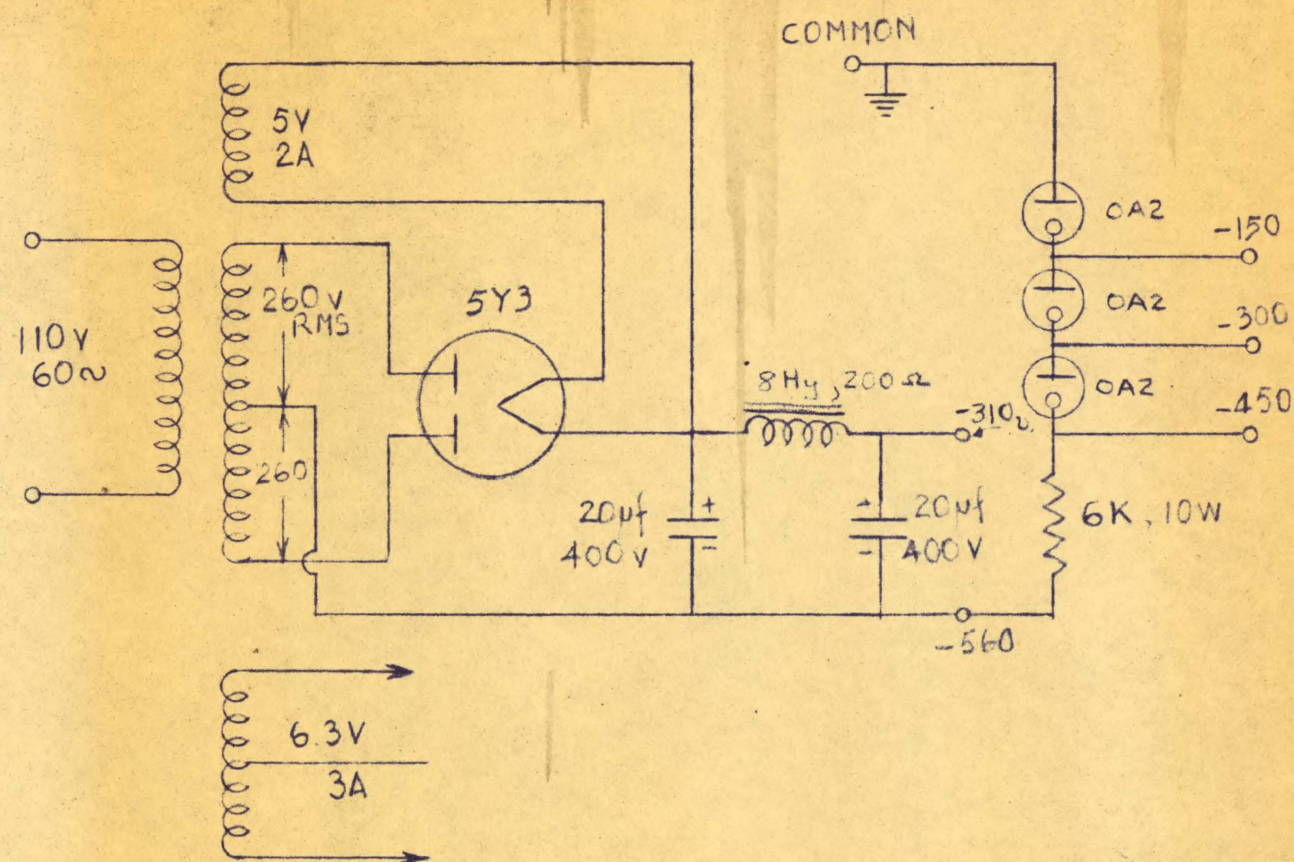
Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec. Eng. Dept. - Computer Research		
				ADDITION - SUBTRACTION SYSTEM (WISC)		
Capacitors in _____ Resistors in _____				UNLESS OTHERWISE NOTED		
				Dr. NDL	Eng. JLA	Date 1/12/55
				Dwg No. W-100-04B		



Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec. Eng. Dept. - Computer Research		
				Divide System of the WISC		
Capacitors in _____ Resistors in _____ 1/2 W, 10% } UNLESS OTHERWISE NOTED				Dr. P.B.	Eng. J.L.A	Date 3.55
				Dwg No. W-100-06B.		

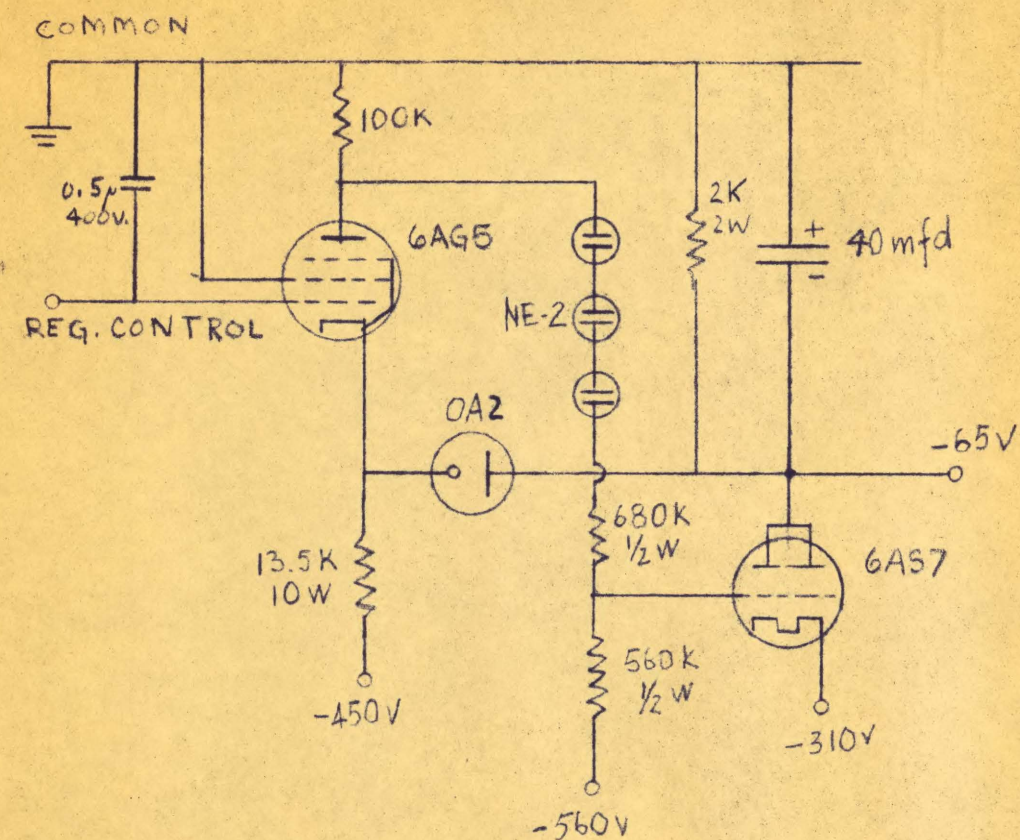


Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec. Eng. Dept. - Computer Research		
				Multiplication System of the WISC		
Capacitors in _____		UNLESS OTHERWISE NOTED		Dr. P. B.	Eng. J. L. A.	Date 7.3.55
Resistors in _____ 1/2 W, 10%				Dwg No. W - 100 - 05 B		

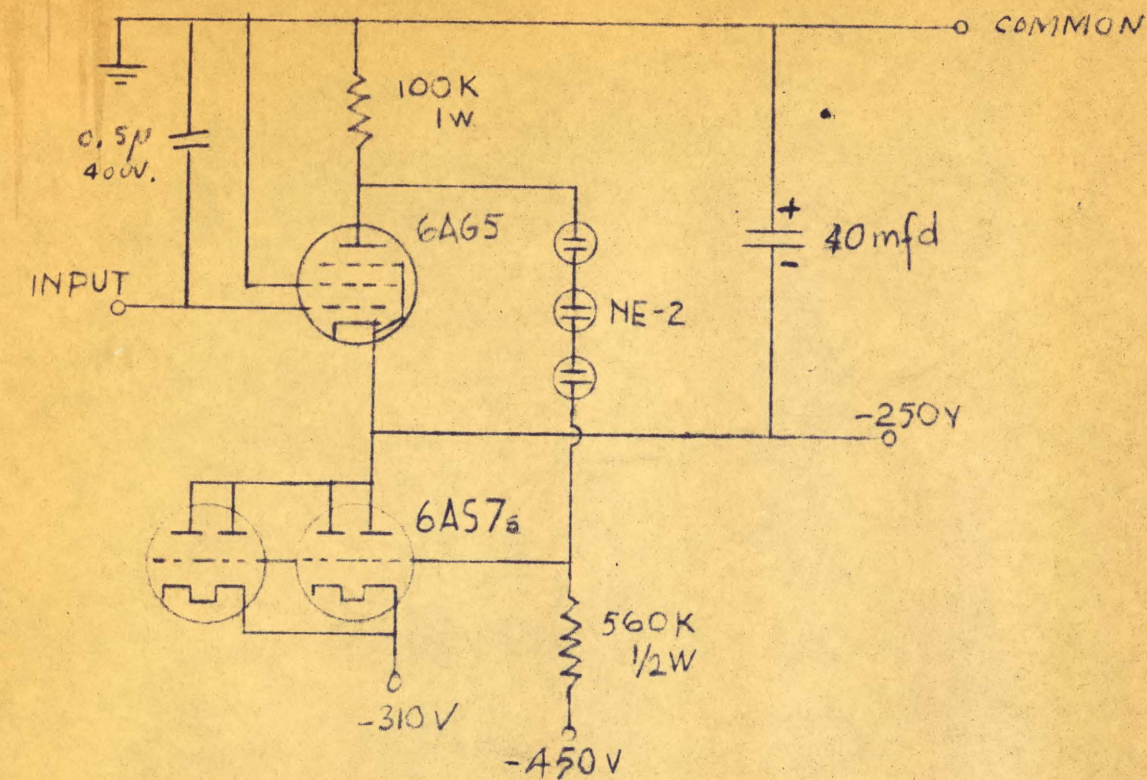


Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec. Eng. Dept.— Computer Research	
				-250 v PILOT SUPPLY	
Capacitors in _____ Resistors in _____ 1/2 W, 10% } UNLESS OTHERWISE NOTED				Dr. NDL	Eng. D.L.D. Date 8/4/55
				Dwg No. W-1735-02A	

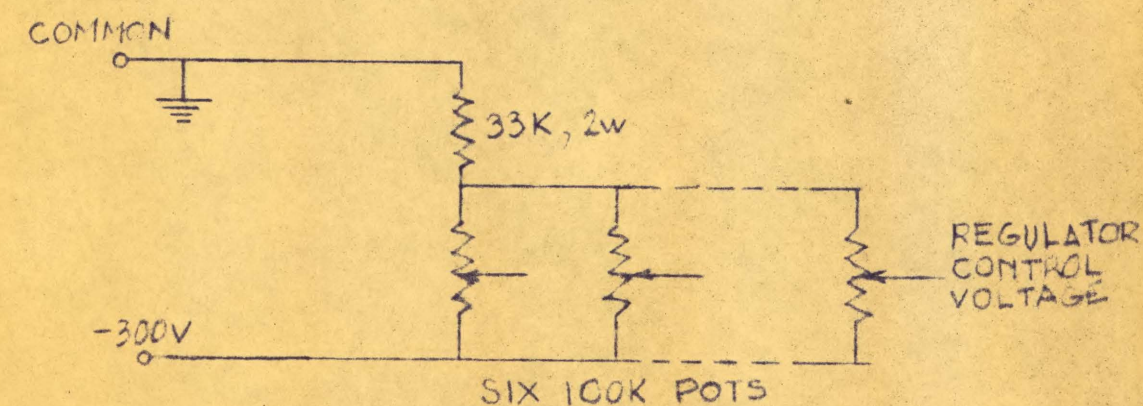
Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec. Eng. Dept.— Computer Research	
2	AMPL. TUBE, INPUT, VR STARTER VR LOAD TO 4K	AKS	3/29/57	+130, +150 v REGULATORS	
Capacitors in _____ Resistors in _____ 1/2 W, 10% } UNLESS OTHERWISE NOTED				Dr. NDL	Eng. AKS Date 8/3/55
				Dwg No. W-1735-02A	



Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec. Eng. Dept. - Computer Research	
2	INPUT C, 2K LOAD; OA2	AKS	3/24/57	-65V REGULATOR	
Capacitors in _____ Resistors in _____ 1/2 W, 10% } UNLESS OTHERWISE NOTED				Dr. NDL	Eng. D.L.D. Date 8/4/55
				Dwg No. W-1735-02A	



REGULATOR CONTROL



Issue No.	Revisions	By	Date	UNIVERSITY of WISCONSIN Elec. Eng. Dept. - Computer Research	
				-250V REGULATOR	
Capacitors in _____ Resistors in _____ 1/2 W, 10% } UNLESS OTHERWISE NOTED				Dr. NDL	Eng. D.L.D. Date 8/3/55
				Dwg No. W-1735-01A	