REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED

DX Ø
DX 1
DX 2
DX 3
DX 4
DX 5
DX 6
DX 7
DX 8
DX 9
DX 1Ø
DX 11
PIN L
POUT L
XTB H
DMAGNT H
XTA H
MEMSEL* L
MEMSEL INH L
CPMEM L
XTC H
MEMSEL L
CPSEL L
DATAF H
LXMAR H
DEVSEL L
IFETCH H
SWSEL L
INTGNT H
LINK L
RUN L
+5 VOLTS
GROUND

U1 _____ 74LSØ3
U2 _____ 74LS137
U3 _____ 74LS27
U4,U8 _____ 74LS74
U5,U11,U14,U17 _____ 74S112
U6 _____ 74LS175
U7 _____ 74LSØ2
U9 _____ 74LS1Ø
U10 _____ 74LS86
U12 _____ 74LS325
U13,U23 _____ 74LSØØ
U15 _____ 74LS138
U16,U19 _____ 74LSØ4
U18 _____ 74LSØ8
U20,U21,U27,U30 _____ 74LS365/367
U22 _____ 74LS253
U24 _____ IM6100I
U25,U28,U29 _____ 74365/367
U26 _____ 74LS2Ø

UNLESS OTHERWISE SPEC. DIMENSIONS ARE IN INCHES TOLERANCES DECIMALS .XX .XXX ANGLES ±3°	APPROVALS	DATE
	DRAWN EDJO	7-3-79
	CHECKED	
MATERIAL	APPROVED	
FINISH		
DO NOT SCALE DRAWING	RELEASED	

NEXT ASSY.

PCM Incorporated San Ramon California

TITLE SCHEMATIC DIAGRAM
PCM-12 OMEGA CPU

SIZE D | DRAWING NO. 12512 | ISSUE

SCALE — | SHEET 1 OF