NIOTA / LT1
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Dear Valued Customer:

Thank you for your purchase of a Motorola Computer Group board-level product.

When you use the onboard debugger on Motorola 68K, 88K, or PowerPC boards, some additional information is needed to use the **NIOT** (Network I / O Teach) command.

NIOT Debugger Command and the NVRAM User Area

If you use the **NIOT** debugger command , the network interface configuration parameters need to be saved / retained in the NVRAM (Non-Volatile RAM), also known as Battery Backed up RAM (BBRAM), somewhere in the address range $FFFC0000 through $FFFC0FFF (for 68K or 88K boards) or offset range $00000000 through $00000FFF (for PowerPC boards). The **NIOT** parameters do not exceed 128 bytes in size. The location for these parameters is determined by a setting of the **ENV** (Set Environment to Bug / Operating System) debugger command. If you have used the exact same space for your own program information or commands, they will be overwritten and lost.

You can relocate the network interface configuration parameters in this space by using the **ENV** command.

For 68K and 88K

For a 68K board (such as the MVME167) or an 88K board (such as the MVME187), change the `Network Auto Boot Configuration Parameters Pointer (NVRAM)` from its default of `00000000` to the value you need so as to be clear of your data within NVRAM.

For PowerPC

For a PowerPC board (such as the MVME1603), change the `Network Auto Boot Configuration Parameters Offset (NVRAM)` from its default of `FFFFFFFF` to the value you need so as to be clear of your data within NVRAM.

Once again, thank you for purchasing Motorola products. If you have any questions or comments, please contact your local Motorola Computer Group sales representative.