

Notes on Luning's 1953 Whirlwind program.

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- Whirlwind opcodes (as deduced from this program, not having a manual available at this time) 16-bit words; 5 bits for the opcode.
- ad $ac \leftarrow ac + (m)$. Set toggle iff result ≤ 0 or something like this. [-1+1 sets it; 0-0 does too].
 - ao equiv to ca m, ad=1, ts m.
 - ca $ac \leftarrow (m)$. toggle apparently not set? see 15a1; but ^{yes} set: see 3a15.
 - ck stop
 - ctc jump if toggle.
 - cs $ac \leftarrow -(m)$ also seems to set toggle, see 21a13
 - dv divide [used for interpreter call only]
 - ex $ac \leftrightarrow (m)$
 - mr multiply [used for interpreter call only]
 - p decimal integer constant or (no-op.)
 - rd read a character in Flexowriter code into ac. [6-bit code]
 - rc
 - sf normalize ac and store number of shifts in (m) [I guess; used only once, 24a1].
 - si unknown; something about I/O?
 - sl shift left
 - sp jump and set J register to address of following instruction
 - sr* shift right into extension of accumulator
 - su $ac \leftarrow ac - (m)$, toggle iff result ≤ 0
 - ta $address(m) \leftarrow J$ register.
 - td $address(m) \leftarrow address(ac)$
 - ts $(m) \leftarrow ac$

Assembly language 4a3 means location of a3 plus 4; 2r means location of current routine plus 2; actual constants 0,77410. Location 0 contains 0.

There is a 128-place switching table (unused positions are filled with miscellaneous constants and snatches of code), divided between lower case and upper case. The codes are:

- a, b, ..., z \rightarrow letter [50a4]
- 0, 1, ..., 9 \rightarrow 1, 2, ..., 10
- 0, 1, ..., 9 \rightarrow 11, 12, ..., 20
- space, backspace, tab, cr, stop, null, . \rightarrow next [2a1].
- = \rightarrow equals [0a10] + \rightarrow plus [52a4] - \rightarrow minus [55a4] / \rightarrow divide [0a9] (\rightarrow print [0a11]) \rightarrow print [0a12] \rightarrow exponent
- o \rightarrow 0 or period [8a2], a switch initially set 0
- g \rightarrow comma [14a10] or next or spcomma, a switch initially set to comma.
- uc \rightarrow upper [42a4] lc \rightarrow lower [0a4] S \rightarrow ucs [40a10] C \rightarrow ucc [50a10] \rightarrow expminus [10a9].
- P \rightarrow print1 [0a2] or print2 [2a2] a switch initially set to print 1

The code uses switched instructions, I'll use Boolean flags all initially false

- e flag: upper case?
- n flag: processing a constant? if so, z = number [0a5] or exponent [0a6]
- r flag: constant is not a label
- p flag: decimal point occurred?
- + flag: implied multiplication in effect rather than numeric label. actually +flag = nflag, so drop it.
- r flag: processing a negative exponent
- e flag: exponent should be negative (or positive if in the denominator)

mem[j] = memory loc j (shared by compiler and object code and interpretive routines) $opprint[j]$ = opcode field of mem[j]
 flac = floating point accumulator [initially 0.0]. $addrpart[j]$ = address field of mem[j].
 addr = next avail prog address [initially 32] $cadadr$ = last address used for constants [initially 197].
 op = operation code initially "mr"; $op1$ initially "dv" (always the opposite of op.)
 k = parentheses level.

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2a1 start: "si 128"; whatever that means
2a1 next: c ← next char; if cflag then x ← uppercase code [c] else x ← lowercase code [c];
9a1 if x is a label then (if nflag then (nflag ← false; go to z) else go to x);
12a1 nflag ← nflag; if x=0 [code for period] then (pflag ← true; go to next);
17a1 if x ≤ 10 then (z ← number; d ← x-1) else (z ← exponent; d ← x-11);
23a1 fd ← float(d); flac ← flac ⊗ 10.0 ⊕ fd;
36a1 if pflag then rdigits ← rdigits+1 else ldigits ← ldigits+1;
37a1 go to next;
50a4 letter: if nflag then (compile(op, c); go to next);
28a10 label: if flag then nflag ← true;
32a10 [19a6] n ← decode; decode = return (unfloat (if eflag then -flac else flac))
33a10 mem[switchtable+n] ← ("sp", addr); comment n=0 not allowed, since 0 always implied as label;
9a6 rset: flac ← 0.0; ldigits ← rdigits ← 0; pflag ← false;
17a6 if rflag then (eflag ← rflag; rflag ← false);
38a4 go to x;
10a10 [0a10] equals: x ← egsub; egsub = (compile("sp", eg routine); stack2[k] ← addr; stack1[k] ← addr-2;
8a10 tmp ← addrpart[addr-2]; mem[addr-2] ← "sp"; return address of this call of egsub;
return(tmp)
↑
garbage
12a10 rsetdvexp; go to next;
18a9 rsetdvexp = (eflag ← false; op ← "mr"; op1 ← "dv")
14a10 comma: compile("ad", "temp"); compile("ts", l);
18a10 addrpart[stack1[0]] ← stack2[0];
22a10 rsetdvexp; eflag ← false nflag ← false; go to next;

```

⊗ means double word
 ⊕ means floating add etc
 all done interpretively

What we have seen so far gives enough information to see how the formula "a=b," is compiled. [Except compile subroutine appears below]

Scanned symbol	actions	here Q = loc of jth compiled instruction.
a	mem[switchtable] ← sp (1)	mem[0] ← mr a
=	mem[2] ← sp eg routine	mem[0] ← sp mem stack1[0] ← 1 stack2[0] ← 2 l ← a
b	mem[3] ← mr b	
,	mem[4] ← ad temp	mem[5] ← ts a mem[1] ← sp (2)

So the compiled code is: jump to *+1
 jump to eg routine
 multiply by b
 add temp
 store in a

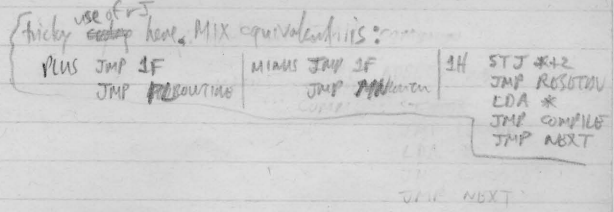
these are interpreted at run time to refer to floating pt accumulator
 i.e. flac ← flac ⊗ b; flac ← flac ⊕ temp; a ← flac

The eg routine ^{does} just what is needed, namely "temp ← 0.0; flac ← 1.0; return!"
 Now let's look at routines needed for ^{slightly} more complex expressions a = 3.1b - c/d⁻² etc.:

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0a5 number: while rdigits > 0 do (flac ← flac ⊗ 10.0; rdigits ← rdigits-1);
4a5 j ← alloc; mem[j], mem[j+1] ← flac;
9a5 compile(op, j); go to rset;
0a6 exponent: tmp ← addrpart[addr-1]; mem[addr-1] ← ("sp", ex routine);
4a6 compile(0, tmp); compile(0, decode); go to rset;
52a4 plus: rsetdvexp; compile("sp", pl routine); go to next;
55a4 minus: rsetdvexp; compile("sp", mn routine); go to next;
0a9 divide: op ← op1;
eflag ← not(eflag); go to next;
29a4 expminus: eflag ← not(eflag); rflag ← true; go to next;

```



Here are the subroutines for compilation and storage allocation:

```

9a8 compile(x,y) = (mem[addr] ← (x,y);
11a8 addr ← addr+1;
12a8 if addr > caddr then emvstop);
0a8 alloc = (caddr ← caddr-2; if addr
4a8 if addr > caddr then emvstop;
6a8 return(caddr))

```

Here are the floating-point routines used at run time:

```

0a13 eg routine = (temp ← 0.0; flac ← 1.0)
5a13 pl routine = (temp ← temp ⊕ flac; flac ← 1.0)
10a13 mn routine = (temp ← temp ⊕ flac; flac ← -1.0)
15a13 ex routine(x,n): (if n ≥ 0 then pwrflag ← false else (pwrflag ← true; n ← -n);
29a13 while n > 0 do (if pwrflag then flac ← flac ⊗ x else flac ← flac ⊗ x; n ← n-1))

```

Note bug: a = -b comes out the same as a = 1-b. [See Laming's letter.]

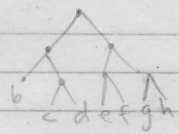
Now, parentheses:

```

0a11 1pren: compile(0,0);
1a11      k ← k+1;
3a11      eqsub; stack?
4a11      stack3[k] ← alloc;
7a11      if op ≤ op1 then go to next;      comment: "mr" < "dv";
11a11     stack1[k] ← stack1[k] + 2";
12a10     resetdexp; go to next;
0a12     1pren: j ← stack1[k] mod 2";
6a12     if stack1[k] < 2" then resetdexp
28a9     else (eflag ← true; op ← "dv"; op1 ← "mr");
9a12     compile("ad", temp); compile("ts", stack3[k]);
19a12     stack2[k] ↔ stack2[k-1]; compile("sp", stack2[k]);
28a12     addrpart[j] ← addr;
30a12     compile(op, stack3[k]); k ← k-1; go to next;
    
```

Interpretation is stack3[k] is location to hold contents of this parenthesis pair
 stack1[k] is location of instruction "sp mr" just preceding the code for this parenthesis pair, it will later jump to an instruction that uses the contents of the parenthesis after evaluation [exc k=0 → it will evaluate the exp]
 stack2[k] is location of instruction to begin the evaluation of the parenthesized expression.

Example a = ((b(cd))(efgh)),



leads to:

Scanned symbol	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	stack1	stack2	stack3
a	mr a																										
=	sp • sp ⊖																								1	2	
(sp • sp ⊖																							13	24	11
(sp • sp ⊖																						135	246	112
b				mr b																					1352	2469	1123
(sp • sp ⊖																						
c						mr c																					
d							mr d																				
)	ts 3	sp 6	mr 3																						1357	249	112
)					ad temp	ts 2	sp 4	mr 2																	13	29	11
(sp 15																					
(sp • sp ⊖																				
(sp • sp ⊖																			
e									mr e																		
f										mr f																	
)																											
(
g																											
h																											
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)																											
)																											
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Final code:

```

1  sp 3
2  sp ⊖
3  sp 45
4  sp ⊖
5  sp 19
6  sp ⊖ mr b
7  sp 15
8  sp ⊖ mr c
9  mr d
10 ad temp ts 3
11 sp 6 mr 3
12 ad temp ts 2
13 sp 4 mr 2
14 sp 41
15 sp ⊖
16 sp 21
17 sp ⊖ mr 5
18 sp 31
19 sp ⊖ mr g
20 mr h
21 ad temp ts 6
22 sp 23 mr 6
23 ad temp ts 4
24 sp 9
25 mr 4
26 ad temp ts 1
27 sp 2 mr 1
28 ad temp ts 2
    
```

which is equiv to:

- 1 × g × h + 0 → 16
- 1 × e × f + 0 → 15
- 1 × 5 × 6 + 0 → 14
- 1 × c × d + 0 → 13
- 1 × b × 3 + 0 → 12
- 1 × 2 × 4 + 0 → 11
- 1 × 1 + 0 → 2

Finally, control flow:

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42 print1: lowercasecode[" "] ← next; lowercasecode["P"] ← print2; lowercasecode["."] ← period; go to label;
2a2 print2: compile ("sp", printroutine);
7a2 go to next;
2a2 period: lowercasecode[","] ← comma; lowercasecode["P"] ← print1; lowercasecode[" "] ← 0; nflag ← false; go to next;

40a10 ucs: scop ← "sp"; go to uesc; comment SP and CP can't be labeled;
50a10 ucc: scop ← "sp";
42a10 ucsc: c ← read next char; if c = "R" then go to 0; comment "provision for future SR, CR instructions";
45a10 if c = "T" then go to 32; comment = START the object program;
47a10 lowercasecode[" "] ← spacemarker; go to next;
53a10 spacemarker1: lowercasecode[" "] ← spacemarker2; compile (scop, switchtable + decode); go to reset;
54a10 spacemarker2: lowercasecode[" "] ← comma; go to next;

```

Runtime print routine:

```

0a15 printroutine: "si 149"; j ← location of first parameter;
3a15 while oppart[j] ≠ "sp" do (print character (addrpart[j]); print character (" = ");
7a15 go to j;
42a4 upper: cflag ← true; go to next;
0a4 lower: cflag ← false; go to next;

```

Total length of compiler 45+20+64+64+19+26+6+19+34+78+19+38+13 words = 445
 (incl 15 for stack storage)
 plus runtime routines 43+19 = 62.

object program goes into locs 32-196; 210-221 is switchtable
 subscripted variables, drum usage, D operator: not yet implemented.