001
(ENTRY POINT A)
THIS MAP SHOULD NOT BE ENTERED UNLESS AN
ERROR HAS OCCURRED WHILE EXECUTING
SYSTEM TEST, AND THEN ONLY WHEN THE
DEVICE TYPE FIELD IS EQUAL TO HEXADECIMAL
'FC'.

DOES RTN = 0000 ?
Y N
| 002
| DOES RTN = 0001 ?
| Y N
| | 003
| | DOES RTN = 0002 ?
| | Y N
| | | 004
| | | DOES RTN = 0003 ?
| | | Y N
| | | | 005
| | | | DOES RTN = 0004 ?
| | | | Y N

4 4 4 2 2 2
A B C D E F

D E F
1 1 1

| | | 006
| | | DOES RTN = 0005 ?
| | | Y N
| | |
| | | 007
| | | A NOT VALID COMMAND WAS EXECUTING.
| | | GO TO PAGE 5, STEP 040,
| | | ENTRY POINT B.
| | |
| | | 008
| | | DOES CKPT = 0000 ?
| | | Y N
| | |
| | | 009
| | | A COMMAND TO CAUSE A DELAYED COMMAND
| | | REJECT WAS EXECUTING.
| | | INSPECT ISB
| | | BITS 0-7 IS THE EXPECTED DATA.
| | | INSPECT DEV4
| | | BITS 0-7 IS THE ACTUAL DATA.
| | | BITS 8-15 IS THE DEVICE ADDRESS.
| | |
| | | 010
| | | A COMMAND TO CAUSE A DCB SPECIFICATION
| | | CHECK WAS EXECUTING.
| | | INSPECT ISB
| | | BITS 0-7 IS THE EXPECTED DATA.
| | | INSPECT DEV4
| | | BITS 0-7 IS THE ACTUAL DATA.
| | | BITS 8-15 IS THE DEVICE ADDRESS.
| | |
| | 011
| | DOES CKPT = 0000 ?
| | Y N
| |
| | 012
| | A DIAGNOSTIC COMMAND TWO EXECUTED.
| | INSPECT DEV4
| | BITS 0-7 IS THE EXPECTED DATA.
| | BITS 8-15 IS THE ACTUAL DATA.
| |
| | 013
| | A DIAGNOSTIC COMMAND TWO WAS EXECUTING.
| | GO TO PAGE 5, STEP 040, ENTRY POINT B.
| |
| 014
| DOES CKPT = 0000 ?
| Y N
|
| 015
| DOES CKPT = 0001 ?
| Y N
| | 016
| | DOES CKPT = 0002 ?
| | Y N
| | | 017
| | | DOES CKPT = 0003 ?
| | | Y N
| | | | 018
| | | | DOES CKPT = 0004 ?
| | | | Y N

3 3 3 3 3 3
G H J K L M

G H J K L M SYNCHRONUS COMMUNICATIONS SINGLE   MAP FCE0-3
2 2 2 2 2 2
          LINE CONTROLLER/HIGH SPEED.

          PAGE   3 OF   7

019
DOES CKPT = 0005 ?
Y N

  020
  A DATA COMPARE ERROR.
  INSPECT DEV4
  BITS 0-7 IS THE EXPECTED DATA.
  BITS 8-15 IS THE ACTUAL DATA.

  021
  BYTE MODE ERROR.
  INSPECT DEV4
  BITS 8-15 IS THE ACTUAL DATA.

  022
  A DOMAIN ADDRESS ERROR.
  INSPECT DEV4
  BITS 0-7 IS THE ADDRESS EXPECTED.
  BITS 8-15 IS THE ADDRESS RECEIVED.

  023
  A CHECKSUM ERROR.
  INSPECT DEV4
  BITS 0-7 IS THE CHECKSUM EXPECTED.
  BITS 8-15 IS THE CHECKSUM RECEIVED.

  024
  A CHECKSUM ERROR.
  INSPECT DEV3
  BITS 0-15 IS THE CHECKSUM EXPECTED.
  INSPECT DEV4
  BITS 0-15 IS THE CHECKSUM RECEIVED.

025
A CHECKSUM ERROR.
INSPECT DEV3
BITS 0-15 IS THE CHECKSUM EXPECTED.
INSPECT DEV4
BITS 0-15 IS THE CHECKSUM RECEIVED.

026
A DIAGNOSTIC ONE COMMAND WAS EXECUTING.
GO TO PAGE 5, STEP 040, ENTRY POINT B.

A B C   SYNCHRONUS COMMUNICATIONS SINGLE   MAP FCE0-4
1 1 1
         LINE CONTROLLER/HIGH SPEED.

         PAGE   4 OF   7

027
DOES CKPT = 0000 ?
Y N

  028
  DOES CKPT = 0001 ?
  Y N

    029
    DOES CKPT = 0002 ?
    Y N

      030
      DOES CKPT = 0003 ?
      Y N

        031
        THE RESIDUAL ADDRESS IS IN ERROR.
        INSPECT DEV4
        BITS 0-15 IS THE RESIDUAL ADDRESS
        EXPECTED.
        INSPECT RSAD
        BITS 0-15 IS THE RESIDUAL ADDRESS
        RECEIVED.

        032
        A CYCLE STEAL STATUS COMMAND WAS
        EXECUTING.
        GO TO PAGE 5, STEP 040,
        ENTRY POINT B.

      033
      THE RESIDUAL ADDRESS IS IN ERROR.
      INSPECT DEV4
      BITS 0-15 IS THE RESIDUAL ADDRESS
      EXPECTED.
      INSPECT RSAD
      BITS 0-15 IS THE RESIDUAL ADDRESS
      RECEIVED.

    034
    A CYCLE STEAL   STATUS   COMMAND   WAS
    EXECUTING.
    GO TO PAGE 5, STEP 040,
    ENTRY POINT B.

  035
  A DIAGNOSTIC ONE COMMAND WAS EXECUTING.
  GO TO PAGE 5, STEP 040,
  ENTRY POINT B.

036
DOES CKPT = 0000 ?
Y N

  037
  A PREPARE WAS EXECUTING.
  GO TO PAGE 5, STEP 040,
  ENTRY POINT B.

038
A RESET DEVICE WAS EXECUTING.
GO TO PAGE 5, STEP 040, ENTRY POINT B.

039
ERROR ON READ ID.
INSPECT DEV3
BITS 0-15 IS THE ID EXPECTED.
INSPECT DEV4
BITS 0-15 IS THE ID RECEIVED.

040
(ENTRY POINT B)
DOES IO=07 ?
Y N

041
DOES IO=06 ?
Y N

042
DOES IO=05 ?
Y N

043
DOES IO=03 ?
Y N

044
DOES IO=02 ?
Y N

045
DOES IO=01 ?
Y N

046
DEVICE NOT ATTACHED.

047
DEVICE BUSY.

048
BUSY AFTER RESET.

049
COMMAND REJECT.

050
INTERFACE DATA CHECK.

051
CONTROLLER BUSY.

052
DOES IN=02 ?
Y N

053
DOES IN=07 ?
Y N

054
DOES IN=06 ?
Y N

055
DOES IN=04 ?
Y N

056
CONTROLLER END.

057
ATTENTION.

058
ATTENTION AND EXCEPTION.

059
ATTENTION AND DEVICE END.

060
IS BIT 0 OF THE ISB OFF ?
Y N

061
IS BIT 2 OF THE ISB OFF ?
Y N

062
NOT CORRECT LENGTH ERROR.

063
IS DEV1 EQUAL TO FFFF ?
Y N

064
THE ERROR BITS IN CS-2 ARE AS FOLLOWS.
BIT 0 = OVERRUN.
BIT 1 = TIMEOUT.
BIT 2 = DCE INTERFACE ERROR.
BIT 3 = BLOCK CHECK ERROR.

BIT 4 = MULTI-POINT TRANSMIT ERROR.
ARE ALL OF THE BITS 0-4 IN CS-2 OFF ?
Y N

065
INSPECT CS-2 FOR ERROR BITS.
ANALYZE BITS 0-4 FOR ERROR INFORMATION.

066
(ENTRY POINT D)

THIS MAP CANNOT DETERMINE THE PROBLEM.
USE   THE ERROR OUTPUT, YOU HAVE BEEN USING
FOR THIS MAP, AS YOUR ERROR INDICATIONS
AND GO TO MAP 0070 ENTRY POINT A.

U V   SYNCHRONUS COMMUNICATIONS SINGLE   MAP FCE0-7
6 6   LINE CONTROLLER/HIGH SPEED.

      PAGE   7 OF   7

067
THE ERROR BITS IN CS-2 ARE AS FOLLOWS.
BIT 0 = OVERRUN.
BIT 1 = ABORT.
BIT 2 = LONG FRAME.
BIT 3 = BLOCK CHECK ERROR.

BIT 4 = TIMEOUT.
BIT 5 = IDLE DETECT.
BIT 6 = NON PRODUCTIVE RECEIVE.
BIT 7 = DCE INTERFACE ERROR.
ARE ALL OF THE BITS 0-7 IN CS-2 OFF ?
Y N

  068
  INSPECT CS-2 FOR ERROR BITS.
  ANALYZE BITS 0-7 FOR ERROR INFORMATION.

069
GO TO PAGE 6, STEP 066, ENTRY POINT D.

070
IS BIT 1 OF THE ISB OFF ?
Y N

  071
  DELAYED COMMAND REJECT.

072
IS BIT 2 OF THE ISB OFF ?
Y N

  073
  NOT CORRECT LENGTH ERROR.

074
IS BIT 3 OF THE ISB OFF ?
Y N

  075
  DCB SPECIFICATION CHECK.

076
IS BIT 4 OF THE ISB OFF ?
Y N

  077
  STORAGE DATA CHECK.

078
IS BIT 5 OF THE ISB OFF ?
Y N

  079
  NOT VALID STORAGE ADDRESS.

080
IS BIT 6 OF THE ISB OFF ?
Y N

  081
  PROTECT CHECK.

082
IS BIT 7 OF THE ISB OFF ?
Y N

  083
  INTERFACE DATA CHECK.

084
(ENTRY POINT C)
IS BIT 0 OF THE FLAGS OFF ?
Y N

  085
  NOT EXPECTED INTERRUPT.

086
IS BIT 5 OF THE FLAGS OFF ?
Y N

  087
  WRONG INTERRUPT LEVEL.

088
IS BIT 6 OF THE FLAGS OFF ?
Y N

  089
  LOST INTERRUPT.

090
PROTECT CHECK.