

LOCTR OBJECT TEXT STMT SOURCE STATEMENT COPYRIGHT IBM CORP 1976
3 COPY LOG4800 ** MAP EC HISTORY **
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35 *****
36 14800 START1 X'2500' START ADDRESS OF ALL '1' TYPE PROG
37 @QUES EQU X'0100' EQUATED VALUE FOR MDI STATEMENT
38 @FRINT EQU X'0101' EQUATED VALUE FOR MDI STATEMENT
39 @STOP EQU X'0102' EQUATED VALUE FOR MDI STATEMENT
40 @GOTO EQU X'0200' EQUATED VALUE FOR MDI STATEMENT
41 @CALL EQU X'0201' EQUATED VALUE FOR MDI STATEMENT
42 @INPT EQU X'0300' EQUATED VALUE FOR MDI STATEMENT
43 @QUXX EQU X'0400' EQUATED VALUE FOR MDI STATEMENT
44 @TUXX EQU X'0500' EQUATED VALUE FOR MDI STATEMENT
45 @NVLD EQU X'0600' EQUATED VALUE FOR MDI STATEMENT
46 @ EQU X'0000' EQUATE FOR EQUAL
47 @ NE EQU X'0004' EQUATE FOR NOT EQUAL
48 @ HI EQU X'0008' EQUATE FOR HIGH
49 @ NB EQU X'000C' EQUATE FOR NOT HIGH
50 @ LO EQU X'0010' EQUATE FOR LOW
51 @ NL EQU X'0014' EQUATE FOR NOT LOW
52 @ LT EQU X'0018' EQUATE FOR LESS THAN
53 @ LE EQU X'001C' EQUATE FOR LESS THAN OR EQUAL TO
54 @ GT EQU X'0020' EQUATE FOR GREATER THAN
55 @ GE EQU X'0024' EQUATE FOR GREATER THAN OR EQUAL TO
56 @ CN EQU X'0200' EQUATE FOR ON
57 @ OF EQU X'0202' EQUATE FOR OFF
58 @ MX EQU X'0204' EQUATE FOR MIXED
59 @ EBC EQU X'0000' EQUATE FOR EBCDIC DATA TRANSFER
60 @ HEX EQU X'0001' EQUATE FOR HEX DATA TRANSFER
61 @ XTRNL EQU X'0002' EQUATE FOR EXTERNAL REFERENCE
62 @ INTRNL EQU X'0003' EQUATE FOR INTERNAL REFERENCE
63 @ FARM EQU X'0000' EQUATE INDICATING PARAMETER
64 @ LA EQU X'0001' EQUATE FOR LOGIC ADDRESS
65 @ UA EQU X'0002' EQUATE FOR UNIT ADDRESS
66 @ DUMMY EQU X'0000' DUMMY EQUATE
67 @ PID EQU *-X'0000' ADDRESS OF MDI HEADER
68 @ PTYPE EQU *-X'22CE' ADDRESS OF PROCESSOR TYPE FIELD
69 @ STFNUM EQU PID+X'000C' ADDRESS OF DECIMAL STEP NUMBER
70 @ OPWD1 EQU PID+X'000E' ADDRESS OF OPTION WORD ONE
71 @ OPWD2 EQU PID+X'0010' ADDRESS OF OPTION WORD TWO
72 @ TUSTATUS EQU PID+X'0016' ADDRESS OF TU STATUS WORD
73 @ TUPARM1 EQU PID+X'001A' ADDRESS OF TU WORK AREA
74 @ TUPARM2 EQU PID+X'001E' ADDRESS OF PARM 1 POINTER
75 @ TUPARM3 EQU PID+X'0022' ADDRESS OF PARM 2 POINTER
76 @ TUPARM4 EQU PID+X'0028' ADDRESS OF PARM 3 POINTER
77 @ TUPARM5 EQU PID+X'002C' ADDRESS OF PARM 4 POINTER
78 @ TUPARM6 EQU PID+X'0030' ADDRESS OF PARM 5 POINTER
79 @ TUPARM7 EQU PID+X'0036' ADDRESS OF PARM 6 POINTER
80 @ TUPARM8 EQU PID+X'003C' ADDRESS OF PARM 7 POINTER
81 @ TUPARM9 EQU PID+X'0040' ADDRESS OF PARM 8 POINTER
82 @ TUPARM10 EQU PID+X'0046' ADDRESS OF PARM 9 POINTER
83 @ TUPARM11 EQU PID+X'004C' ADDRESS OF PARM 10 POINTER
84 @ TUPARM12 EQU PID+X'0050' ADDRESS OF PARM 11 POINTER
85 @ TUPARM13 EQU PID+X'0056' ADDRESS OF PARM 12 POINTER
86 @ TUPARM14 EQU PID+X'005C' ADDRESS OF PARM 13 POINTER
87 @ TUPARM15 EQU PID+X'0060' ADDRESS OF PARM 14 POINTER
88 @ TUPARM16 EQU PID+X'0066' ADDRESS OF PARM 15 POINTER
89 @ TMSGWTR EQU PID+X'006A' ADDRESS OF -> TO COMMON MSG WRITER
90 @ TUA EQU PID+X'006E' ADDRESS OF UNIT ADDRESS IN EBC
91 @ TUDA EQU PID+X'0070' ADDRESS OF DEVICE ADDRESS IN EBC
92 @ TUBUFF EQU PID+X'0072' ADDRESS OF LAST USED WORD IN MAP
93 @ TULAST EQU PID+X'0074' ADDRESS OF LAST ADDRESSABLE WORD
94 @ TUBESULN EQU PID+X'0076' ADDRESS OF LENGTH OF TU RESULTS
95 @ TUBESUL EQU PID+X'0078' ADDRESS OF TU RESULTS FIELD
96 @ MAPNAME EQU PID+X'007C' ADDRESS OF MAP NAME FIELD IN HEX
97 @ TUINPT EQU PID+X'0148' ADDRESS OF \$INPT DATA
98 @ FARMARA EQU PID+X'016E' ADDRESS OF \$INPT INPUT AREA
99 @ LCADD1 EQU PID+X'01B8' MDI POINTER
100 @ DCADD2 EQU PID+X'01BA' MDI POINTER
101 @ SUPSTAT EQU PID+X'01C4' ADDRESS OF MDI STATUS
102 @ EVALD EQU PID+X'01D0' ADDRESS OF DEVICE ADDRESS TABLE 0
103 @ DLVALD1 EQU PID+X'01D4' ADDRESS OF DEVICE ADDRESS TABLE 1
104 @ DLVALD2 EQU PID+X'01E0' ADDRESS OF DEVICE ADDRESS TABLE 2
105 @ DLVALD3 EQU PID+X'01E4' ADDRESS OF DEVICE ADDRESS TABLE 3
106 @ DLVALD4 EQU PID+X'01F0' ADDRESS OF DEVICE ADDRESS TABLE 4
107 @ DLVALD5 EQU PID+X'0202' ADDRESS OF DEVICE ADDRESS TABLE 5
108 @ DLVALD6 EQU PID+X'020C' ADDRESS OF DEVICE ADDRESS TABLE 6
109 @ DEVADD7 EQU PID+X'0216' ADDRESS OF DEVICE ADDRESS TABLE 7
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111 *****
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00189A
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0018AA
0018AC
0018AE
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0018B2
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0018BA
0018BE
0018C0
0018C2
0018C4
0018C6
0018C8
0018FC
001948
00196E
001980
00198A
0019C4
0019D0
0019DA
0019E4
0019EE
0019F8
001A02
001A0C
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LOCTR OBJECT TEXT STMT SOURCE STATEMENT COPYRIGHT IBM CORP 1976
198 ***** DC A(ENTPT) POINT TO MAP ENTRY POINT TABLE *****
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THE FOLLOWING TABLES ARE USED BY THE MDI SUPERVISOR (D3C00) TO LOCATE THE CORRECT RULE TO INVOKE, TO OBTAIN THE PROPER PARAMETERS TO PASS TO THE TU'S AND TO PASS TO THE OPERATOR THE INDICATED MESSAGE(S). THERE ARE FOUR TABLES USED FOR THIS PURPOSE THEY ARE:
STEP AND RULE ADDRESS TABLE
THIS TABLE GIVES THE ADDRESS OF THE RULE TO INVOKE AND THE ASSOCIATED STEP DECIMAL STEP NUMBER OF THAT RULE. ENTRIES ARE AS FOLLOWS
A) AN ADDRESS OF THE RULE DC START AREA
B) THE STEP NUMBER IN DECIMAL
C) AN EQUATE FOR THE STEP NUMBER
RULE INFORMATION TABLE
THIS TABLE CONTAINS THE REQUIRED INFORMATION TO EXECUTE THE APPROPRIATE RULE UNDER MDI. EACH RULE HAS ITS OWN UNIQUELY DEFINED AREA INDICATED BELOW. END OF TABLE IS INDICATED WITH A X'0000' FOR THE RULE EQUATE.
\$QUES
A) RULE EQUATE X'0100'
B) ADDRESS OF THE YES LEG RULE
\$FRINT
A) RULE EQUATE X'0101'
B) ADDRESS OF MESSAGE TO FRINT
\$STOP
A) RULE EQUATE X'0102'
B) ADDRESS OF MESSAGE
\$GOTO
A) RULE EQUATE X'0200'
B) ADDRESS OF MESSAGE
C) NAME OF MAP TO GO TO
D) ENTRY POINT WITHIN GO TO MAP TO USE
L) INDICATOR FOR EXTERNAL OR INTERNAL REFERENCE
\$CALL
A) RULE EQUATE X'0201'
B) ADDRESS OF MESSAGE
C) NAME OF MAP TO CALL
D) ENTRY POINT WITHIN CALLED MAP TO USE
E) INDICATOR FOR EXTERNAL OR INTERNAL REFERENCE
\$INPT
A) RULE EQUATE X'0300'
B) INPUT TYPE (EBCDIC OR HEX)
C) ADDRESS OF YES LEG RULE
D) DESTINATION LOCATION OF INPUT DATA
E) LENGTH OF INPUT DATA
F) LOWER LIMIT OF GOOD DATA
G) HIGHER LIMIT OF GOOD DATA
\$QUXX
A) RULE EQUATE X'0400'
B) ADDRESS OF YES LEG RULE
C) TU BRANCH TO ADDRESS (INITIAL)
D) TU BRANCH TO ADDRESS (SECONDARY)
E) LENGTH OF PARAMETER IN BYTES
F) PARAMETER TO PASS TO TU
G) STORE ADDRESS FOR FIRST 8 WORDS OF PARAMETER
\$TUXX
A) RULE EQUATE X'0500'
B) ADDRESS OF YES LEG RULE
C) TU BRANCH TO ADDRESS
D) TYPE OF COMPARE TO MAKE ON RESULTS
E) LENGTH OF COMPARE RESULTS
F) MASK FIELD FOR COMPARE
G) LENGTH OF PARAMETER IN BYTES
H) PARAMETER TO PASS TO THE TU
I) STORE ADDRESS FOR FIRST 8 WORDS OF PARAMETER
\$NVLD
A) RULE EQUATE X'0600'
ENTRY POINT TABLE
THIS TABLE CONTAINS THE ENTRY POINTS WITHIN THE MAP THAT THE MAP CAN BE ENTERED FROM THESE ENTRY POINTS ARE REFERENCED BY NAME AND ADDRESS. ENTRIES ARE AS FOLLOWS:
A) NAME OF ENTRY POINT
B) ADDRESS OF ENTRY POINT RULE TABLE
THE ENTRY POINT TABLE END IS INDICATED BY A X'0000'
MESSAGE TABLE
THIS TABLE CONTAINS THE MESSAGE PASSED TO THE OPERATOR VIA THE MDI SUPERVISOR. THE TABLE IS AS FOLLOWS:
A) EQUATE FOR START OF MESSAGE BLOCK
B) NUMBER OF LINES OF MESSAGE
C) LENGTH OF FOLLOWING LINE
D) FIRST LINE OF MESSAGE
E) LENGTH OF FOLLOWING LINE
F) SECOND LINE OF MESSAGE
G) ETC.

LOCTR	OBJECT TEXT	STMT	SOURCE STATEMENT
308	*****		*****
309	*****		*****
310	**		**
311	**		**
312	**		**
313	*****		*****
314	*****		*****
315	DC	AL2(N00001)	
316	DC	XL2'0001'	
317	EQN00001	EQU	0001
318	DC	AL2(N00002)	
319	DC	XL2'0002'	
320	EQN00002	EQU	0002
321	DC	AL2(N00003)	
322	DC	XL2'0003'	
323	EQN00003	EQU	0003
324	DC	AL2(N00004)	
325	DC	XL2'0004'	
326	EQN00004	EQU	0004
327	DC	AL2(N00005)	
328	DC	XL2'0005'	
329	EQN00005	EQU	0005
330	DC	AL2(N00006)	
331	DC	XL2'0006'	
332	EQN00006	EQU	0006
333	DC	AL2(N00007)	
334	DC	XL2'0007'	
335	EQN00007	EQU	0007
336	DC	AL2(N00008)	
337	DC	XL2'0008'	
338	EQN00008	EQU	0008
339	DC	AL2(N00009)	
340	DC	XL2'0009'	
341	EQN00009	EQU	0009
342	DC	AL2(N00010)	
343	DC	XL2'0010'	
344	EQN00010	EQU	0010
345	DC	AL2(N00011)	
346	DC	XL2'0011'	
347	EQN00011	EQU	0011
348	DC	AL2(N00012)	
349	DC	XL2'0012'	
350	EQN00012	EQU	0012
351	DC	AL2(N00013)	
352	DC	XL2'0013'	
353	EQN00013	EQU	0013
354	DC	AL2(N00014)	
355	DC	XL2'0014'	
356	EQN00014	EQU	0014
357	DC	AL2(N00015)	
358	DC	XL2'0015'	
359	EQN00015	EQU	0015
360	DC	AL2(N00016)	
361	DC	XL2'0016'	
362	EQN00016	EQU	0016
363	DC	AL2(N00017)	
364	DC	XL2'0017'	
365	EQN00017	EQU	0017
366	DC	AL2(N00018)	
367	DC	XL2'0018'	
368	EQN00018	EQU	0018
369	DC	AL2(N00019)	
370	DC	XL2'0019'	
371	EQN00019	EQU	0019
372	DC	AL2(N00020)	
373	DC	XL2'0020'	
374	EQN00020	EQU	0020
375	DC	AL2(N00021)	
376	DC	XL2'0021'	
377	EQN00021	EQU	0021
378	DC	AL2(N00022)	
379	DC	XL2'0022'	
380	EQN00022	EQU	0022
381	DC	AL2(N00023)	
382	DC	XL2'0023'	
383	EQN00023	EQU	0023
384	DC	AL2(N00024)	
385	DC	XL2'0024'	
386	EQN00024	EQU	0024
387	DC	AL2(N00025)	
388	DC	XL2'0025'	
389	EQN00025	EQU	0025
390	DC	AL2(N00026)	
391	DC	XL2'0026'	
392	EQN00026	EQU	0026
393	DC	AL2(N00027)	
394	DC	XL2'0027'	
395	EQN00027	EQU	0027
396	DC	AL2(N00028)	
397	DC	XL2'0028'	
398	EQN00028	EQU	0028
399	DC	AL2(N00029)	
400	DC	XL2'0029'	
401	EQN00029	EQU	0029
402	DC	AL2(N00030)	
403	DC	XL2'0030'	
404	EQN00030	EQU	0030
405	DC	AL2(N00031)	
406	DC	XL2'0031'	
407	EQN00031	EQU	0031
408	DC	AL2(N00032)	
409	DC	XL2'0032'	
410	EQN00032	EQU	0032
411	DC	AL2(N00033)	
412	DC	XL2'0033'	
413	EQN00033	EQU	0033
414	DC	AL2(N00034)	
415	DC	XL2'0034'	
416	EQN00034	EQU	0034
417	DC	AL2(N00035)	
418	DC	XL2'0035'	
419	EQN00035	EQU	0035
420	DC	AL2(N00036)	
421	DC	XL2'0036'	
422	EQN00036	EQU	0036
423	DC	AL2(N00037)	

LOCTR	OBJECT TEXT	STMT	SOURCE STATEMENT
002594	0037	424	DC XL2'0037'
000025		425	EQN00037 EQU 0037
002596	2848	426	DC AL2(N00038)
002598	0038	427	DC XL2'0038'
000026		428	EQN00038 EQU 0038
00259A	284C	429	DC AL2(N00039)
00259C	0039	430	DC XL2'0039'
000027		431	EQN00039 EQU 0039
00259E	2862	432	DC AL2(N00040)
0025A0	0040	433	DC XL2'0040'
000028		434	EQN00040 EQU 0040
0025A2	2866	435	DC AL2(N00041)
0025A4	0041	436	DC XL2'0041'
000029		437	EQN00041 EQU 0041
0025A6	287C	438	DC AL2(N00042)
0025A8	0042	439	DC XL2'0042'
00002A		440	EQN00042 EQU 0042
0025AA	2880	441	DC AL2(N00043)
0025AC	0043	442	DC XL2'0043'
00002B		443	EQN00043 EQU 0043
0025AE	2894	444	DC AL2(N00044)
0025B0	0044	445	DC XL2'0044'
00002C		446	EQN00044 EQU 0044
0025B2	2898	447	DC AL2(N00045)
0025B4	0045	448	DC XL2'0045'
00002D		449	EQN00045 EQU 0045
0025B6	28AA	450	DC AL2(N00046)
0025B8	0046	451	DC XL2'0046'
00002E		452	EQN00046 EQU 0046
0025BA	28AE	453	DC AL2(N00047)
0025BC	0047	454	DC XL2'0047'
00002F		455	EQN00047 EQU 0047
0025BE	28C4	456	DC AL2(N00048)
0025C0	0048	457	DC XL2'0048'
000030		458	EQN00048 EQU 0048
0025C2	28C8	459	DC AL2(N00049)
0025C4	0049	460	DC XL2'0049'
000031		461	EQN00049 EQU 0049
0025C6	28DE	462	DC AL2(N00050)
0025C8	0050	463	DC XL2'0050'
000032		464	EQN00050 EQU 0050
0025CA	28E2	465	DC AL2(N00051)
0025CC	0051	466	DC XL2'0051'
000033		467	EQN00051 EQU 0051
0025CE	28F4	468	DC AL2(N00052)
0025D0	0052	469	DC XL2'0052'
000034		470	EQN00052 EQU 0052
0025D2	28F8	471	DC AL2(N00053)
0025D4	0053	472	DC XL2'0053'
000035		473	EQN00053 EQU 0053
0025D6	290E	474	DC AL2(N00054)
0025D8	0054	475	DC XL2'0054'
000036		476	EQN00054 EQU 0054
0025DA	2912	477	DC AL2(N00055)
0025DC	0055	478	DC XL2'0055'
000037		479	EQN00055 EQU 0055
0025DE	2926	480	DC AL2(N00056)
0025E0	0056	481	DC XL2'0056'
000038		482	EQN00056 EQU 0056
0025E2	292A	483	DC AL2(N00057)
0025E4	0057	484	DC XL2'0057'
000039		485	EQN00057 EQU 0057
0025E6	2940	486	DC AL2(N00058)
0025E8	0058	487	DC XL2'0058'
00003A		488	EQN00058 EQU 0058
0025EA	2944	489	DC AL2(N00059)
0025EC	0059	490	DC XL2'0059'
00003B		491	EQN00059 EQU 0059
0025EE	2956	492	DC AL2(N00060)
0025F0	0060	493	DC XL2'0060'
00003C		494	EQN00060 EQU 0060
0025F2	295A	495	DC AL2(N00061)
0025F4	0061	496	DC XL2'0061'
00003D		497	EQN00061 EQU 0061
0025F6	2970	498	DC AL2(N00062)
0025F8	0062	499	DC XL2'0062'
00003E		500	EQN00062 EQU 0062
0025FA	2974	501	DC AL2(N00063)
0025FC	0063	502	DC XL2'0063'
00003F		503	EQN00063 EQU 0063
0025FE	2988	504	DC AL2(N00064)
002600	0064	505	DC XL2'0064'
000040		506	EQN00064 EQU 0064
002602	298C	507	DC AL2(N00065)
002604	0065	508	DC XL2'0065'
000041		509	EQN00065 EQU 0065
002606	29A2	510	DC AL2(N00066)
002608	0066	511	DC XL2'0066'
000042		512	EQN00066 EQU 0066
00260A	29A6	513	DC AL2(N00067)
00260C	0067	514	DC XL2'0067'
000043		515	EQN00067 EQU 0067
00260E	29B8	516	DC AL2(N00068)
002610	0068	517	DC XL2'0068'
000044		518	EQN00068 EQU 0068
002612	29BC	519	DC AL2(N00069)
002614	0069	520	DC XL2'0069'
000045		521	EQN00069 EQU 0069
002616	29D2	522	DC AL2(N00070)
002618	0070	523	DC XL2'0070'
000046		524	EQN00070 EQU 0070
00261A	29D6	525	DC AL2(N00071)
00261C	0071	526	DC XL2'0071'
000047		527	EQN00071 EQU 0071
00261E	29EA	528	DC AL2(N00072)
002620	0072	529	DC XL2'0072'
000048		530	EQN00072 EQU 0072
002622	29EE	531	DC AL2(N00073)
002624	0073	532	DC XL2'0073'
000049		533	EQN00073 EQU 0073
002626	2A00	534	DC AL2(N00074)
002628	0074	535	DC XL2'0074'
00004A		536	EQN00074 EQU 0074
00262A	2A04	537	DC AL2(N00075)
00262C	0075	538	DC XL2'0075'
00004B		539	EQN00075 EQU 0075

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00262E 2A16 540 DC AL2(N00076)
002630 0076 541 DC XL2'0076'
00004C 542 EQU00076 EQU
002632 2A22 543 DC AL2(N00077)
002634 0077 544 DC XL2'0077'
00004D 545 EQU00077 EQU
002636 2A38 546 DC AL2(N00078)
002638 0078 547 DC XL2'0078'
00004E 548 EQU00078 EQU
00263A 2A38 549 DC AL2(N00079)
00263C 0079 550 DC XL2'0079'
00004F 551 EQU00079 EQU
00263E 2A4A 552 DC AL2(N00080)
002640 0080 553 DC XL2'0080'
000050 554 EQU00080 EQU
002642 2A56 555 DC AL2(N00081)
002644 0081 556 DC XL2'0081'
000051 557 EQU00081 EQU
002646 2A68 558 DC AL2(N00082)
002648 0082 559 DC XL2'0082'
000052 560 EQU00082 EQU
00264A 2A6C 561 DC AL2(N00083)
00264C 0083 562 DC XL2'0083'
000053 563 EQU00083 EQU
00264E 2A7E 564 DC AL2(N00084)
002650 0084 565 DC XL2'0084'
000054 566 EQU00084 EQU
002652 2A8A 567 DC AL2(N00085)
002654 0085 568 DC XL2'0085'
000055 569 EQU00085 EQU
002656 2AA4 570 DC AL2(N00086)
002658 0086 571 DC XL2'0086'
000056 572 EQU00086 EQU
00265A 2AB0 573 DC AL2(N00087)
00265C 0087 574 DC XL2'0087'
000057 575 EQU00087 EQU
00265E 2ACA 576 DC AL2(N00088)
002660 0088 577 DC XL2'0088'
000058 578 EQU00088 EQU
002662 2AD6 579 DC AL2(N00089)
002664 0089 580 DC XL2'0089'
000059 581 EQU00089 EQU
002666 0000 582 DC AL2(DUMMY)
583 *****
584 *****
585 **
586 ** RULE INFORMATION TABLE **
587 **
588 *****
589 *****
590 N00001 $TUXX T3C00,02,0708,EQ,PLNG=6,PARM=F00000,QT=(Q00044), X
591 N00001 A(@TUXX)
592 DC AL2(N00003)
593+ DC A(T3C00)
594+ DC AL2(EQ)
595+ DC AL2(02)
596+ DC X'0708'
597+ ALIGN WORD
598+ DC AL2(6)
599+ DC C'F00000'
600+ ALIGN WORD
601+ DC AL2(PARMARA)
602 N00002 $FIXT FT=(F00008),GTO=((0070,A))
603 N00002 A(@FIXT)
604+ DC A(F00008)
605 N00003 $TUXX T3C00,02,0708,EQ,PLNG=6,PARM=6F0000,QT=(Q00051), X
606+ N00003 A(@TUXX)
607+ DC AL2(N00009)
608+ DC A(T3C00)
609+ DC AL2(EQ)
610+ DC AL2(02)
611+ DC X'0708'
612+ ALIGN WORD
613+ DC AL2(6)
614+ DC C'F00000'
615+ ALIGN WORD
616+ DC AL2(PARMARA)
617 N00004 $TUXX T3C02,C2,0008,EQ,,QT=(Q00054),YES=N00006 X
618+ N00004 A(@TUXX)
619+ DC AL2(N00006)
620+ DC A(T3C02)
621+ DC AL2(EQ)
622+ DC AL2(02)
623+ DC X'0008'
624+ ALIGN WORD
625+ DC AL2(0)
626+ DC C'AA'
627+ ALIGN WORD
628+ DC AL2(PARMARA)
629 N00005 $FIXT FT=(F00014)
630+ N00005 A(@FIXT)
631+ DC A(F00014)
632 N00006 $TUXX T3C00,04,00080000,EQ,PLNG=6,PARM=200000,QT=(Q00059), X
633+ N00006 A(@TUXX)
634+ DC AL2(N00008)
635+ DC A(T3C00)
636+ DC AL2(EQ)
637+ DC X'00080000'
638+ DC X'00800000'
639+ ALIGN WORD
640+ DC AL2(6)
641+ DC C'200000'
642+ ALIGN WORD
643+ DC AL2(PARMARA)
644 N00007 $FIXT FT=(F00014)
645+ N00007 A(@FIXT)
646+ DC A(F00014)
647 N00008 $FIXT FT=(F00064)
648+ N00008 A(@FIXT)
649+ DC A(F00064)
650 N00009 $TUXX T3C01,02,0708,EQ,PLNG=4,PARM=FFFF,QT=(Q00071), X
651+ N00009 A(@TUXX)
652+ DC AL2(N00011)
653+ DC A(T3C01)
654+ DC AL2(EQ)
655+ DC AL2(02)

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0026DB 0708 656+ DC X'0708'
657+ ALIGN WORD
0026DA 0004 658+ DC AL2(4)
0026DC C6C6C6C6 659+ DC C'FFFF'
660+ ALIGN WORD
0026E0 196E 661+ DC AL2(PARMARA)
662 N00010 $FIXT FT=(F00014)
663+ N00010 A(@FIXT)
664+ DC A(F00014)
665 N00011 $TUXX T3C00,02,0708,EQ,PLNG=6,PARM=200000,QT=(Q00077), X
666+ N00011 A(@TUXX)
667+ DC AL2(N00023)
668+ DC A(T3C00)
669+ DC AL2(EQ)
670+ DC AL2(03)
671+ DC X'0708'
672+ ALIGN WORD
673+ DC AL2(6)
674+ DC C'200000'
675+ ALIGN WORD
676+ DC AL2(PARMARA)
677 N00012 $TUXX T3C02,02,0508,EQ,QT=(Q00080),YES=N00022,ST=(S00037) X
678+ N00012 A(@TUXX)
679+ DC AL2(N00022)
680+ DC A(T3C02)
681+ DC AL2(EQ)
682+ DC AL2(03)
683+ DC X'0508'
684+ ALIGN WORD
685+ DC AL2(0)
686+ DC C'AA'
687+ ALIGN WORD
688+ DC AL2(PARMARA)
689 N00013 $TUXX T3C02,02,0308,EQ,QT=(Q00083),YES=N00021,ST=(S00037) X
690+ N00013 A(@TUXX)
691+ DC AL2(N00021)
692+ DC A(T3C02)
693+ DC AL2(EQ)
694+ DC AL2(03)
695+ DC X'0308'
696+ ALIGN WORD
697+ DC AL2(0)
698+ DC C'AA'
699+ ALIGN WORD
700+ DC AL2(PARMARA)
701 N00014 $TUXX T3C02,02,0208,EQ,QT=(Q00086),YES=N00016,ST=(S00037) X
702+ N00014 A(@TUXX)
703+ DC AL2(N00016)
704+ DC A(T3C02)
705+ DC AL2(EQ)
706+ DC X'0208'
707+ DC X'0208'
708+ ALIGN WORD
709+ DC AL2(0)
710+ DC C'AA'
711+ ALIGN WORD
712+ DC AL2(PARMARA)
713 N00015 $FIXT FT=(F00014)
714+ N00015 A(@FIXT)
715+ DC A(F00014)
716 N00016 $TUXX T3C01,02,0208,EQ,PLNG=4,PARM=FFFF,QT=(Q00092), X
717+ N00016 A(@TUXX)
718+ DC AL2(N00018)
719+ DC A(T3C01)
720+ DC AL2(EQ)
721+ DC AL2(02)
722+ DC X'0208'
723+ ALIGN WORD
724+ DC AL2(4)
725+ DC C'FFFF'
726+ ALIGN WORD
727+ DC AL2(PARMARA)
728 N00017 $FIXT FT=(F00014)
729+ N00017 A(@FIXT)
730+ DC A(F00014)
731 N00018 $TUXX T3C00,04,07080000,EQ,PLNG=6,PARM=200000,QT=(Q00098), X
732+ N00018 A(@TUXX)
733+ DC AL2(N00020)
734+ DC A(T3C00)
735+ DC AL2(EQ)
736+ DC AL2(04)
737+ DC X'07080000'
738+ ALIGN WORD
739+ DC AL2(6)
740+ DC C'200000'
741+ ALIGN WORD
742+ DC AL2(PARMARA)
743 N00019 $FIXT FT=(F00014)
744+ N00019 A(@FIXT)
745+ DC A(F00014)
746 N00020 $GOTO TYPE=INTRNL,EP=C,FT=(F00103),GTO=(N00023)
747+ N00020 A(@GOTO)
748+ DC A(F00103)
749+ DC CL4'3C00'
750+ DC C12'C'
751+ DC AL2(INTRNL)
752 N00021 $FIXT FT=(F00010),CT=(C00041)
753+ N00021 A(@FIXT)
754+ DC A(F00010)
755+ DC FT=(F00012),CT=(C00041)
756+ N00022 A(@FIXT)
757+ DC A(F00012)
758 N00023 $TUXX T3C02,04,07080000,EQ,QT=(Q00112),YES=N00025,ST=(S00037) X
759+ N00023 A(@TUXX)
760+ DC AL2(N00025)
761+ DC A(T3C02)
762+ DC AL2(EQ)
763+ DC AL2(04)
764+ DC X'07080000'
765+ ALIGN WORD
766+ DC AL2(0)
767+ DC C'AA'
768+ ALIGN WORD
769+ DC AL2(PARMARA)
770 N00024 $FIXT FT=(F00014)
771+ N00024 A(@FIXT)

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002794 2B1E 772+ DC A(F00014)
 773+ N00025 \$TUXX T3C00,02,0308,EQ,PLNG=6,PARM=000000,QT=(Q00119), X
 774+ N00025 DC A(@FIXT)
 775+ DC AL2(N00027)
 002796 0500 776+ DC A(T3C00)
 002798 27B0 777+ DC AL2(EQ)
 00279A 3476 778+ DC AL2(02)
 00279C 0000 779+ DC X*0308
 00279E 0002 780+ ALIGN WCFD
 0027A0 0308 781+ DC AL2(6)
 782+ DC C*000000
 783+ ALIGN WORD
 784+ DC AL2(PARMARA)
 0027A2 0006 785+ N00026 \$FIXT FT=(F00014)
 0027A4 F0F0F0F0F0F0 786+ N00026 DC A(@FIXT)
 787+ DC A(F00014)
 0027AA 196E 788+ N00027 \$TUXX T3C00,02,0308,EQ,PLNG=6,PARM=100000,QT=(Q00126), X
 789+ N00027 DC A(@TUXX)
 0027B0 0500 790+ DC AL2(N00029)
 0027B2 27CA 791+ DC A(T3C00)
 0027B4 3476 792+ DC AL2(EQ)
 0027B6 0000 793+ DC AL2(02)
 0027B8 0002 794+ DC X*0308
 0027BA 0308 795+ ALIGN WCFD
 796+ DC AL2(6)
 0027BC 0006 797+ DC C*100000
 0027BE F1F0F0F0F0F0 798+ ALIGN WCFD
 799+ DC AL2(PARMARA)
 0027C4 196E 800+ N00028 \$FIXT FT=(F00014)
 801+ N00028 DC A(@FIXT)
 802+ DC A(F00014)
 0027CA 0500 803+ N00029 \$TUXX T3C00,02,0308,EQ,PLNG=6,PARM=270000,QT=(Q00133), X
 0027CC 27E4 804+ N00029 DC A(@TUXX)
 0027CE 3476 805+ DC AL2(N00031)
 0027D0 0000 806+ DC A(T3C00)
 0027D2 0002 807+ DC AL2(EQ)
 0027D4 0308 808+ DC AL2(02)
 809+ DC X*0308
 810+ ALIGN WORD
 0027D6 0006 811+ DC AL2(6)
 0027D8 F2F7F0F0F0F0 812+ DC C*270000
 813+ ALIGN WORD
 814+ DC AL2(PARMARA)
 0027DE 196E 815+ N00030 \$FIXT FT=(F00014)
 816+ N00030 DC A(@FIXT)
 0027E0 0101 817+ DC A(F00014)
 0027E2 2B1E 818+ N00031 \$TUXX T3C00,02,0708,EQ,PLNG=6,PARM=600000,QT=(Q00140), X
 819+ N00031 DC A(@TUXX)
 0027E4 0500 820+ DC AL2(N00033)
 0027E6 27E4 821+ DC A(T3C00)
 0027E8 3476 822+ DC AL2(EQ)
 0027EA 0000 823+ DC AL2(02)
 0027EC 0002 824+ DC X*0708
 0027EE 0708 825+ ALIGN WORD
 826+ DC AL2(6)
 0027F0 0006 827+ DC C*600000
 0027F2 F6F0F0F0F0F0 828+ ALIGN WORD
 829+ DC AL2(PARMARA)
 0027F8 196E 830+ N00032 \$FIXT FT=(F00014)
 831+ N00032 DC A(@FIXT)
 0027FA 0101 832+ DC A(F00014)
 0027FC 2B1E 833+ N00033 \$TUXX T3C00,02,0708,EQ,PLNG=6,PARM=602222,QT=(Q00147), X
 834+ N00033 DC A(@TUXX)
 0027FE 0500 835+ DC AL2(N00035)
 002800 2818 836+ DC A(T3C00)
 002802 3476 837+ DC AL2(EQ)
 002804 0000 838+ DC AL2(02)
 002806 0002 839+ DC X*0708
 002808 0708 840+ ALIGN WORD
 841+ DC AL2(6)
 00280A 0006 842+ DC C*602222
 00280C F6F0F2F2F2F2 843+ ALIGN WORD
 844+ DC AL2(PARMARA)
 002812 196E 845+ N00034 \$FIXT FT=(F00014)
 846+ N00034 DC A(@FIXT)
 002814 0101 847+ DC A(F00014)
 002816 2B1E 848+ N00035 \$TUXX T3C00,02,0708,EQ,PLNG=6,PARM=60FFFE,QT=(Q00154), X
 849+ N00035 DC A(@TUXX)
 002818 0500 850+ DC AL2(N00037)
 00281A 2832 851+ DC A(T3C00)
 00281C 3476 852+ DC AL2(EQ)
 00281E 0000 853+ DC AL2(02)
 002820 0002 854+ DC X*0708
 002822 0708 855+ ALIGN WORD
 856+ DC AL2(6)
 002824 0006 857+ DC C*60FFFE
 002826 F6F0C6C6C6C5 858+ ALIGN WORD
 859+ DC AL2(PARMARA)
 00282C 196E 860+ N00036 \$FIXT FT=(F00014)
 861+ N00036 DC A(@FIXT)
 002830 0101 862+ DC A(F00014)
 002832 2B1E 863+ N00037 \$TUXX T3C00,02,0308,EQ,PLNG=6,PARM=650000,QT=(Q00161), X
 864+ N00037 DC A(@TUXX)
 002834 0500 865+ DC AL2(N00039)
 002836 284C 866+ DC A(T3C00)
 002838 3476 867+ DC AL2(EQ)
 00283A 0000 868+ DC AL2(02)
 00283C 0002 869+ DC X*0308
 00283E 0308 870+ ALIGN WORD
 871+ DC AL2(6)
 002840 0006 872+ DC C*650000
 002842 F6F5F0F0F0F0 873+ ALIGN WORD
 874+ DC AL2(PARMARA)
 002846 196E 875+ N00038 \$FIXT FT=(F00014)
 876+ N00038 DC A(@FIXT)
 002848 0101 877+ DC A(F00014)
 00284A 2B1E 878+ N00039 \$TUXX T3C00,02,0708,EQ,PLNG=6,PARM=510000,QT=(Q00168), X
 879+ N00039 DC A(@TUXX)
 00284C 0500 880+ DC AL2(N00041)
 00284E 2866 881+ DC A(T3C00)
 002850 3476 882+ DC AL2(EQ)
 002852 0000 883+ DC AL2(02)
 002854 0002 884+ DC X*0708
 002856 0708 885+ ALIGN WORD
 886+ DC AL2(6)
 002858 0006 887+ DC C*510000
 00285A F5F1F0F0F0F0

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002860 196E 888+ ALIGN WORD
 889+ DC AL2(PARMARA)
 890+ N00040 \$FIXT FT=(F00014)
 891+ N00040 DC A(@FIXT)
 892+ DC A(F00014)
 893+ N00041 \$TUXX T3C00,02,0108,EQ,PLNG=6,PARM=510000,QT=(Q00175), X
 894+ N00041 DC A(@TUXX)
 002862 0101 895+ DC AL2(N00043)
 002864 2B1E 896+ DC A(T3C00)
 002866 0500 897+ DC AL2(EQ)
 002868 2880 898+ DC AL2(02)
 00286A 3476 899+ DC X*0108
 00286C 0000 900+ ALIGN WORD
 00286E 0002 901+ DC AL2(6)
 002870 0108 902+ DC C*510000
 903+ ALIGN WORD
 904+ DC AL2(PARMARA)
 002872 0006 905+ N00042 \$FIXT FT=(F00014)
 002874 F5F1F0F0F0F0 906+ N00042 DC A(@FIXT)
 907+ DC A(F00014)
 908+ N00043 \$TUXX T3C01,02,0708,EQ,PLNG=4,PARM=FFFF,QT=(Q00181), X
 909+ N00043 DC A(@TUXX)
 002876 2B1E 910+ DC AL2(N00045)
 002878 0500 911+ DC A(T3C01)
 002880 2898 912+ DC AL2(EQ)
 002882 34EA 913+ DC AL2(02)
 002884 0000 914+ DC X*0708
 002886 0002 915+ ALIGN WORD
 002888 0002 916+ DC AL2(4)
 00288A 0708 917+ DC C*FFFF
 918+ ALIGN WORD
 919+ DC AL2(PARMARA)
 002892 196E 920+ N00044 \$FIXT FT=(F00014)
 921+ N00044 DC A(@FIXT)
 922+ DC A(F00014)
 923+ N00045 \$TUXX T3C02,02,0708,EQ,QT=(Q00186),YES=N00047,ST=(S00037) X
 924+ N00045 DC A(@TUXX)
 002894 0500 925+ DC AL2(N00047)
 002896 2B1E 926+ DC A(T3C02)
 002898 28AE 927+ DC AL2(EQ)
 00289A 2EF6 928+ DC AL2(02)
 00289C 0000 929+ DC X*0708
 00289E 0002 930+ ALIGN WORD
 0028A0 0002 931+ DC AL2(0)
 0028A2 0708 932+ DC C*AA
 933+ ALIGN WORD
 934+ DC AL2(PARMARA)
 0028A4 0000 935+ N00046 \$FIXT FT=(F00014)
 0028A6 C1C1 936+ N00046 DC A(@FIXT)
 937+ DC A(F00014)
 0028A8 196E 938+ N00047 \$TUXX T3C00,02,0708,EQ,PLNG=6,PARM=6F0000,QT=(Q00193), X
 939+ N00047 DC A(@TUXX)
 0028AA 0101 940+ DC AL2(N00049)
 0028AC 2B1E 941+ DC A(T3C00)
 0028AE 0500 942+ DC AL2(EQ)
 0028B0 28C8 943+ DC AL2(02)
 0028B2 3476 944+ DC X*0708
 0028B4 0000 945+ ALIGN WORD
 0028B6 0002 946+ DC AL2(6)
 0028B8 0708 947+ DC C*6F0000
 948+ ALIGN WORD
 949+ DC AL2(PARMARA)
 0028C2 196E 950+ N00048 \$FIXT FT=(F00014)
 951+ N00048 DC A(@FIXT)
 0028C4 0101 952+ DC A(F00014)
 0028C6 2B1E 953+ N00049 \$TUXX T3C00,02,0708,EQ,PLNG=6,PARM=600001,QT=(Q00200), X
 954+ N00049 DC A(@TUXX)
 0028C8 0500 955+ N00049 DC AL2(N00051)
 0028CA 28E2 956+ DC A(T3C00)
 0028CC 3476 957+ DC AL2(EQ)
 0028CE 0000 958+ DC AL2(02)
 0028D0 0002 959+ DC X*0708
 0028D2 0708 960+ ALIGN WORD
 0028D4 0006 961+ DC AL2(6)
 0028D6 F6F0F0F0F0F1 962+ DC C*600001
 963+ ALIGN WORD
 964+ DC AL2(PARMARA)
 0028D8 196E 965+ N00050 \$FIXT FT=(F00014)
 966+ N00050 DC A(@FIXT)
 967+ DC A(F00014)
 968+ N00051 \$TUXX T3C02,02,0708,EQ,QT=(Q00205),YES=N00053,ST=(S00037) X
 969+ N00051 DC A(@TUXX)
 0028E2 0500 970+ DC AL2(N00053)
 0028E4 28F8 971+ DC A(T3C02)
 0028E6 2EF6 972+ DC AL2(EQ)
 0028E8 0000 973+ DC AL2(02)
 0028EA 0002 974+ DC X*0708
 0028EC 0708 975+ ALIGN WORD
 976+ DC AL2(0)
 977+ DC C*AA
 978+ ALIGN WORD
 979+ DC AL2(PARMARA)
 0028E4 0101 980+ N00052 \$FIXT FT=(F00014)
 0028E6 2B1E 981+ N00052 DC A(@FIXT)
 982+ DC A(F00014)
 983+ N00053 \$TUXX T3C03,01,07,EQ,PLNG=6,PARM=5F0000,QT=(Q00213), X
 984+ N00053 DC A(@TUXX)
 0028F0 0500 985+ DC AL2(N00055)
 0028F2 2912 986+ DC A(T3C03)
 0028F4 3514 987+ DC AL2(EQ)
 0028F6 0000 988+ DC AL2(01)
 0028F8 0001 989+ DC X*07
 0028FA 0000 990+ ALIGN WORD
 002900 0006 991+ DC AL2(6)
 002902 F5C6F0F0F0F0 992+ DC C*510000
 993+ ALIGN WORD
 994+ DC AL2(PARMARA)
 002904 196E 995+ N00054 \$FIXT FT=(F00014)
 996+ N00054 DC A(@FIXT)
 997+ DC A(F00014)
 998+ N00055 \$TUXX T3C02,03,070240,EQ,QT=(Q00222),YES=N00057,CT=(C00218), X
 999+ N00055 DC A(@TUXX)
 002906 0500 1000+ DC AL2(N00057)
 002908 292A 1001+ DC A(T3C02)
 002910 2EF6 1002+ DC AL2(EQ)
 002912 0000 1003+ DC AL2(03)

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00291C 070240 1004+ DC X*070240*

00291E 00 1005+ ALIGN WCRD

002920 0000 1006+ DC AL2(0)

002922 C1C1 1007+ DC C*AA*

002924 196E 1008+ ALIGN WORD

1009+ DC AL2(PARMARA)

1010 N00056 \$FIXT FT=(P00014)

002926 U101 1011+N00056 DC A(@FIXT)

002928 2B1E 1012+ DC A(F00014)

1013 N00057 \$TUXX T3C00,02,0708,EQ,PLNG=6,PARM=600003,QT=(Q00228), X

1014+N00057 EC A(@TUXX)

00292C 2944 1015+ DC AL2(N00059)

00292E 3476 1016+ DC A(T3C00)

002930 0000 1017+ DC AL2(EQ)

002932 0002 1018+ DC AL2(O)

002934 0708 1019+ DC X*0708*

1020+ ALIGN WORD

002936 0006 1021+ DC AL2(6)

002938 F6F0F0F0F0F3 1022+ DC C*600003*

1023+ ALIGN WCRD

00293E 196E 1024+ DC AL2(PARMARA)

1025 N00058 \$FIXT FT=(P00014)

002940 0101 1026+N00058 DC A(@FIXT)

002942 2B1E 1027+ DC A(F00014)

1028 N00059 \$TUXX T3C02,02,0708,EQ,QT=(Q00233),YES=N00061,ST=(S00037)

002944 0500 1029+N00059 DC A(@TUXX)

002946 295A 1030+ DC AL2(N00061)

002948 2EF6 1031+ DC A(T3C02)

00294A 0000 1032+ DC AL2(EQ)

00294C 0002 1033+ DC AL2(O)

00294E 0708 1034+ DC X*0708*

1035+ ALIGN WCRD

002950 0000 1036+ DC AL2(0)

002952 C1C1 1037+ DC C*AA*

1038+ ALIGN WCRD

002954 196E 1039+ DC AL2(PARMARA)

1040 N00060 \$FIXT FT=(P00014)

002956 0101 1041+N00060 DC A(@FIXT)

002958 2B1E 1042+ DC A(F00014)

1043 N00061 \$TUXX T3C00,01,07,EQ,PLNG=6,PARM=5F0000,QT=(Q00241), X

00295A 0500 1044+N00061 DC A(@TUXX)

00295C 2974 1045+ DC AL2(N00063)

00295E 3514 1046+ DC A(T3C03)

002960 0000 1047+ DC AL2(EQ)

002962 0001 1048+ DC AL2(O)

002964 07 1049+ DC X*07*

002966 00 1050+ ALIGN WORD

002968 F5C6F0F0F0F0 1051+ DC AL2(6)

1052+ DC C*5F0000*

1053+ ALIGN WORD

00296E 196E 1054+ DC AL2(PARMARA)

1055 N00062 \$FIXT FT=(P00014)

002970 0101 1056+N00062 DC A(@FIXT)

002972 2B1E 1057+ DC A(F00014)

1058 N00063 \$TUXX T3C02,03,070240,EQ,QT=(Q00250),YES=N00065,CT=(C00246), X

002974 0500 1059+N00063 DC A(@TUXX)

002976 298C 1060+ DC AL2(N00065)

002978 2EF6 1061+ DC A(T3C02)

00297A 0000 1062+ DC AL2(EQ)

00297C 0003 1063+ DC AL2(O)

00297E 070240 1064+ DC X*070240*

1065+ ALIGN WORD

002980 0000 1066+ DC AL2(0)

002982 C1C1 1067+ DC C*AA*

1068+ ALIGN WORD

002986 196E 1069+ DC AL2(PARMARA)

1070 N00064 \$FIXT FT=(P00014)

002988 0101 1071+N00064 DC A(@FIXT)

00298A 2B1E 1072+ DC A(F00014)

1073 N00065 \$TUXX T3C00,02,0708,EQ,PLNG=6,PARM=600005,QT=(Q00256), X

00298C 0500 1074+N00065 EC A(@TUXX)

00298E 29A6 1075+ DC AL2(N00067)

002990 3476 1076+ DC A(T3C00)

002992 0000 1077+ DC AL2(EQ)

002994 0002 1078+ DC AL2(O)

002996 0708 1079+ DC X*0708*

1080+ ALIGN WCRD

002998 0006 1081+ DC AL2(6)

00299A F6F0F0F0F0F5 1082+ DC C*600005*

1083+ ALIGN WCRD

0029A0 196E 1084+ DC AL2(PARMARA)

1085 N00066 \$FIXT FT=(P00014)

0029A2 0101 1086+N00066 DC A(@FIXT)

0029A4 2B1E 1087+ DC A(F00014)

1088 N00067 \$TUXX T3C02,02,0708,EQ,QT=(Q00261),YES=N00069,ST=(S00037)

0029A6 0500 1089+N00067 DC A(@TUXX)

0029A8 298C 1090+ DC AL2(N00069)

0029AA 2EF6 1091+ DC A(T3C02)

0029AC 0000 1092+ DC AL2(EQ)

0029AE 0002 1093+ DC AL2(O)

0029B0 0708 1094+ DC X*0708*

1095+ ALIGN WORD

0029B2 0000 1096+ DC AL2(0)

0029B4 C1C1 1097+ DC C*AA*

1098+ ALIGN WCRD

0029B6 196E 1099+ DC AL2(PARMARA)

1100 N00068 \$FIXT FT=(P00014)

0029B8 0101 1101+N00068 DC A(@FIXT)

0029BA 2B1E 1102+ DC A(F00014)

1103 N00069 \$TUXX T3C03,01,07,EQ,PLNG=6,PARM=5F0000,QT=(Q00269), X

0029BC 0500 1104+N00069 EC A(@TUXX)

0029BE 29D6 1105+ DC AL2(N00071)

0029C0 3514 1106+ DC A(T3C03)

0029C2 0000 1107+ DC AL2(EQ)

0029C4 0001 1108+ DC AL2(O)

0029C6 07 1109+ DC X*07*

0029C8 00 1110+ ALIGN WORD

0029CA 0006 1111+ DC AL2(6)

0029CB F5C6F0F0F0F0 1112+ DC C*5F0000*

1113+ ALIGN WORD

0029D0 196E 1114+ DC AL2(PARMARA)

1115 N00070 \$FIXT FT=(P00014)

0029D2 0101 1116+N00070 DC A(@FIXT)

0029D4 2B1E 1117+ DC A(F00014)

1118 N00071 \$TUXX T3C02,03,070240,EQ,QT=(Q00278),YES=N00073,CT=(C00274), X

0029D6 0500 1119+N00071 DC A(@TUXX)

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0029D8 29EE 1120+ DC AL2(N00073)

0029DA 2EF6 1121+ DC A(T3C02)

0029DC 0000 1122+ DC AL2(0)

0029DE 0003 1123+ DC AL2(O)

0029E0 070240 1124+ DC X*070240*

0029E3 00 1125+ ALIGN WORD

0029E4 0000 1126+ DC AL2(0)

0029E6 C1C1 1127+ DC C*AA*

1128+ ALIGN WORD

0029E8 196E 1129+ DC AL2(PARMARA)

1130 N00072 \$FIXT FT=(P00014)

0029EA 0101 1131+N00072 DC A(@FIXT)

0029EC 2B1E 1132+ DC A(F00014)

1133 N00073 \$TUXX T4801,02,FFF7,OF,QT=(Q00285),YES=N00075,CT=(C00283)

1134+N00073 DC A(@TUXX)

1135+ DC AL2(N00075)

1136+ DC A(T4801)

1137+ DC AL2(OF)

1138+ DC AL2(O)

1139+ DC X*FFF7*

1140+ ALIGN WORD

0029FA 0000 1141+ DC AL2(0)

0029FC C1C1 1142+ DC C*AA*

1143+ ALIGN WORD

0029FE 196E 1144+ DC AL2(PARMARA)

1145 N00074 \$FIXT FT=(P00287)

002A00 0101 1146+N00074 DC A(@FIXT)

002A02 2D24 1147+ DC A(F00287)

1148 N00075 \$TUXX T3C02,02,0008,OF,QT=(Q00290),YES=N00077,ST=(S00037)

002A04 0500 1149+N00075 DC A(@TUXX)

002A06 2A22 1150+ DC AL2(N00077)

002A08 2EF6 1151+ DC A(T3C02)

002A0A 0202 1152+ DC AL2(OF)

002A0C 0002 1153+ DC AL2(O)

002A0E 0008 1154+ DC X*0008*

1155+ ALIGN WORD

002A10 0000 1156+ DC AL2(0)

002A12 C1C1 1157+ DC C*AA*

1158+ ALIGN WORD

002A14 196E 1159+ DC AL2(PARMARA)

1160 N00076 \$CALL TYPE=XTRNL,MAP=4813,EP=E,FT=(F00293),GTO=((4813,E))

002A16 0201 1161+N00076 DC A(@CALL)

002A18 2D00 1162+ DC A(F00293)

002A1A F4F8F1F3 1163+ DC CL4*4813*

002A1C C540 1164+ DC CL2*E*

1165+ DC AL2(XTRNL)

002A22 0500 1166 N00077 \$TUXX T4853,02,0004,OF,QT=(Q00299),YES=N00079,CT=(C00297)

002A24 2A38 1167+N00077 DC A(@TUXX)

002A26 2FC2 1168+ DC AL2(N00079)

002A28 0202 1169+ DC A(T4853)

002A2A 0002 1170+ DC AL2(OF)

002A2C 0004 1171+ DC AL2(O)

1172+ DC X*0004*

1173+ ALIGN WORD

002A2E 0000 1174+ DC AL2(0)

002A30 C1C1 1175+ DC C*AA*

1176+ ALIGN WORD

002A32 196E 1177+ DC AL2(PARMARA)

1178 N00078 \$FIXT FT=(P00301)

002A34 0101 1179+N00078 DC A(@FIXT)

002A36 2D88 1180+ DC A(F00301)

1181 N00079 \$TUXX T3C02,02,0008,OF,QT=(Q00305),YES=N00081,CT=(C00304), X

002A38 0500 1182+N00079 DC A(@TUXX)

002A3A 2A56 1183+ DC AL2(N00081)

002A3C 2EF6 1184+ DC A(T3C02)

002A3E 0202 1185+ DC AL2(OF)

002A40 0002 1186+ DC AL2(O)

002A42 0008 1187+ DC X*0008*

1188+ ALIGN WORD

002A44 0000 1189+ DC AL2(0)

002A46 C1C1 1190+ DC C*AA*

1191+ ALIGN WORD

002A48 196E 1192+ DC AL2(PARMARA)

1193 N00080 \$CALL TYPE=XTRNL,MAP=4813,EP=A,FT=(F00308),GTO=((4813,A))

002A4A 0201 1194+N00080 DC A(@CALL)

002A4C 2DB6 1195+ DC A(F00308)

002A4E F4F8F1F3 1196+ DC CL4*4813*

002A52 C140 1197+ DC CL2*A*

002A54 0001 1198+ DC AL2(XTRNL)

1199 N00081 \$TUXX T3C02,02,0002,OF,QT=(Q00312),YES=N00083,CT=(C00311), X

002A56 0500 1200+N00081 DC A(@TUXX)

002A58 2A6C 1201+ DC AL2(N00083)

002A5A 2EF6 1202+ DC A(T3C02)

002A5C 0202 1203+ DC AL2(OF)

002A5E 0002 1204+ DC AL2(O)

002A60 0002 1205+ DC X*0002*

1206+ ALIGN WORD

002A62 0000 1207+ DC AL2(0)

002A64 C1C1 1208+ DC C*AA*

1209+ ALIGN WORD

002A66 196E 1210+ DC AL2(PARMARA)

1211 N00082 \$FIXT FT=(P00315)

002A68 0101 1212+N00082 DC A(@FIXT)

002A6A 2DD2 1213+ DC A(F00315)

1214 N00083 \$TUXX T3C02,02,0001,OF,QT=(Q00319),YES=N00085,CT=(C00318), X

002A6C 0500 1215+N00083 DC A(@TUXX)

002A6E 2A80 1216+ DC AL2(N00085)

002A70 2EF6 1217+ DC A(T3C02)

002A72 0202 1218+ DC AL2(OF)

002A74 0002 1219+ DC AL2(O)

002A76 0001 1220+ DC X*0001*

1221+ ALIGN WORD

002A78 0000 1222+ DC AL2(0)

002A7A C1C1 1223+ DC C*AA*

1224+ ALIGN WORD

002A7C 196E 1225+ DC AL2(PARMARA)

1226 N00084 \$CALL TYPE=XTRNL,MAP=4813,EP=C,FT=(F00322),GTO=((4813,C))

002A7E 0201 1227+N00084 DC A(@CALL)

002A80 2E00 1228+ DC A(F00322)

002A82 F4F8F1F3 1229+ DC CL4*4813*

002A84 C340 1230+ DC CL2*C*

1231+ DC AL2(XTRNL)

1232 N00085 \$TUXX T3C02,10,0000000000000000000000000000,ON,QT=(Q00327),YES=N00087, X

002A8A 0500 1233+N00085 DC A(@TUXX)

002A8C 2AB0 1234+ DC AL2(N00087)

002A8E 2EF6 1235+ DC A(T3C02)

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002A90 0200 1236+ DC AL2(ON)
002A92 000A 1237+ DC AL2(10)
002A94 00000000000000000000 1238+ DC X'000000000000000000E00'

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002D78 000E 1352 DC A(0014)
002D7A C7D640E3D640D4C1D 1353 DC C10014'GO TO MAP 4813'
002D88 0001 1354 F00301 EQU *

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002E0E 0000 1471+CSTL5 DC A(*-*) CYCLE STEAL WD 5, DEVICE DEPEND
002E0F 0000 1472+CSTL6 DC A(*-*) CYCLE STEAL WD 6, DEVICE DEPEND
002E10 0000 1473+CSTL7 DC A(*-*) CYCLE STEAL WD 7, DEVICE DEPEND
002E11 0000 1474+CSTL8 DC A(*-*) CYCLE STEAL WD 8, DEVICE DEPEND
1475+*
1476+\$SUBN DC A(*-*) LAST SUBROUTINE ADDRESS USED
1477+\$DATA DC 2A(*-*) OPTIONAL DATA
1478+\$INTL DC X'0021' INTERRUPT LEVEL REQUESTED
1479+\$TURTN DC A(*-*) TEST UNIT RETURN ADRS TO MDI
1480+\$DVID DC X'0106' DEVICE ID
1481+\$SVCAL DC A(DEVADD) ADRS OF DEVICE ADDRESS
1482+ DC A(*-*) IBIS CYLINDER ADDRESS
1483+*
1484+* THIS TEST UNIT WILL RETURN TO MDI WITHOUT DOING ANY PROGRAM
1485+* FUNCTION. THE RESULTS THAT WERE SET UP IN THE RESULTS AREA ARE
1486+* STILL VALID BUT A DIFFERENT TEST IS TO BE PERFORMED.
1487+*
1488+T3C02 MVWI X'3C02', \$TUID SET UP TEST UNIT ID
1489+ BXS (R7) RETURN TO MDI SUPVR
1491 COPY COMEQU
1492 *****
1493 *
1494 * EQUATED NAMES FOR SUPPORTED SVC'S
1495 *
1496 *****
1497 OUT EQU 0 OUT SVC
1498 NOTIN EQU 1 NOTIN SVC
1499 IDLE EQU 2 IDLE SVC
1500 ASCII EQU 3 HEX TO ASCII SVC
1501 CHNGE EQU 4 CHANGE LEVEL SVC
1502 PGMCK EQU 5 ALLOW RETURN ON PROGRAM CHECK SVC
1503 EXIT EQU 6 EXIT SVC
1504 TERM EQU 7 TERMINATE SVC
1505 RESET EQU 8 RESET DEVICE SVC
1506 RID EQU 9 READ ID SVC
1507 START EQU 10 START CYCLE STEAL SVC
1508 SICSS EQU 11 START CYCLE STEAL STATUS SVC
1509 PREP EQU 12 PREPARE DEVICE SVC
1510 READ EQU 13 READ WITH FUNCTION BIT 3 OFF SVC
1511 READ EQU 14 READ WITH FUNCTION BIT 3 ON SVC
1512 RSTAT EQU 15 READ STATUS SVC
1513 WRIT EQU 16 WRITE WITH FUNCTION BIT 3 OFF SVC
1514 WRIT1 EQU 17 WRITE WITH FUNCTION BIT 3 ON SVC
1515 CTRL EQU 18 CONTROL SVC
1516 RIBC EQU 19 RELEASE INTERRUPT CONTROL BLOCK SVC
1517 CIBC EQU 20 CONNECT INTERRUPT CONTROL BLOCK SVC
1518 HIO EQU 21 HALT I/O
1519 RECDSD EQU 22 REQUEST USE OF DCP DISK SVC
1520 RELSD EQU 23 RELEASE USE OF DCP DISK SVC
1521 HALT EQU 24 HALT SVC
1522 EBCDIC EQU 25 EBCDIC TO HEX SVC (STRING)
1523 TEO EQU 26 HEX TO EBCDIC SVC (STRING)
1524 ATOH EQU 27 ASCII TO HEX SVC (STRING)
1525 HTOA EQU 28 HEX TO ASCII SVC (STRING)
1526 ETOA EQU 29 EBCDIC TO ASCII SVC (STRING)
1527 ATOE EQU 30 ASCII TO EBCDIC SVC (STRING)
1528 READI EQU 31 READ DATA SETS FOR MDI/UTIL
1529 WRITI EQU 32 WRITE DATA SETS FOR UTIL
1531 *****
1532 *
1533 * EQUATES USED BY TU'S AS CONSTANTS
1534 *
1535 *****
1536 PLUS EQU C'+-' PLUS CHAR
1537 MINUS EQU C'-+' MINUS CHAR
1538 *
1539 ZERO EQU 0
1540 ONE EQU 1
1541 TWO EQU 2
1542 THREE EQU 3
1543 FOUR EQU 4
1544 FIVE EQU 5
1545 SIX EQU 6
1546 SEVEN EQU 7
1547 EIGHT EQU 8
1548 NINE EQU 9
1549 TEN EQU 10
1550 ELEVN EQU 11
1551 TWELV EQU 12
1552 THRTN EQU 13
1553 FIVIN EQU 15
1554 SIXTN EQU 16
1555 THRY2 EQU 32
1556 SIXT4 EQU 64
1557 CNE28 EQU 128
1558 TWO56 EQU 256
1559 ONEK EQU 1024
1560 TWOK EQU 2048
1561 THREK EQU 3072
1562 FOURK EQU 4096
1564 M1 EQU -1
1565 M2 EQU -2
1566 M3 EQU -3
1567 M4 EQU -4
1569 *****
1570 *
1571 * THE FOLLOWING ARE EQUATES FOR BIT DISPLACEMENTS FROM THE
1572 * BEGINNING OF THE BYTE TO EACH BIT IN THE WORD OF SWITCHES.
1573 *
1574 *****
1575 BS0 EQU 0
1576 BS1 EQU 1
1577 BS2 EQU 2
1578 BS3 EQU 3
1579 BS4 EQU 4
1580 BS5 EQU 5
1581 BS6 EQU 6
1582 BS7 EQU 7
1583 BS8 EQU 8
1584 BS9 EQU 9
1585 BS10 EQU 10
1586 BS11 EQU 11
1587 BS12 EQU 12
1588 BS13 EQU 13
1589 BS14 EQU 14
1590 BS15 EQU 15
1592 COPY T4801

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1593 T4801 TUIT T01R 3/03/76
1594 *****06FEB76**
1595+*
1596+* TEST UNIT
1597+*
1598+* CHANNEL INTERFACE TEST AUTOMATIC SELECTION
1599+*
1600+* PURPOSE
1601+*
1602+* TO VERIFY THE CHANNEL INTERFACE CAN INTERRUPT ON ALL LEVELS
1603+*
1604+* CALLING SEQUENCE
1605+*
1606+* THE HOST WILL PREPARE THE I/O DEVICE TO INTERRUPT ON LEVEL ZERO
1607+* AND CAUSE AN INTERRUPT. WHEN THE INTERRUPT OCCURS, THE LEVEL IS
1608+* COMPARED TO THE EXPECTED LEVEL. THIS IS DONE ON ALL LEVELS.
1609+* LEVEL THREE WILL NOT OCCUR BECAUSE THIS PROGRAM WILL BE RUNNING
1610+* AS A BACKGROUND PROGRAM.
1611+* PROGRAM PASSES STATUS OF ALL LINES IN FOLLOWING FORMAT:
1612+* . TURESUL BIT 0-----NOT USED
1613+* . TURESUL BIT 1-----NOT USED
1614+* . TURESUL BIT 2-----NOT USED
1615+* . TURESUL BIT 3-----NOT USED
1616+* . TURESUL BIT 4-----NOT USED
1617+* . TURESUL BIT 5-----NOT USED
1618+* . TURESUL BIT 6-----NOT USED
1619+* . TURESUL BIT 7-----NOT USED
1620+* . TURESUL BIT 8-----NOT USED
1621+* . TURESUL BIT 9-----NOT USED
1622+* . TURESUL BIT 10-----NOT USED
1623+* . TURESUL BIT 11-----NOT USED
1624+* . TURESUL BIT 12-----NOT READY
1625+* . TURESUL BIT 13-----OIO CC ERROR
1626+* . TURESUL BIT 14-----DEVICE ID MISCOMPARE
1627+* . TURESUL BIT 15-----SEEK NO-OP ERROR
1628+* . TURESUL BIT 16-31-----CYCLE STEAL STATUS FOR FAILING OP
1629+* . TURESUL BIT 32-47-----CC 32-39 OIO CC, 40-47 INT CC
1630+* . TURESUL BIT 48-63-----IBS
1631+* . TURESUL BIT 64-79-----OPTION WORD 3 (ERROR INDICATORS)
1632+*
1633+* RETURN CONTROL
1634+* B TURTN* RETURN TO MDI SUPERVISOR
1635+* RETURN CONTROL
1636+*
1637+* B TURTN* RETURN TO MDI SUPERVISOR
1638+*
1639+*****
1640+T4801 MVW R7, TURTN SAVE RETURN ADDRESS
1641+ MVW X'4801', \$TUID SAVE TU ID FOR DISPLAY
1642+ MVA OPTN3, R4 SET UP POINTER ADRS IN R4
1643+ BAL \$CONC, R6 CLEAR DEV DEP STG AND CONNECT I/O BL
1644+ DC A(T01R) ERROR ADRS FOR INVALID PREP
1645+*
1646+*
1647 *****
1648 *
1649 MVWZ TURESUL, R2 CLEAR RESULTS WORD
1650 MVWZ TURESUL+2, R2 CLFAR RESULTS WORD 2
1651 MVWZ TURESUL+4, R2 CLEAR RESULTS WORD 3
1652 MVWZ TURESUL+6, R2 CLEAR RESULTS WORD 4
1653 MVWZ TURESUL+8, R2 CLEAR RESULTS WORD 5
1654 MVA TURESUL, R2 ADDRESS OF RESULTS
1655 MVA IOBLK, R7 RESET DEVICE
1656 SVC RESET
1657 *****
1658 * TEMPORARY DELAY
1659 *
1660 *****
1661 *
1662 MVW X'0FFF', R0 DELAY ROUTINE TO GET BY BUSY AFTER
1663 SVC IDLE * RESET
1664 JCI TEMP, R0 *
1665 MVA IOBLK, R7 READ DEVICE ID
1666 SVC *
1667 CH \$WID, IOMOD+4 CHECK DEVICE ID
1668 JNE T01A, DEVICE ID ERROR
1669 MVW 5, SKDCB SET UP CONTRCL WORD FOR TESTING
1670 MVW 0, SKDCB+2 SET UP SEEK NO DIFFERENCE (NO-OP)
1671 MVW X'FFFF', \$INTL SET UP INTERRUPT LEVEL FOR PREP
1672 IIST1 AWI X'10', \$INTL ADV INTR LEVEL, STARTING AT 0
1673 BAL \$CONC, R6 CONNECT DEV CNTL BLOCK AND PREP DEV
1674 DC A(T01R) ERROR
1675 ITST3 BAL \$SEEK, R6 EXEC NO-OP TO GET AN INTR
1676 DC A(T01R) ERROR
1677 TBR (R4, R) CT ERROR?
1678 JNE YES, YES
1679 ITST5 JWI X'21', \$INTL HAS INTR LEVEL COME DOWN TO 2
1680 JNE IIST1, * NO BCH AND CONTINUE TEST
1681 T01C MVW CSTL2, TURESUL+2 CYCLE STEAL STATUS FOR FAILING OP
1682 MVW \$IOIN, TURESUL+4 CONDITION CODES
1683 MVW \$ISB, TURESUL+6 ISE
1684 MVW OPTN3, TURESUL+8
1685 TXIT EXIT
1686+ B \$CONX RETURN TO MDI CONTROLLER
1687+*****
1688 *
1689 T01Z TWI X'0800', CSTL2 WAS NOT READY BIT ON?
1690 JOFF T01B IF NO, JUMP
1691 TBTS (R2, 12) YES
1692 J (R2, 12) EXIT
1693 T01A TBTS (R2, 14) DEVICE ID MISCOMPARE
1694 J T01C EXIT
1695 T01B TBTS (R2, 15) SEEK NO OP ERROR
1696 J T01C EXIT
1697 T01R MVWZ TURESUL, R2 CLEAR RESULTS WORD
1698 MVWZ TURESUL+2, R2 CLEAR RESULTS WORD 2
1699 MVWZ TURESUL+4, R2 CLEAR RESULTS WORD 3
1700 MVWZ TURESUL+6, R2 CLEAR RESULTS WORD 4
1701 MVWZ TURESUL+8, R2 CLEAR RESULTS WORD 5
1702 MVA TURESUL, R2 ADDRESS OF RESULTS
1703 T01ER JTBTS (R2, 13) OIO CC ERROR
1704 J T01C EXIT
1705 *
1706 COPY T4853
1707 T4853 TUIT \$ERR\$
1708 *****06FEB76**
002EFE 6F0D 2EEE 4801
002F02 4020 2EB6 4801
002F08 4424 2E80
002F0C 9E03 32BC
002F10 2FA6
002F12 CA25 18C8
002F16 CA25 18CA
002F1A CA25 18CC
002F1E CA25 18CE
002F22 CA25 18D0
002F26 4224 18C8
002F2A 4724 32A8
002F2E 6008
002F30 4024 0FFF
002F34 6002
002F36 B8FE
002F38 4724 32A8
002F3C 882B 2EFO 32B2
002F40 182C
002F46 4020 309E 0005
002F4C 4020 30A0 0000
002F52 4020 2EEC FFF1
002F58 4029 2EEC 0010
002F5E 6E03 32BC
002F62 2FA6
002F64 6E03 314A
002F68 2FBE
002F6A 4CA1
002F6C 1212
002F6E 402F 2EEC 0021
002F74 18F1
002F76 8828 2ED8 18CA
002F7C 8828 2EB8 18CC
002F82 8828 2EBA 18CE
002F88 8828 2EB4 18D0
002F8E 6802 334A
002F92 402B 2ED8 0800
002F98 1004
002F9A 4A4C
002F9C 50EC
002F9E 4A4E
1694 J 101C
002FA2 4A4F
002FA4 50E8
002FA6 CA25 18C8
002FAA CA25 18CA
002FAE CA25 18CC
002FB2 CA25 18CE
002FB6 CA25 18D0
002FB8 4224 18C8
002FBA 4A4D
002FC0 50DA

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1709** TEST UNIT
1710** ATTACHMENT CARD/VFO CHECK OUT TEST #10. 3/11/76
1711** PURPCE
1712** DETERMINE THE FOLLOWING:
1. ATTACHMENT CARD ROS IS FUNCTIONING CORRECTLY.
2. ECHO CHECKS SHOW ATTACHMENT CARD FAILURE.
3. VFO DATA WRAP WORKS.
4. DISKETTE SPEED IS CORRECT.
1713** CALLING SEQUENCE
1714** PERFORM THE FOLLOWING:
1. ISSUE START DIAGNOSTIC COMMAND.
2. CHECK ROS HASH TOTALS.
3. CHECK DISKETTE SPEED WITH HEADS LOADED.
4. VERIFY ECHO CHECKS.
5. VERIFY DATA WRAP THROUGH VFO CARD IN FILE.
1715** PROGRAM PASSES STATUS OF ALL LINES IN FOLLOWING FORMAT:
1716** TURESUL BIT 00---NOT USED
1717** TURESUL BIT 01---NOT USED
1718** TURESUL BIT 02---NOT USED
1719** TURESUL BIT 03---NOT USED
1720** TURESUL BIT 04---NOT USED
1721** TURESUL BIT 05---NOT USED
1722** TURESUL BIT 06---NOT USED
1723** TURESUL BIT 07---NOT USED
1724** TURESUL BIT 08---NOT USED
1725** TURESUL BIT 09---NOT USED
1726** TURESUL BIT 10---NOT USED
1727** TURESUL BIT 11---NOT USED
1728** TURESUL BIT 12---NO INTERRUPT RECEIVED.
1729** TURESUL BIT 13---ECHO CHECK MISCOMARE.
1730** TURESUL BIT 14---ECHO CHECK ERROR.
1731** TURESUL BIT 15---DISK SPEED INCORRECT.
1732** TURESUL BIT 16-31-FIRST ROS CHECK SUM.
1733** TURESUL BIT 32-47-SECOND ROS CHECK SUM.
1734** TURESUL BIT 48-63-INDEX PERIOD (5F7C=162.5MS/6934=170.9MS)
1735** TURESUL BIT 64-79-DIAGNOSTIC DATA RESULTS FROM WRAP TEST.
1736** RETURN CONTROL
1737** B TURTN* RETURN TO MDI SUPERVISOR
1738** *****
1739** MVW R7,TURTN SAVE RETURN ADDRESS
1740** MVWI X'4853', \$TUID SAVE TU ID FOR DISPLAY
1741** MVA CPTN1,R4 SET UP POINTER ADRS IN R4
1742** BAL \$CONC,R6 CLEAR DEV DEP STG AND CONNECT I/O BL
1743** DC A(\$ERR\$) ERROR ADRS FOR INVALID PREP
1744** MVWZ TURESUL,R2 CLEAR RESULTS WORD
1745** MVWZ TURESUL+2,R2 CLEAR RESULTS WORD 2
1746** MVWZ TURESUL+4,R2 CLEAR RESULTS WORD 3
1747** MVWZ TURESUL+6,R2 CLEAR RESULTS WORD 4
1748** MVWZ TURESUL+8,R2 CLEAR RESULTS WORD 5
1749** MVWI X'5000',R0 DELAY TO GET BY BUSY AFTER RESET
1750** JCT *,R0 *
1751** BAL \$DIAG,R6 READ DIAGNOSTIC
1752** DC A(T53Z) ERROR
1753** TBTR (R4,ER) CHECK FOR CC ERROR
1754** BON \$ERR\$ ERROR
1755** HVW DIAGW,TURESUL+2 STCRE DIAG RESULTS (1ST WD)
1756** MVW DIAGW+4,TURESUL+4 * (3RD WD)
1757** MVW DIAGW+8,TURESUL+6 * (5TH WD)
1758** MVW DIAGW+12,TURESUL+8 * (7TH WD)
1759** MVW DIAGW+16,TURESUL+10 *
1760** CWI X'FFFF',DIAGW+2 CHECK ROS HASH TOTALS
1761** JE T53A CK
1762** TBTS (R2,13) ROS ERROR
1763** AW DIAGW+4,DIAGW+6 CHECK ROS HASH TOTALS
1764** CWI X'FFFF',DIAGW+6 *
1765** JE T53B OK
1766** TBTS (R2,13) ROS ERROR
1767** CWI X'5F7C',DIAGW+8 IS 5TH WD BETWEEN X'5F7C' & '6934'
1768** JLT T53C ERROR
1769** CWI X'6934',DIAGW+8 *
1770** JGT T53D ERROR
1771** J T53D *
1772** TBTS (R2,15) DISK SPEED INCORRECT
1773** TWI X'00FF',DIAGW+10 ANY BITS ON IN 8-15 OF 6TH WD
1774** JOFF T53X OK
1775** TBTS (R2,14) ECHO CHECK ERROR
1776** J T53X *
1777** TBTS (R2,12) NO INTERRUPT RECEIVED
1778** TXIT EXIT
1779** B \$CONX RETURN TO MDI CONTROLLER
1780** *****
1781** COPY T48DCB
1782** *****
1783** *****2/17/76*****
1784** *****
1785** *****
1786** *****
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1789** *****
1790** *****
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1822** *****
1823** *****
1824** *****

LOCTR OBJECT TEXT STMT SOURCE STATEMENT COPYRIGHT IBM CORP 1976
00306E 0007 1825 CLDCB DC X'0007' RECALIBRATE DCB
003070 000000000000000000 1826 * 7A(*-*)
1827 *
1828 ***** FORMAT DCB *****
1829 *
00307E 0002 1830 FRDCB DC X'0002' FORMAT CONTROL WORD
003080 0000 1831 DC X'0000' NOT USED
003082 0000 1832 DC A(*-*) FORMAT DATA WORD
003084 0000 1833 DC A(*-*) N - C BYTES
003086 0001 1834 DC X'0001' H - R BYTES
003088 0000 1835 DC A(*-*) CHAIN ADDRESS
00308A 0000 1836 DC F'0' NOT USED
00308C 0000 1837 DC F'0' NOT USED
1838 *
1839 ***** READ SECTOR ID DCB *****
1840 *
00308E 200A 1841 RSDCB DC X'200A' READ SECTOR ID
003090 0000 1842 DC X'0000' NOT USED
003092 0000 1843 DC X'0000' NOT USED
003094 0000 1844 DC X'0000' NOT USED
003096 0000 1845 DC X'0000' NOT USED
003098 0000 1846 DC X'0000' CHAIN ADDRESS
00309A 0004 1847 DC X'0004' BYTE COUNT FOR READ SECTOR ID
00309C 2EBE 1848 DC A(\$STCID) SECTOR ID DATA ADDRESS
1849 *
1850 ***** SEEK DCB *****
1851 *
00309E 0005 1852 SKDCB DC X'0005' SEEK DCB
0030A0 0000 1853 DC X'0000' BIT 3=HEAD;BIT 4=DIRECTION;8-15=DIFF
0030A2 0000 1854 DC F'0'
0030A4 0000 1855 DC F'0'
0030A6 0000 1856 DC F'0' 0-7 HEAD SELECT (NEW ARCH)
0030A8 0000 1857 DC F'0'
0030AA 0000 1858 DC F'0'
0030AC 0000 1859 DC F'0'
1860 *
1861 *
1862 ***** CYCLE STEAL STATUS DCB *****
1863 *
0030AE 2000 1864 CSDCB DC X'2000' CONTROL WORD
0030B0 0000 1865 DC F'0' NOT USED
0030B2 0000 1866 DC F'0' NOT USED
0030B4 0000 1867 DC F'0' NOT USED
0030B6 0000 1868 DC F'0' NOT USED
0030B8 0000 1869 DC F'0' NOT USED
0030BA 0004 1870 DC X'0004' 2 WORDS OF STATS
0030BC 2ED6 1871 DC A(\$SBUF) ADDRESS OF CYCLE STEAL STATUS DATA
1872 *
1873 ***** WRITE DCB *****
1874 *
0030BE 0001 1875 WRDCB DC X'0001' 8-15=1- ATA AH;8-15=2-CONTROL AH
0030C0 0000 1876 DC F'0' NOT USED
0030C2 0000 1877 DC F'0'
0030C4 0000 1878 DC X'0000' SERCH ARGUMENT N-C
0030C6 0000 1879 DC X'0000' SEARCH ARGUMENT H-R
0030C8 0000 1880 DC A(*-*) CHAIN ADDRESS
0030CA 0000 1881 DC F'0' BYTE COU T
0030CC 0000 1882 DC A(*-*) WRITE DATA ADDRESS
1883 *
1884 ***** VERIFY DCB *****
1885 *
0030CE 000C 1886 VRDCB DC X'000C' CONTROL WORD
0030D0 0000 1887 DC F'0' NOT USED
0030D2 0000 1888 DC F'0' NOT USED
0030D4 0000 1889 DC A(*-*) N-C
0030D6 0000 1890 DC A(*-*) H-R
0030D8 0000 1891 DC A(*-*) CHAIN ADDRESS
0030DA 0000 1892 DC F'0' BYTE COUNT
0030DC 0000 1893 DC A(*-*) VERIFY DATA ADDRESS
1894 *
1895 ***** READ DCB *****
1896 *
0030DE 2009 1897 RDCB DC X'2009' READ DCB CONTROL WORD
0030E0 0000 1898 DC F'0' NOT USED
0030E2 0000 1899 DC F'0' NOT USED
0030E4 0000 1900 DC X'0000' SEARCH ARGUMENT N-C
0030E6 0101 1901 DC X'0101' SEARCH ARGUMENT H-R
0030E8 0000 1902 DC A(*-*) CHAIN ADDRESS
0030EA 0D00 1903 DC F'332B' BYTE COUNT
0030EC 0000 1904 DC A(*-*) READ DATA ADDRESS
1905 *
1906 *
1907 *
1908 *
0030EE 1000 1909 COUNT DC F'4096' BYTE COUNT (4096)
0030F0 0C80 1910 CTN32 DC F'3200' BYTE COUNT (3200)
0030F2 0000 1911 SAVE DC X'0000' SCTID INFO
0030F4 0000 1912 DC X'0000' *
0030F6 0000 1913 DIFF DC X'0000' SEEK DIFFERENCE
0030F8 00C8 1914 FDATA DC X'00C8' FORMAT DATA BYTE FOR COMPARE
0030FA 0000 1915 XXX DC X'0000' WORK WORD INT TC ZERO
0030FC 0046 1916 ENDEX DC X'0046' TERMINATING SEEK DIFFERENCE
0030FE 0000 1917 ZERCO DC X'0000' CONSTANT ZERO
003100 0001 1918 CNE1 DC X'0001' CONSTANT ONE
003102 0800 1919 REVR DC X'0800' SEEK REVERSE
003104 0000 1920 HRRR DC X'0000' H-R
003106 0000 1921 BCNT DC X'0000' BYTE COUNT
003108 0000 1922 JCE DC X'0000' WRITE PARAMETER POINTER
00310A 0000 1923 JOE1 DC X'0000' SAVE LOC FOR PARM LIST ADDRESS
00310C 7A5E 1924 WDATA DC X'7A5E' WRITE DATA
00310E 69BD 1925 DC X'69BD' *
003110 0000 1926 CYLND DC X'0000' TEMP SAVE AREA FOR CYLINDER #
003112 0000 1927 DC X'0000' *
003114 0000 1928 FORMT DC X'0000' FROMAT BIT FROM OPERATOR
003116 004C 1929 CYLIN DC F'004C' CYLINDER NUM SELECTED FROM OPERATOR
003118 0000 1930 HEAD DC F'0000' HEAD NUM SELECTED FROM OPERATOR
00311A 0001 1931 SECT DC F'0001' SECTOR # SELECTED BY OPERATOR
00311C 0D00 1932 BYCNO DC F'332B' BYT COUNT SELECTED BY OPER
00311E 0000 1933 TABLE DC A(*-*) ADDR OF WRT PAR LIST FOR FORMAT RTNS
003120 0000000000000000 1934 DIAGW DC 7A(*-*) DIAGNOSTIC BUFFER
003122 0000 1935 CONST DC X'0000' SECTOR # PLUS ONE FOR N='X'
003124 0000 1936 SBYT DC X'0000' FULL BYTE COUNT FOR N='X'
003126 00FF 1937 CDAT DC X'00FF' CONSTANT '00' & 'FF'
003128 0000 1938 CTR01 DC X'0000' COUNTER 1
00312A 0000 1939 CTR02 DC X'0000' COUNTER 2
00312C 0000 1940 CTR03 DC X'0000' COUNTER 3

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00313A 0000 1941 CTR04 DC X'0000' COUNTER 4
00313C 0000 1942 CTR05 DC X'0000' COUNTER 5
00313E 0000 1943 SAVR3 DC X'0000' SAVE AREA
003140 0000 1944 SAVR5 DC X'0000' SAVE AREA
003142 0000 1945 SIDE DC X'0000' SIDE BEING TESTED
003144 0000 1946 TRK DC X'0000' CURRENT CYLINDER NUMBER
003146 0000 1947 WIDAT DC X'0000' WORK AREA
003148 4C00 1948 SVSIX DC X'4C00' CYLINDER NUMBER 76
1950 * COPY T48IG
1951 *
1952 * EXECUTE INPUT & OUTPUT COMMANDS 4/15/76
1953 * TO EXECUTE ALL I/O COMMANDS FROM A COMMON PLACE.
1954 * EACH OF THESE ENTRIES SET R7 WITH THE ADRS OF ITS PARAMETER
1955 * LIST AND ANY SPECIAL SWITCHES BEFORE BRANCHING TO THE
1956 * SUPVR CALL.
1957 *
1958 * THIS SUBROUTINE WILL CHECK FOR THE FOLLOWING:
1959 *
1960 * 1. LOST INTERRUPTS BY TIMING OUT A COUNTING LOOP
1961 * 2. ERROR INTERRUPTS RECEIVED FROM SUPVR
1962 * 3. LCOF ON ERROR, THE CALL MUST HAVE A 'DC' STATEMENT AFTER
1963 * THE CALL WITH THE ADDRESS OF THE RETRY STATEMENT
1964 * 4. CYCLE STEAL IN PROGRESS WITH AN ERROR
1965 * 5. SOMETHING ELSE
1966 *
1967 * THIS ROUTINE HAS THE FOLLOWING ENTRIES:
1968 *
1969 * 1 BAL \$SEK,R6 SEEK
1970 *
1971 * 2 BAL \$RECL,R6 RECALIBRATE
1972 *
1973 * 3 BAL \$RDID,R6 READ SECTOR ID
1974 *
1975 * 4 BAL \$RD,R6 READ
1976 *
1977 * 5 BAL \$RDVY,R6 READ VERIFY
1978 *
1979 * 6 BAL \$WRT,R6 WRITE
1980 *
1981 * 7 BAL \$FMT,R6 FORMAT
1982 *
1983 * 8 BAL XIOCS,R6 CYCLE STEAL STATUSB
1984 *
1985 * 9 BAL \$DIAG,R6 READ DIAGNOSTICS
1986 *
1987 *
1988 * \$SEK MVA SKDCB,IODCB SET UP CONTROL BLOCK FOR SVC CALL
1989 * J XIO
1990 *
1991 * \$RECL MVA CLDCB,IODCB SET UP BLOCK FOR SVC CALL
1992 * J XIO
1993 *
1994 * \$RDID MVA RSDCB,IODCB SET UP BLOCK FOR SVC CALL
1995 * MVWI X'9999',SCTID INVALIDATE SECTOR ID BUFFER AREA
1996 * MVWI X'9999',SCTID+2 *
1997 * J XIO
1998 *
1999 * \$RD MVBI 255,R3 INIT READ BUFFER TO FF'S
2000 * MVW RDDCB+14,R5 *
2001 * MVWI X'0400',R7 *
2002 * FFN R3,(R5) *
2003 * \$RDS MVA RDDCB,IODCB SET UP BLOCK FOR SVC CALL
2004 * J XIO
2005 *
2006 * \$RDVY MVA VRDCB,IODCB SET UP CONTROL BLOCK FOR SVC CALL
2007 * J XIO
2008 *
2009 * \$WRT MVA WRDCB,IODCB SET UP CONTROL BLOCK FOR SVC CALL
2010 * J XIO
2011 *
2012 * \$FMT MVA FRDCB,IODCB SET UP CONTROL BLOCK FOR SVC CALL
2013 * J XIO
2014 * \$DIAG MVA DGDCH,IODCB SET UP CONTROL BLOCK FOR SVC CALL
2015 * MVWI X'0000',ICMOD MODIFIER FOR DIAG OP
2016 * J XIC1
2017 * CEOP2 BXS (R6,2) DUMMY RETURN TO USER
2018 *
2019 * XEQIT 1
2020 * *****29JUL76**
2021 *
2022 * SUB-ROUTINE
2023 *
2024 * EXECUTE INPUT AND OUTPUT CCMNANDS
2025 *
2026 * PURPOSE
2027 *
2028 * TO EXECUTE ALL I/O COMMANDS FROM A COMMON PLACE.
2029 * THIS SUBROUTINE WILL DO THE FOLLOWING FUNCTIONS:
2030 *
2031 * 1. SAVE THE ADDRESS THAT PCINTS TO THE INSTRUCTION THAT STARTED
2032 * THE I/O COMMAND.
2033 * 2. SAVE THE DCB BLOCK USED UNLESS IT IS A START CYCLE STATUS
2034 * ISSUED BY THIS SUBROUTINE.
2035 * 3. CLEAR OUT THE CYCLE STEAL STATUS STORAGE UNLESS THE
2036 * START CYCLE STATUS WAS ISSUED BY THIS SUBROUTINE.
2037 * 4. RESETS THE INTERRUPT INDICATOR AND CHECKS FOR ANY INTERRUPT
2038 * SINCE THE LAST EXPECTED INTERRUPT. IF AN INTERRUPT IS FOUND,
2039 * MYSTERY INTERRUPT (MI) CONTROL BIT IS SET.
2040 * 5. MOVES THE ADDRESS OF THE I/O CONTROL BLOCK IN R7, SET THE
2041 * EXPECTED INTERRUPT CONTROL BIT AND ISSUE THE 'SVC START'.
2042 * 6. WHEN THE SUPVR RETURNS AFTER ISSUING THE I/O COMMAND, TIMING
2043 * STARTS TO DETERMINE A LOST INTERRUPT.
2044 * 7. EXCEPT THE INTERRUPT AND GATHER INFORMATION TO DETERMINE IF IT
2045 * WAS AN ERROR OR OKAY AND EXIT OFF THE INTERRUPT LEVEL.
2046 * 8. CHECK IF THERE WAS A WRONG INTERRUPT LEVEL.
2047 * 9. CHECK IF AN ERROR WAS EXPECTED AND IF THERE WAS RETURN.
2048 * 10. CHECK IF THERE WAS AN ERROR CONDITION, IF NOT RETURN.
2049 * 11. CHECK TO SEE IF THE EXERCISER IS TO BE TERMINATED.
2050 * 12. CHECK IF A CYCLE STEAL OPERATION WAS IN PROGRESS THAT WAS
2051 * ISSUED BY THIS SUBROUTINE.
2052 * 13. CHECK THE ISB BITS THAT ARE ON. IF BIT 0 IS ON, ISSUE A
2053 * CYCLE STEAL STATUS COMMAND. CHECK FOR ANY OTHER BIT BEING ON,
2054 * COUNT IT AND SET UP THE PROPER ERROR MESSAGE TO BE PRINTED.
2055 *
2056 * CALLING SEQUENCE
2057 *

LOCTR OBJECT TEXT STMT SOURCE STATEMENT COPYRIGHT IBM CORP 1976
2058** THIS ROUTINE HAS THE FOLLOWING ENTRIES:
2059**
2060** --> BAL XIO OR XEQ ANY CYCLE STEAL COMMAND, MOD=0
2061** --> BAL XIO1 MOD PARM PRELOADED IN 'IOMOD'
2062** --> BAL XIOCS,R6 OR XEQ START CYCLE STEAL STATUS, MOD=F
2063** --> BAL XIOCS-4,R6 AUTO CS STATUS (FOLLOWING OTHER XIO
2064** AND DOES NOT POST INTERRUPT STATUS)
2065**
2066** RETURN CONTROL
2067**
2068** BXS (R6,2) RETURN TO USER NO ERROR
2069** OR B (R6)* RETURN AND RETRY ON ERROR
2070** *****
2072** XIO MVWZ IOMOD,R3 SET MOF OF 0 FOR CYCLE STEAL OP
2073** J XIO1 CS I/O'S ARE NOT RETRIED
2074**
2075** TBTR (R4,CE) RESET CS STATUS INTER ERROR INDICAT.
2076** TBTS (R4,CS) SET 'CYCLE STEAL STATUS' IN PROGRESS
2077** XIOCS MVA CSDCB,IODCB SET UP CONTROL BLOCK FOR SVC CALL
2078** MVWI X'0000',IOMOD IS CS IN PROGRES. ERROR CONDITION
2079** TBTR (R4,CS) * YES, BYPASS SAVING I/O ADRS
2080** JON XIO1 * YES, YPASS SAVING I/O ADRS
2081** XIO1 MVW R6,I,STIO SAVE IAR FOR RETRY IF REQUESTED
2082** MVA DCBUF,R3 SET UP TO ADRS TO MOVE DCB TABLE
2083** MVW IODCE,R5 * AND THE FROM ADRS, ALONG WITH
2084** MVBI 16,R7 * THE NUMBER OF MOVES
2085** MVFN (R5),(R3) MOVE 1 STATUS WORD AND ADJUST
2086** MVBI 255,R3 CLEAR CYCLE STATUS BUFFER
2087** MVA CSBUF,R5 * TO ALL ONES *
2088** MVBI 16,R7 *
2089** FFN R3,(R5) *
2090** MVWI X'0708',SIOIN OVERLAY OLD CONDITION CODES
2091** MVWZ \$ISB,R3 ZERO OUT OLD ISB VALUE
2092**
2093** TBTR (R4,ER) RESET ANY ERROR BEFORE I/O COMMAND
2094** XIO2 TBTR (R4,IN) CLEAR INTERRUPT RECEIVED CMTL BIT
2095** MVA ICBLK,R7 SET UP CONTROL BLOCK FOR SUPVR
2096** TBTR (R4,\$LE) RESET LEVEL ERROR INDICATOR
2097** TBTS (R4,XI) SET EXPECTED INTR CONTROL BIT
2098** SVC START CALL SUPVR FOR I/O COMMAND
2099**
2100** TBTR (R4,NI) IS AN INTR EXPECTED
2101** BN (R6,2) * NO, RETURN TO USER
2102**
2103** THE INTR SHOULD OCCUR WHILE SPINNING IN THE NEXT SECTION
2104**
2105** MVBI X'00',R5 SET UP WCRK REG FOR 'LOST INTR'
2106** XIO8 TBTR (R4,IN) HAS INTERRUPT BEEN RECEIVED
2107** JON XIOCK * YES, CHECK IF ALL WAS SATISFACTORY
2108** SVC IDLE ALLOW ANCTHER PROGRAM A CHANCE TO RUN
2109** SUPVR WILL RETURN HERE
2110** AWI 1,R5 ADVANCE TIME OUT COUNT
2111** JNZ XIO8 BCH IF TIME OUT NOT REACHED
2112** TBTS (R4,ER) SET ON ERROR CONTROL BIT
2113** B (R6) ERR NO INTERRUPT
2114** *****03FEB76**
2115**
2116** SUBROUTINE
2117**
2118** I/O EXECUTE ERROR HANDLING ROUTINE
2119**
2120** PUPRCSE
2121**
2122** THIS ROUTINE WILL COLLECT INFORMATION TO HELP DETERMINE THE
2123** PROBLEM THAT WAS FOUND WHEN THE I/O COMMAND WAS ISSUED BY THE
2124** SUPERVISOR AND IT WAS NOT ACCEPTED.
2125**
2126** CALLING SEQUENCE
2127**
2128** SUPVR WILL ENTER WHEN AN ERROR OCCURS ON AN I/O COMMAND
2129**
2130** RETURN CONTROL
2131**
2132** B (R6)* RETURN TO USERS ERROR HANDLER
2133**
2134** *****
2135** *****
2136**
2137** CC 0= DEVICE NOT ATTACHED
2138** FOR 1= DEVICE BUSY
2139** I/O 2= DEVICE BUSY AFTER RESET
2140** 3= COMMAND REJECT
2141** 4= INTERVENTION REQUIRED
2142** 5= INTERFACE DATA CHECK
2143** 6= CCNTROLLER BUSY
2144** 7= I/O COMMAND EXCEPTED
2145**
2146** XIOER DC X'706E' COPY STATUS ANY LEVEL INTO R3
2147** SRL 13,R3 POSITION CC CODE TO BITS 13-15
2148** MVB R3,SIOIN * PUT IN LOG OUT AREA
2149** B (R6)* RETURN TO USER ERROR HANDLER
2150** *****14APR76**
2151** *****
2152**
2153** SUB-ROUTINE
2154**
2155** ERROR INTERRUPT RUNS ON INTERRUPT LEVEL '\$INTL'
2156**
2157** PUPRCSE
2158**
2159** THIS ROUTINE WILL BE ENTERED WHEN THE SUPVR DETECTS AN ERROR
2160** OR THE INTERRUPTING CONDITION CODE DOES NOT AGREE WITH THE
2161** EXPECTED CODE.
2162**
2163** CALLING SEQUENCE
2164**
2165** SUPVR WILL ENTER WHEN AN ERROR OCCURS ON AN I/O INTERRUPT
2166**
2167** RETURN CONTROL
2168**
2169** SVC EXIT RETURN TO USER VIA SUPVR
2170**
2171** *****
2172**
2173** CC 0= CONTROLLER END ISB 0= ADD STATUS
2174** FOR 1= PROGRAM CONTROL INTERRUPT BIRS 1= COMD REJECT
2175** INTR 2= EXCEPTION INTERRUPT FOR 2= INCOR LENGTH
2176** 3= DEVICE END INTERRUPT INTR 3= DCB SPEC CK IL

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2177** 4= ATTENTION INTERRUPT 4= STG DATA CK IL
2178** 5= ATTENTION / PROGRAM CNTL INTR 5= INV STG ADRS IL
2179** 6= ATTENTION / EXCEPTION INTR 6= PROTRCT CK IL
2180** 7= ATTENTION / DEVICE END INTR 7= I-FACE DATA IL
2181** DC X'706E' COPY STATUS ANY LEVEL INTO R3 IL
2182+INTR SRL 13,R3 POSITION INDICATORS IN R3 IL
2183+ MVA OPTN1,R4 SET UP BASE ADRS IL
2184+ TBT (R4,CS) IS CS IN PROGRESS IL
2185+ JOFF INTN3 * NO IL
2186+ TBT (R4,CE) TURN ON CYCLE STEAL INTER ERROR IL
2187+ TBT (R4,CE) SAVE CS ERR ISB VALUE, BITS 0-7 IL
2188+ MVB R7,CSTL8 * AND THE COND CODE IL
2189+ MVB R3,CSTL8+1 IL
2190+ J INTR1 IL
2191+INTES TBT (R4,XE) TEST EXPECTED ATTN / ERROR IND IL
2192+ JOFF INTN3 BCH IF NOT EXPECTED IL
2193+ CBI 4,R3 IS THIS AN 'ATTENTION' INTR IL
2194+ JE INTR1 * YES, BCH TO END INTR SEQUENCE IL
2195+INTET TBT (R4,ER) SET ERROR ON I/O COMMAND CNTL BIT IL
2196+ J INTR1 IL
2197+ THE ERROR INTERRUPT USES THE SAME IL
2198+ ENDING SEQUENCE AS THE NORMAL INTR IL
2200+*****14APR76***** IL
2201+ SOUBROUTINE IL
2202+ OKAY INTERRUPT RUNS ON INTERRUPT LEVEL '\$INTL' IL
2203+ IL
2204+ PURPOSE IL
2205+ TO CHECK THE INTERRUPT AND CONTINUE THE TEST IL
2206+ CALLING SEQUENCE IL
2207+ SUPERVISOR WILL ENTER HERE IF INTR CC IS AS REQUESTED IL
2208+ THE ERROR INTERRUPT HANDLER WILL BRANCH TO THIS ROUTINE IL
2209+ AFTER THE SPECIAL PART HAS BEEN COMPLETED AND THE IL
2210+ COMMON SECTION IS HANDLED HERE. IL
2211+ RETURN CONTROL IL
2212+ SVC EXIT RETURN TO USER VIA SUPVR IL
2213+***** IL
2214+***** IL
2215+***** IL
2216+***** IL
2217+***** IL
2218+***** IL
2219+***** IL
2220+***** IL
2221+***** IL
2222+***** IL
2223+***** IL
2224+***** IL
2225+***** IL
2226+***** IL
2227+***** IL
2228+***** IL
2229+***** IL
2230+***** IL
2231+***** IL
2232+***** IL
2233+***** IL
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2237+***** IL
2238+***** IL
2239+***** IL
2240+***** IL
2241+***** IL
2242+***** IL
2243+***** IL
2244+***** IL
2245+***** IL
2246+***** IL
2247+***** IL
2248+***** IL
2249+***** IL
2250+***** IL
2251+***** IL
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2254+***** IL
2255+***** IL
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2295+***** IL

LOCTR OBJECT TEXT STMT SOURCE STATEMENT COPYRIGHT IBM CORP 1976
2296** PURPOSE IL
2297** TO CONNECT THE INTERRUPT CONTROL BLOCK TO THIS DEVICE AND IL
2298** PREPARE ON THE DESIRED INTERRUPT LEVEL AND TO ALLOW THE DEVICE IL
2299** TO INTERRUPT. IL
2300** CALLING SEQUENCE IL
2301** THIS SUBROUTINE HAS THE FOLLOWING ENTRIES: IL
2302** --> BAL \$CONC,R6 CLEAR DEV DEP STG AND CONNECT I/O BLK IL
2303** --> BAL \$CONP,R6 PREPARE DEVICE ONLY, ALREADY CONNECT IL
2304** RETURN CONTROL IL
2305** BXS (R6,2) RETURN TO USER VIA REG 6 IF OKAY IL
2306** OR B (R6)* IF THE DEVICE COULD NOT BE CONNECTED IL
2307** ***** IL
2308** ***** IL
2309** ***** IL
2310** ***** IL
2311** ***** IL
2312** ***** IL
2313** ***** IL
2314** ***** IL
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2408** ***** IL
2409** ***** IL
2410** ***** IL
2411** ***** IL
2412** ***** IL

LOCTR OBJECT TEXT STMT SOURCE STATEMENT COPYRIGHT IBM CORP 1976
00F1F0 2413+PIDMSG10 EQU X'F1F0'
00J080 2414+BIT0080 EQU X'0080'
2415+**
2416+** DATA CONTROL BLOCK FOR CONVERTING HEX TO EBCDIC
2417+**
2418+HEBLK DC A(48) NUMBER OF BYTES TO CONVERT
2419+ DC A(\$IUID) FROM ADRS
2420+ DC A(TUWRK) AND THE TO ADRS
2421 *
2423 COPY T3C00
2424 T3C00 TUIT 1
2425+*****06FEB76**
2426+**
2427+** TEST UNIT
2428+**
2429+** DIRECT PROGRAM CONTROL TEST UNIT 04MAY76
2430+**
2431+** PURPOSE
2432+**
2433+** THREE PARAMETERS ARE NEEDED FOR THE EXECUTION OF THIS TU AND ARE
2434+**
2435+** 1. ONE BYTE OF FUNCTION-MODIFIER, IE, X'60' FOR PREPARE
2436+** 2. TWO BYTES OF DATA TO BE USED IN THE SECOND PART OF THE IDCB,
2437+** - IE, X'0005' TO SELECT LEVEL 2 FOR AN INTERRUPT.
2438+**
2439+** CALLING SEQUENCE
2440+**
2441+** MDI=@TUXX,T3C00,2,0708,EQ,PLNG=6,PRAM=FFXXXX'
2442+**
2443+** RETURN CONTROL
2444+**
2445+** B TURTN* RETURN TO MDI SUPERVISOR
2446+**
2447+*****
2448+T3C00 MVW R7,TURTN SAVE RETURN ADDRESS
2449+ MVWI X'3C00',STUID SAVE TU ID FOR DISPLAY
2450+ MVA CPTN1,R4 SET UP POINTER ADRS IN R4
2451+**
2452 MVA INTBL,R7 SET R7 TO CONTROL BLOCK AND
2453 SVC CICE * AND CONNECT IT TO THIS DEVICE
2454 MVWI X'0708',SIOIN INIT THE CONDITION CODES
2455 MVW TUPARM1,R1 SET UP PARM ADRS
2456 MVB (R1),T3C00I * AND SET IN FUNCTION-MODIFIER
2457 MVB DEVADD,T3C00I+1 * FOLLOWED BY THE DEVICE ADRS
2458 MVE (R1),T3C00I+2 * AND SET IN EVEN BYTE DATA
2459 MVB (R1),T3C00I+3 * AND SET IN ODD BYTE DATA
2460 MVD T3C00I,RO GET FUNCTION, MODIFIER AND DEV ADRS
2461 *
2462 IO T3C00I ISSUE THE I/O COMMAND AND
2463 DC X'70AE' * GET THE I/O CONDITION CODE IN R5
2464 SRL 13,R5 POSITION CC IN THE RESULTS FIELD
2465 MVB R5,\$ICIN * AND SAVE IT IN THE RESULTS
2466 S&L 12,RO * AND POSITION IT IN THE REG TO
2467 T3C00S JZ T3C00S * SEND BACK THE RESULTS IF READ DPC
2468 CBI X'02',RO IS IT A READ STATUS
2469 JNE T3C00N * NO, CONTINUE TO CHECK
2470 MVW T3C00I+2,R2 * YES, GET ID RECEIVED AND
2471 XW \$DVID,R2 CHECK AGAINST SHOULD BE VALUE
2472 JNZ T3C00S * SEND BACK ACTUAL DATA
2473 MVW R2,TURESUL+2 AND SEND BACK THE RESULTS (ZERO)
2474 J T3C00X
2475 T3C00N CBI X'01',RO IS IT A READ DPC COMMAND
2476 JE T3C00S * YES, SEND RESULTS TO MDI
2477 CBI X'0F',RO * IF IT IS A READ ID FUNCTION
2478 JNE T3C00X * NO, GO TO EXIT
2479 *
2480 T3C00S MVW T3C00I+2,TURESUL+2 SEND BACK DATA RECEIVED AND EXIT
2481 T3C00X MVW \$I0IN,TURESUL PUT ANY INTR COND CODE FOUND IN
2482 TXIT * RESULTS AND EXIT
2483+ B \$CONX RETURN TO MDI CONTROLLER
2484+*****
2485 *
2486 * IDCE FOR DIRECT PROGRAM CONTROL CCMAND
2487 *
2488 T3C00I DC X'0000' FUNCTION-MODIFIER-DEVICE ADDRESS
2489 DC X'0000' IMMEDIATE DATA BUFFER
2490 CCOPY T3C01
2491 T3C01 TUIT 1
2492+*****06FEB76**
2493+**
2494+** TEST UNIT
2495+**
2496+** DELAY COUNTER COMMON
2497+**
2498+** EURECSE
2499+**
2500+** TO DELAY WHILE THE DEVICE IS DOING A PREVIOUS REQUESTED FUNCTION
2501+**
2502+** CALLING SEQUENCE
2503+**
2504+** ONE PARAMETER IS NEEDED FOR THIS TEST UNIT, THE DELAY TIME.
2505+** NORMAL FUNCTIONS APPLY, IE, TWO BYTES, 0000 AND EQUAL.
2506+** FOR EACH COUNT THE DELAY TIME IS ABOUT 3 MICROSECONDS.
2507+**
2508+** RETURN CONTROL
2509+**
2510+** B TURTN* RETURN TO MDI SUPERVISOR
2511+**
2512+*****
2513+T3C01 MVW R7,TURTN SAVE RETURN ADDRESS
2514+ MVWI X'3C01',STUID SAVE TU ID FOR DISPLAY
2515+ MVA CPTN1,R4 SET UP POINTER ADRS IN R4
2516+**
2517 *
2518 * MDI=\$TUXX,'TUDLY,2,0000,EQ,PLNG=2,PRAM=XXXX'
2519 *
2520 MVA INTBL,R7 SET UP INTR ADDRESSES
2521 SVC CICE * AND CONNECT THIS DEVICE
2522 MVWI X'0708',SIOIN INITIALIZE THE CONDITION CODES
2523 TUPARM1,R0 GET THE DELAY COUNT
2524 JCT *R0 * AND COUNT IT DOWN TO ZERO
2525 MVW \$I0IN,TURESUL SENT BACK ANY CHANGE IN INTR CONDITION
2526+ TXIT
2527+ B \$CONX RETURN TO MDI CONTROLLER
2528+*****
2529 T3C03 TUIT

LOCTR OBJECT TEXT STMT SOURCE STATEMENT COPYRIGHT IBM CORP 1976
2530+*****06FEB76**
2531+**
2532+** TEST UNIT
2533+**
2534+** DIRECT PROGRAM CONTROL TEST UNIT 14APP76
2535+**
2536+** PURPOSE
2537+**
2538+** THREE PARAMETERS ARE NEEDED FOR THE EXECUTION OF THIS TU AND ARE:
2539+**
2540+** 1. ONE BYTE OF FUNCTION-MODIFIER, IE, X'5F' FOR DPC WRITE,
2541+** 2. TWO BYTES OF DATA TO BE USED IN THE SECOND PART OF THE IDCB,
2542+** - IE, X'0005' TO BE SENT TO THE DEVICE.
2543+**
2544+** THIS TEST UNIT PREPARES THE DEVICE AND EXPECTS AN INTERRUPT
2545+** AND WILL SEND BACK THE CONDITION CODES OF THE I/O AND INTR.
2546+**
2547+** CALLING SEQUENCE
2548+**
2549+** MDI=@TUXX,T3C03,2,0708,EQ,PLNG=6,PRAM=FMXXXX'
2550+**
2551+** RETURN CONTROL
2552+**
2553+** B TURTN* RETURN TO MDI SUPERVISOR
2554+**
2555+*****
2556+T3C03 MVW R7,TURTN SAVE RETURN ADDRESS
2557+ MVWI X'3C03',STUID SAVE TU ID FOR DISPLAY
2558+ MVA OPTN1,R4 SET UP POINTER ADRS IN R4
2559+ BAL \$CONC,R6 CLEAR DEV DEP STG AND CONNECT I/O EL
2560+ DC A(\$ERR\$) ERROR ADRS FOR INVALID PREP
2561+**
2562 MVWI X'0708',SIOIN INIT THE CONDITION CODES
2563 MVW TUPARM1,R1 SET UP PARM ADRS
2564 MVB (R1),T3C03L * AND SET IN FUNCTION-MODIFIER
2565 MVB DEVADD,T3C03L+1 * FOLLOWED BY THE DEVICE ADRS
2566 MVB (R1),T3C03L+2 * AND SET IN EVEN BYTE DATA
2567 MVE (R1),T3C03L+3 * AND SET IN ODD BYTE DATA
2568 MVD T3C03L,RO GET FUNCTION, MODIFIER AND DEV ADRS
2569 *
2570 IO T3C03L ISSUE THE I/O COMMAND AND
2571 DC X'70AE' * GET THE I/O CONDITION CODE IN R5
2572 SFL 13,R5 POSITION CC IN THE RESULTS FIELD
2573 MVB R5,\$I0IN * AND SAVE IT IN THE RESULTS
2574 MVI -1,R5 SET UP FOR DELAY
2575 T3C03K SVC IDLE WAIT FOR INTERRUPT
2576 TETR (R4,IN) HAS IT COME YET
2577 JN T3C03M * YES, GET OUT OF DELAY
2578 JCT T3C03K,R5 * NO, CHECK FOR TIME OUT
2579 T3C03M MVD \$I0IN,TURESUL PUT ANY INTR COND CODE FOUND IN
2580 TXIT * RESULTS AND EXIT
2581+ B \$CONX RETURN TO MDI CONTROLLER
2582+*****
2583 *
2584 * IDCB FOR DIRECT PROGRAM CONTROL CCMAND
2585 *
2586 T3C03L DC X'0000' FUNCTION-MODIFIER-DEVICE ADDRESS
2587 DC X'0000' IMMEDIATE DATA BUFFER
2588 END

DECLARED	NAME	ATTRIBUTES AND REFERENCES
645	N00007	ADDRESS. HEX LOCATION(000026C6) IN CSECT(I4800) LENGTH(2)
648	N00008	ADDRESS. HEX LOCATION(000026CA) IN CSECT(I4800) LENGTH(2)
651	N00009	ADDRESS. HEX LOCATION(000026CE) IN CSECT(I4800) LENGTH(2)
663	N00010	ADDRESS. HEX LOCATION(000026E2) IN CSECT(I4800) LENGTH(2)
666	N00011	ADDRESS. HEX LOCATION(000026E6) IN CSECT(I4800) LENGTH(2)
678	N00012	ADDRESS. HEX LOCATION(000026FC) IN CSECT(I4800) LENGTH(2)
690	N00013	ADDRESS. HEX LOCATION(0000270E) IN CSECT(I4800) LENGTH(2)
702	N00014	ADDRESS. HEX LOCATION(00002720) IN CSECT(I4800) LENGTH(2)
714	N00015	ADDRESS. HEX LOCATION(00002732) IN CSECT(I4800) LENGTH(2)
717	N00016	ADDRESS. HEX LOCATION(00002736) IN CSECT(I4800) LENGTH(2)
729	N00017	ADDRESS. HEX LOCATION(0000274A) IN CSECT(I4800) LENGTH(2)
732	N00018	ADDRESS. HEX LOCATION(0000274E) IN CSECT(I4800) LENGTH(2)
744	N00019	ADDRESS. HEX LOCATION(00002766) IN CSECT(I4800) LENGTH(2)
747	N00020	ADDRESS. HEX LOCATION(0000276A) IN CSECT(I4800) LENGTH(2)
753	N00021	ADDRESS. HEX LOCATION(00002776) IN CSECT(I4800) LENGTH(2)
756	N00022	ADDRESS. HEX LOCATION(0000277A) IN CSECT(I4800) LENGTH(2)
759	N00023	ADDRESS. HEX LOCATION(0000277E) IN CSECT(I4800) LENGTH(2)
771	N00024	ADDRESS. HEX LOCATION(00002792) IN CSECT(I4800) LENGTH(2)
774	N00025	ADDRESS. HEX LOCATION(00002796) IN CSECT(I4800) LENGTH(2)
780	N00026	ADDRESS. HEX LOCATION(000027AC) IN CSECT(I4800) LENGTH(2)
789	N00027	ADDRESS. HEX LOCATION(000027B0) IN CSECT(I4800) LENGTH(2)
801	N00028	ADDRESS. HEX LOCATION(000027C6) IN CSECT(I4800) LENGTH(2)
804	N00029	ADDRESS. HEX LOCATION(000027CA) IN CSECT(I4800) LENGTH(2)
816	N00030	ADDRESS. HEX LOCATION(000027E0) IN CSECT(I4800) LENGTH(2)
819	N00031	ADDRESS. HEX LOCATION(000027F4) IN CSECT(I4800) LENGTH(2)
831	N00032	ADDRESS. HEX LOCATION(000027FA) IN CSECT(I4800) LENGTH(2)
834	N00033	ADDRESS. HEX LOCATION(000027FE) IN CSECT(I4800) LENGTH(2)
846	N00034	ADDRESS. HEX LOCATION(00002814) IN CSECT(I4800) LENGTH(2)
849	N00035	ADDRESS. HEX LOCATION(00002818) IN CSECT(I4800) LENGTH(2)
861	N00036	ADDRESS. HEX LOCATION(0000282E) IN CSECT(I4800) LENGTH(2)
864	N00037	ADDRESS. HEX LOCATION(00002832) IN CSECT(I4800) LENGTH(2)
876	N00038	ADDRESS. HEX LOCATION(00002848) IN CSECT(I4800) LENGTH(2)
879	N00039	ADDRESS. HEX LOCATION(0000284C) IN CSECT(I4800) LENGTH(2)
891	N00040	ADDRESS. HEX LOCATION(00002862) IN CSECT(I4800) LENGTH(2)
894	N00041	ADDRESS. HEX LOCATION(00002866) IN CSECT(I4800) LENGTH(2)
906	N00042	ADDRESS. HEX LOCATION(0000287C) IN CSECT(I4800) LENGTH(2)
909	N00043	ADDRESS. HEX LOCATION(00002880) IN CSECT(I4800) LENGTH(2)
921	N00044	ADDRESS. HEX LOCATION(00002894) IN CSECT(I4800) LENGTH(2)
924	N00045	ADDRESS. HEX LOCATION(00002898) IN CSECT(I4800) LENGTH(2)
936	N00046	ADDRESS. HEX LOCATION(000028AA) IN CSECT(I4800) LENGTH(2)
939	N00047	ADDRESS. HEX LOCATION(000028AE) IN CSECT(I4800) LENGTH(2)
951	N00048	ADDRESS. HEX LOCATION(000028C4) IN CSECT(I4800) LENGTH(2)
954	N00049	ADDRESS. HEX LOCATION(000028C8) IN CSECT(I4800) LENGTH(2)
966	N00050	ADDRESS. HEX LOCATION(000028DE) IN CSECT(I4800) LENGTH(2)
969	N00051	ADDRESS. HEX LOCATION(000028E2) IN CSECT(I4800) LENGTH(2)
981	N00052	ADDRESS. HEX LOCATION(000028F4) IN CSECT(I4800) LENGTH(2)
984	N00053	ADDRESS. HEX LOCATION(000028F8) IN CSECT(I4800) LENGTH(2)
996	N00054	ADDRESS. HEX LOCATION(0000290E) IN CSECT(I4800) LENGTH(2)
999	N00055	ADDRESS. HEX LOCATION(00002912) IN CSECT(I4800) LENGTH(2)
1011	N00056	ADDRESS. HEX LOCATION(00002926) IN CSECT(I4800) LENGTH(2)
1014	N00057	ADDRESS. HEX LOCATION(0000292A) IN CSECT(I4800) LENGTH(2)
1026	N00058	ADDRESS. HEX LOCATION(00002940) IN CSECT(I4800) LENGTH(2)
1029	N00059	ADDRESS. HEX LOCATION(00002944) IN CSECT(I4800) LENGTH(2)
1041	N00060	ADDRESS. HEX LOCATION(00002956) IN CSECT(I4800) LENGTH(2)
1044	N00061	ADDRESS. HEX LOCATION(0000295A) IN CSECT(I4800) LENGTH(2)
1056	N00062	ADDRESS. HEX LOCATION(00002970) IN CSECT(I4800) LENGTH(2)
1059	N00063	ADDRESS. HEX LOCATION(00002974) IN CSECT(I4800) LENGTH(2)

DECLARED	NAME	ATTRIBUTES AND REFERENCES
1071	N00064	ADDRESS. HEX LOCATION(00002988) IN CSECT(I4800) LENGTH(2)
1074	N00065	ADDRESS. HEX LOCATION(0000298C) IN CSECT(I4800) LENGTH(2)
1086	N00066	ADDRESS. HEX LOCATION(000029A2) IN CSECT(I4800) LENGTH(2)
1089	N00067	ADDRESS. HEX LOCATION(000029A6) IN CSECT(I4800) LENGTH(2)
1101	N00068	ADDRESS. HEX LOCATION(000029B8) IN CSECT(I4800) LENGTH(2)
1104	N00069	ADDRESS. HEX LOCATION(000029BC) IN CSECT(I4800) LENGTH(2)
1116	N00070	ADDRESS. HEX LOCATION(000029D2) IN CSECT(I4800) LENGTH(2)
1119	N00071	ADDRESS. HEX LOCATION(000029D6) IN CSECT(I4800) LENGTH(2)
1131	N00072	ADDRESS. HEX LOCATION(000029EA) IN CSECT(I4800) LENGTH(2)
1134	N00073	ADDRESS. HEX LOCATION(000029EE) IN CSECT(I4800) LENGTH(2)
1146	N00074	ADDRESS. HEX LOCATION(00002A00) IN CSECT(I4800) LENGTH(2)
1149	N00075	ADDRESS. HEX LOCATION(00002A04) IN CSECT(I4800) LENGTH(2)
1161	N00076	ADDRESS. HEX LOCATION(00002A16) IN CSECT(I4800) LENGTH(2)
1167	N00077	ADDRESS. HEX LOCATION(00002A22) IN CSECT(I4800) LENGTH(2)
1179	N00078	ADDRESS. HEX LOCATION(00002A34) IN CSECT(I4800) LENGTH(2)
1182	N00079	ADDRESS. HEX LOCATION(00002A38) IN CSECT(I4800) LENGTH(2)
1194	N00080	ADDRESS. HEX LOCATION(00002A4A) IN CSECT(I4800) LENGTH(2)
1200	N00081	ADDRESS. HEX LOCATION(00002A56) IN CSECT(I4800) LENGTH(2)
1212	N00082	ADDRESS. HEX LOCATION(00002A68) IN CSECT(I4800) LENGTH(2)
1215	N00083	ADDRESS. HEX LOCATION(00002A6C) IN CSECT(I4800) LENGTH(2)
1227	N00084	ADDRESS. HEX LOCATION(00002A7E) IN CSECT(I4800) LENGTH(2)
1233	N00085	ADDRESS. HEX LOCATION(00002A8A) IN CSECT(I4800) LENGTH(2)
1245	N00086	ADDRESS. HEX LOCATION(00002AA4) IN CSECT(I4800) LENGTH(2)
1251	N00087	ADDRESS. HEX LOCATION(00002AE0) IN CSECT(I4800) LENGTH(2)
1263	N00088	ADDRESS. HEX LOCATION(00002ACA) IN CSECT(I4800) LENGTH(2)
1269	N00089	ADDRESS. HEX LOCATION(00002AD6) IN CSECT(I4800) LENGTH(2)
58	OF	ABSOLUTE. HEX VALUE(00000202)
57	ON	ABSOLUTE. HEX VALUE(00000200)
1392	OPTN1	ADDRESS. HEX LOCATION(00002EB0) IN CSECT(I4800) LENGTH(2)
1415	OPTN3	ADDRESS. HEX LOCATION(00002EB4) IN CSECT(I4800) LENGTH(2)
101	PARMARA	ADDRESS. HEX LOCATION(0000196E) IN CSECT(I4800) LENGTH(1)
		601 616 628 643 651 676 688 700 712
		727 742 769 784 799 814 829 844 859
		874 889 904 919 934 949 964 979 994
		1009 1024 1039 1054 1069 1084 1099 1114 1129
		1144 1159 1177 1192 1210 1225 1243 1261
69	PID	ADDRESS. HEX LOCATION(00001800) IN CSECT(I4800) LENGTH(1)
		71 72 73 74 75 76 77 78 79
		80 81 82 83 84 85 86 87 88
		89 90 91 92 93 94 95 96 97
		98 99 100 101 102 103 104 105 106
		107 108 109 110 111 112 2378
2413	PIDMSG10	ABSOLUTE. HEX VALUE(0000F1F0)
1509	PREP	ABSOLUTE. HEX VALUE(0000000C)
1897	RDDCB	ADDRESS. HEX LOCATION(000030DE) IN CSECT(I4800) LENGTH(2)
1520	RELSD	ABSOLUTE. HEX VALUE(C0000017)
1519	REQSD	ABSOLUTE. HEX VALUE(00000016)
1505	RESET	ABSOLUTE. HEX VALUE(00000008)
1516	RICB	ABSOLUTE. HEX VALUE(00000013)
1506	RID	ABSOLUTE. HEX VALUE(00000009)
1841	RSDCB	ADDRESS. HEX LOCATION(0000308E) IN CSECT(I4800) LENGTH(2)
1455	SCTID	ADDRESS. HEX LOCATION(00002EBE) IN CSECT(I4800) LENGTH(2)
1848		1995 1996 2386
1852	SKDCB	ADDRESS. HEX LOCATION(0000309E) IN CSECT(I4800) LENGTH(2)
1669		1670 1988
1507	START	ABSOLUTE. HEX VALUE(C000000A)
2098		
104	SUPSTAT	ADDRESS. HEX LOCATION(000019C4) IN CSECT(I4800) LENGTH(1)
2381		
1481	SVCAL	ADDRESS. HEX LOCATION(00002EF2) IN CSECT(I4800) LENGTH(2)
2320		2386 2387
1663	TLMF	ADDRESS. HEX LOCATION(00002F34) IN CSECT(I4800) LENGTH(2)
1664		
92	TUMSGWTR	ADDRESS. HEX LOCATION(000018BA) IN CSECT(I4800) LENGTH(1)
2383		
76	TUPARM1	ADDRESS. HEX LOCATION(0000189A) IN CSECT(I4800) LENGTH(1)
2455		2522 2563
98	TURESUL	ADDRESS. HEX LOCATION(000018C8) IN CSECT(I4800) LENGTH(1)
1649		1650 1651 1652 1653 1654 1681 1682 1683
1684		1697 1698 1699 1700 1701 1702 1765 1766
1767		1768 1769 1770 1777 1778 1779 1780 2473
2480		2481 2524 2579
1479	TURTN	ADDRESS. HEX LOCATION(00002EEB) IN CSECT(I4800) LENGTH(2)

CROSS-REFERENCE LISTING

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DECLARED	NAME	ATTRIBUTES AND REFERENCES
74	TUSTATUS	1640 1759 2391 2448 2513 2556 ADDRESS. HEX LCCATION(00001818) IN CSECT(I4800) LENGTH(1)
75	TUWORK	2361 ADDRESS. HEX LOCATION(0000181A) IN CSECT(I4800) LENGTH(1)
1693	TO1A	2365 2420 ADDRESS. HEX LOCATION(00002F9E) IN CSECT(I4800) LENGTH(2)
1695	TO1B	1668 ADDRESS. HEX LCCATION(00002FA2) IN CSECT(I4800) LENGTH(2)
1681	TO1C	1690 ADDRESS. HEX LOCATION(00002F76) IN CSECT(I4800) LENGTH(6)
1703	TO1ER	1692 1694 1696 1704 ADDRESS. HEX LCCATION(00002FEE) IN CSECT(I4800) LENGTH(2)
1697	TO1R	1676 ADDRESS. HEX LOCATION(00002FA6) IN CSECT(I4800) LENGTH(4)
1689	TO1Z	1644 1674 ADDRESS. HEX LOCATION(00002F92) IN CSECT(I4800) LENGTH(6)
2448	T3C00	1678 ADDRESS. HEX LCCATION(00003476) IN CSECT(I4800) LENGTH(4)
2488	T3C00I	593 608 635 668 734 776 791 806 821 836 851 866 881 896 941 956 1016 1076 ADDRESS. HEX LCCATION(000034E6) IN CSECT(I4800) LENGTH(2)
2475	T3C00N	2456 2457 2458 2459 2460 2462 2470 2480 ADDRESS. HEX LOCATION(000034CE) IN CSECT(I4800) LENGTH(2)
2480	T3C00S	2469 ADDRESS. HEX LOCATION(000034D6) IN CSECT(I4800) LENGTH(6)
2481	T3C00X	2467 2472 2476 ADDRESS. HEX LCCATION(000034DC) IN CSECT(I4800) LENGTH(6)
2513	T3C01	2474 2478 ADDRESS. HEX LOCATION(000034EA) IN CSECT(I4800) LENGTH(4)
1488	T3C02	653 719 911 ADDRESS. HEX LOCATION(00002EF6) IN CSECT(I4800) LENGTH(6)
2556	T3C03	620 680 692 704 761 926 971 1001 1031 1061 1091 1121 1151 1184 1202 1217 1235 1253 ADDRESS. HEX LOCATION(00003514) IN CSECT(I4800) LENGTH(4)
2575	T3C03K	986 1046 1106 ADDRESS. HEX LCCATION(00003556) IN CSECT(I4800) LENGTH(2)
2586	T3C03L	2578 ADDRESS. HEX LOCATION(00003568) IN CSECT(I4800) LENGTH(2)
2579	T3C03M	2564 2565 2566 2567 2568 2570 ADDRESS. HEX LCCATION(0000355E) IN CSECT(I4800) LENGTH(6)
1640	T4801	2577 ADDRESS. HEX LOCATION(00002EFE) IN CSECT(I4800) LENGTH(4)
1759	T4853	1136 ADDRESS. HEX LOCATION(00002FC2) IN CSECT(I4800) LENGTH(4)
1785	T53A	1169 ADDRESS. HEX LCCATION(00003028) IN CSECT(I4800) LENGTH(6)
1789	T53B	1783 ADDRESS. HEX LOCATION(00003038) IN CSECT(I4800) LENGTH(6)
1794	T53C	1787 ADDRESS. HEX LOCATION(0000304A) IN CSECT(I4800) LENGTH(2)
1795	T53D	1790 1792 ADDRESS. HEX LOCATION(0000304C) IN CSECT(I4800) LENGTH(6)
1801	T53X	1793 ADDRESS. HEX LOCATION(0000305A) IN CSECT(I4800) LENGTH(4)
1799	T53Z	1796 1798 ADDRESS. HEX LCCATION(00003058) IN CSECT(I4800) LENGTH(2)
1886	VRDCB	1774 ADDRESS. HEX LOCATION(000030CE) IN CSECT(I4800) LENGTH(2)
1875	WRDCB	2006 ADDRESS. HEX LOCATION(000030BE) IN CSECT(I4800) LENGTH(2)
1431	XE	2009 ABSOLUTE. HEX VALUE(00000024)
1429	XI	2191 2253 ABSOLUTE. HEX VALUE(00000022)
2072	XIO	2097 2238 ADDRESS. HEX LCCATION(000031AA) IN CSECT(I4800) LENGTH(4)
2253	XIOCK	1989 1992 1997 2004 2007 2010 2013 ADDRESS. HEX LOCATION(00003272) IN CSECT(I4800) LENGTH(2)
2260	XIOCO	2107 ADDRESS. HEX LOCATION(00003284) IN CSECT(I4800) LENGTH(2)
2077	XIOCS	2258 ADDRESS. HEX LOCATION(000031B4) IN CSECT(I4800) LENGTH(6)
2262	XIOCV	2269 ADDRESS. HEX LOCATION(00003288) IN CSECT(I4800) LENGTH(2)
2271	XIOCX	2256 ADDRESS. HEX LCCATION(000032A2) IN CSECT(I4800) LENGTH(4)
2146	XIOER	2263 ADDRESS. HEX LOCATION(00003210) IN CSECT(I4800) LENGTH(2)
2081	XIO1	2277 ADDRESS. HEX LOCATION(000031C4) IN CSECT(I4800) LENGTH(4)
2094	XIO2	2016 2073 ADDRESS. HEX LOCATION(000031EA) IN CSECT(I4800) LENGTH(2)
2106	XIO8	2080 ADDRESS. HEX LOCATION(000031FE) IN CSECT(I4800) LENGTH(2)
62	XTRNL	2111 ABSOLUTE. HEX VALUE(00000001) 1165 1198 1231 1249 1267 1273