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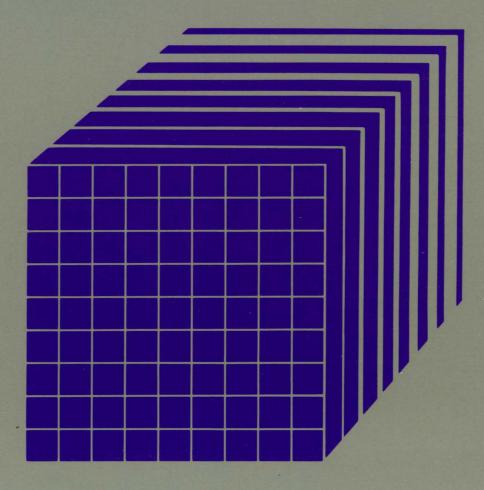


Virtual Machine/ System Product High Performance Option

System Logic and Problem Determination Guide-CP

Release 5

LY20-0897-7



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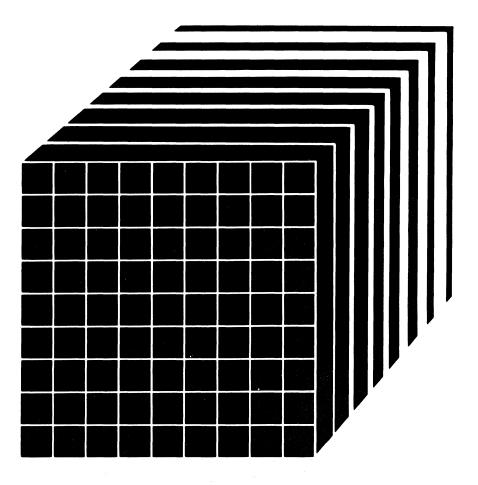
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Virtual Machine/ System Product High Performance Option

System Logic and Problem Determination Guide-CP

Release 5

LY20-0897-7



The term "VM/SP High Performance Option" applies to the VM/SP High Performance Option Licensed Program when used in conjunction with the VM/System Product Licensed Program.

Eighth Edition (August 1987)

This is a major revision of LY20-0897-6. See "Summary of Changes" on page iii for the changes made to this manual. Technical changes or additions made to the text and illustrations for this release are indicated by a vertical line to the left of the change.

This edition applies to Release 5.0 of IBM Virtual Machine/System Product High Performance Option (Program Number 5664-173), and to later releases and modifications until otherwise indicated in new editions or Technical Newsletters. This (eighth) edition is a revision of the seventh edition (Release 4.2).

To order the previous edition that still applies to Release 4.2, use the following temporary order number:

Release 4.2 Seventh Edition LT00-1912

Changes are made periodically to the information herein; before using this publication to operate IBM systems, consult the latest *IBM System/370, 30xx, and 4300 Processors Bibliography*, GC20-0001, for the editions that are applicable and current.

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Summary of Changes

Summary of Changes for LY20-0897-7 as updated August 1987 for VM/SP HPO Release 5.0

SELECTION OF PAGES ON A SYSTEMWIDE "LEAST RECENTLY USED" BASIS

Changed: Programming Support

These changes will improve performance in several ways:

- Improving the memory management of large working sets, shared pages, and the <16 Mb area. The free list becomes the major source for page replenishment. The flush list will be deemphasized. Core table scan becomes the primary method for free list replenishment—the disposable page collector is eliminated.
- Streamlining the QDROP and QADD processes. When a virtual machine drops from queue, its pages will no longer be logically swapped and trimmed.
- Making Expanded Storage (called Paging Storage in this manual) more attractive as a swapping device.
- Preserving interactive response times in CMS intensive environments.
- Simplifying tuning.

SPOOL FILE LIMIT ENHANCEMENTS

Changed: Programming Support

The former limit of 9900 spool files on a system has been removed. With this change, there may exist up to 9900 spool files *for each user*. (Actually, the systemwide maximum is determined by the size of the checkpoint area. At present, this theoretical maximum would be over 100,000 for most systems. It is reasonable to assume that no system would ever approach that amount.)

Spool files will now have a user-unique spool ID as well as a systemwide ID. Reader spool file blocks (SFBLOKs) will now be kept in the virtual storage of a special userid, SYSSPOOL. Printer and punch SFBLOKs will remain in FREE storage. In conjunction with these enhancements, the checkpoint/forced start process has been improved to reconstruct the spool files more rapidly.

A new module, DMKCKR, has been generated.

SCHEDULER ENHANCEMENT

Changed: Programming Support

With this enhancement, virtual machines are moved from the eligible list to the run list only if processor time and sufficient main storage are available. This will help eliminate storage overcommitment and reduce response times.

NEW 'NOQ2' OPTION ON THE 'SET QDROP OFF' COMMAND

New: Programming Support

Specify the SET QDROP OFF NOQ2 command for service virtual machines (like GCS/VSCS) that use system resources in small, frequent bursts. This command will keep that virtual machine in Q1, improving performance for the users of that service.

EXPANDED STORAGE (PAGING STORAGE) ENHANCEMENTS

New: Programming Support

A new macro, SYSXSTOR, is added. This macro controls the allocation of Paging Storage. You may continue to use SYSPAG to allocate Paging Storage, but SYSXSTOR offers these advantages:

- It is easier to use than SYSPAG
- For the 3090 Model 400, it allows you to generate Paging Storage greater than 64 increments.

A new module, DMKXST, has been generated.

AUTOMATIC REORDERING OF SYSPAG AREAS

New: Programming Support

A new parameter is added to the SYSPAG macro,

ORDER = SYSTEM/USER. If ORDER = SYSTEM (the default) is coded, HPO will automatically order the devices allocated on the SYSPAG macro to decrease I/O contention by evenly distributing the I/O activity over the available I/O paths.

ERROR RECORDING ENHANCEMENTS

New: Programming Support

For a 3090 Processor, the channel check handler and machine check handler will now provide more information in the error records. Specifically, these records will now tell you whether or not the hardware error actually affected system performance.

NEW 'NOVF' PARAMETER ON THE 'OPTION' DIRECTORY ENTRY

New: Programming Support

Specifying the NOVF parameter on a user's OPTION control statement in the directory will deny that user access to the Vector Facility.

4381 PROCESSOR COMPLEX MODELS 11, 12, 13, AND 14

Changed: Hardware Support

The 4381 Processor Complex Models 1, 2, and 3 are replaced and extended by the Models 11, 12, 13, and 14.

3090 PROCESSOR COMPLEX MODELS 150, 180, AND 400

Changed: Hardware Support

VM/SP HPO now supports the 3090 Processor Complex Models 150, 180, and 400.

LOGICAL DEVICE HOST LIMIT RELIEF

Changed: Programming Support

This support removes the restriction that no more than 8 virtual machines can create and use logical devices concurrently. Now, any number of virtual machines can create up to 512 logical devices as long as the number of logical devices in the system does not exceed 4096.

3480 VOLUME SERIAL ERROR RECORDING

Changed: Programming Support

Users can now keep track of the error frequency for 3480 tapes by examining the volume serial in 3480 outboard records (OBR) or miscellaneous data records (MDR).

3422 MAGNETIC TAPE SUBSYSTEM

Changed: Programming Support

VM/SP HPO provides programming support for the 3422 Magnetic Tape Subsystem.

TRANSPARENT SERVICES ACCESS FACILITY (TSAF)

New: Programming Support

The transparent services access facility lets users connect to and communicate with local or remote virtual machines within a group of systems. This facility consists of the TSAF virtual machine component, APPC/VM, and two CP system services. APPC/VM is a modified subset of IUCV. With the TSAF virtual machine, it provides services within a single system and throughout a group of systems, unlike IUCV, which provides services only within a single system. The TSAF virtual machine component handles communication between systems by letting APPC/VM paths span more than one system. The QUERY CPTRAP command has also been added.

The following new modules have been generated: DMKCQC, DMKIDR, DMKIUB, DMKIUN, DMKIUP, DMKIUS, DMKTRX, DMKCRM.

NATIONAL LANGUAGES SUPPORT

New: Programming Support

VM/SP HPO now supports a variety of national languages for use in their native countries. Updates have been made to modules and data areas providing this support, specifically, those handling CP messages. The following new modules have been generated: DMKHVF, DMKMES, DMKVBM.

ALTERNATE NUCLEUS SUPPORT

New: Programming Support

Alternate nucleus support makes it easier to create and IPL backup copies of the CP nucleus when the primary nucleus is damaged or unavailable.

PRINTER SUPPORT ENHANCEMENTS

Changed: Programming Support

The printer support enhancements include the addition of a SPOOL system service facility which provides support for a printer subsystem. The DESTination option allows you to select a specific printer or punch to process your print, punch, or console file. Two new DIAGNOSE codes allow a user to specify additional information about a print file. The CMS PRINT command has been enhanced to support an OVersize option and a special carriage control character to allow a longer data line.

LOGON/LOGOFF ENHANCEMENTS

Changed: Programming Support

The LOGON/LOGOFF enhancements improve system availability to users and resolve the problem of conflicting messages during LOGOFF processing. A new module, DMKUSQ, has been generated.

ERROR LOGGING SYSTEM SERVICE

Changed: Programming Support

The error logging system service, a new CP system service, allows a virtual machine to receive a copy of all records currently written to the CP error recording area.

SPOOL FILE COMPRESSION SUPPORT ENHANCEMENT

Changed: Programming Support

An enhancement to SPOOL File Compression Support improves the reliability of transmitting spooled data between systems.

ASCII ENHANCEMENTS

Changed: Programming Support

Various enhancements have been made to the support of ASCII devices.

OTHER MODULE SPLITS

New: Programming Support

Resulting Modules
DMKACR and DMKACS
DMKCCW, DMKCCD, DMKCCF, DMKCCO,
DMKCCS, and DMKCCT
DMKCNS and DMKCNT
DMKCPT and DMKCPN
DMKCQH and DMKCPI
DMKCSU and DMKCSW
DMKCSV, DMKCSX, and DMKCSY
DMKDIA and DMKDIF
DMKGRF, DMKGRD, DMKGRE,
DMKGRF, and DMKGRI
DMKIUA, DMKIUC, DMKIUE, DMKIUB,
DMKIUN, DMKIUP, and DMKIUS
DMKQCN and DMKQCQ
DMKSVC and DMKSVD
DMKTRT, DMKTRU, and DMKTRX
DMKTTY and DMKTTX
DMKVCP and DMKVCU
DMKVDD and DMKVDF

DOCUMENTATION CHANGES

Minor editorial and technical changes have been made throughout this publication.

Summary of Changes for LY20-0897-6 for VM/SP HPO Release 4.2

AUTO-DEACTIVATION OF RESTRICTED PASSWORDS AND DIRECTORY ENHANCEMENTS

New: Programming Support

Adds support to enhance system integrity by minimizing the exposure of unauthorized system access through the use of restricted passwords. The directory enhancements removes the restriction on the number of USER entries that can be defined in the directory. Also, directory PROFILE support provides a means by which installations can optimize the number of commonly repeated control statements in USER entries in the source directory.

ACCESS VERIFICATION ROUTINES

New: Programming Support

While VM/SP HPO provides many security functions, added support for access verification routines provides a standard interface to the RACF/VM Support PRPQ or user-written routines that can provide a higher level of security. Although the access verification routines support does not by itself provide security functions, it allows you to install software that does.

For example, to increase security of minidisk accesses, logon passwords, and movement of spool files, you can install access verification routines with the Resource Access Control Facility (RACF) (Program Number 5740-XXH) and RACF/VM Support PRPQ (Program Number 5767-002).

VECTOR FACILITY

New: Hardware Support

Support is provided for the Vector Facility in System/370 mode configured to a 3090 Processor. The Vector Facility is a synchronous vector/scalar instruction processor that can manipulate values (usually floating-point) at a high speed. Compiled engineer and scientific FORTRAN applications can use the array processing capability of the Vector Facility. VM/HPO supports multiple virtual machines' use of this facility.

PAGE MIGRATION

Changed: Programming support

Page migration is changed to select pages (rather than segments) for migration on a reference basis instead of by time-stamp (age basis). Also, pages are migrated down the demand page hierarchy, instead of being migrated directly to the pre-allocated migration area. This improves the time required to retrieve those pages that become active in the near future. Because migration of swap tables is sometimes necessary even when page migration is not actively moving pages, swap table migration is now invoked independently of page migration (rather than after page migration). Swap table migration is further improved by migrating swap tables regardless of whether all the pages in the segment have been migrated.

DOCUMENTATION CHANGES

Minor technical and editorial changes have been made throughout this publication.

Summary of Changes for LY20-0897-5 for VM/SP HPO Release 4

Note: Release 4 does not support 3090 processors. 3090 processors are supported by Release 3 Modification 6. For information on Release 3 Modification 6, order the manual using the pseudo number shown above.

GROUP CONTROL SYSTEM (VM/SP HPO GCS)

New: Programming Support

This new component of VM/SP HPO is a virtual machine supervisor that provides simulated MVS services and supports a multitasking environment. For more information on the Group Control System (GCS), refer to the VM/SP Group Control System Guide. A new module, DMKVMG, has been generated.

STAND-ALONE DUMP

Changed: Programming Support

The Stand-alone Dump facility is an enhancement to VM/SP HPO Serviceability. It provides the support personnel with capability of dumping up to 16 megabytes of real storage. It is required to dump real storage when VM/SP HPO cannot create a CP abend dump. A new module, DMKSAD, has been generated.

CP FRET TRAP

New: Programming Support

The CP FRET trap can be used as an aid in solving problems caused by improper use of CP storage and to solve many storage overlay problems.

VMDUMP ENHANCEMENTS

Changed: Programming Support

DIAGNOSE Code X'94' is available to allow a virtual machine to request dumping of its virtual storage. Also, the three address range restriction has been removed from the VMDUMP command. A new module, DMKVME, has been generated.

SHARED/NONSHARED RESTRICTION

New: Programming Support

With the addition of this support, any attempt to construct a virtual device configuration that would mix SHARED and NONSHARED device types on the same virtual control unit is rejected.

IUCV Enhancements

Changed: Programming Support

The Inter-User Communications Vehicle (IUCV) has been enhanced to support data movement from discontiguous buffers on the SEND, RECEIVE, and REPLY functions. The IUCV macro has been updated to handle a "BUFLIST =" parameter on the SEND and RECEIVE functions, and an "ANSLIST =" parameter on the SEND and REPLY functions. New modules generated are DMKVCQ and DMKVCS.

CPTRAP ENHANCEMENTS

Changed: Programming Support

CPTRAP is a service aid used in problem determination. Enhancements to the CPTRAP command provide two additional functions, GROUPID and WRAP, and one additional entry type, X'3D'.

Enhancements to TRAPRED makes reviewing the trap data easier by providing more selectivity for X'3D', X'3E', and X'3F' entries and by providing a way to display formatted output of the trapped data. A new module, DMKTRU, has been generated.

TERMINAL ENHANCEMENTS

New: Programming Support

The DIAL command is now supported for SNA devices. A new module, DMKRGE, has been generated.

EXPANSION OF USER CLASSES

Changed: Programming Support

The user class structure has been modified such that the user may now define up to 32 privilege classes, beyond (or in place of) the seven IBM defined privilege classes.

REMOTE SPOOLING COMMUNICATIONS SUBSYSTEM NETWORKING VERSION 2

Changed: Programming Support

With the release of the Remote Spooling Communications Subsystem Networking Version 2 program product (5664-188), any reference to RSCS in this manual applies to RSCS Version 2. Information about RSCS can be found in the VM/SP Remote Spooling Communications Subsystem Version 2 General Information.

VM/SP HPO 3800 MODEL 3 COMPATIBILITY SUPPORT

New: Hardware Support

Compatibility support allows VM/SP users to access the 3800 Model 3 Printing Subsystem. Existing programs designed to produce 3800 Model 1 printer output may produce output for the 3800 Model 3 printer with little or no program change. Use of this support provides improved print quality (240 x 240 pel resolution) and the addition of a 10 lines-per-inch (LPI) vertical space option. The following new modules have been generated: DMKRSF (split from DMKRSE), DMKSEQ (split from DMKSEP), DMKVSX (split from DMKVSP).

3480 MAGNETIC TAPE SUBSYSTEM SUPPORT

New: Hardware Support

The 3480 is a buffered magnetic subsystem consisting of one control unit which can address up to 8 drives, or two control units which can each address up to 16 drives.

3370 DIRECT ACCESS STORAGE MODELS A2 AND B2

New: Hardware Support

Support is added for the 3370 Direct Access Storage Models A2 and B2. A new module, DMKVDB, is generated.

DMKFRE/DMKFRT SPLIT

Changed: Programming Support

The module DMKFRE has been split into two modules, DMKFRE and DMKFRT. DMKFRE handles all requests for free storage and calls to DMKFRET to release free storage. DMKFRT handles all requests to return free storage.

DMKCKP SPLIT

Changed: Programming Support

The module DMKCKP has been split into six modules: DMKCKD, DMKCKF, DMKCKH, DMKCKM, DMKCKN, and DMKCKP.

- DMKCKD is called by DMKCKP and issues HALT I/O instructions to system generated devices and drains existing I/O interrupts. It also finds a usable path to a desired device and executes all processor switch request.
- DMKCKF is called by DMCKP and saves vital system data on the system's warm start cylinders.
- DMKCKH is called by many different routines and contains several utility routines used by the checkpoint program. These routines do tasks such as checking the validity of the warm start cylinders, handling record buffers, saving system spool files, and handling I/O interrupts.
- DMKCKM is called by many different routines and saves selected virtual machines during system shutdown or abend. It also displays messages for the checkpoint program.
- DMKCKN is called by DMKCKM and handles I/O requests for the checkpoint program. It also preprocesses various fields in RDEVBLOKs so that they can be used by DMKCKN and DMKCKM.
- DMKCKP is the main driver for the checkpoint program. It contains the IPL entry point for system initialization, system shutdown, and system abend. It also displays error messages and loads disabled wait state PSWs.

DMKCPI/DMKCPS REORGANIZATION

Changed: Programming Support

The logic of DMKCPI and DMKCPJ has been reorganized to create six new modules: DMKALO, DMKIDU, DMKMNT, DMKMNT, DMKOPE, DMKSEG, DMKTOD.

- DMKALO is called by DMKMNT and builds the system ALOCBLOKs for CP-owned volumes that are online at IPL.
- DMKIDU is called by DMKCPJ and locates DASD space for and allocates the CP DUMP if DASD space is found. It also clears TDISK space at IPL.
- DMKMNT is called by DMKCPI and checks all devices in DMKRIO and marks them online if the device is operational.

- DMKOPE is called by DMKCPI to locate the system operator's console and to disconnect the operator on a system restart if the operator was disconnected before the system abend. It is also called by DMKCPJ to start the auto MONITOR, to autolog users, and to logon the system operator.
- DMKSEG is called by DMKCPI to initialize CP's PAGE, SEGMENT, CORE, and SWAP tables. It is also called by DMKCPJ to page in CP pageable modules and to write the symbol table (DMKSYM) and DMKVMI to DASD.
- DMKTOD is called by DMKCPT to initialize the TOD clock.

3880 STORAGE SUBSYSTEM MODEL 21 SUPPORT

New: Hardware Support

VM/SP HPO now supports the 3880 Storage Subsystem Model 21. The 3880 Model 21 is designed as a high-performance paging and swapping subsystem. It is an enhancement over the 3880 Model 11. In addition, the 3880 Storage Subsystem Model 11 is supported for swapping.

4381 MODEL 3

New: Hardware Support

VM/SP HPO now supports the 4381 Processor Model 3 (dyadic). ECPS:VM/370 level 22 specifically supports the 4381 Model 3. It includes:

- Assists for VM/SP HPO free storage and dispatching algorithms and a CP assist for DIAGNOSE code '18' (Disk I/O).
- Enhancements to existing assists to support the extended real addressing facility of VM/SP HPO.
- Enhancements to existing assists to support a multiprocessor system.

OTHER MODULE SPLITS

New: Programming Support

Modules(s) Being Split	Resulting Modules
DMKCPU	DMKCPM and DMKCPU
DMKCSO	DMKCPM and DMKCPU
DMKCQP	DMKCPM and DMKCPU
DMKEMA, DMKEMB,	DMKEMA, DMKEMB, DMKEMC,
and DMKEMC	DMKEMD, and DMKEME
DMKIOS	DMKIOQ and DMKIOS
DMKLOG	DMKLOG and DMKLOJ
DMKLOH	DMKLOH and DMKLOM
DMKSST	DMKSST and DMKSSV
DMKTAP	DMKTAP and DMKTAQ
DMKVCA	DMKVCA and DMKVCB

Modules(s) Being Split

Resulting Modules

DMKVDG DMKVRR DMKPTS DMKVDG and DMKVDH DMKVRR and DMKVRS DMKPTS and DMKPTT

DOCUMENTATION CHANGES

Minor technical and editorial changes have been made throughout this publication.

Preface

Purpose

This manual provides information needed to analyze problems that may occur on the IBM Virtual Machine/System Product: High Performance Option (VM/SP High Performance Option) when used with VM/System Product Release 5. The descriptions are general and serve as a guide in understanding VM/SP High Performance Option. They are not meant to be a detailed analysis of VM/SP High Performance Option programming and cannot be used as such.

Audience

This manual is intended for persons who are analyzing errors to the Control Program (CP) component of VM/SP High Performance Option. Persons performing this task could be:

- IBM support personnel
- Customer systems programmers.

Organization

This manual contains a logic description for the designated component. It is divided into four parts: Introduction, CP Method of Operation and Program Organization, CP Directories, and CP Diagnostic Aids.

CP Method of Operation and Program Organization contains the functions and relationships of the program routines in VM/SP High Performance Option.

CP Directories contains descriptions of all the assemble modules in CP. It also contains extensive cross-references between modules and labels within a VM/SP High Performance Option component.

CP Diagnostic Aids contains additional information useful for determining the cause of a problem.

Appendix A contains a description of VM/370 Extended Control-Program Support (ECPS:VM/370).

Appendix B describes support for the IBM 3850 Mass Storage System (MSS).

Appendix C contains information about MVS/System Extensions and MVS/System Product Support.

Appendix D describes in detail the access verification routines (DMKRPW, DMKRPI, and DMKRPD).

Related Information

Information on the Conversational Monitor System (CMS) is contained in System Logic and Problem Determination Guide Volume 2 (CMS), Order No. LT00-1604.

Information on the Remote Spooling Communications Subsystem (RSCS), a VM/370 Release 6 component, is contained in VM/370 System Logic and Problem Determination Guide, Volume 3 Remote Spooling Communications Subsystem (RSCS), Order No. SY20-0888.

The control blocks supporting the RSCS Logic are contained in VM/SPData Areas and Control Block Logic- Volume 2 (CMS), Order No. LT00-1606.

Logic Information on the Interactive Problem Control System (IPCS), a VM/370 Release 6 component, is totally contained in VM/SP HPO Service Routines Program Logic, Order No. LY20-0898.

For information on the Group Control System, see the VM/SP Group Control System Guide, Order No. SC24-5249.

For information on VM/VTAM, see the ACF/VTAM Version 3 General Information (for VM/SP), Order No. GC30-3246.

How to Use This Manual

- Isolate the component of VM/SP High Performance Option in which the problem occurred.
- Use the list of restrictions in VM/SP HPO Planning Guide and Reference to be certain that the operation that was being performed was valid.
- Use the directories and use VM/SP High Performance Option Data Areas and Control Block Logic - CP to help you to isolate the problem.
- Use the "CP Method of Operation and Program Organization" sections, if necessary, to understand the operation that was being performed.

Device Terminology

For terms in this manual that are unfamiliar to you, refer to IBM Vocabulary for Data Processing, Telecommunications, and Office Systems, GC20-1699.

For device terminology that may be specific to VM/SP High Performance Option, refer to VM/SP HPO Library Guide.



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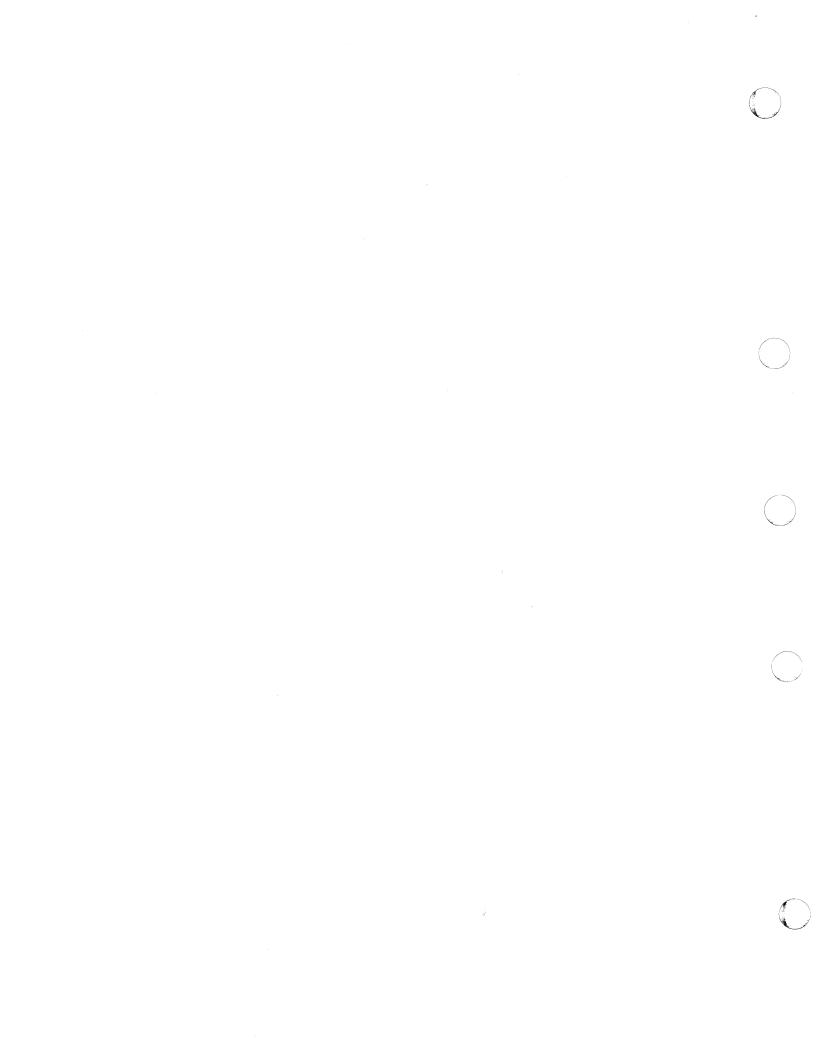
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Introduction

This part contains the following information:

- Introduction to the Control Program (CP)
- Program States
- Using Processor Resources
- Functional Information
- Performance Guidelines
- Interrupt Handling.

The Control Program (CP)

The VM/SP High Performance Option (VM/SP HPO) Control Program manages the resources of a single computer so that multiple computing systems appear to exist. Each "virtual" computing system, or virtual machine, is the functional equivalent of a single processor IBM System/370.

A virtual machine is configured by recording appropriate information in a directory called the *system directory*. The virtual machine configuration includes counterparts of the components of a real IBM System/370:

- A virtual operator's console
- Virtual storage
- A virtual processor
- Virtual I/O devices.

CP makes these components appear real to whichever operating system is controlling the work flow of the virtual machine.

The virtual machines operate concurrently via multiprogramming techniques. CP overlaps the idle time of one virtual machine with execution in another.

Each virtual machine is managed at two levels:

- An operating system manages the work the virtual machine is to do.
- The control program (CP) manages the concurrent execution of multiple virtual machines.

Some system functions perform differently when running in attached processor mode or multiprocessor mode. For a description of the additional processing performed when in attached processor mode, see "Virtual Machine Storage Management" on page 5.

Introduction to the Control Program

A virtual machine is created for a user when he logs on VM/SP HPO, on the basis of information stored in his system directory entry. The entry for each user identification includes a list of the virtual input/output devices associated with the particular virtual machine. Additional information about the virtual machine that also is kept in the directory entry includes:

- A list of the virtual I/O devices associated with that virtual machine
- The command privilege class
- Accounting data
- Normal and maximum virtual storage sizes
- Dispatching priority
- Optional virtual machine characteristics, such as extended control mode.

The Control Program supervises the execution of virtual machines by (1) permitting only problem state execution except in its own routines, and (2) receiving control after all real computing system interrupts. Generally, CP intercepts each privileged instruction¹ and simulates it if the current program status word of the issuing virtual machine indicates a virtual supervisor state; if the virtual machine is executing in virtual problem state, the attempt to execute the privileged instruction is reflected to the virtual machine as a program interrupt. All virtual machine interrupts (including those caused by attempting privileged instructions) are first handled by CP, and are reflected to the virtual machine if an analogous interrupt would have occurred on a real machine.

Virtual Machine Time Management

The real processor simulates multiple virtual processors. Virtual machines that are executing in a conversational manner are given access to the real processor more frequently than those that are not; these conversational machines are assigned the smaller of two possible time slices. CP determines execution characteristics of a virtual machine at the end of each time slice on the basis of the recent frequency of its console requests or terminal interrupts. The virtual machine is queued for subsequent processor utilization according to whether or not it is a conversational user of system resources.

A virtual machine can gain control of the processor only if it is not waiting for some activity or resource. The virtual machine itself may enter a virtual wait state after an input/output operation has begun. The virtual machine cannot gain control of the real processor if it is waiting for a page of storage, if it is waiting for an input/output operation to be translated and started, or if it is waiting for a CP command to finish execution.

A virtual machine can be assigned a priority of execution. Priority is a parameter affecting the execution of a particular virtual machine as compared with other virtual machines that have the same general execution characteristics. It is a parameter in the virtual machine's directory entry. The system operator can reset the value with the privilege class A SET command.

When preferred machine assist is active, the MVS virtual machine runs in the real supervisor state in the V = R area. Also, when an MVS virtual machine runs in single processor mode and preferred machine assist in an AP, MP, or dyadic processor, it runs in the supervisor state.

Virtual Machine Storage Management

The normal and maximum storage sizes of a virtual machine are defined as part of the virtual machine configuration in the directory. The user can redefine virtual storage size to any value that is a multiple of 4K and not greater than the maximum defined value. VM/SP HPO implements this storage as virtual storage. The storage may appear as paged or not paged to the virtual machine, depending upon whether or not the extended control mode option was specified for that virtual machine. This option is required if operating systems that control virtual storage, such as OS/VS1, VM/SP HPO, or VM/370, are run in the virtual machine.

Storage in the virtual machine is logically divided into 4096-byte areas called *pages*. Contiguous 64K areas of virtual storage are called *segments*. A complete set of segment and page tables is used to describe the storage of each virtual machine. These tables are updated by CP and reflect the allocation of virtual storage pages to blocks of real storage. These page and segment tables allow virtual storage addressing in a System/370 machine. Storage in the real machine is logically and physically divided into 4096-byte areas called *page frames*.

If the virtual machine is executing in extended control mode with translate on, then two additional sets of segment and page tables are kept. The virtual machine operating system is responsible for mapping the virtual storage created by it to the storage of the virtual machine. CP uses this set of tables in conjunction with the page and segment tables created for the virtual machine at logon time to build *shadow* page tables for the virtual machine. These shadow tables map the virtual storage created by the virtual machine operating system to the storage of the real computing system. The tables created by the virtual machine operating system may describe any page and segment size permissible in the IBM System/370.

When there is a shortage of real storage available, CP keeps only referenced virtual storage pages in real storage. CP can bring pages into any available page frames. During program execution, a combination of CP and dynamic address translation on the System/370 relocates a page. (During relocation, new absolute addresses are assigned to a page so that the program can execute in the assigned area of real storage.) The active pages from all logged-on virtual machines and from the pageable routines of CP compete for available page frames. When the number of page frames available for allocation falls below a threshold value, CP determines which virtual storage pages currently allocated to real storage are relatively inactive, and starts page-out operations for them.

Inactive pages are kept on a direct access storage device (DASD). DASD space is managed so that only one copy of a given page exists at one time. CP assigns certain inactive pages to a paging device according to the following rules:

• If the page was referenced during virtual machine execution, and if space is available in a TYPE = SW area, the page is assigned to that area (regardless of whether or not the page was changed during virtual machine execution).

- If the page was referenced and changed during virtual machine execution, and no space is available in a TYPE = SW area, the page is assigned a TYPE = PP area.
- If the page was changed during virtual machine execution but not referenced, it is assigned to a TYPE = PP area.
- Note: If a page is read in from TYPE = SW, it is marked as "changed" because the backup copy in the TYPE = SW area is released once the page is read in.

(See the heading "DASD and Paging Storage Management" for more information.)

A virtual machine program can use the DIAGNOSE instruction to tell CP that the information from specific pages of virtual storage is no longer needed. CP then releases the DASD areas that were assigned to hold the specified pages.

CP can prepage a number of virtual machine pages that were resident and referenced at the last queue drop. It prepages when a virtual machine is added to queue.

When a page fault is resolved for a page in a swap set, all the pages in the swap set are paged in. While CP is paging for one virtual machine, another virtual machine can be executing. Any paging operation started by CP is transparent to the virtual machine.

Storage and Processor Utilization

The system operator can assign reserved page frames to multiple virtual machines by using the SET RESERVE command. This command assigns to a virtual machine a specific amount of storage from the real machine, enabling the specified number of a virtual machine's active pages to remain in real storage. CP will dynamically build up a set of reserved real storage page frames for a virtual machine during its execution until the maximum number of reserved frames is reached. The page frames that are reserved are defined by core table scan, so that referenced pages are more likely to be held in storage than unreferenced pages. See the VM/SP HPO Operator's Guide for more information on the SET RESERVE command.

During CP system generation, the installation may specify an option called virtual = real. With this option, the virtual machine's storage is allocated directly from real storage at the time the virtual machine logs on (if it has the VIRT = REAL option in its directory). All pages except page zero are allocated to the corresponding real storage locations. In order to control the real computing system, real page zero must be controlled by CP. Consequently, the real storage size must be large enough to accommodate the CP nucleus, the entire virtual = real virtual machine, and the remaining pageable storage requirements of CP and the other virtual machines.

The virtual = real option improves performance in the selected virtual machine since it removes the need for CP paging operations for the selected virtual machine. The virtual = real option is necessary whenever programs

that contain dynamically modified channel programs (except those of OS ISAM and OS/VS TCAM Level 5) are to execute under control of CP. For additional information on running systems with dynamically modified channel programs, see the VM/SP HPO CP for System Programming.

Virtual Storage Preservation

CP tries to preserve the contents of a virtual machine if the system operator forces the machine off the system, if the system abnormally terminates the machine, or if the system itself abnormally terminates.

At system generation time, you can specify which virtual machines are to be saved. The contents of these virtual machines are written out and saved in DASD space that must be previously allocated during system generation; the sequence in which virtual machines are saved can also be established. If a sequence for saving systems is not defined, then the systems are saved in the order in which virtual storage preservation was invoked for each. After the user logs on to the system again, the saved DASD area is restored by issuing the IPL command, specifying the name of the defined DASD area. System generation parameters also allow another designated user to IPL the named SAVESYS area.

Either the VMSAVE directory option or the SET VMSAVE command may be used for saving the contents of a specific virtual machine. The VMSAVE facility can be nullified by SET VMSAVE OFF, SYSTEM CLEAR, DEFINE STORAGE, or normal LOGOFF.

The V=R area (if active) of the real machine is preserved if the system is performing a warm start. The V=R area is cleared if the system terminates to a hard wait state or if a different V=R user logs on.

You can specify multiple VMSAVE target areas (areas in which the virtual machine is to be saved) for a single user; you do this by including in the DMKSNT module more than one NAMESYS macro with the same USERID = operand. Different target areas are required if a user wishes to IPL a VMSAVE system and have the VMSAVE option enabled at the same time. Once the VMSAVE is enabled, the IPL command cannot refer to the area until a recovery operation has taken place. Similarly, if a VMSAVE area currently contains a saved system, it can be released only by the user who caused the system to be stored there. Until the user releases that area, no other user can use it as a VMSAVE target area.

For more information on the VMSAVE facility, refer to VM/SP HPO Planning Guide and Reference and VM/SP HPO CP for System Programming.

MVS/SP V = R Virtual Machine Recovery

When the system performs an automatic warm start due to an abnormal termination, CP recovers the MVS/SP V=R virtual machine. VM/SP HPO saves the status of the MVS/SP virtual machine environment after CP software ABENDs. MVS/SP virtual machine recovery restores pending

interrupts, dedicated I/O devices, I/O control blocks, real storage, and SPMODE (including AP/MP support if active). All open spool files are closed and checkpointed as in a normal system shutdown.

The recovered V=R machine will have its **original** directory characteristics. For example, if a different storage size was defined after logon, the original directory storage size will be in effect after recovery.

When recovery is completed, the MVS/SP virtual machine immediately resumes execution. However, the virtual machine is in a disconnected state.

CP saves the MVS virtual machine after an ABEND if the following are true:

- Dump to disk was specified using the SET DUMP AUTO command
- NOTRANS is set ON in the V = R machine
- The MVS/SP V = R machine is logged on at system ABEND
- Module DMKVRR is present in the system.

MVS/SP virtual machine recovery is not possible if CP is unable to dump, checkpoint, and re-IPL the system. If the system operator requests a dump using RESTART, recovery may be only partially successful. IUCV, VMCF, and SNA applications are not recovered for the MVS/SP virtual machine operating system.

IBM recommends that the guest operating system operator's console be separate from the virtual machine operator's console so that no console output is lost. All devices the guest virtual machine uses except virtual console and virtual spool devices should be dedicated devices with real addresses corresponding to virtual addresses. I/O interrupt data is saved only for dedicated devices.

Virtual Machine I/O Management

A real disk device can be shared among multiple virtual machines. Virtual device sharing is specified in the directory entry or by a user command. If specified by the user, an appropriate password must be supplied before gaining access to the virtual device. A particular virtual machine may be assigned read-only or read/write access to a shared disk device. CP checks each virtual machine input/output operation against the parameters in the virtual machine configuration to ensure device integrity.

Virtual Reserve/Release support can be used to further enhance device integrity for data on shared *minidisks*. Reserve/Release operation codes are simulated on a virtual basis for minidisks, including full-extent minidisks. For details on Reserve/Release support, refer to "Reserve/Release" under "Scheduling I/O Requests" in this part.

The virtual machine operating system is responsible for the operation of all virtual devices associated with it. These virtual devices may be defined in the directory entry of the virtual machine, or they may be attached to (or detached from) the virtual machine's configuration, dynamically, for the duration of the terminal session. Virtual devices may be dedicated, as when

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mapped to a fully equivalent real device; shared, as when mapped to a minidisk or when specified as a shared virtual device; or spooled by CP to intermediate direct access storage.

There is a limit to the number of virtual devices that can be defined for a virtual machine. This limit is established by the MAXDEV xxxx option in the machine's directory. The maximum limit is obtained from the formula:

Maximum Device Limit = 7FFF/VDEVSIZE + 1

There is, however, one restriction: the device limit for a BC mode virtual machine cannot be greater than 1536.

If the MAXDEV xxxx option is not specified in a machine's directory, the device limit is 410.

Consult VM/SP HPO CP for System Programming for current values of VDEVSIZE and the maximum and default device limits.

In a real machine running under control of OS, input/output operations are normally initiated when a problem program requests OS to issue a START I/O instruction to a specific device. Device error recovery is handled by the operating system. In a virtual machine, OS can perform these same functions, but the device address specified and the storage locations referenced will both be virtual. It is the responsibility of CP to translate the virtual specifications to real. Using the Diagnose '98' command, a virtual machine can execute its own real channel programs. See VM/SPHPO CP for System Programming for more information.

In attached processor or multiprocessor environments, virtual I/O can be initiated by either processor; in attached processor systems, all real I/O requests must be executed by the main processor and all I/O interrupts must be received on the main processor (the processor with I/O capability). Any I/O requests by the attached processor (the processor without I/O capability) are transferred to the main processor. In a multiprocessor system, real I/O can be handled by both processors as both processors have I/O capability.

In addition, the interrupts caused by the input/output operation (including channel errors) are reflected to the virtual machine for its interpretation and processing. If input/output errors occur, CP records them but does not initiate error recovery operations. The virtual machine operating system must handle error recovery, but does not record the error (if SVC 76 is used).

Input/output operations initiated by CP for its own purposes (paging and spooling) are performed directly and are not subject to translation.

See Appendix B for an explanation of additional processing when the virtual I/O request results in a real I/O request to an MSS 3330V volume.

Dedicated Channels

In most cases, the I/O devices and control units on a channel are shared among many virtual machines as minidisks and dedicated devices, and shared with CP system functions such as paging and spooling. Because of this sharing, CP has to schedule all the I/O requests to achieve a balance between virtual machines. In addition, CP must reflect the results of the subsequent I/O interruption to the appropriate storage areas of each virtual machine.

By specifying a dedicated channel (or channels) for a virtual machine via the Class B ATTACH CHANNEL command, the CP channel scheduling function is bypassed for that virtual machine. A virtual machine assigned a dedicated channel has that channel and all of its devices for its own exclusive use. CP translates the virtual storage locations specified in channel commands to real locations and performs any necessary paging operations, but does not perform any device address translations. The virtual device addresses on the dedicated channel must match the real device addresses; thus, a minidisk cannot be used.

Spooling Functions

A virtual unit record device that is mapped directly to a real unit record device is said to be dedicated. The real device is then controlled completely by the virtual machine's operating system.

CP facilities allow multiple virtual machines to share unit record devices. Since virtual machines controlled by CMS ordinarily have modest requirements for unit record input/output devices, such device sharing is advantageous, and it is the standard mode of system operation.

Spooling operations cease if the direct access storage space assigned to spooling is exhausted, and the virtual unit record devices appear in a not-ready status. The spooling operator may make additional spooling space available by using the class D SPTAPE command to dump output spool files to tape. He can also use the SPTAPE command to retrieve those files from the tape for output processing when spooling space requirements are not critical. See the description of the SPTAPE command in the VM/SP HPO Operator's Guide for further information. In an extreme situation, the system operator may make additional spooling space available by purging existing spool files or by assigning additional direct access storage space to the spooling function.

Specific files can be transferred from the spooled card punch or printer of a virtual machine to the card reader of the same or another virtual machine. Files transferred between virtual unit record devices by the spooling routines are not physically punched or printed. With this method, files can be made available to multiple virtual machines, or to different operating systems executing at different times in the same virtual machine.

CP spooling includes options for the virtual machine user and the real machine operator. These options include printing multiple copies of a single spool file, backspacing any number of printer pages, and defining spooling forms and classes and for the scheduling of real output. Each

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output spool file has associated with it a 136-byte area known as the spool file tag. The information contained in this area and its syntax are determined by the originator and receiver of the file. Both programs expect to find the destination identification in the file tag. Tag data is set, changed, and queried using the CP TAG command.

It is possible to spool terminal input and output. All data sent to the terminal, whether it be from the virtual machine, the control program, or the virtual machine operator, can be spooled. Spooling is particularly desirable when a virtual machine is run with its console disconnected. Console spooling is usually started via the command

SPOOL CONSOLE START

An exception to this is when a system operator logs on using a graphics device. In this instance, console spooling is automatically started and continues in effect even if the system operator should disconnect from the graphics device and log on to a nongraphic device. In order to stop automatic console spooling, the system operator must issue the command

SPOOL CONSOLE STOP

Spool File Recovery

If the system should suffer an abnormal termination, there are three degrees of recovery for the system spool files: warm start (WARM), checkpoint start (CKPT), and force start (FORCE). Warm start is automatically invoked if SET DUMP AUTO is in effect. Otherwise, the choice of recovery method is selected when the following message is issued:

Start ((WARM|CKPT|FORCE|COLD)(DRAIN))|(SHUTDOWN):

Note that a cold (COLD) start does not recover any spool files.

Warm Start

After a system failure, the warm start procedure copies the following data to the warm start area on an auxiliary DASD:

- Print spool files
- Punch spool files
- Open reader spool files
- Reader hash table (plus extension pages)
- SYSSPOOL's virtual storage
- Accounting data
- System message data.

When the system is reloaded, this information is retrieved and restored to its original status. If the warm start procedure cannot be implemented, because certain required areas of storage are invalid, the operator is notified to take other recovery procedures.

Checkpoint Start

Any new or revised status of spool file blocks, spooling devices, and spool hold queue blocks is dynamically copied to checkpoint area on an auxiliary DASD as it occurs. When a checkpoint (CKPT) start is requested, the information is used to re-create the users' spool file chains. It differs from warm start data in that only spool file data is restored—accounting and system messages information is not recovered. Also, the order of spool files on any particular restored chain is not the original sequence but a random one.

Force Start

A force start is required when checkpoint start encounters I/O errors while reading files, or invalid data. The procedure is the same as for checkpoint start except that unreadable or invalid files are erased. They cannot be recovered.

CP Commands

The CP commands allow you to control the virtual machine from the terminal, much as an operator controls a real machine. Each CP command is defined by a COMMD macro entry in module DMKCFC. Entries for logged-on users are placed beyond label COMNBEG1. Module DMKCMD also contains COMMD macro entries for subcommands. The COMMD macro has parameters defining command or subcommand name, class, type, entry point label, and the label of valid subcommands in DMKCMD.

Virtual machine execution can be stopped at any time by use of the terminal's attention key (for 3066 and 3270 terminals, the ENTER key is used); it can be restarted by entering the appropriate CP command. External, attention, and device ready interrupts can be simulated on the virtual machine. Virtual storage and virtual machine registers can be inspected and modified, as can status words such as the PSW and the CSW. Extensive trace facilities are provided for the virtual machine, as well as a single-instruction mode. Commands are available to invoke the spooling and disk sharing functions of CP.

CP commands are classified by privilege classes. The directory entry for each user assigns one or more privilege classes. The IBM-supplied classes are primary system operator (class A), system resource operator (class B), system programmer (class C), spooling operator (class D), system analyst (class E), service representative (class F), and general user (class G). Commands in the system analyst class may be used to inspect real storage locations, but may not be used to make modifications to real storage. Commands in the operator class provide real resource control capabilities. System operator commands include all commands related to virtual machine performance options, such as assigning a set of reserved page frames to a selected virtual machine. For descriptions of all the CP commands, see the VM/SP HPO CP Command Reference and the VM/SPHPO Operator's Guide.

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You can extend the eight IBM-defined classes to up to 32 classes by adding an optional Class Control statement to the Directory. See VM/SP HPO CP for System Programming and the VM/SP HPO Planning Guide and Reference for more information.

CP Messages/National Language Support

CP dynamically builds some of its messages in modules; these messages are then issued directly from the modules. CP, however, issues most of its messages from a single static repository file.

CP dynamically creates a table for each language that a user sets. These tables determine the page of the message repository that contains the text associated with a particular message. Each table contains the range of message IDs on each page of the repository and the corresponding virtual address for that page.

The LANGBLOK contains the message table for a particular language. One LANGBLOK exists for each language, and these are chained together. A user's VMBLOK points to the LANGBLOK for the language set for the user's virtual machine session. Changing a language means changing the LANGBLOK pointer in the user's VMBLOK.

During system initialization, CP creates the LANGBLOK for the installation default language's message repository. This repository is part of the CP nucleus, so virtual storage has already been allocated from the SYSTEM VMBLOK for the pages of the repository. The installation default language is always the first LANGBLOK in the LANGBLOK chain.

To handle an additional message repository for another language, CP creates a new VMBLOK through the virtual buffer manager. This VMBLOK has the userid LANGUAGE; it allows CP to access enough virtual storage for the message repository pages.

Unlike the repository in the nucleus, message repositories defined in the System Name Table (DMKSNT) must have virtual storage specifically allocated. When building the LANGBLOK for a message repository defined in the System Name Table, CP allocates a virtual page from the LANGUAGE VMBLOK for each page of the message repository on DASD. CP then saves the virtual addresses in the LANGBLOK and initializes the swap table entry for each virtual page using the DASD address indicated in DMKSNT.

When a message is to be issued for a user, DMKERM finds the appropriate LANGBLOK. It then uses the table in LANGBLOK to get the virtual address of the repository page containing the message text. For the installation default language, CP uses information in the SYSTEM VMBLOK's page and swap tables to bring messages into real storage. For other supported languages, CP uses information in the LANGUAGE VMBLOK's page and swap tables to bring messages into real storage.

The CP Message Repository

The message compiler, the GENMSG command, generates a pageable text file from a message repository source file. The message repository text file consists of:

- A single general information page
- A variable number of data pages.

Information Page: The first page of a repository contains general information about the repository. This information page is made up of an identifier, a header, and table entries. CP uses the information on this page to build various control blocks.

Figure 1 shows the layout of the information page.

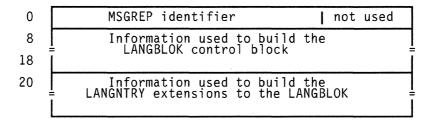


Figure 1. Information Page for a Message Repository

• Identifier

The compiler sets the first six bytes of the information page to "MSGREP". This identifies the file as a message repository. The next two bytes are unused.

Header

This information is used to build the control block for a language, LANGBLOK. The compiler initializes this portion of the repository as follows:

Label	Value
LANGNEXT	0
LANGPAGE	Number of data pages in the file (which corresponds to the number of table entries on the information page).
LANGFLAG	X'80' for a DBCS repository X'00' otherwise
LANGLOCK	0

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Label	Value
LANGLANG	5-character langid, left-justified, padded with blanks
LANGVMBK	0
All reserved fields	0

CP initializes or modifies LANGNEXT, LANGFLAG, LANGLOCK, and LANGVMBK as appropriate when it builds the LANGBLOK control block for this repository.

• Table entries

These are used to build LANGNTRY extension on the LANGBLOK. The compiler generates one table entry for each data page in the repository; CP then builds one LANGNTRY extension to the LANGBLOK for each table entry on this page.

The compiler initializes each table entry as follows:

Label	Value
LANGLOW	Message-ID of the lowest number stored on the data page
LANGHIGH	Message-ID of the highest number stored on the data page
LANGADDR	0

CP initializes LANGADDR when it builds the LANGNTRY extension.

Data Pages: All pages following the repository page are data pages. Data pages contain CP error messages and responses. DMKERM references these data pages in order to display CP messages. Each data page consists of a header, message entries, and index entries. (Refer to Figure 2.)

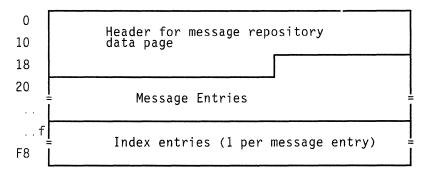


Figure 2. Data Page for a Message Repository

• Header

The header on repository data pages is mapped by REPHEAD DSECT in module DMKERM.

Label	Value
REPREP	String identifying the page as part of the message repository.
REPLANG	String indicating the language. This should be 5 bytes, with the language left-justified and padded with blanks.
REPAPPL	String indicating this is a CP repository. The compiler sets this to DMK for a CP message repository.
REPSUB	Substitution character that was specified in the message repository source file.
REPHDRLN	Number of digits to display in the error message. (It is specified in the repository source file; however, CP ignores this value and always displays a 3-digit message number.)
REPCNT	Number of message entries contained on this data page.
REPINXPT	Displacement from the start of the 4K page to the index portion of the data page.
REPTXTPT	Displacement from the start of the 4K page to the text of the first message on this data page.
REPDBCS	Indicates whether the repository is DBCS $(X'80')$ or not $(X'00')$.

• Message entries

Message entries are grouped together and follow immediately after the header. Message entries are variable in length; they consist of:

- An action character (1 byte)
- A length field, which indicates the length of the message text (1 byte)
- Message text.

• Index entries

Index entries on repository data pages are grouped together and follow the message entries. There is one index entry for each message entry on the data page. Index entries are mapped by the MSGINDEX DSECT in module DMKERM. They consist of:

- A message identifier
 - Message number (2 bytes)
 - Format number (1 byte)
 - Line number (1 byte).

For example, X'00050201' is message 5, format 2, line 1.

- The displacement from the start of the 4K page to the message entry on the data page.

Module DMKERM

Many CP modules call DMKERM to display error messages. Module DMKERM builds the message-ID from the input parameters and gets the address of the LANGBLOK that identifies the message repository used for the virtual machine. (The VMBLOK contains this LANGBLOK address.)

DMKERM then:

- Scans the LANGNTRY extensions to determine which data page of the message repository has the message to be displayed
- Pages in the repository data page containing the message
- Does a binary search on the index to find the index entry for the message to be displayed
- Gets the displacement to the message from the index entry
- Calculates the address of the message
- Sets up the message to be displayed.

Note: The message repository object file maintains 4K page boundaries:

- Message texts do not cross page boundaries.
- All lines of a multiple line message are on the same 4K page.
- Not all formats for a given message have to be on the same 4K page.

Program States

When instructions in the Control Program are being executed, the real computer is in the supervisor state; at all other times, when running virtual machines, the real computer is in the problem state. Therefore, privileged instructions cannot be executed by the virtual machine. Programs running on a virtual machine can issue privileged instructions; but such an instruction either (1) causes an interruption that is handled by the Control Program, or (2) is intercepted and handled by the processor, if the virtual machine assist feature or VM/370 Extended Control-Program Support is enabled and supports that instruction. CP examines the operating status of the virtual machine PSW. If the virtual machine indicates that it is functioning in supervisor mode, the privileged instruction is simulated according to its type. If the virtual machine is in problem mode, the privileged interrupt is reflected to the virtual machine.

The control program and the MVS/SP virtual machine using preferred machine assist may operate in supervisor state on the real processor. All other programs operate in problem state. The hardware microcode routes interrupts for the preferred virtual machine directly to this virtual machine. All other user interrupts, including those generated by privileged operations, are handled by either the control program or the processor (if the processor has the virtual machine assist feature or VM/370 Extended Control Program Support is available). Only those interrupts that the user program would expect from a real machine are reflected to it. A problem program will execute on the virtual machine in a manner identical with its execution on a real System/370 processor, as long as it does not violate the CP restrictions. See the VM/SP HPO Planning Guide and Reference for a list of the restrictions.

Using Processor Resources

CP allocates the processor resource to virtual machines according to their operating characteristics, priority, and the system resources available.

Virtual machines are dynamically categorized at the end of each *time slice* as interactive or noninteractive, depending upon the frequency of operations to or from either the virtual system console or a terminal controlled by the virtual machine.

Virtual machines are dispatched from one of three queues, called Queue 1, Queue 2, and Queue 3. In order to be dispatched from one of these queues, a virtual machine must be considered executable (that is, not waiting for some activity or for some other system resource). Virtual machines are not considered dispatchable if the virtual machine:

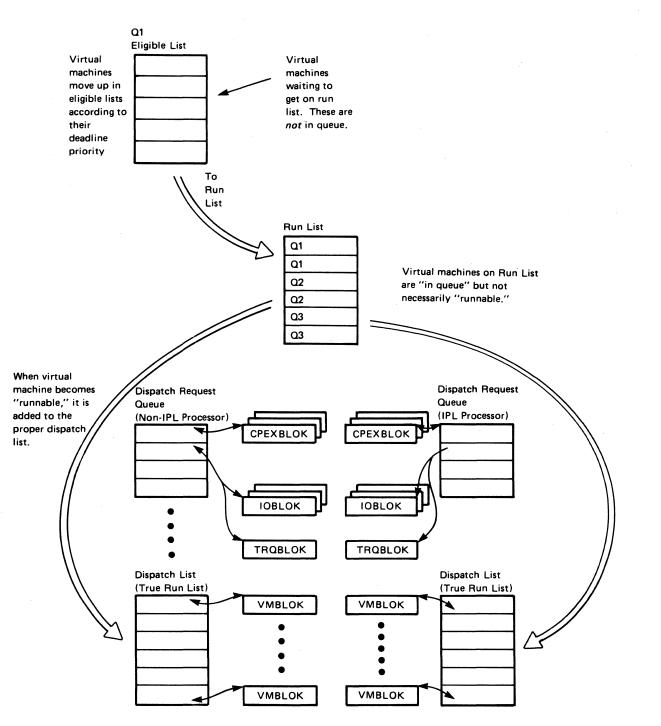
- Enters a virtual wait state after an I/O operation has begun
- Is waiting for a page frame of real storage
- Is waiting for an I/O operation to be translated by CP and started
- Is waiting for CP to simulate its privileged instructions
- Is waiting for a CP console function to be performed.

Selecting a Virtual Machine to Run

CP uses several queues and lists to determine which CP task or virtual machine should next receive a time slice from the processor. Figure 3 shows the relationship between these queues and lists, which are:

- The eligible list, which contains virtual machines waiting to be added to the run list. Virtual machines on the eligible list are not considered to be in queue.
- The run list, which is a list of virtual machines that are considered in queue but not necessarily runnable.
- The dispatch request queue, which contains pointers to CP tasks like CPEXBLOKs, IOBLOKs, and TRQBLOKs. In AP and MP systems, there is a dispatch request queue for each processor.
- The dispatch list, also called the true run list, which contains pointers to virtual machines that are both in queue and runnable. In AP and MP systems, there is a dispatch list for each processor.

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CPEXBLOKs are for CP tasks, IOBLOKs are for I/O tasks, TRQBLOKs are for timer requests, and VMBLOKs are for virtual machines.



Queue 1

Virtual machines in Q1 are considered conversational or interactive users and enter this queue when an interrupt is reflected to the virtual machine user after it has been idle for at least 300 milliseconds. The Q1 virtual machines are ordered in the dispatch list by their deadline priorities. A deadline priority is a value calculated by the scheduler every time a user is dropped from a queue (queue drop time). This value is based on paging activity, processor usage, the load on the system, and user priority. Deadline priority is used to determine when the virtual machine receives its next time slice.

A Q1 virtual machine will usually have a better (earlier) deadline priority than a Q2 virtual machine. For information on how CP calculates deadline priorities, refer to "Dispatching and Scheduling" under "CP Program Organization" in the second part of this manual.

When a virtual machine completes its time slice, it is dropped from the run list (dropped from queue) and placed on an eligible list. A virtual machine that enters CP command mode is also dropped from the run list.

When a write is pending to a Q1 or Q2 virtual machine's console, the scheduler may keep it in queue when the virtual machine would otherwise be dropped. The scheduler uses a 300-millisecond delay to eliminate queue drops for such virtual machines.

Queue 2

Virtual machines in Q2 can be either interactive or noninteractive. In CP mode, Q1 virtual machines are normally dispatched before Q2 virtual machines. This means that CMS users entering commands that do not involve disk or tape I/O operations should get fast responses from the system even if the system has many Q1 virtual machines. Some virtual machines in Q2 are dispatched before virtual machines in Q1 because of their user priority, current resource level, or for other reasons.

For Q1 virtual machines, the user bias factor is divided by 8 because the Q1 time slice is 1/8th the Q2 time slice. Compared with a Q2 virtual machine, a Q1 virtual machine receives 1/8th the amount of processor time, eight times as often. Operating constantly in either queue, a virtual machine should receive the same amount of processor resources over an extended period of time. The first time that CP changes your virtual machine from Q1 to Q2, CP classifies it as interactive. If your virtual machine completes a time slice without moving back to Q1, the scheduler changes your virtual machine to noninteractive.

Queue 3

Virtual machines in Q3 are considered noninteractive. CP changes your virtual machine from Q2 to Q3 when it has used six consecutive Q2 time slices without entering long idle wait. The differences between Q2 and Q3 virtual machines are reflected in their deadline priority calculations and the amounts of such processor time they are allowed in queue. Q3 virtual machines are allowed eight consecutive Q2 processor time slices before they are dropped from the queue. Because of the eightfold increase in processor time allowed each time in queue, the user bias factor is multiplied by 8 before adding to the current time-of-day to form the deadline priority. Q3 virtual machines should receive eight times as much processor time each time in queue as Q2 virtual machines, but only 1/8th as often.

To reiterate the Q1/Q2 statement: Operating constantly in any queue, a virtual machine should receive the same amount of processor resources over an extended period of time. This does not necessarily mean that a virtual machine will perform the same when operating in Q3 mode as when operating in standard Q2 mode. An amount of overhead (roughly proportional to the small number of resident pages) is used for each virtual machine when it drops from queue. When operating in Q3 mode, a virtual machine may perform much better than in normal Q2 mode because it is undergoing fewer queue drops.

You can alter the queue scheme by using the NOQ2 or NOQ3 option on the SET QDROP OFF command. Specifying NOQ3 will force a virtual machine to be kept in Q1 or Q2. Specifying NOQ2 will force a virtual machine to be kept only in Q1.

When the SET QDROP userid OFF command is specified with the USERS operand, the QDROP OFF status is extended to any virtual machine communicating through VMCF or IUCV to the service virtual machine specified. See the VM/SP HPO CP Command Reference for more information on the SET QDROP command.

Functional Information

The functional diagrams that follow describe the program logic associated with various control program functions. Not all CP functions are described. These functional diagrams are meant to describe the CP functions about which you may want more detailed information if you are debugging, modifying, or updating CP.

Figure 4 describes the CP initialization process.

Figure 5 describes system shutdown and automatic warm start.

Figure 6 and Figure 7 describe the real and virtual I/O control blocks used by CP in its I/O control.

Figure 8, Figure 9, and Figure 10 show how CP handles SVC, external, and program interrupts.

Figure 11 describes the CP paging function.

Figure 12 and Figure 13 describe the CP spooling function (both virtual and real).

Figure 14 shows how virtual tracing is performed.

Figure 15 describes CP PER command processing.

Figure 16 describes CP PER interrupt processing.

Figure 17 shows the steps involved in translating a virtual address to a real address and gives an example of address translation.

Figure 18 shows how SNA Consoles Communication Services (SNA CCS) communicates with the VTAM Communications Network Application (VM/VCNA) and with the rest of VM/SP HPO.

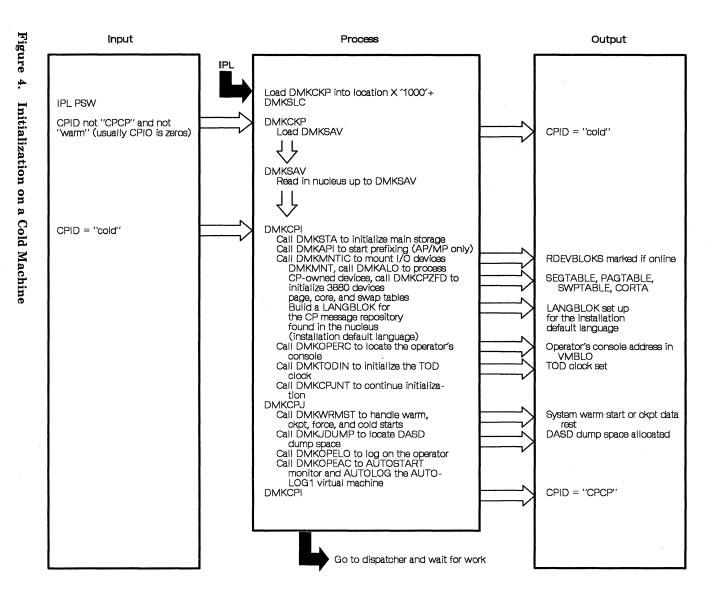
Figure 19 shows the structure of SNA Console Communication Services (SNA CCS) control blocks.

Figure 20 shows the structure of SNA DIAL control blocks.

The functional information contained in these diagrams is intended for system programmers and IBM Field Engineering program support representatives.

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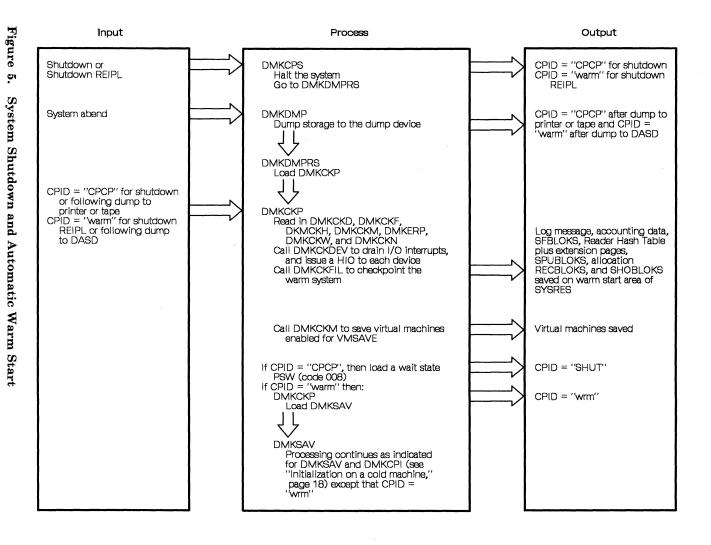


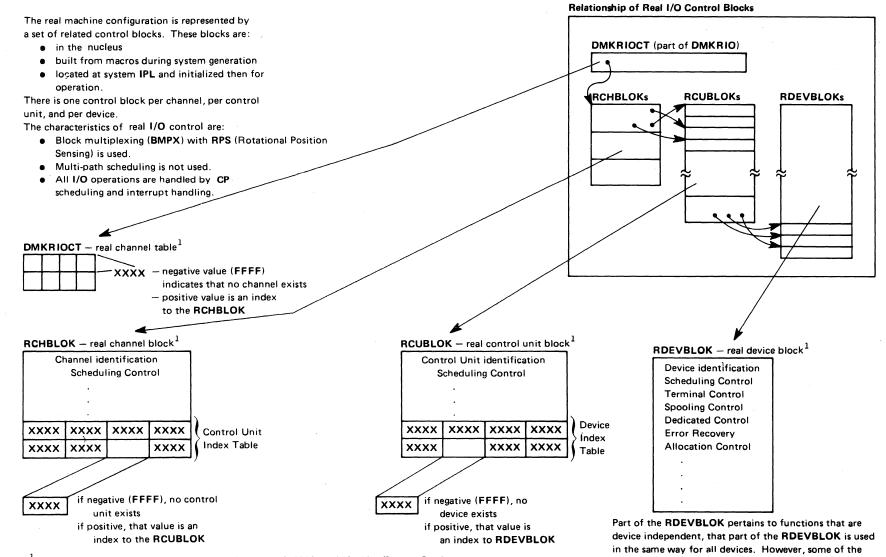
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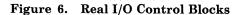
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¹ For a complete description of CP control blocks, see *IBM Virtual Machine/System Product High Performance Option: Data Areas and Control Blocks – CP.*



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fields in the RDEVBLOK have multiple uses, depending

on the device type and function.

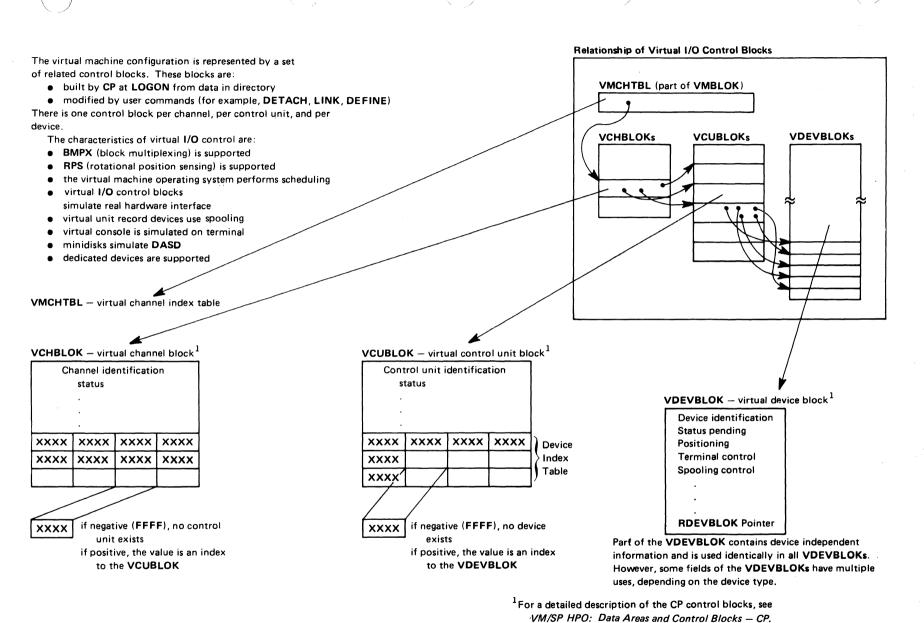
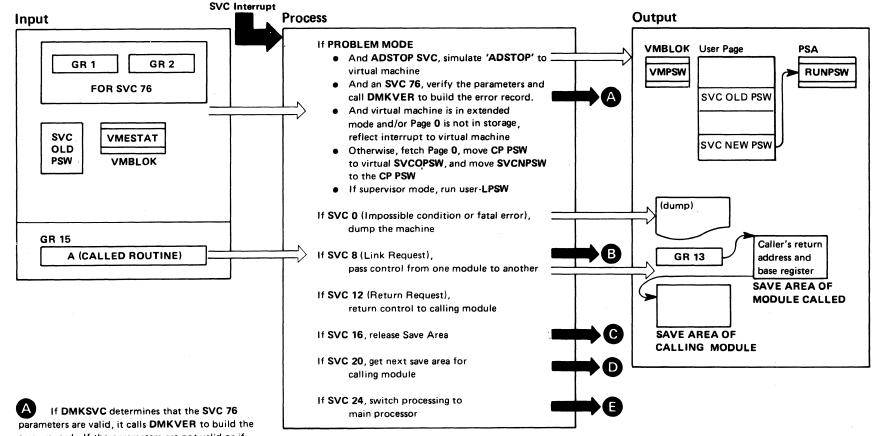


Figure 7. Virtual I/O Control Blocks



A If DMKSVC determines that the SVC 76 parameters are valid, it calls DMKVER to build the error record. If the parameters are not valid or if DMKVER cannot build the error record, DMKSVC reflects the SVC back to the virtual machine. If the error record is recorded, DMKVER gives control to the dispatcher with the user's running status set to return to the next sequential instruction following the SVC 76.

A new save area is acquired and passed on. The caller's addressability register (R 12), the save area address (R 13), and the return address (SVCOPSW) are saved in the new save area.

Figure 8. SVC Interrupt Handling

Control is returned to module issuing SVC 16, rather than to calling module as in SVC 12.

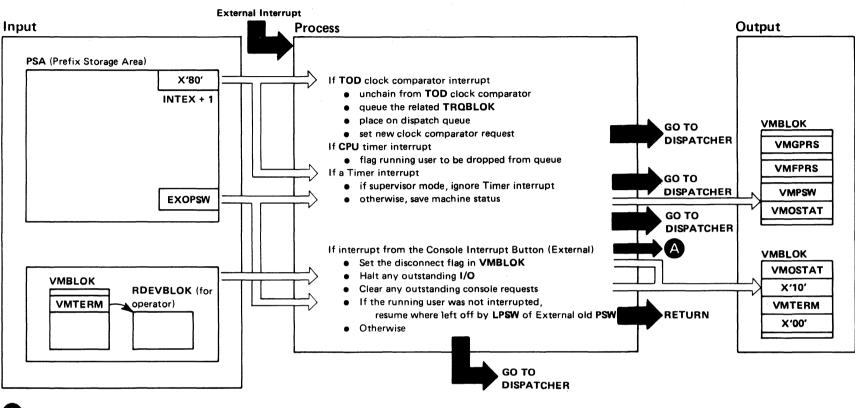


Return is to module issuing SVC 20.

Return is on other processor to module issuing SVC 24.

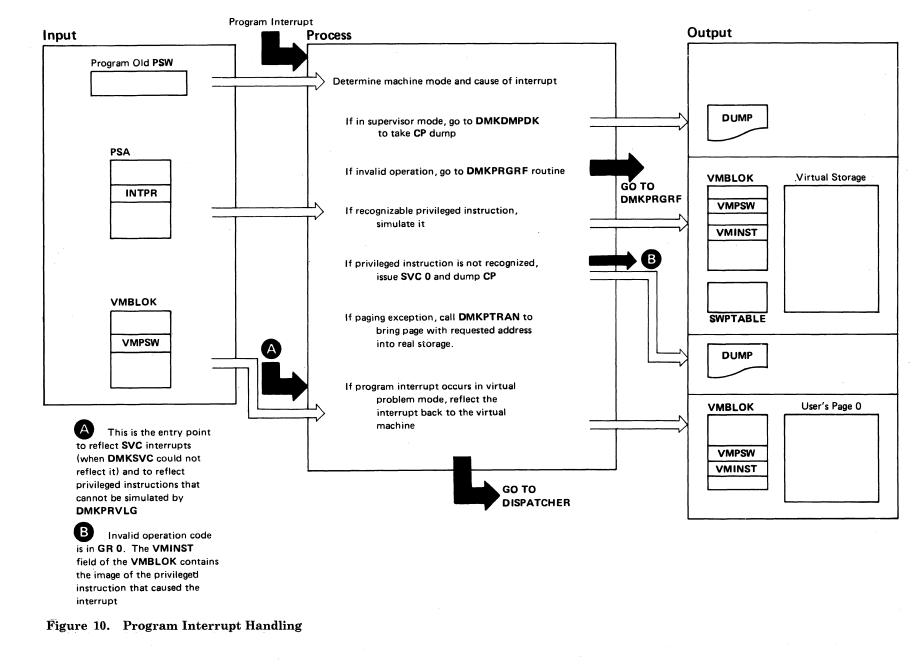
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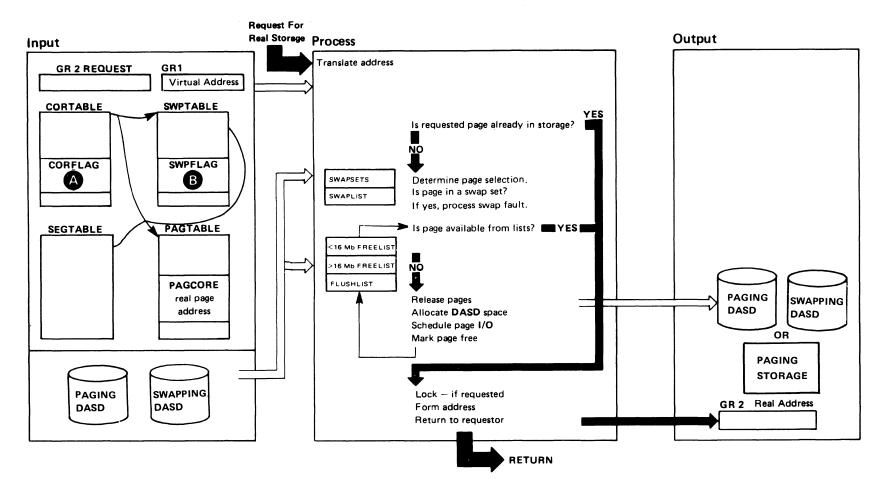


A External interrupt from control panel is used to disconnect the system operator's terminal. The system operator may reconnect at any other terminal via the LOGON command.



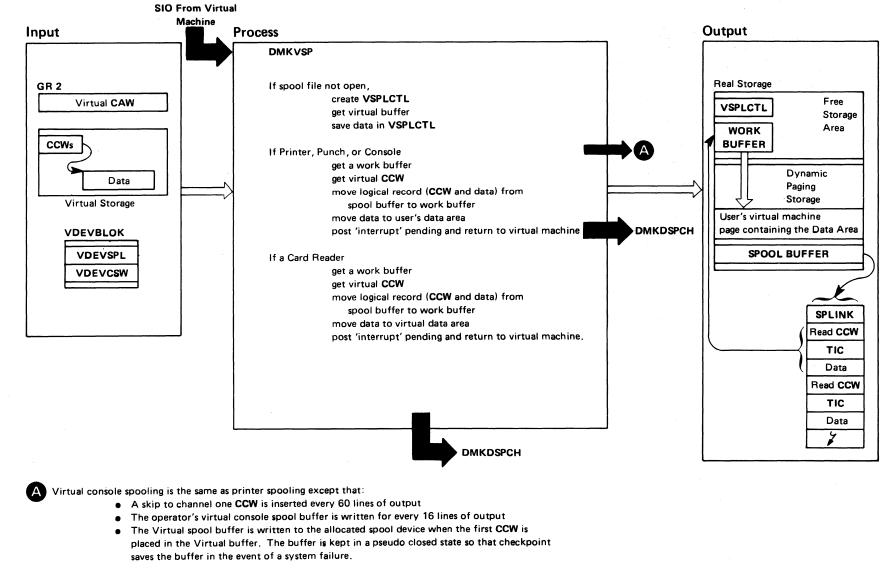


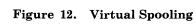
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A Bits defined for CORFLAG		B Bits defined for SWPFLAG					
CORIOLCK	EQU	X'80'	Page locked for I/O	SWPTRANS	EQU	X'80'	Page in transit
CORCFLCK	EQU	X'40'	Page locked by console function	SWPRECMP	EQU	X'40'	Page permanently assigned
CORFLUSH	EQU	X'20′	Page is in flush list	SWPALLOC	EQU	X'20′	Page enqueued for allocation
CORFREE	EQU	X'10'	Page is in free list	SWPSHR	EQU	X'10′	Page shared
CORSHARE	EQU	X'08'	Page is shared	SWPREF1	EQU	X'08'	1st half page referenced
CORSWAP	EQU	X'04'	Page is in swap list	SWPCHG1	EQU	X'04'	1st half page changed
CORCP	EQU	X'02'	Page belongs to CP	SWPREF2	EQU	X'02′	2nd half page referenced
CORDISA	EQU	X'01'	Page disable not available	SWPCHG2	EQU	X'01'	2nd half page changed

Figure 11. Paging

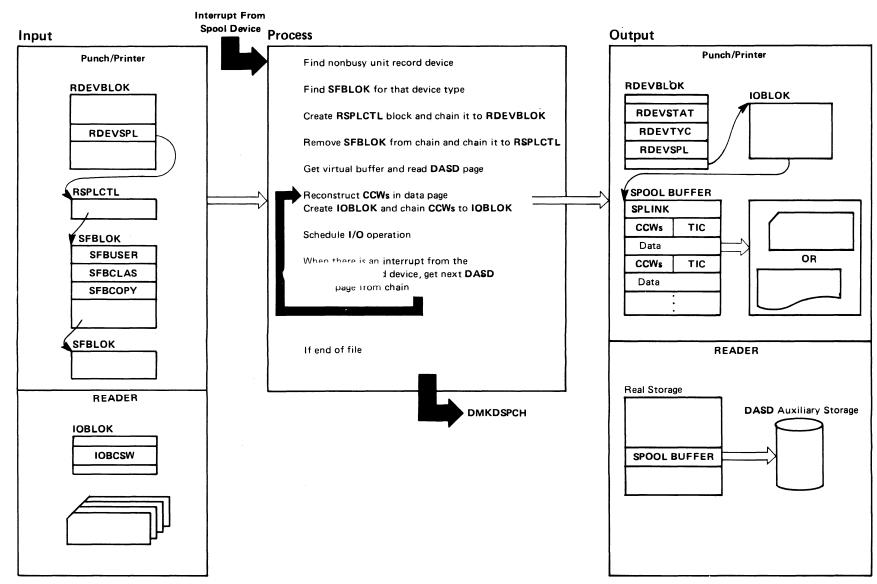




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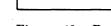
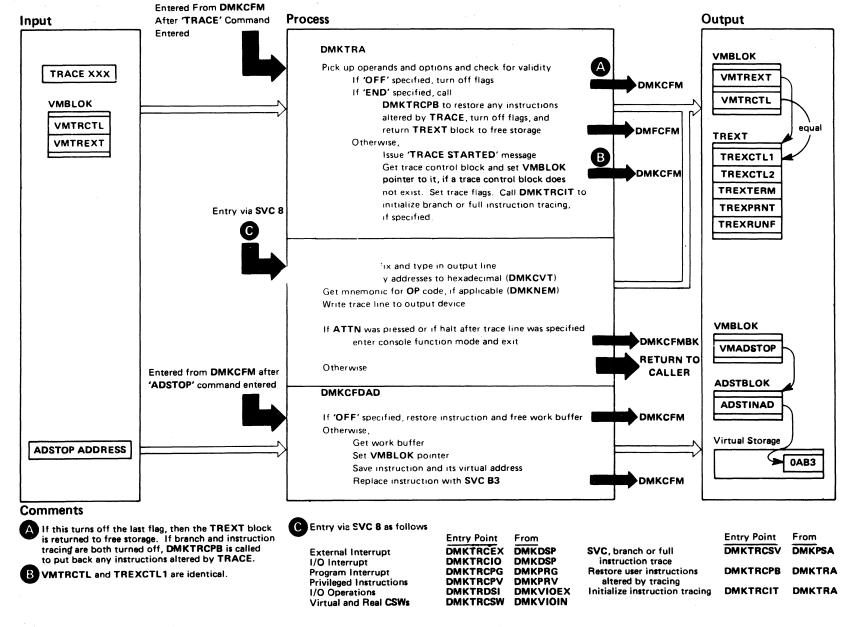
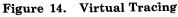


Figure 13. Real Spooling

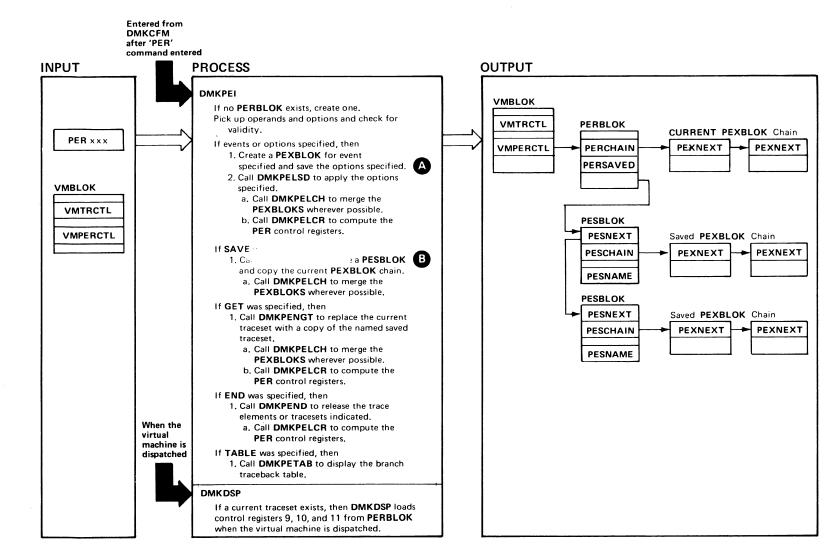
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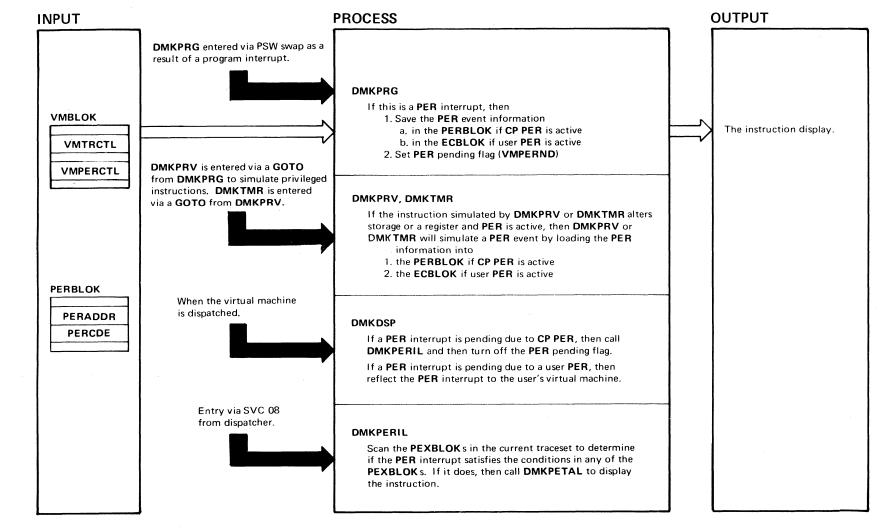
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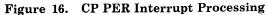
Each trace element is represented by a **PEXBLOK**.

The **PEXBLOK** s for each saved traceset are chained from a **PESBLOK**.



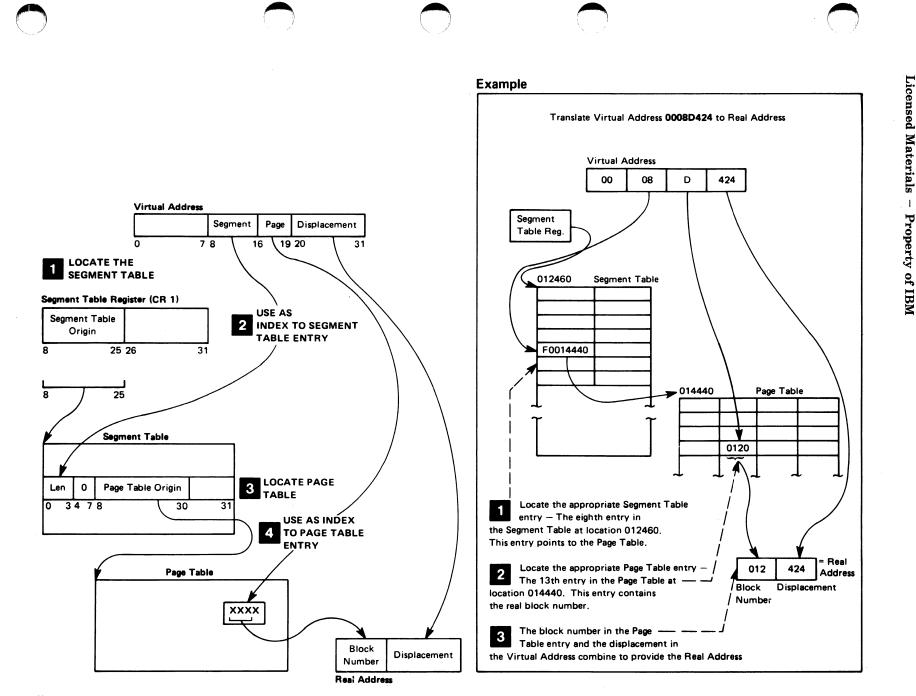
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Figure 17. Virtual-to-Real Address Translation

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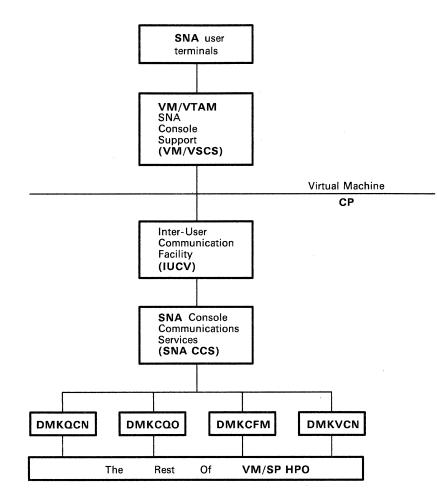


Figure 18. SNA CCS Interfaces

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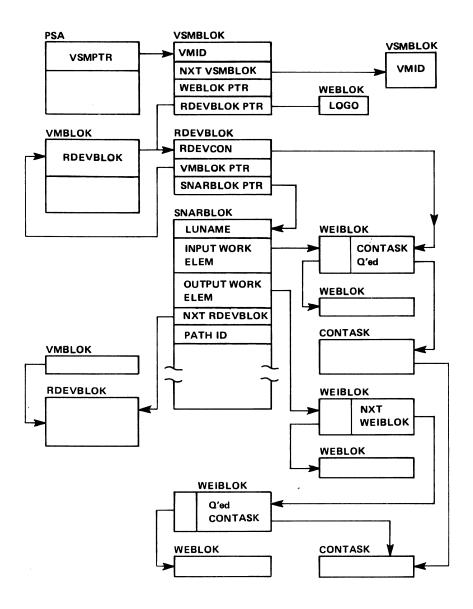


Figure 19. SNA CCS Control Block Structure

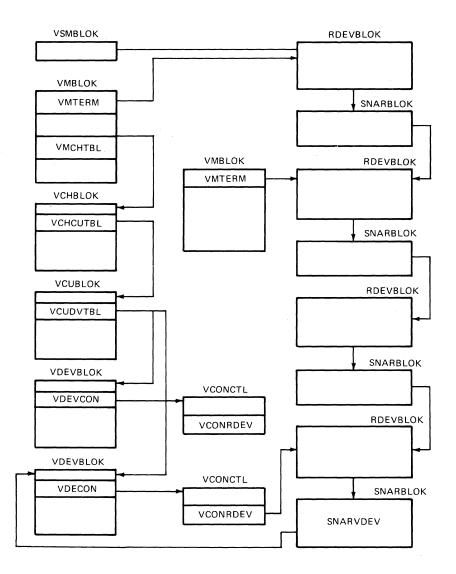


Figure 20. SNA DIAL Control Block Structure

Performance Guidelines

The performance characteristics of an operating system, when it is run in a virtual machine environment, are difficult to predict. This unpredictability is a result of several factors:

- The System/370 model used
- The characteristics of the operating system and its work level
- The total number of virtual machines executing
- The type of work being done by each virtual machine
- The speed, capacity, and number of the paging devices
- The order in which devices are selected for swapping, paging, and spooling
- The amount of real storage available
- The degree of channel and control unit contention, as well as arm contention, affecting the paging device
- The type and number of VM/SP HPO performance options in use by one or more virtual machines
- The degree of access to MSS 3330V volume
- The amount of fixed-head paging storage (drum, 3340, 3350).

Performance of any virtual machine may be improved up to some limit by the choice of hardware, operating system, and VM/SP HPO options. The topics discussed in this section address:

- 1. The options available to improve the performance of a particular virtual machine.
- 2. The system options and operational characteristics of operating systems running in-virtual machines that will affect their execution in the virtual machine environment.

The performance of a specific virtual machine may never equal that of the same operating system running standalone on the same System/370, but the total throughput obtained in the virtual machine environment may equal or better that obtained on a real machine.

When executing in a virtual machine, any function that cannot be performed wholly by the hardware causes some degree of degradation in the virtual machine's performance. As the control program for the real machine, CP initially processes all real interrupts. A virtual machine operating system's instructions are usually executed in *problem* state.² Any privileged instruction issued by the virtual machine causes a real privileged instruction exception interruption. The amount of work to be done by CP to analyze and handle a virtual machine-initiated interrupt depends upon the type and complexity of the interrupt.

The simulation effort required of CP may be trivial, as for a supervisor call (SVC) interrupt (which is generally reflected back to the virtual machine), or may be more complex, as in the case of a Start I/O (SIO) interrupt, which initiates extensive CP processing.

When planning for the virtual machine environment, consider the number and type of privileged instructions to be executed by the virtual machines. Any reduction in the number of privileged instructions issued by the virtual machine's operating system will reduce the amount of extra work CP must do to support the machine.

Directory Considerations

Two techniques involving the user directory will improve system performance by:

- Decreasing the size of the directory
- Decreasing the search time needed to locate a USER control statement when attaching that user's virtual machine.

To decrease the size of the directory, commonly used control statements can be contained in a directory profile. The profile is defined by a PROFILE control statement, and each user entry may reference this profile via an INCLUDE control statement. The advantage gained is that these commonly used statements are defined only once (in the profile), instead of several times (in each user's entry). See the "DMKDIR - The Directory Program" section in the VM/SP HPO Service Routines Program Logic for more information.

To decrease the search time needed to locate USER control statements, you should alphabetically sort the directory according to the userid field in the USER control statements. Routines that search the directory list (for example, when logging on or spooling) will do so more efficiently and therefore greatly reduce the search time. For more information, see the VM/SP HPO Planning Guide and Reference or VM/SP HPO CP for System Programming.

² When preferred machine assist is active, the MVS virtual machine runs in supervisor state in the V=R area and does I/O operations on dedicated channels.

Virtual Machine I/O

To support I/O processing in a virtual machine, CP must translate all virtual machine channel command word (CCW) sequences to refer to real storage and real devices and, in the case of minidisks, real cylinders or blocks. When a virtual machine issues an SIO, CP must:

- 1. Intercept the virtual machine SIO interrupt
- 2. Allocate real storage space to hold the real CCW list to be created
- 3. Translate the virtual addresses referred to in the virtual CCWs to real addresses
- 4. Page into real storage and lock, for the duration of the I/O operation, all virtual storage pages required to support the I/O operation
- 5. Generate a new CCW sequence building a Channel Indirect Data Address list if the real storage locations cross page boundaries
- 6. Schedule the I/O request
- 7. Present the SIO condition code to the virtual machine
- 8. Intercept, retranslate, and present the channel end and device end interrupts to the appropriate virtual machine, where they must then be processed by the virtual machine operating system.

CP's handling of SIOs for virtual machines can be one of the most significant causes of reduced performance in virtual machines.

The overhead associated with SIO operations required by a virtual machine can be significantly reduced in several ways:

- Use of large blocking factors (of up to 4096 bytes) for user data sets to reduce the total number of SIOs needed.
- Use of preallocated data sets.
- Use of virtual machine operating system options (such as chained scheduling in OS) that reduce the number of SIO instructions.
- Substitution of a faster resource (virtual storage) for I/O operations, by building small temporary data sets in virtual storage rather than using an I/O device.
- Use of NOTRANS option with dedicated channels for the V=R virtual machine operating system.

Frequently, there can be a performance gain when CP paging is substituted for virtual machine I/O operations. The performance of an operating system such as OS can be improved by specifying as resident as many frequently used OS functions (transient subroutines, ISAM indexes, and so forth) as are possible. In this way, paging I/O is substituted for virtual machine-initiated I/O. In this case, the only work to be done by CP is to place into real storage the page that contains the desired routine or data. The following performance options are available to reduce the CP overhead associated with virtual machine I/O instructions or other privileged instructions the virtual machine's I/O supervisor uses:

- The virtual = real option eliminates the need for CP to perform storage reference translation and paging before each I/O operation for the V=R virtual machine. Only one V=R virtual machine can be executing at any time.
- The virtual machine assist feature reduces the real supervisor state time used by the control program.
- VM/370 Extended Control Program Support further reduces the real supervisor state time used by the control program.

The section Virtual Machine Performance Options describes assigning and using these options.

See the VM/SP HPO Planning Guide and Reference for a list of processors that support virtual machine assist and VM/370 Extended Control Program Support. This publication, along with the VM/SP HPO Installation Guide, describes procedures for setting up a V=R area and the requirements for using preferred machine assist.

Paging Considerations

When virtual machines refer to virtual storage addresses that are not currently in real storage, they cause a paging exception and the associated CP paging activity.

The addressing characteristics of programs executing in virtual storage have a significant effect on the number of page exceptions experienced by that virtual machine. Routines that have widely scattered storage reference tend to increase the paging load of a particular virtual machine. When possible, modules of code that are dependent upon each other should be located in the same page. Reference tables, constants, and literals should also be located near the routines that use them. Exception or error routines that are infrequently used should not be placed within main routines, but located elsewhere.

CP assigns certain inactive pages to a paging device according to the following rules:

- If the page was referenced during virtual machine execution, and if space is available in a TYPE = SW area, the page is assigned to that area (regardless of whether or not the page was changed during virtual machine execution).
- If the page was referenced and changed during virtual machine execution, and no space is available in a TYPE = SW area, the page is assigned a TYPE = PP area.

- If the page was not referenced during virtual machine execution, it is assigned to a TYPE = PP area.
- Note: If a page is read in from TYPE = SW, it is marked as "changed" because the backup copy in the TYPE = SW area is released once the page is read in.

Virtual machines that reduce their paging activity by controlling their use of addressable space improve resource management for that virtual machine, the VM/SP HPO System, and all other virtual machines. The total paging load that must be handled by CP is reduced, and more time is available for productive virtual machine use.

The system programmer can gain additional dynamic paging storage by using the SYSCOR macro statement at system generation time. One way he can do this is by reducing the number of free storage page frames and prime storage page frames to be allocated at system load time. The amount of free storage to be allocated is specified in the FREE operand of the SYSCOR macro. The amount of prime storage to be allocated is specified in the PRIME operand of the SYSCOR macro. Reducing free storage, however, is not a good idea on large systems. Another way to gain additional dynamic paging storage is to reduce the size of the trace table (again, by using the SYSCOR macro). See the VM/SP HPO Planning Guide and Reference for more information.

Another way the system programmer can add dynamic paging storage is to specify a value of more than 16 Mb in the RMSIZE or RSSIZE operand of the SYSCOR macro. This should be done *only* on processors that can support extended storage. The area between the 16 Mb line and RMSIZE will be used as additional dynamic paging storage; however, these page frames can be used for virtual machine pages only. This area above the 16 Mb line cannot be used for CP pageable pages, virtual page 0, or virtual machine pages requiring service by CP. When CP requires access to a virtual machine page that is resident above the 16 Mb line, that page must be moved to the dynamic paging area below the 16 Mb line before CP can reference it. CP references virtual machine storage for such functions as privileged operation simulation, channel program translation, interrupt reflection, and console functions.

See "Extended Storage Support" under "CP Interrupt Handling" in this part of the manual for more information.

CP provides four performance options (locked pages, reserved page frames, QDROP OFF option, and a virtual=real area) to reduce the paging requirements of virtual machines. Generally, these facilities require some dedication of real storage to the chosen virtual machine and, therefore, improve its performance at the expense of other virtual machines.

Locked Pages Option

The LOCK command, which is available to the system operator (with privilege class A), can be used to permanently fix or lock specific user pages of virtual storage into real storage. In so doing, all paging I/O for these page frames is eliminated.

Since this facility reduces total real storage resources (real page frames) that are available to support other virtual machines, only frequently used pages should be locked into real storage. Since page zero (the first 4096 bytes) of a virtual machine storage is referred to and changed frequently (for example, whenever a virtual machine interrupt occurs or when a CSW is stored), it should be the first page of a particular virtual machine that an installation considers locking. The virtual machine interrupt handler pages are also good candidates for locking.

Other pages to be locked depend upon the work being done by the particular virtual machine and its usage of virtual storage.

The normal CP paging mechanism selects unreferenced page frames in real storage for replacement by active pages. Page frames belonging to inactive virtual machines will all eventually be selected and paged out if the real storage frames are needed to support active virtual machine pages.

When virtual machine activity is initiated on an infrequent or irregular basis, such as from a remote terminal in a teleprocessing inquiry system, some or all of its virtual storage may have been paged out before the time the virtual machine must begin processing. Some pages will then have to be paged in so that the virtual machine can respond to the teleprocessing request compared with running the same teleprocessing program on a real machine. This paging activity may cause an increase in the time required to respond to the request compared with running the teleprocessing program on a real machine. Further response time is variable, depending upon the number of paging operations that must occur.

Locking specific pages of the virtual machine's program into real storage may ease this problem, but it is not always easy or possible to identify which specific pages will always be required.

In general, once a page is locked, it remains locked until one of the following occurs:

- The user logs off
- The system operator (privilege class A) issues the UNLOCK command for that page
- The user re-IPLs his system by device address and specifies the CLEAR option
- The user re-IPLs his system by name (Shared System), and the locked pages are not in the shared segment

- The user re-IPLs his system by name, the locked pages are in the shared segment, and the user who is re-IPLing is the last user of that shared segment
- The user issues DIAGNOSE instruction X'14', X'30', X'34', or X'38' against a locked page.

However, if the user re-IPLs his system by device address and does not specify the CLEAR option, all pages remain locked except the page given to DMKVMI for IPL.

The SYSTEM CLEAR command, when invoked, clears virtual machine storage and unlocks the user's locked pages.

You cannot lock pages into the greater than 16 Mb area. Page frames above this limit are dedicated to the preferred machine assist guest or are used by CP as additional dynamic paging area for user pages only. Pages above the 16 Mb line are moved to the less than 16 Mb area before they are locked.

Shared Pages

In a system generated for attached processor or multiprocessor operation, no shared pages are locked. If the system operator attempts to lock a shared page or an address range containing one or more shared pages, he will receive the message

DMKCPV165I Page hexloc not locked; shared page

for each of the shared pages within the range.

Reserved Page Frames Option

A more flexible approach than locked pages is the reserved page frames option. This option provides a specified virtual machine with an essentially private set of real page frames, the maximum number of frames being designated by the system operator, when he issues the CP SET RESERVE command line. Pages will not be locked into these frames. The most recently referenced pages, as determined by the core table scan, will be held in storage. When a temporarily inactive virtual machine having this option is reactivated, these page frames are immediately available. If the program code or data required to satisfy the request was in real storage at the time the virtual machine became inactive, no paging activity is required for the virtual machine to respond.

This option is usually more efficient than locked pages in that the pages that remain in real storage are those pages with the greatest amount of activity at that moment, as determined automatically by the system.

The syntax of the SET RESERVE command is:

SET RESERVE userid nnnn

where nnnn is the maximum number of reserved page frames required. Nnnn can be a value from 1 to 4096. The number of frames reserved will be nnnn or number of pages resident for the user, whichever is smaller. Note: The sum of all reserved pages should never approach the total available pages, since CP overhead is substantially increased in this situation, and excessive paging activity is likely to occur in other virtual machines.

The reserved page frames user's paging activity is generally consistent from run to run. This can be especially valuable for production-oriented virtual machines with critical schedules, or those running teleprocessing applications where response times must be kept as short as possible.

Note: Multiple machines may have reserved page frames.

QDROP OFF Option

By setting QDROP OFF for a specific virtual machine, DMKSEL will examine that user's resident pages less often during core table scan (and, therefore, those pages will be more likely to remain resident). This increased page residency will improve performance for that user. See the VM/SP HPO CP Command Reference for more information on the SET QDROP command.

Virtual = Real Option

The virtual = real option eliminates CP paging for the selected virtual machine. All pages of virtual machine storage, except page zero, are locked in the real storage locations they would use on a real computer. CP controls real page zero, but the remainder of the CP nucleus is relocated and placed beyond the virtual = real machine in real storage. For a more detailed discussion of this option, see "Preferred Virtual Machine Initialization" under "Preferred Machine Assist Feature."

Since the entire address space required by the virtual machine is locked, these page frames are not available for use by other virtual machines except when the virtual = real area has been unlocked. This option often increases the paging activity for other virtual machine users, and in some cases for VM/SP HPO. (Paging activity on the system may increase substantially, since all other virtual machine storage requirements must be managed with fewer remaining real page frames.)

The virtual=real option may be desirable or mandatory in certain situations. The virtual=real option is desirable when running a virtual machine operating system (like DOS/VS or OS/VS) that performs paging of its own because the possibility of double paging is eliminated. The option must be used to allow programs that execute self-modifying channel programs or have a certain degree of hardware timing dependencies to run under VM/SP HPO.

Virtual Machine Performance Options

VM/SP HPO provides several functions that create a special virtual machine environment. The following functions improve the performance of a selected virtual machine:

- Favored execution
- User priority
- Reserved page frames
- Virtual=real option
- QDROP OFF option
- Affinity
- Multiple shadow table support
- Shadow table bypass
- Single processor mode
- Dynamic system control program transition to or from native mode
- Preferred machine assist.

Although these functions can be applied to different virtual machines, they are usually applied to only one if optimum performance is required for that specific virtual machine.

The following functions improve the performance of the VM/SP HPO system. They can be applied to as many virtual machines as desired:

- Reserved page frames
- Virtual machine assist
- VM/370: Extended Control Program Support
- Dual address space assist
- MVS page fault assist.

Favored Execution

1

The favored execution options allow an installation to modify the normal CP deadline priority calculations in the scheduler to force the system to devote more of its processor resources to a given virtual machine than would ordinarily be the case. The options provided are:

- 1. The basic favored execution option.
- 2. The favored execution percentage option.

The basic favored execution option means that the virtual machine so designated is to remain in the dispatch list at all times, unless it becomes nonexecutable. When the virtual machine is executable, it is to be placed in the dispatchable list at its normal priority position. However, any active virtual machine represents either an explicit or implicit commitment of main storage. An explicit storage commitment can be specified by either the virtual = real option or the reserved page frames option. An implicit commitment exists if neither of these options is specified, and the scheduler recomputes the virtual machine's projected work-set at what it would normally have been at queue-drop time. Multiple virtual machines can have the basic favored execution option set. However, if their combined main storage requirements exceed the system's capacity, performance can suffer because of thrashing.

If the favored task is highly compute bound and must compete for the processor with many other tasks of the same type, you can define the processor allocation to be made. In this case, you can select the favored execution percentage option. This option specifies that the selected virtual machine, in addition to remaining in queue, is guaranteed a specified minimum percentage of the total processor time if it can use it.

Note: The percentage of processor time that has been requested via the SET FAVORED command with the percentage option is not an absolute value.

The percentage actually received by the favored user will vary depending on the total load and/or the type of load on the system. Generally, it will remain close to the percentage specified in the command. However, if the run list contains multiple virtual machines that are compute bound, the favored user may not receive the requested percentage of processor time. The favored execution option can only be invoked by a system operator with command privilege class A. The format of the command is as follows:

SET FAVORED userid nnn OFF

where:

userid identifies the virtual machine to receive favored execution status.

nnn is any value from 1 through 100 and specifies the percentage of the in-queue time slice that the system will attempt to provide for this virtual machine. In addition, specifying 100 causes the user to be kept at the top of the dispatch list.

OFF specifies that the virtual machine is to be removed from favored execution status.

If a percentage is not specified, a virtual machine with the favored execution option active is kept in the dispatch list except under the following conditions:

- Entering CP console function mode
- Loading a disabled PSW
- Loading an enabled PSW with no active I/O in process
- Logging on or off.

When the virtual machine becomes executable again, it is put back on the dispatch list in Q1. If dropped from Q1, the virtual machine is placed directly in the Q2 dispatch list. If the percentage option of the SET FAVORED command is specified, the deadline priority is calculated at queue drop time by:

current time-of-day + length of allowed processor in-queue time slice favored percentage For example, if the processor in-queue time slice is 1 second, and the specified percentage is 10 percent (1/10), then the value added to the current time-of-day is 10 seconds. The virtual machine should receive one processor time slice (1 second) once every 10 seconds.

Although the SET FAVORED command prevents specifying more than 100% for a particular virtual machine, nothing is done to prevent allocating more than 100% to a number of virtual machines. Where more than 100% has been allocated, the favored virtual machines compete for the available resources on a pro-rata basis. That is, an individual virtual machine's allocation is, roughly, proportional to the percentage allocated to it, divided by the total percentage allocated to all virtual machines. The effect of allocating more than 100% of the system on interactive (Q1) responses is unpredictable.

User Priority

The system operator can assign specific priority values to different virtual machines. In so doing, the virtual machine with a higher priority is allocated a larger share of the system resources before a virtual machine with a lower priority. User priorities are set by the following class A command:

SET PRIORITY userid nn

where userid is the user's identification and nn is an integer value from 1 to 99. The value of nn affects the user's dispatching priority in relation to other users in the system. The priority value (nn) is one of the factors considered in the calculation of the deadline priority. The deadline priority is the basis on which all virtual machines in the system are ordered on both the eligible list and the dispatch list. The deadline priority calculation is based on the assumption that the average or normal (default) user priority is 64.

Virtual = Real

For this option, the nucleus must be reorganized to provide an area in real storage large enough to contain the entire virtual = real machine. In the virtual machine, each page from page 1 to the end is in its true real storage location; only its page zero is relocated. The virtual machine is still run in dynamic address translation mode, but since the virtual page address is the same as the real page address, no CCW translation is required. Since CCW translation is not performed, no check is made to ensure that I/O data transfer does not occur into page zero or any page beyond the end of the virtual = real machine's storage.

Systems that are generated with the virtual=real option use the system loader (DMKLD00E). For information about generating a virtual=real system, see the VM/SP HPO Installation Guide.

Figure 21 is an example of a real storage layout with the virtual=real option. The V=R area is 8Mb and real storage is 32Mb.

There are several considerations for the virtual=real option that affect overall system operation:

- 1. The area of contiguous storage built for the virtual=real machine must be large enough to contain the entire addressing space of the largest virtual=real machine. The virtual=real storage size that a VM/SP HPO system allows is defined during system generation when the option is selected.
- 2. The storage reserved for the virtual=real machine can only be used by a virtual machine with that option specified in the system directory. It is not available to other users for paging space, nor for VM/SP HPO use until released from virtual=real status by a system operator via the CP UNLOCK command. Once released, VM/SP HPO must be loaded again before the virtual=real option can become active again.

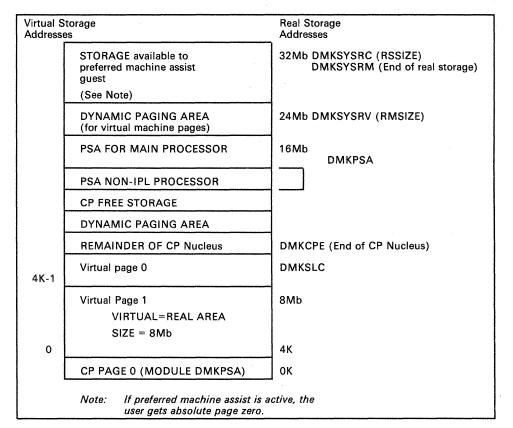


Figure 21. Storage Layout in a Virtual=Real Machine with Extended Storage Support

- 3. The virtual machine with the virtual = real option operates in the preallocated storage area with normal CCW translation in effect until the CP SET NOTRANS ON command is issued. At that time, with several exceptions, all subsequent I/O operations are performed from the virtual CCWs in the virtual = real space without translation. The exceptions occur under any of the following conditions:
 - SIO tracing active
 - First CCW not in the V = R region

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- I/O operation is a sense command
- I/O device is a dial-up terminal
- I/O is for a nondedicated device (spooled unit record console virtual CTCA or minidisks that are less than a full volume)
- Pending device status
- I/O device has an alternate path.

Any of the above conditions will force CCW translation. Since minidisks are nondedicated devices, they may be used by programs running in the V = R region even though CP SET NOTRANS ON is in effect.

- 4. If the virtual = real machine performs a virtual reset or IPL, then the normal CCW translation goes into effect until the CP SET NOTRANS ON command is again issued. This permits simulation of an IPL sequence by CP. Only the virtual = real virtual machine can issue the command. A message is issued if normal translation mode is entered.
- 5. A virtual=real machine is not allowed to IPL a named or shared system. It must IPL by device address.
- 6. When NOTRANS is in effect for a virtual=real machine, no meaningful SEEK data is collected by MONITOR operations.
- 7. The reliability and availability of an operating system running virtual=real on a 308x, 3090, or 4381 processor can be enhanced when the TEST BLOCK instruction (TB) is used to validate the V=R storage area.

Affinity

1

This virtual machine option allows virtual machines that operate on attached processor systems or multiprocessor systems to select, if desired, the processor of their choice for program execution. The selection can be made by the system directory OPTION statement, or it can be made dynamically by an operand of the CP SET command:

For class G users

SET AFFINITY nn OFF For class A users SET AFFINITY userid

ON OFF

nn

where nn is the processor address of a processor in an attached or multiprocessor configuration.

In application, the affinity setting of a virtual machine implies a preference of operation to either (or neither) processor. Affinity of operation for a virtual machine means that the program of that virtual machine will be executed on the selected or named processor. It does not imply that supervisory functions and the CP housekeeping functions associated with that virtual machine will be handled by the same processor.

In attached processor systems, all real I/O operations and associated interrupts are handled by the main processor. Virtual I/O initiated on the attached processor that is mapped to real devices must transfer control to the main processor for real I/O execution. Therefore, benefits may be realized in a virtual machine "mix" by relegating those virtual machines that have a high I/O-to-compute ratio to the main processor, and those virtual machines that have a high compute-to-I/O ratio to the attached processor. Such decisions should be carefully weighed as every virtual machine is in contention with other virtual machines for resources of the system.

In multiprocessor configurations, both processors have the capability of executing real I/O. If the path to a user's primary minidisk or to a user's dedicated volumes is configured asymmetrically to one processor, performance benefits for the virtual machine may be derived by setting the virtual machine's affinity to that processor.

A more important use of the affinity setting would be in applications where there are virtual machine program requirements for special hardware features that are available on one processor and not the other. Such features could be a performance enhancement such as virtual machine assist (described later in the text) or a special RPQ that is a requirement for a particular program's execution.

Multiple Shadow Table Support

To reduce the number of shadow table purges when the virtual machine changes control register 1 (CR1) values, CP maintains a queue of segment table origins (STO) and associated shadow tables for the virtual machine. Thus, each time an MVS or SVS system dispatches a new address space (changes CR1), CP can use the proper shadow table.

Multiple shadow table support adds one control block to CP, the segment table origin control block (STOBLOK) pointed to by the ECBLOK. The STOBLOK, created by DMKVAT, contains all information pertaining to the shadow segment table, the shadow segment table itself and the virtual CR1 value. It also provides forward and backward queue pointers to the next STOBLOK on the queue. The first STOBLOK on the queue always contains the shadow STO to be loaded into CR1 when the virtual machine is dispatched in translation mode. The queue of STOBLOKs is maintained by DMKVAT in the following manner:

1. If the virtual machine loads a new CR1 value, DMKVATAB searches the queue of STOBLOKs for the virtual CR1 value.

2. If the proper STO is found, the STOBLOK is ordered first on the queue.

3. If the proper STO is not found, the maximum STO count is checked.

- 4. If the number of STOBLOKs equals the maximum STO count, DMKVATAB steals the last STOBLOK, purges the shadow tables, and reinitializes and reuses the STOBLOK by chaining it first on the queue.
- 5. If the number of STOBLOKs is less than the maximum STO count, then free storage is obtained from VM/SP HPO, and the STOBLOK is reinitialized and chained first on the queue.

Multiple shadow table support is controlled by the SET STMULTI command.

Shadow Table Bypass

Shadow table bypass is controlled by the SET STBYPASS command.

Note: If virtual machine assist is enabled on the system, the virtual machine must have the STFIRST directory option to be allowed to issue the SET STBYPASS command.

Shadow Table Bypass for the V = V User

This technique is based on several characteristics of VS systems:

- 1. VS systems have a large area of addressing space starting with location zero where the virtual address is equal to the real address.
- 2. This addressing space is common to each segment table when multiple segment tables are used (MVS or SVS address space).
- 3. The VS system never pages within this fixed area.

Thus, an area starting with location zero can be established where the second-level address equals the third-level address or virtual-virtual = virtual-real (VV = VR). This allows a high-water mark, the highest VV = VR address, for a VS system to be established. Because the second-level address is the same as the third-level address, a reverse translation allows the shadow tables to be indirectly indexed. Then, whenever VM/SP HPO steals a page from the VV = VR area, it invalidates the shadow page table entry and executes a PTLB instruction before redispatching the VS system's virtual machine.

In addition, whenever a shadow table is purged because a page frame was stolen from above the high-water mark or because the virtual machine executed a PTLB or LCTL instruction, the invalidation starts above the high-water mark, thus reducing purge and revalidation time.

Shadow Table Bypass for the V = R User

Using V = R shadow table bypass, you can eliminate both the shadow tables and the overhead associated with maintaining them. Do this by modifying the virtual operating system's page table to relocate virtual page zero to the highest real address within the V = R area. Then dispatch the virtual machine with control register 1 pointing to its own virtual page and segment tables.

Single Processor Mode

In multiprocessor (MP) and attached processor (AP) systems, single processor mode allows you to dedicate a processor to an MVS V=R virtual machine. With single processor mode active, VM/SP HPO executes in uniprocessor mode on the IPL processor while the V=R virtual machine, which continues to operate under VM/SP HPO, has exclusive control of the non-IPL processor for AP or MP operations.

You activate single processor mode for the system by issuing the CP command SPMODE ON, but only after the processor has been varied off to VM. Before using single processor mode, you should generate VM/SP HPO as an AP or MP system with a V=R area defined. You can generate VM/SP HPO as a UP system if you have the MP feature installed on the machine. CP issues an error message if you violate any of the requirements.

Virtual Prefixing

When your VM/SP HPO AP or MP system is operational, CP supports virtual prefixing before you activate single processor mode. This allows you to use the functions of the shadow table bypass assist by creating an environment in which the MP feature is made available to the V=R guest even though only one processor (the VM/SP HPO IPL processor) is online to the guest.

CP performs virtual prefixing by modifying the VMSEG page table entry, segment tables, and shadow tables. CP stores the prefix value for the V = R virtual machine in segment zero of the page table entry for virtual page zero. Thus, when the V = R virtual machine is not in translate mode (the translation bit is off in the EC mode PSW), access to virtual page zero is through the prefix value that is already stored in the page table entry for the V = R virtual machine. If the V = R virtual machine references its virtual prefix page, absolute page zero is accessed through reverse prefixing.

When the V = R virtual machine is in translate mode, virtual prefixing is accomplished through a shadow segment table. This table is essentially a copy of the segment table for the virtual machine. CP maintains a private shadow page table for segment zero and for the segment containing the virtual prefix page. The shadow page table entries for virtual page zero and the virtual prefix page are maintained in the same way as they are maintained in the VMSEG segment tables.

Since shadow tables are necessary to simulate virtual prefixing, the SET STBYPASS VR command is effectively treated as a no-operation function. Use the SET STMULTI command to minimize shadow table maintenance overhead. VM/SP HPO support for virtual prefixing allows CP to simulate the MP instructions CONCS, STAP, SPX, STPX, and SIGP even though single processor mode is not activated.

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Real Prefixing

When you activate single processor mode (using the SPMODE ON command), CP sets the VMSEG page table entry that corresponds to the virtual prefix page to access absolute page zero. When the virtual prefix is not zero, the page table entry for virtual page zero is set to the virtual prefix page. When the virtual prefix is zero, the page table entry for virtual page zero is set to access absolute page zero.

If the shadow table bypass assist feature is present on the processor, CP activates the STNOSM and STOSM functions of the assist by setting the appropriate control bits in the MICBLOK. The V=R virtual machine running in single processor mode can issue the SET STBYPASS command to set a high-water mark, so you do not have to do so using the STFIRST directory option.

If you issue SPMODE OFF and the V=R virtual machine has a nonzero prefix, the system returns to virtual prefixing. CP adjusts the page table entries in the VMSEG tables to access virtual page zero instead of absolute page zero and reestablishes shadow tables. The shadow table bypass assist bits are set to zero in the MICBLOK.

When a PSW RESTART function is invoked under normal use, it forces VM/SP HPO to take a storage dump (PSA002). When the V=R virtual machine is running in single processor mode, the PSW RESTART function causes a restart interrupt to the V=R virtual machine. If you require a forced storage dump (PSA002) while running in single processor mode, set a flag byte in DMKPSA before invoking the PSW RESTART function.

For information about the commands used in this support, see the VM/SPHPO CP Command Reference. A full description of the operating procedures for this support is contained in VM/SP HPO Operating Systems in a Virtual Machine.

Dynamic SCP Transition to or from Native Mode

This function allows the operating system running in the V=R virtual machine to make a transition from the VM/SP HPO environment to the native environment and back again. It eliminates the necessity of system shutdown and reinitialization for the operating system and VM/SP HPO.

To make the transition to native mode, the indicated users must perform the following steps:

- All I/O used by the V = R virtual machine must be dedicated and the virtual I/O addresses must be the same as the real I/O addresses.
- The virtual machine operator must stop the virtual spooling devices (nondedicated) and detach them from the virtual machine.
- The system operator must drain the real unit-record devices.
- All users, except the V = R virtual machine and the system operator, must be logged off.

When these conditions are met, the system operator can issue the CP command

QVM userid

The VM/SP HPO machine's page zero is saved, and the V=R virtual machine's page zero is moved to absolute page zero. The timers, control registers, and general registers are initialized to the virtual machine's values. Control is then given to the V=R virtual machine in native mode.

The operating system in the V = R virtual machine is not given native control if any of the following conditions are detected:

- CP wait conditions are present
- I/O interruptions are pending
- External interruptions are pending
- I/O requests are queued within VM/SP HPO
- An ADSTOP has been set or tracing is active.

The transition to native mode is handled by the DMKQVMRT subroutine in the DMKQVM module.

This support also allows the transition from native mode back to VM/SP HPO. When the V=R virtual machine is given native control, the RESTART PSW is modified to point to an entry point in the DMKQVM module.

The system operator must set a flag byte within DMKQVM and perform a PSW RESTART function. This gives control to a subroutine in the DMKQVM module (DMKQVMRS or DMKQVMRX for 370E systems), and the transition from native mode back to the VM/SP HPO environment is performed. After this transition is completed, the operating system is running in the V=R virtual machine again.

When making the transition back to the VM/SP HPO environment, the operating system operator should ensure that the operating system's I/O configuration and environment are the same as when the transition to native mode took place. If a PSW RESTART function is invoked while running in native mode and without setting the flag byte to return to VM/SP HPO, a restart interruption is reflected to the native system.

Information about the commands used in this support is found in the VM/SP HPO CP Command Reference. A full description of the operating procedures for this support is contained in the Virtual Machine Running Guest Operating Systems.

Virtual Machine Assist Feature

The virtual machine assist feature is a processor hardware feature. It improves system performance. Virtual storage operating systems, which run in problem state under CP, use many privileged instructions and SVCs that cause interrupts that CP must handle. With the virtual machine assist feature, many of these interrupts are intercepted and handled by the

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processor; and, consequently, performance is improved. See the functional characteristics manual for your processor model to see if the virtual machine assist feature is available.

The virtual machine assist feature intercepts and handles interrupts caused by SVCs (other than SVC 76), invalid page conditions, and several privileged instructions. An SVC 76 is never handled by the assist feature; it is always handled by CP.

Although the assist feature was designed to improve system performance, virtual machines may see a performance improvement because more resources are available for virtual machine users.

Using the Virtual Machine Assist Feature

Whenever you IPL VM/SP HPO on a processor with the virtual machine assist feature, the feature is available for all virtual machines. However, the system operator's SET command can make the feature unavailable to CP and, subsequently, available again for all users. The format of the system operator's SET command is:

SET SASSIST ON [[PROC] xx] OFF

If you do not know whether or not the virtual machine assist feature is available, use the class A and E QUERY command. For a complete description of the Class A and E QUERY and SET commands, see the VM/SP HPO CP Command Reference.

If the virtual machine assist feature is available when you log on your virtual machine, it is also supported for your virtual machine. If your directory entry has the SVCOFF option, the SVC handling portion of the assist feature is not available when you log on. The class G SET command can disable the assist feature (or only disable SVC handling). It can also enable the assist feature, or if the assist feature is available, enable the SVC handling. The format of the command is:

SET ASSIST [ON] [SVC] [TMR] [NOSVC] [NOTMR] OFF

You can use the class G QUERY SET command line to find whether you have full, partial, or none of the assist feature available. For a complete description of the Class G QUERY and SET commands, see the VM/SP HPO CP Command Reference.

Restricted Use of the Virtual Machine Assist Feature

Certain interrupts must be handled by CP. Consequently, the assist feature is not available under certain circumstances. CP automatically turns off the assist feature in a virtual machine if it:

- Has an instruction address stop set (ADSTOP).
- Traces SVC and program interrupts.

Since an address stop is recognized by an SVC interrupt, CP must handle SVC interrupts while address stops are set. Whenever you issue the ADSTOP command, CP automatically turns off the SVC handling portion of the assist feature for your virtual machine. The assist feature is turned on again after the instruction is encountered and the address stop removed. If you issue the QUERY SET command line while an address stop is in effect, the response will indicate that the SVC handling portion of the assist feature is off.

Whenever a virtual machine issues a TRACE command with the SVC, PRIV, BRANCH, INSTRUCT, or ALL operands, the virtual assist feature is automatically turned off for that virtual machine. The assist feature is turned on again when the tracing is completed. If the QUERY SET command line is issued while SVCs or program interrupts are being traced, the response will indicate the assist feature is off.

Extended Control-Program Support (ECPS) for VM/370

Extended Control-Program Support for VM/370 (ECPS:VM/370) improves the performance of the processor when executing VM/SP HPO beyond the improvement attained by the virtual machine assist feature described above. ECPS:VM/370 consists of three parts: CP assist, expanded virtual machine assist, and virtual interval timer assist.

CP Assist

The control program assist part of ECPS assists various CP routines that are frequently used. Because these routines are assisted by the hardware without involving VM/SP HPO, performance is improved.

Expanded Virtual Machine Assist

Expanded virtual machine assist handles the processing of additional instructions not handled by the virtual machine assist feature.

Virtual Interval Timer Assist

Virtual interval timer assist provides hardware updating of the virtual interval timer at virtual location X'50'. This results in an update frequency of approximately 300 times per second, the same as for the real interval timer. Procedures that use the virtual interval timer for job accounting, performance measurements, and the like, will therefore generate more accurate and repeatable time data than they would if the virtual timer was being updated by CP routines. Timer updating occurs only while the virtual machine is in control of the real processor.

Using the Extended Control-Program Support:VM/370

Extended Control-Program Support (ECPS) is controlled at two levels: the system and the virtual machine.

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At the system level, ECPS is selectively enabled when the system is loaded. Those parts of the assist microcode installed on your processor that are inconsistent with the software logic are disabled. The class A command:

SET CPASSIST OFF

will disable both CP assist and expanded virtual machine assist. The class A command:

SET SASSIST OFF

disables only the expanded virtual machine assist part of ECPS as well as the virtual machine assist. CP assist is the only part of ECPS that is truly independent.

At the virtual machine level, whenever ECPS is enabled on the system, both expanded virtual machine assist and virtual interval timer assist are automatically enabled when you log on. If you issue the class G command:

SET ASSIST OFF

both assists as well as the existing virtual machine assist are disabled. If you issue:

SET ASSIST NOTMR

only the virtual interval timer assist is disabled. If CP assist is disabled for the system, the class A command:

SET SASSIST ON

will enable the virtual machine assist. You can then enable virtual machine assist and virtual interval timer assist for your virtual machine by issuing the class G command:

SET ASSIST ON TMR

Restricted Use of ECPS

The restrictions on the use of ECPS are the same as those described for the virtual machine assist feature with two additions. Whenever a virtual machine traces external interrupts, the virtual interval timer assist is automatically disabled. When external interrupt tracing is completed, virtual interval timer assist is reenabled. Also, parts of ECPS are disabled when the FRET trap logic is present.

Preferred Machine Assist Feature

Preferred machine assist is a machine feature that improves the performance of the MVS/System Product V=R virtual machine. It allows the MVS/SP V=R virtual machine to operate in real supervisor state. In real supervisor state, the MVS/SP virtual machine uses dedicated channels to directly control its I/O operations. This support eliminates simulation of most I/O operations by CP. I/O operations from the preferred virtual machine to nondedicated channels (controlled by CP), however, are routed to CP for processing. The preferred machine assist support also allows the MVS/SP Release 1.3 or later V=R virtual machine to utilize greater than 16Mb of real storage.

When a processor is running with greater than 16 megabytes of storage online, the preferred machine assist guest can use some, all, or none of the storage above the 16 Mb line, depending on the storage configuration specified by the system programmer at system generation time. See the VM/SP HPO Planning Guide and Reference for more information.

Hardware and software requirements for using preferred machine assist are not presented here. For more information about these requirements, see the VM/SP HPO Planning Guide and Reference.

Preferred Machine Assist Control Switch Assist

Control switch assist offers a variant of preferred machine assist. It is invoked by entering the PMAV parameter on the IPL command. Under control switch assist, certain CP Diagnose and IUCV commands are supported. They are handled as on any nonpreferred machine assist machine.

In addition, with control switch assist installed, a control switch occurs for a preferred machine assist virtual machine (in this case regardless of whether the PMAV parameter has been entered on the IPL command) when that machine becomes enabled for I/O interrupts (from a guest-owned device on a CP-owned channel). This control switch allows CP to gain control and reflect the I/O interrupt to the preferred machine assist virtual machine.

Control switch assist is not available on the 3033 processor. For more information about control switch assist and its requirements, see the VM/SP HPO Planning Guide and Reference.

System Initialization

The system initialization process establishes the preferred machine assist environment. Prefixing starts even when the system is generated as a uniprocessor.

CP sets up the preferred machine assist environment as follows:

Module DMKCPI determines whether the system can operate in preferred machine assist mode (it checks for DMKPMA in the load list). If present, DMKPMA checks for module DMKSLC in the load list and verifies the existence of the preferred machine assist feature on the processor. If these requirements are met, DMKPMA initializes fields in the PSA and other preferred machine assist-related areas.

DMKPMA uses the routine at entry point DMKPMAI1 to initialize the PMAAVAIL field in the PSA. If PMAV is specified on the IPL command, and the control switch assist is installed, then the VPMAAVAI field is also set on in the PSA. DMKPMAI1 also examines the contents of the Real Channel Index Table to determine the channels that are not known to CP. (Any channel that was not defined to CP at system generation is assumed to be dedicated to the preferred machine assist virtual machine.) DMKPMA builds two channel masks— one for the system and the other for the preferred virtual machine. Preferred machine assist uses the masks to route I/O operations either directly to devices on dedicated channels or to CP, for processing certain privileged operations when requested by the preferred machine assist virtual machine, and for routing I/O interrupts.

DMKPMA initializes at entry point DMKPMAI2 the fields used by routines that process privileged operations. DMKPMAI2 also contains routines that give the preferred virtual machine control of absolute page 0 and initialize the prefix registers for the processor. Control returns to DMKCPI to complete system initialization.

Preferred Virtual Machine Initialization

When the virtual machine user issues the IPL command with the preferred machine assist or preferred machine assist V option, CP activates preferred machine assist if the following are true:

- Preferred machine assist (with or without control switch assist) hardware is present.
- CP is AP or MP, the V = R user has affinity set.
- The MVS/SP virtual machine is operating V = R. CP ensures that the contents of register 11 are the same as the AVMREAL field in the PSA.
- The directory option PMA is in the directory entry for the virtual machine. If specified, PMA must immediately follow the V=R directory option. To determine whether this requirement is met, CP examines the VMPMENAB field in the VMBLOK for the virtual machine.
- The preferred virtual machine is operating in extended control mode. To determine whether this requirement is met, CP examines the VMV370R field in the VMBLOK for the virtual machine.
- Minidisks used by the preferred virtual machine are full extent virtual disks.
- To ensure data integrity, devices dedicated to the preferred virtual machine (including full extent virtual disks) must meet the following requirements:
 - Virtual and real addresses must be the same or there should be no RDEVBLOK for the device specified by the virtual address
 - Virtual channel addresses must correspond to channels controlled by CP.
- Devices that are not dedicated to the preferred virtual machine (excluding full extent virtual disks) must meet the following requirements:
 - There should be no RDEVBLOK for the device specified by the virtual address
 - Virtual channel addresses must correspond to channels controlled by CP.

CP calls module DMKPMA (at entry point DMKPMACD) to verify virtual device requirements. If the preferred virtual machine uses a device that violates a virtual device requirement, an error message results and preferred machine assist is not activated.

At virtual machine initialization, CP examines the IPL command and options. Module DMKCFG processes the IPL command at entry point DMKCFGIP. If the IPL command with the preferred machine assist option is correct and all other requirements for using preferred machine assist are met, DMKCFG builds parameters in page 0 for the virtual machine. These parameters allow the system and the IPL simulator to properly initialize and execute the preferred virtual machine. DMKCFG places the following information in page 0:

Location Contents

00	Virtual IPL cylinder or block number
08	Address of the IPL device
0A	VMMLEVEL field from VMBLOK for the virtual machine
0C	Flag bits

X'80'	- STOP requested
X'40'	– ATTN requested
X'20'	 Preferred machine assist requested
X'10'	- Preferred machine assist with control switch
assis	t requested

$0\mathbf{E}$	VDEVTYPC for IPL device
$0\mathbf{F}$	VDEVTPE for IPL device
10	Virtual console address

After initializing page 0, DMKCFG performs the following for the preferred virtual machine:

- Sets on NOTRANS (by initializing to 1 the field VMNOTRAN in VMPSTAT).
- Indicates preferred machine assist is active in VMBLOK (using preferred machine assistON field in VMMCR6 and VMPMUSER).
- Disables virtual interval timer, real timer, Virtual Machine Assist, CP assist, System/370 Extended Facility, and the dual address space facility (cross memory).
- Initializes to 1 the PMAMODE field in PSA. (This field is initialized in both PSAs for AP/MP systems.)
- Initializes to 0 the real address of virtual page 0 and the pointer to VMSEGTBL. CP relocates the page at DMKSLC-4096 to absolute page 0, allowing the preferred virtual machine to control absolute page 0.

DMKCFG passes control to DMKVMI to IPL the virtual machine. DMKVMI saves the parameters and passes control to the preferred virtual machine.

Dual Address Space Assist

MVS/System Product cross-memory services increase the efficiency of communication between address spaces. Using cross-memory, a program operating in the primary address space can pass control directly to another program operating in the secondary address space. Thus, an MVS/SP virtual machine can use cross-memory services for data movement, data access, and program sharing.

The Dual Address Space Assist is a processor microcode enhancement that allows MVS to use XMEM services while running in a virtual machine. Dual Address Space Assist extends the capability of programs to communicate between address spaces through enhanced data movement and program calling procedures. Under VM/SP HPO, MVS/SP cross-memory services are improved for 3033 processors equipped with the 3033 Extensions feature (an architectural extension to the processor). If this assist is installed on the processor (or both processors of an AP or MP system), you activate it for the system by issuing the CP command SET S370E ON XMEM. Activate the assist for the MVS/SP virtual machine by issuing the CP command SET 370E ON XMEM. If the directory option XMEM is entered in the directory entry for the MVS/SP virtual machine, the cross-memory assist is available when the virtual machine logs on.

MVS Page Fault Assist

MVS Page Fault Assist is an MVS-oriented performance enhancement in the form of hardware microcode. It is a modification to the existing hardware-detected Page Translation Exception process that reduces the amount of time required to process the interrupt. When the hardware determines that a page translation exception interrupt should be generated, it attempts to resolve the page exception without generating an interrupt. Page Fault Assist attempts to resolve first-reference page exceptions, which occur when a program makes a reference to a page that has not been referenced since it was allocated or released from storage. It attempts to resolve it by allocating a 4K real frame cleared to zeros. If the page exception is not a first reference, Page Fault Assist generates a page exception.

MVS Page Fault Assist works in conjunction with the Dual Address Space Assist. MVS Page Fault Assist is supported for MVS/System Product Release 3 systems on 3033 processors equipped with the 3033 Extensions feature. The STBYPASS VR option activates MVS Page Fault Assist for the virtual machine.

Virtual Machine Communication Facility

The Virtual Machine Communication Facility (VMCF) allows any logged-on user to transfer messages, control data, data files, or combinations of all three to another virtual machine running under the same VM/SP HPO system. Information is transferred directly from one virtual storage to the other virtual storage with CP buffering the information. Only one data page frame must be locked at any one time. The amount of data that can be transferred is limited only by the virtual storage sizes of the virtual machines involved. VMCF contains five data movement and seven control functions and is invoked by a virtual machine via the DIAGNOSE interface (code X'0068'). A special external interrupt code, X'4001', notifies a virtual machine that a VMCF communication is pending. A virtual machine can have a maximum of 50 messages active at any one time. The number of messages is an equate in the DMKVMC module and can be changed to accommodate different storage sizes.

VMCF Diagnose Interface

When a virtual machine issues a DIAGNOSE instruction with a function code of X'0068', the Rx register contains the virtual address, doubleword-aligned, of a 40-byte parameter list. This parameter list (VMCPARM) contains a hexadecimal code to identify the specific VMCF subfunction. It also contains the data addresses, data lengths, and control information that are required to execute the particular subfunction.

The DIAGNOSE instruction, a privileged operation, is processed by DMKPRV, which passes control to DMKHVC, the DIAGNOSE interface module. DMKHVC, in turn, validates the function code and, if the code is X'0068', turns control over to DMKVMC, the VMCF module. DMKVMC validates the VMCPARM address and length, the subfunction code, and passes control to the appropriate subroutine. The VMCF subfunctions and their codes are as follows:

Code	Subfunction
X'0000'	Allow virtual machine communication
X'0001'	Disallow virtual machine communication
X'0002'	Initiate a SEND request
X'0003'	Initiate a SEND/RECV request
X'0004'	Initiate a SENDX request
X'0005'	Accept data from a SEND or SEND/RECV request
X'0006'	Cancel specific request you initiated
X'0007'	Reply to a SEND/RECV request
X'0008'	Quiesce incoming communications
X'0009'	Resume accepting communications
X'000A'	Notify a user that you are ready for communications
X'000B'	Reject a specific incoming communication

Special VMCF External Interrupt

Whenever a source virtual machine uses VMCF to correspond with another virtual machine (sink), the sink is notified of the pending communication via a special external interrupt (code X'4001'). When this interrupt is unstacked and processed, a copy of the information in the source's parameter list is passed to the sink in an external interrupt buffer. The buffer is defined when a user allows virtual machine communication. The contents are referred to as the external interrupt message header. When certain transactions (SEND, SEND/RECV, SENDX) have been completed, a final response external interrupt is passed back to the source. The message header associated with this interrupt contains residual counts pertaining to the transferred data and data transfer return codes.

VMCF Control Blocks and Data Areas

Figure 22 shows the relationship between the various VMCF control blocks and data areas. When a virtual machine allows virtual machine communication, VMCF generates a master VMCBLOK and places it at the head of a queue pointed to by the VMCPNT field of the user's VMBLOK. Two fields in this master VMCBLOK define the address (VMCVADA) and length (VMCLENA) of the user's external interrupt buffer. The length must include the maximum size of any potential SENDX data in addition to the 40 bytes for the external interrupt message header.

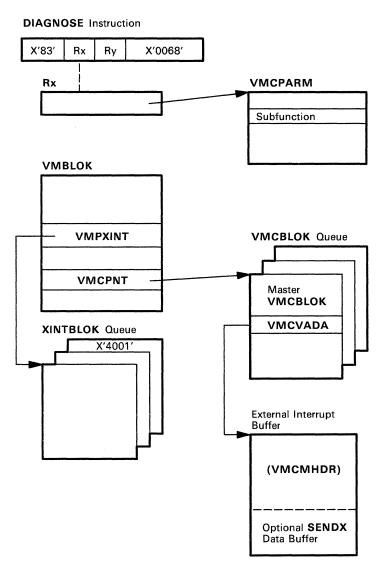


Figure 22. VMCF Control Block Relationships

When a source virtual machine executes a VMCF subfunction, a VMCBLOK is built, initialized with data from the parameter list (VMCPARM), and stacked on the VMCBLOK queue pointed to by the VMCPNT field in the sink's VMBLOK. If an XINTBLOK for a X'4001' external interrupt has not already been stacked for the sink machine, DMKVMC builds one and stacks it on the XINTBLOK queue pointed to by the VMPXINT field in the sink's VMBLOK. VMCF external interrupts are assigned a sort code of X'7FFFFFF', giving them the lowest priority in the external interrupt queue. Each virtual machine clears its own VMCF control blocks.

Special Messages Facility

The Special Messages Facility allows a user on one virtual machine to send special messages to another virtual machine via the SMSG command. The Special Messages Facility may be used with the Virtual Machine Communications Facility (VMCF) or the Inter-User Communications Facility (IUCV) to send the messages. In the Special Message environment, CP acts as a source machine with the receiver of the special message being the sink. This relieves the burden from the issuer of SMSG of having to perform authorization and other setup necessary for sending messages to the receiving virtual machine. Authorization is performed by CP.

The issuer of SMSG is responsible for sending message text that is meaningful to the receiving virtual machine. The format and handling of special messages is entirely up to the receiving machine, which may be one designed by the installation or prepared by others.

Before the receiving virtual machine can accept special messages, it must be running with the Special Message flag ON, and it must have issued AUTHORIZE (via DIAGNOSE X'68') with CP. The authorization includes supplying the External Interrupt Buffer address and size. To ensure receiving the entire message, the receiving virtual machine should specify the size as 280 bytes (room for a 40-byte header and a 240-byte message buffer).

Note: A 'MSG TOO LARGE' condition may occur if you issue an SMSG command on a 3279 or 3278 Model 5 terminal to send a message that is longer than the message length the receiving virtual machine has specified.

Set SMSG ON by setting on the SMSG flag in the VMCF parameter list when issuing an AUTHORIZE. You may also issue the CP command SET SMSG ON. Either method sets the Special Message flag on in the VMBLOK. When this is done, any other virtual machine can issue the SMSG command to the userid of the receiving virtual machine.

Before the receiving virtual machine can receive special messages via IUCV, it must do the following:

- Enable itself to receive external interrupts
- Set bit 30 of control register 0 to a value of one
- Issue the IUCV DECLARE BUFFER function

- Issue the IUCV CONNECT function to the CP Message System Service
- Turn on the special message flag by issuing the class G command SET SMSG IUCV.

If the receiving virtual machine chooses not to accept special messages at any time, it can merely issue SET SMSG OFF. CP would then inform any machine issuing the SMSG command that the virtual machine is not receiving special messages. When it is ready to resume accepting special messages, the virtual machine need only to issue SET SMSG ON.

Figure 23 shows the processing when an SMSG command is issued.

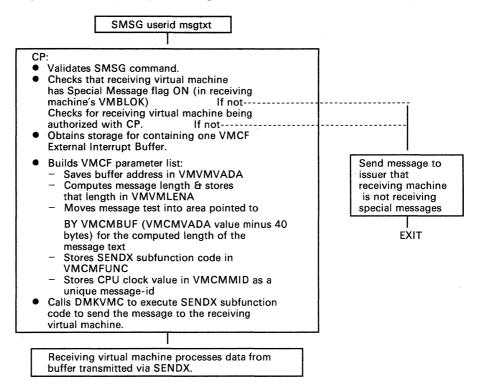


Figure 23. SMSG Command Processing

Inter-User Communications Vehicle

There are 17 IUCV functions with which you can initiate, interrogate, receive, reply to, and terminate individual communications. You can interrogate, receive, or terminate any communication over a particular path. You can interrogate and/or terminate any communication over any path. The IUCV functions are:

ACCEPT
CONNECT
DECLARE BUFFER
DESCRIBE
PURGE
QUERY

QUIESCE RECEIVE REJECT REPLY RESUME RETRIEVE BUFFER SEND SET CONTROL MASK SET MASK SEVER TEST COMPLETION TEST MESSAGE

ACCEPT Function

The target communicator uses the ACCEPT function to respond to a connect request from a virtual machine or from a CP system service. If the target communicator chooses not to complete the connection, it invokes the SEVER function. If the ACCEPT function initiator does not declare a buffer, the system generates a specification exception.

The ACCEPT function initiator receives a return code if one of the following occurs:

- The path specified is not a pending connection.
- The path has been severed by the originator of the CONNECT function.
- An IUCV ACCEPT was issued in response to an APPC/VM CONNECT, or an APPC/VM ACCEPT was issued in response to an IUCV CONNECT.

Both the initiator and the originator have their path descriptor entries set to valid, and an external interrupt is built. A request for QUIESCE mode when the originator invokes CONNECT results in an indicator's being set in both the path descriptor and the external interrupt. The QUIESCE option is ignored for APPC/VM CONNECTs and ACCEPTs.

When the ACCEPT function is invoked from CP system code, the message limit and priority setup is based on the parameter list values. If the message limit is not specified, a default value is used. When an APPC/VM ACCEPT is invoked, the message limit for the path is one.

When the ACCEPT function is not invoked from CP system code, a directory check is made to determine whether or not the target communicator entry exists. The directory check search sequence is:

- 1. The initiator's IUCV entries to find an entry for the source communicator.
- 2. The initiator's IUCV entries to find an entry for ALL.

If neither of these authorizations exists, then communication is not allowed.

For the message limit, the lower value of the parameter list limit or the directory limit is used. If you specify neither, the default value prevails. The established message limit returns to the initiator in the parameter list and then passes to the source communicator in the connection complete external interrupt.

To receive data in the parameter list as well as in a buffer, the communicator must specify the PARMDATA = YES option. Parameter list data is limited to two fullwords, or eight bytes, of data. On APPC/VM paths, however, parameter list data is *not* permitted. The PARMDATA = YES option is ignored for APPC/VM ACCEPTS.

If the originator of the IUCV CONNECT or APPC/VM CONNECT is a virtual machine, and WAIT = NO was specified, then an external interrupt is stacked and the initiator receives a normal return code. If the originator of the APPC/VM CONNECT specified WAIT = YES, then the CPEXBLOK created during CONNECT processing is stacked to complete the originator's CONNECT and the initiator receives a normal return code.

If the originator is CP system code, the pending connection is located and dequeued from the CP pending connection chain and a CPEXBLOK is stacked to indicate to the originator that the connection is complete. Finally, the initiator receives a normal return code.

CONNECT Function

To start communication between virtual machines or between a virtual machine and a CP system, use the CONNECT function. During the CONNECT process:

- If a buffer is not declared, a specification exception is generated.
- If the source communicator is a CP system service, directory checking is skipped.
- If the target communicator is not logged on or has not declared a buffer, a return code is sent to the initiator.
- If the CONNECT function is invoked from CP system code, the message limit and priority setup is based on the parameter list values. If the message limit is not specified, a default value is used.
- If the target communicator is a CP system service (where the ID starts with an asterisk), the name is verified by looking it up in a table. If not found, a return code is sent to the initiator.
- If directory checking is needed, the directory is checked to verify that this connection is authorized. The directory check search sequence is:
 - The initiator's IUCV entries to find an entry for the target communicator.
 - The initiator's IUCV entries to find an entry for ALL.
- When directory authorization is not found, a return code is sent to the initiator.
- To receive data in the parameter list as well as in a buffer, the communicator must specify the PARMDATA = YES option. Parameter list data is limited to two fullwords, or eight bytes, of data. On APPC/VM paths, however, parameter list data is *not* permitted. The PARMDATA = YES option is ignored for APPC/VM CONNECTs.
- When an APPC/VM CONNECT is invoked, the message limit for the path is one.

The message limit is specified either in the IUCV directory control statement, which provides connection authorization, or in the parameter list, or in both. When a message limit is specified in both, use the lower one of the two. The established message limit returns to the initiator in the parameter list and then passes to the target communicator in the pending connection external interrupt.

In the directory entry, priority authorization for IUCV CONNECTs is allowed only if specified. For APPC/VM CONNECTs, priority is ignored.

If either the initiator or the target communicator exceeds its maximum connection limit, the initiator receives a return code.

When the target communicator is a virtual machine, a pending connection external interrupt is stacked for it. When the target communicator is a CP system service, the connect entry point for that service is invoked. When the originator is CP system code, a pending connection is added to the CP pending connection chain. When an APPC/VM CONNECT with WAIT = YES is specified, a CPEXBLOK is obtained and chained off the initiator's IUCVBLOK. IUCV then exits to the dispatcher to wait for the CPEXBLOK to be stacked. After the CPEXBLOK is stacked, the initiator receives a message that the CONNECT is complete.

DECLARE BUFFER Function

To specify the buffer address where the virtual machine external interrupt should be stored, use the DECLARE BUFFER function.

It is unnecessary for CP system code to issue the DECLARE BUFFER function, because CP does not receive external interrupts.

If the initiator already has a buffer, the initiator receives a return code.

Based on the maximum number of connections, the system builds an IUCVBLOK and a communication control table (CCT) for the initiator. The directory contains the maximum number of connections permitted for each user; if none appears, the default is four. When errors are encountered reading the directory, the initiator receives a return code.

Notes:

- 1. You can reduce the overhead involved in reflecting IUCV external interrupts to the virtual machine by declaring the buffer (with the DECLARE BUFFER function) so that it fits entirely within one page. You can reduce the overhead even further by declaring the buffer so that it fits entirely within page zero of the virtual machine.
- 2. The IUCV external interrupt submask bits are set on when executing the DECLARE BUFFER function. You must invoke the SET MASK function to change any of these bit settings.

DESCRIBE Function

The target communicator uses the DESCRIBE function to determine the presence of any messages not previously described or reflected in a message pending IUCV external interrupt. The parameter list will receive the pertinent information about the message.

The search routine for the SEND queue uses a first-in first-out (FIFO) approach. The first undescribed and unreflected MSGBLOK found is selected. APPC/VM MSGBLOKs are described only if the corresponding path is in RECEIVE state. Because of their position in the SEND queue, priority MSGBLOKs are scanned first. The DESCRIBE function causes the following MSGBLOK information to be stored:

- Path ID
- Target message class
- Message ID
- Message flags
- Length of message
- Length of answer area.
- Note: The target message class, message ID, and length of answer area are not stored for APPC/VM messages.

This information eventually allows the target communicator to accept the MSGBLOK data through use of the RECEIVE function.

The DESCRIBE function sets the condition code to indicate:

- No undescribed MSGBLOK found
- MSGBLOK found; description stored.

If the message being described contains the message data in the parameter list, or is an APPC/VM SENDREQ MSGBLOK, the message is considered to be received and the MSGBLOK is removed from the SEND queue.

Note that IUCV describes each MSGBLOK once and the target communicator is responsible for removing those MSGBLOK(s) from the SEND queue. Use of either the RECEIVE function or REJECT function removes the MSGBLOK(s).

The DESCRIBE function clears the pending message external interrupt for each MSGBLOK.

Note: CP system code (aside from IUCV support) cannot use the DESCRIBE function.

PURGE Function

The source communicator uses the PURGE function to terminate a single message. The message is destroyed immediately under the following circumstances:

- If it has not been described to the target communicator, which means the target communicator is completely unaware that the message was ever sent.
- If it is on the source communicator's REPLY queue.

However, the message, when previously described to the target communicator, is marked as purged and the target communicator receives a return code in the parameter list when next invoking either the RECEIVE function or the REPLY function, whereupon the message is destroyed. Note that the PURGE function moves no data.

The PURGE function parameter list describes the to-be-purged message; this list includes:

- A path ID. (The value of the path ID determines which target communicator queues are to be searched.)
- A message ID.

When both of these are specified, then the source message class must also be specified. However, when the message ID is not specified, then the message class is optional.

Note: The parameter list flags indicate which fields are to be used in locating the message.

The search for the message starts with the source communicator's REPLY queue, then the target communicator's RECEIVE queue, and then the target communicator's SEND queue. The first MSGBLOK that matches the message specification terminates the search. When a message is purged, the message ID, the path ID, the source message class, the message tag, the message flags, and the audit trail are all stored in the parameter list.

A condition code reflects the completion status of the PURGE function. Detection of certain error conditions stores a return code, which indicates the type of error found.

QUERY Function

Use the QUERY function to extract IUCV information about the virtual machine. The IUCV external interrupt buffer size is returned in general register 0. General register 1 holds the data for the maximum number of connections outstanding for the particular virtual machine.

Notes:

- 1. The QUERY function does not take a parameter list.
- 2. If errors are encountered, a condition code is set.
- 3. CP system code cannot invoke the QUERY function.

QUIESCE Function

Use the QUIESCE function when you wish to suspend temporarily the ability of the communicating partner to send messages.

When the request is to quiesce all paths, each path quiesces individually and the initiator receives a return code. For each path on a QUIESCE ALL or for the single specified path (if the path is marked severed or if the path is already quiesced), the initiator receives a normal return code. Otherwise, the system builds an external interrupt based on the path being quiesced and marks the path as quiesced.

When the communicating partner is a CP system service, the quiesce entry point for the service is located and external interrupt data is passed to it via a CALL linkage. The initiator receives a return code upon return of control.

If a virtual machine is the communicating partner, the external interrupt for the communicating partner is stacked and the initiator receives a return code.

RECEIVE Function

The target communicator uses the RECEIVE function to accept messages.

When the complete message moves from the send area to the specified receive area, the MSGBLOK for the designated message moves from the SEND queue to the RECEIVE queue. If the receive area cannot contain the message, the MSGBLOK stays in the SEND queue and the length of the remaining data is stored in the parameter list. When the RECEIVE function is again activated, the remainder of the message becomes available.

RECEIVE function input specifies which message is to be processed, identified by:

- Message ID
- Path ID
- Target message class.

Notes:

- The message ID and target message class are not valid for APPC/VM RECEIVEs. If the message ID is not specified, then any combination of path ID and target message class can be specified. The flag fields of the RECEIVE parameter list indicate the type of message description to be used. If no parameter list search fields are specified (for IUCV RECEIVEs), then the first message that has not been partially received is presented. If message data is contained in the parameter list and a REPLY is expected, the MSGBLOK is moved from the SEND queue to the RECEIVE queue. On a one-way (no REPLY) message, the MSGBLOK is destroyed.
- 2. After a partially received message, the message must be completely specified in order for the remainder of the message to be received.

The target communicator obtains the required message description in one of the following ways:

- By use of the DESCRIBE function.
- Via presentation by an external interrupt.

Note: A different linkage is used for a CP system service because neither the DESCRIBE function nor an external interrupt is available.

The RECEIVE function input identifies a receive area to which the message goes if BUFLIST = YES is not specified. A beginning address and a length describes the receive area. The address must be real to the virtual machine, and there is no alignment requirement for the beginning address. Data movement terminates when the message length exceeds the receive area length or the message ends.

Note: If length specification is 0 for either data area, there is no data movement.

If BUFLIST = YES is specified, the BUFFER = parameter of the IUCV macro instruction provides the address of a list of addresses and lengths of discontiguous buffers to contain the message text. Also, the value specified with the BUFLEN = parameter is the total of the individual buffer lengths in the list pointed to be BUFFER =.

Completion of the RECEIVE function results in update of the parameter list receive area. If BUFLIST = NO, the address is set to the originally designated length plus the number of bytes moved. The updated length is the residual count when the return code indicates that the buffer was too short, or the return code indicates that it is the remaining length of the buffer.

If BUFLIST = YES is specified, the address in the parameter list points to the entry in the buffer list to continue processing. The buffer list is updated throughout IUCV processing. As data is moved, the address in the list entry is incremented by the length moved, and the length in the list entry is decremented by that length. When the length is zero, the list pointer is incremented to indicate the next entry to process. The total length specified in the parameter list is also decremented with each move. It reflects the amount of data to be received.

The MSGBLOK moves directly to the REPLY queue when either the send area or the receive area has addressing exceptions and/or protection exceptions. When this happens, the target communicator receives a return code to that effect while the audit trail notifies the source communicator.

A condition code is set to report RECEIVE function completion status. If an error condition occurs, its detection activates the setting of a return code to indicate the type of error.

When a priority MSGBLOK moves to the RECEIVE queue, the concept of priority disappears. The REPLY function can reintroduce priority.

REJECT Function

To reject a single message from the source communicator, the target communicator executes the REJECT function. The MSGBLOK for the designated message moves from the target communicator's SEND queue or RECEIVE queue to the source communicator's REPLY queue. Use of the REJECT function moves no data.

REJECT function input specifies which message is to be rejected. The information is identified by:

- Message ID
- Path ID
- Target message class.

If the message ID is not specified, then any path ID and target message class combination are valid. The flag field of the REJECT parameter list indicates the type of message description to be used. If flags are not specified, the REJECT function is terminated with a specification exception.

The program searches the target communicator's queues for the designated message: first, the RECEIVE queue; then, the SEND queue. The first MSGBLOK that matches the designated message moves to the source communicator's REPLY queue. Rejection of the designated message is via setting a condition code.

The REJECT function parameter list stores the message ID, the path ID, and the target message class on completion.

To indicate message rejection, the audit trail is updated.

A condition code is set to report REJECT function completion status. If certain types of errors occur, storing of a return code indicates it.

REPLY Function

The target communicator uses the REPLY function to respond to a message. The MSGBLOK for the designated message moves from the target communicator's RECEIVE queue to the source communicator's REPLY queue. Data moves from the specified reply area to the source communicator's answer area.

It is assumed that REPLY function input has a complete description of the designated message requiring a reply. Partial descriptions of a message are not supported. If insufficient information is supplied the parameter list, locating the designated message will be impossible. The message description comprises:

- Message ID
- Path ID
- Target message class.

If the designated message is not found, a parameter list return code is set.

The target communicator turns on the parameter list flag to specify that the reply message has priority. The reply MSGBLOK thus precedes any nonpriority MSGBLOK(s) in the queue, immediately following any earlier-designated priority MSGBLOK(s).

Input to the REPLY function identifies a reply area if ANLIST is not specified. The description includes a beginning address and a length. The address must be real to the virtual machine although no alignment requirement is made for the beginning address. Data moves between the target communicator's reply area and the source communicator's answer area terminate when either area length is exhausted. A length of 0 for either area prevents data transfer. On completion of the REPLY function, any length mismatch results in an error condition being posted.

If ANSLIST = YES is specified, the ANSBUF = parameter of the IUCV macro instruction provides the address of a list of addresses and lengths of discontiguous buffers to contain the message reply text. Also, the value specified with the BUFLEN = parameter is the total of the individual buffer lengths in the list pointed to be ANSBUF =.

When addressing exceptions and protection exceptions occur while accessing either the reply area or the answer area, the MSGBLOK moves to the REPLY queue, a return code is sent to the target communicator, and the audit trail notifies the source communicator.

If ANSLIST = NO, completion of the REPLY function updates the reply area description in the parameter list and sets the address at the original length *plus* the number of bytes moved. Note that the updated length is the residual count if the return code indicates the buffer was too short or is the remaining length of the buffer on a normal return code.

If ANSLIST = YES is specified, the address in the parameter list points to the entry in the answer list to continue processing. As data is moved, the address in the list entry is incremented by the length moved, and the length

in the list entry is decremented by that length. When the length is zero, the list pointer is incremented to indicate the next entry to process. The total length specified in the parameter list is also decremented with each move. It reflects the amount of data in the reply.

The REPLY function cannot execute if a message is sent by a one-way SEND function. Such a message never resides on the target communicator's RECEIVE queue. Thus, a "no message found" condition results.

A condition code reflects the REPLY function completion status. If an error condition occurs, its detection activates storing of a return code.

RESUME Function

Use the RESUME function to restore IUCV communications after you invoke the QUIESCE function.

If your request is to resume all paths, each path resumes individually and the initiator receives a return code.

After a RESUME function is issued for a single specified path or for ALL paths (if the path is marked severed or is not quiesced), a normal return code is returned to the initiator.

If the communicating partner is not CP, the external interrupt is stacked for the virtual machine and the initiator receives a return code.

If the communicating partner is a CP system service, the program locates the RESUME entry point for the service and the external interrupt data passes to it via a CALL linkage.

When the CALL linkage processing is completed, the initiator receives a return code.

RETRIEVE BUFFER Function

Use of the RETRIEVE BUFFER function notifies IUCV that the virtual machine no longer needs IUCV.

The program generates a SEVER ALL to terminate all messages on all paths for the designated communicator. Any control blocks built for this communicator at DECLARE BUFFER time are dismantled and released.

Note: CP system code, outside of IUCV support, cannot use the RETRIEVE BUFFER function.

SEND Function

The SEND function initiates communication by creating a MSGBLOK and enqueuing it on the target communicator's SENDQ.

The input to the SEND function must completely describe the message being sent. It must specify the source communicator's path ID and the source communicator's and target communicator's message classes. Also required when invoked from a virtual machine is the message tag that is presented to the source communicator upon completion of the message. The MSGTAG field is used by IUCV for CP-initiated messages and is not available to a CP service.

Note: The message class and message tag are not valid on APPC/VM SENDs.

If BUFLIST = YES and ANSLIST = YES are not specified, the user can specify two data areas that are used to move data between the source communicator and the target communicator. The send area contains the data to be moved from the source communicator to the target communicator. The answer area, for IUCV SENDs, is the area into which the target communicator's REPLY data is moved. For APPC/VM SENDs, the target communicator's SENDDATA data is moved into the answer area. Each area is defined by a beginning address and a length. Each address must be real to the virtual machine. Either data area can be anywhere within the source address space. There is no boundary alignment requirement on the beginning addresses.

The user may choose to send the data in the parameter list and not specify a send area, or he may choose to have the REPLY data returned in the parameter list and not specify an answer area. When using the parameter list data option, the user is limited to two fullwords, or eight bytes. Parameter list data is not valid on APPC/VM SENDs.

If BUFLIST = YES is specified, the BUFFER = parameter of the IUCV macro instruction provides the address of a list of addresses and lengths of discontiguous buffers to contain the message text. Also, the value specified with the BUFLEN = parameter is the total of the individual buffer lengths in the list pointed to be BUFFER =.

If ANSLIST = YES is specified, the ANSBUF = parameter of the IUCV macro instruction provides the address of a list of addresses and lengths of discontiguous buffers to contain the message text. Also, the value specified with the BUFLEN = parameter is the total of the individual buffer lengths in the list pointed to be ANSBUF =.

The IUCV SEND function does not move any data. The target communicator invokes the RECEIVE and/or REPLY functions to move data. Because of this, a description of the send area and the answer area is stored in the MSGBLOK for use during either function. The description consists of the beginning buffer or list addresses, the total length of each area, and the PSW key to be used for protection checking during access to each area. The APPC/VM SEND function moves the data if a receive or answer area has been previously defined by the target for the path. Data areas are not validity-checked during the SEND operation, unless the data is actually moved. The check occurs when the areas are used. Access exceptions in the source address space are recognized and reported during processing of the RECEIVE function and/or REPLY function.

Using a parameter list flag field, you can optionally alter the SEND function to either a priority message or a one-way message. The SEND function with the priority flag set enqueues the MSGBLOK on the target communicator's SEND queue preceding all nonpriority MSGBLOK(s) and following all earlier priority MSGBLOK(S). The SEND function with the one-way flag set designates the MSGBLOK as one that does not allow a reply. When the target communicator receives a one-way message, the MSGBLOK skips the target communicator's RECEIVE queue, and is immediately placed on the source communicator's REPLY queue.

A condition code is set to report SEND function completion status. If an error condition occurs, its detection activates the setting of a condition code as well as the storing of a return code to indicate which error was detected.

SET CONTROL MASK Function

The SETCMASK function enables or disables external interrupts for the five types of IUCV control interrupts:

- Connection Pending
- Connection Complete
- Path Severed
- Path Quiesced
- Path Resumed.

Specify all mask bits in the parameter list. All mask bits are used and override all previous mask specifications.

Before the Control Mask bits are interrogated, a virtual machine must first be enabled for IUCV control type external interrupts by using the SETMASK function.

Note: The SETCMASK function cannot be used from CP system code.

SET MASK Function

The SET MASK function enables or disables external interrupts for priority messages and nonpriority messages, priority replies and nonpriority replies, and IUCV controls. Specify all mask bits in the parameter list. All mask bits are used and override any and all previous mask specifications. Use of the mask is in addition to the global external interrupt mask in the PSW.

Note: The SET MASK function cannot be used from CP system code.

SEVER Function

Use the SEVER function to terminate IUCV communications capabilities.

If the path is complete, both communicators must invoke a SEVER function to the path. After one communicator's invocation, all messages still on the path terminate and the communicating partner receives a sever external interrupt. The communicating partner then, if desired, can dequeue the terminated message(s). When finished, the communicating partner invokes the SEVER function.

For the SEVER ALL, each message on either the SEND queue or the RECEIVE queue is designated as severed in the audit trail. Then the REJECT function is invoked to terminate each message. The communication control table (CCT), part of the IUCVBLOK, contains a list of the valid path ID(s). Once message termination is complete, each valid path from 0 through the highest valid path in the CCT also terminates. The process completes with the release of all the space used by the identification control blocks. All designated paths are masked as invalid.

Path termination for a valid path proceeds as follows:

- If the path is invalid or out of range, the initiator receives a return code.
- If the path is valid, the QUIESCE function is invoked to prevent further communication.
- If the path is marked as severed, the entry is set to available and the initiator receives a return code. Any messages in the REPLY queue for the path are dequeued and the space returned to storage.
- For each message on either the SEND queue and/or the RECEIVE queue for this path, a REJECT is issued.
- Each message generated by the initiator for this path terminates with a PURGE operation.

If the communicating partner is a CP system service, the program locates the sever entry point for the service and the external interrupt data passes to it via a CALL linkage.

When the CALL linkage processing completes, the initiator receives a return code.

If the communicating partner is not CP, the external interrupt is stacked for the virtual machine and the initiator receives a return code.

Path termination for an invalid path proceeds as follows:

• If the path is severed because the SEVER function has been invoked from the target communicator, the path is set to available and the initiator receives a normal return code.

- If the communicating partner has received the pending connection interrupt, the blocks are marked as severed and the communicating partner must also invoke the SEVER function to fully dismantle the control blocks.
- If the communicating partner has not received the pending connection interrupt, the path terminates without intervention from the communicating partner.
- Note: CP system code (aside from IUCV support) cannot use the SEVER ALL function.

TEST COMPLETION Function

The source communicator executes the TEST COMPLETION function to complete a communication. The procedure includes:

- Dequeuing the MSGBLOK of the completed message from the source communicator's REPLY queue.
- Destroying the dequeued MSGBLOK.

Data is not moved into the answer buffer by the TEST COMPLETION function. However, if the REPLY function is used with the DATA=PRMMSG option, the eight bytes of data appear in the TEST COMPLETION parameter list.

TEST COMPLETION function specifies which message is to be processed; the message is identified by:

- Message ID
- Path ID
- Source message class.

Note: The message ID and message class are not recognized for APPC/VM messages.

If the message ID is not specified, then any path ID and source message class combination is valid for the TEST COMPLETION function. The flag field of the TEST COMPLETION parameter list indicates the type of message description to be used. If no parameter list search fields are specified, the first REPLY queue message is presented. If the specified message is found, then along with the setting of a normal condition code, the following parameter list fields are stored:

- Message ID
- Path ID
- Flags
- Audit trail
- Message tag
- Source message class.

Notes:

- 1. The message ID, message tag, and source message class are not stored for APPC/VM messages. If the specified message is not found, just a condition code is set. Note that the TEST COMPLETION function clears the pending message complete external interrupt for the REPLY queue message.
- 2. CP system code (aside from IUCV support) cannot use the TEST COMPLETION function.

TEST MESSAGE Function

The virtual machine communicator invokes the TEST MESSAGE function to determine whether or not messages or replies are pending. The virtual machine enters a wait state when neither message or replies are pending.

The virtual machine communicator uses the TEST MESSAGE function to poll for IUCV current messages and/or replies as well as to wait for future messages and/or replies. An APPC/VM message pending on a path which is not in RECEIVE state is ignored by the TEST MESSAGE function.

Use of the TEST MESSAGE function allows an instruction stream to poll for a message and, simultaneously, to enter a wait state. If, during a wait state, an IUCV message and/or reply pending occurs, the virtual machine communicator reinvokes the TEST MESSAGE function to continue processing and a proper condition code is sent to the initiator.

Because IUCV messages are also presented as external interrupts, the TEST MESSAGE function introduces the anomaly of identifying IUCV messages simultaneously through the external interrupts and through the TEST MESSAGE function's polling capability. If the PSW external interrupt mask bit or the SET MASK function are disabled for IUCV messages and replies, the TEST MESSAGE function may be used to poll, and the DESCRIBE, RECEIVE, and TEST COMPLETION functions used to receive all information about IUCV messages and replies. This polling reduces external interrupt handling overhead.

The condition code setting indicates that there is at least one message and/or reply in the SEND queue or the REPLY queue. Status information is forthcoming when you execute the DESCRIBE function and the TEST COMPLETION function. Note that the condition code setting indicates the TEST MESSAGE function completion status.

Note: CP system code (aside from IUCV support) cannot use the TEST MESSAGE function.

Return codes applicable to IUCV are described in VM/SP HPO System Facilities for Programming.

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IUCV Restrictions

The following areas of IUCV are limited:

- The use of IUCV is supported for a second-level CP system. The IUCV functions are not simulated, but are reflected to the second-level system.
- Each virtual machine is limited to less than 65,536 outstanding connections at one time.
- IUCV does not recognize anything smaller than a virtual machine. If two communicators choose to establish multiple communication paths, it is the responsibility of these communicators to manage these paths.
- A CP system service cannot establish communication with itself.
- The sum total of all CP system service connections cannot be greater than 4096.

IUCV Trace Table Entries

IUCV support generates a trace table entry for each IUCV function. There is one trace table entry type for IUCV entries (X'15') with a subtype field to indicate exactly which IUCV function was invoked. All uses of IUCV, except for the three functions listed above, whether invoked from a virtual machine or from CP system code, are recorded in the CP trace table. The address portion of the old PSW is recorded as part of the entry. A bit in the flag byte indicates whether this address is to be interpreted as a real address (when invoked from CP) or a virtual machine address (when invoked from a virtual machine). For virtual machine addresses, the address of the associated VMBLOK can be obtained from preceding trace table entries.

The IUCV trace facilities can be suppressed at assembly time by setting &TRACE (9) to 0 or at execution time by setting the X'80' bit to 0 in the TRACFLG3 field of the PSA.

The trace table entries for IUCV are built in two sections. The first part, consisting of IUCV subtype, the address of the USER who invoked the IUCV function, and the bit indicating whether the USER's address is real or virtual (guest real), is built only after preliminary tests are successful. These tests check to see if:

- The parameter list is valid.
- The parameter list is on a doubleword boundary.

When the first part of the trace table entry is built, a bit is set in the flags byte indicating that it is a partial trace table entry. If the function should terminate with a return code, this partial entry is not updated in the trace table. If the function completes without error, the rest of the trace table entry is filled in and the 'partial entry' bit is reset. For IUCV functions that invoke other functions, the secondary functions are also recorded as having been invoked from CP. Examples of these secondary functions are:

- Retrieve Buffer generates a Sever ALL.
- Sever generates a Reject for each incoming outstanding message and a purge for each outgoing outstanding message.
- A Connect to a CP system service causes control to go to that service and will usually invoke the Accept function.
- The IUCV support invokes the Test Completion function to dequeue messages intended for the CP system.

IUCV External Interrupts

Prior to establishing any connections, the virtual machine must invoke the Declare Buffer function to indicate to IUCV where data associated with an external interrupt is to be stored.

There is one external interrupt type for external interrupts generated by IUCV. This external interrupt type is X'4000'. When an IUCV external interrupt is reflected to the virtual machine, the interrupt code is stored for the virtual machine and an 'External Interrupt Buffer' is stored at the address specified in the DECLARE BUFFER function. One field of this buffer, IPTYPE, is an external interrupts subtype to indicate exactly why the external interrupt occurred. The possible codes and their meanings are as follows:

- 01 IUCV Connection Pending
- 81 APPC/VM Connection Pending
- 02 IUCV Connection Complete
- 82 APCC/VM Connection Complete
- 03 IUCV Path Severed
- 83 APCC/VM Path Severed
- 04 Path Quiesced
- 05 Path Resumed
- 06 Incoming Priority Reply
- 07 Incoming Nonpriority Reply
- 87 Function Complete
- 08 Incoming Priority Message
- 88 SENDREQ Interrupt
- 09 IUCV Incoming Nonpriority Message
- 89 APCC/VM Incoming Message.

While the Connect, Accept, Sever, Quiesce, and Resume functions always cause a pending external interrupt to be queued for the target virtual machine or passed to a CP service, incoming messages and incoming replies can be fielded by the target virtual machine as either external interrupts or by the satisfaction of the Describe or Test Completion functions.

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When a virtual machine executes a Send, a pending external interrupt of subtype 08, 09, 88, or 89 is queued for the target virtual machine. If the target virtual machine is both enabled for external interrupts (bit 7 in the virtual PSW is set to 1) and enabled for messages (via the Set Mask function), then the external interrupt will be reflected. (In the case of APPC/VM incoming message interrupts, the corresponding path must be in RECEIVE state before the interrupt is presented.) If either condition is not met, the external interrupt will remain queued. If the target virtual machine is not enabled but instead executes the Describe function, the information about the pending message will be returned in the parameter list and the pending external interrupt for that particular message will be cleared.

The condition of being enabled for IUCV messages and issuing a Describe will cause unpredictable results. In a similar manner, the condition of being enabled for IUCV replies and issuing a Test Completion will cause unpredictable results. Although it is unpredictable as to whether the external interrupt is presented or the IUCV function (Describe or Test Completion) is satisfied, it is never the case that both the external interrupt and the IUCV function completion will occur for the same message/reply.

All IUCV external interrupts are controlled by the external mask bit in the virtual PSW (bit 7) and the submask bit in control register zero (bit 30).

There are separate additional mask bits for IUCV external interrupts that can be enabled and disabled by the Set Mask function. Five mask bits are defined for use by the Set Mask function. These mask bits are used to separately mask incoming priority messages, incoming nonpriority messages, incoming priority replies, incoming nonpriority replies, incoming APPC/VM messages, incoming APPC/VM SENDREQ interrupts, APPC/VM function complete interrupts, and IUCV control interrupts of subtypes 01, 02, 03, 04, and 05. When both the external interrupt mask and the appropriate Set Mask bits are enabled, the external interrupt can occur.

The SETCMASK function lets you set masks for the individual IUCV control interrupts. The IPCMASK field specifies which of the five types of IUCV control interrupts for the virtual machine are enabled. These interrupts are:

- Connection Pending
- Connection Complete
- Path Severed
- Path Quiesced
- Path Resumed.

The SETMASK function is interrogated before the SETCMASK function mask. If you use the SETMASK function to specify that all control interrupts are disabled, then the SETCMASK settings are not interrogated. If you specify that all control interrupts are enabled by using the SETMASK function, then the SETCMASK settings are interrogated to determine how to handle the individual types of control interrupts. After IUCV initialization and until you issue the SETMASK and SETCMASK functions, all IUCV submask bits are on, enabling all IUCV external interrupts.

When the virtual machine has completed all communications, the virtual machine may invoke the Retrieve Buffer function to cause IUCV to stop using the external interrupt buffer and prevent further IUCV communication.

External Interrupts are not reflected to CP system code. For communications to CP services, external interrupts are replaced with one of two possible linkages depending on whether the function was initiated outside CP or from within CP.

The order of reflection for IUCV external interrupts is as follows:

- Control interrupts (Subtype X'01', X'81', X'02', X'82', X'03', X'83', X'04', X'05') in FIFO (First In, First Out) order
- 2. Priority Replies (Subtype X'06')
- 3. Nonpriority Replies (Subtype X'07')
- 4. Priority Messages (Subtype X'08') and SENDREQs (Subtype X'88')

5. Nonpriority Messages (Subtype X'09')

6. APPC/VM Messages (Subtype X'89').

IUCV Control Blocks and Data Areas

Figure 24 shows the relationships between the various IUCV control blocks and data areas. IUCV identifies and describes a communicator with an IUCVBLOK. The Communication Control Table (CCT), part of the IUCVBLOK, contains a Path Description Entry (PDENT) for each path defined for the communicator. There is a PDENT in the source CCT and a different PDENT in the target CCT for each path defined. Each of these PDENTs is identified, or named, by a Path Description Identifier (PDID or Path ID). There is PDID for the source communicator's view of a path (its PDENT for the path) and another PDID for the target communicator's view of the same path (its PDENT). At the interface to IUCV, there is no relationship assumed between the two PDID values. A particular communicator can address a path only by that communicator's PDID.

Messages are represented by Message Blocks (MSGBLOKs). A MSGBLOK is created when a communication is initiated and is destroyed when a communication is terminated. A message, and its representation as a MSGBLOK, is fully identified by three values. These values are the PDID, the Message Class, and the Message ID. The source and target communicators each have their own description of a particular message.

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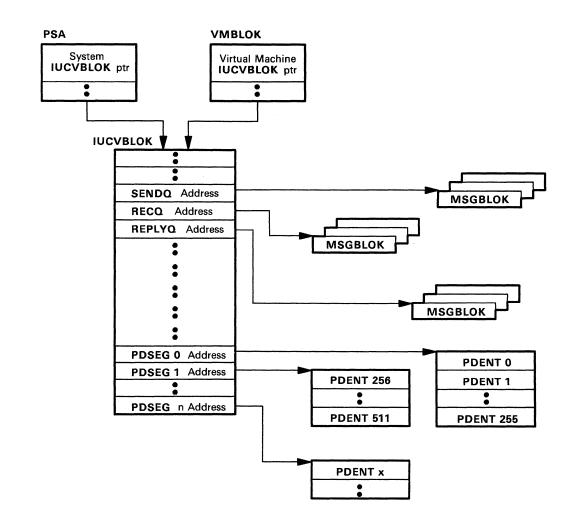


Figure 24. IUCV Control Block Relationships

A MSGBLOK represents an active communication and is chained onto one of the queues anchored in a CCT. A CCT contains 3 queues. These queues keep track of communication status. Queued MSGBLOKs can be handled FIFO, enqueued by priority, and dequeued by value.

The Send Queue (SENDQ) is defined for target communicators. It is the queue of MSGBLOKs that have been created by the source communicator but not yet accepted by the target communicator. The Receive Queue (RECQ) is also defined for target communicators. It is the queue of MSGBLOKs that have been accepted by the target communicator, but to which the target communicator has not yet replied. The Reply Queue (REPLYQ) is defined for source communicators. It contains those MSGBLOKs that have been replied to by a target communicator, but which have not yet been terminated.

A normal communication uses a MSGBLOK in a predefined manner. The MSGBLOK moves in sequence from a SENDQ, to an RECQ, and finally to a REPLYQ.

VM/VS Handshaking

The VM/VS Handshaking feature provides a communication path between CP and virtual machine operating systems that makes each system control program aware of certain capabilities or requirements of the other.

The following is a discussion of VM/VS Handshaking as it relates to OS/VS1. Functions of VM/VS Handshaking incorporated in the control program are available and applicable to any operating system that can be system generated to use this function.

VM/VS Handshaking for OS/VS1 performs the following functions:

- Closes CP spool files when the VS1 job output from its DSO, terminator, and output writer is complete
- Processes VS1 pseudo page faults
- Provides an optional nonpaging mode for VS1 when it is run in the VM/SP HPO environment.

When a VS1 virtual machine with the handshaking feature is loaded (via IPL), its initialization routines determine whether the handshaking feature should be enabled. First, VS1 determines if it is running under the control of VM by issuing a STIDP (Store Processor ID) instruction. STIDP returns a version code; a version code of X'FF' indicates VS1 is running with VM/SP HPO. If VS1 finds a version code of X'FF', it then issues a DIAGNOSE (X'00') instruction to store the VM extended-identification code. If an extended-identification code is returned to VS1, VS1 knows handshaking is supported. At this time or any time after IPL, the operator of the VS1 virtual machine can issue the CP SET PAGEX ON command to enable the pseudo page fault handling portion of handshaking. If the VS1 virtual machine is in the nonpaging mode and, if the pseudo page fault handling is active, full handshaking support is available.

Because the VS1 system does no paging, any ISAM programs run under VS1 are treated as though they are running in an ADDRSPC = REAL partition. Therefore, the ISAM option is required for the VS1 machine to successfully execute the ISAM program.

Closing CP Spool Files

If the handshaking feature is active, VS1 closes the CP spool files when its job output from the DSO, terminator, and output writer is complete. Once the spool files are closed, CP processes them and they are sent to the real printer or punch. During its job termination processing, VS1 issues a DIAGNOSE (X'08') instruction to pass the CP CLOSE command to CP for each spool file.

Pseudo Page Faults

A page fault is a program interruption that occurs when a page marked "not in storage" is referred to by an instruction with an active page. The virtual machine referring to the page is placed in a wait state while the page is brought into real storage. Without the handshaking feature, the entire VS1 virtual machine is placed in page wait until the needed page is available.

However, with the handshaking feature, a multiprogramming (or multitasking) VS1 virtual machine can dispatch one task while waiting for a page request to be answered for another task. CP passes a pseudo page fault (program interrupt X'14') to VS1. When VS1 recognizes the pseudo page fault, it places only the task waiting for the page in page wait and can dispatch another tasks.

When a page fault occurs for a VS1 virtual machine, CP checks that the pseudo page fault portion of handshaking is active and that the VS1 virtual machine is in EC mode and enabled for I/O interruptions. Then, CP reflects the page fault to VS1 by:

- Storing the virtual machine address that caused the page fault at location X'90' (the translation exception address)
- Indicating a program interruption (interrupt code X'14') to VS1
- Removing the VS1 virtual machine from page wait and execution wait.

When VS1 recognizes program interruption code X'14', it places the associated task in wait state. VS1 can then dispatch other tasks.

When the requested page becomes available in real storage, CP indicates the same program interruption to VS1, except that the leftmost bit in the translation exception address field is set on to indicate completion. VS1 removes the task from page wait; the task is then eligible to be dispatched.

VS1 Nonpaging Mode

When VS1 runs under VM, it executes in nonpaging mode if:

- Its virtual storage size is equal to the size of the virtual machine.
- Its virtual machine size is at least 1024K bytes and no more than 4096K bytes. For VS1 Release 6, the maximum size is 16,384K bytes.
- The VM/VS Handshaking feature is available.

When VS1 executes in nonpaging mode, it uses fewer privileged instructions and avoids duplicate paging. The VS1 Nucleus Initialization Program (NIP) fixes all VS1 pages to avoid the duplicate paging.

Note: The working set size may be larger for a VS1 virtual machine in nonpaging mode than for one in paging mode.

Miscellaneous Enhancements

A VS1 virtual machine with the handshaking feature avoids many of the instructions or procedures that would duplicate the function that VM provides. For example, VS1 avoids:

- ISK (Insert Storage Key) instructions and uses a key table
- ENABLE/DISABLE sequences in the VS1 I/O Supervisor (IOS)
- TCH (Test Channel) instructions preceding SIO (Start I/O) instructions.

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CP Interrupt Handling

Interrupt processing occurs within the CP environment. More than 30 modules control the process of interrupting events brought about by CP or virtual machine activity. Each module handles a particular I/O device or class or a function of CP (for example, timers, paging, SVCs). For an overview of interrupt handling, see Figure 25.

Program Interrupt

Program interrupts occur in two states. If the CPU is in the supervisor state, the interrupt usually indicates a system failure in the CP nucleus and causes a system abnormal termination. If the CPU is in the problem state, a virtual machine is in execution. If the program interrupt indicates that the Dynamic Address Translation (DAT) feature has an exception, a virtual machine issued a privileged instruction, or a protection exception occurred for a shared segment system, CP takes control and performs any required processing to satisfy the exception. Usually, the interrupt is not apparent to the virtual machine. Most other program interrupts result from virtual machine processing and are reflected to the virtual machine for handling.

When a program interrupt occurs, the program interrupt handler (DMKPRG) is entered. Program interrupts can result from:

- Normal paging requests
- A paging request by a virtual machine in EC mode (virtual relocate mode)
- Privileged instructions
- PER events
- Program errors
- Monitor calls.

For information about paging requests, see "Allocation Management" in this section.

Туре	Module
SVC	DMKSVCIN
External	DMKPSAEX
Machine Check	DMKMCHIN
1/0	DMKIOSIN
Program Check	DMKPRGIN

Interrupt Handler Modules

Interrupt From	Action/Module
Unknown channel	Ignored - DMKDSPCH
Unsolicited device end and for:	Build IOBLOK
Console	DMKCNSIN
3270s on BSC lines	DMKRGA or DMKRGB
Local 3270, 3158, and 3066 consoles	DMKGRF
Unit record, real spooling	DMKRSPEX
Solicited device end	DMKSTKIO
Channel error	DMKCCHNT
Monitor tape I/O operation	DMKMONIO
Dedicated device error - DASD	DMKDASER, DMKDADER or DMKDAUER
Dedicated device error - Tape	DMKTAPER
3270 BSC line and channel errors	DMKBSC
Recoverable errors	DMKSTKIO
Unrecoverable errors	DMKIOERR

I/O Interrupt Handler (DMKIOT) Actions

Reason for Program Check	Module
Normal paging	DMKPTRAN
Paging - virtual machine in EC mode	DMKVAT
Supervisor State	DMKDMP
Vector Facility	DMKPRVLG
Privileged instruction	DMKPRVLG or DMKFPS
DIÃGNOSE	DMKHVC
Timers	DMKTMR
Virtual Machine I/O	DMKVSIEX
console	DMKVCNEX
unit record, virtual spooling	DMKVSPEX

Program Check Interrupt Handler (DMKPRG) Actions

Figure 25. Overview of Interrupt Handling

Privileged Instructions

If a program interrupt is caused by the virtual machine issuing a privileged instruction when it is running in supervisor state, DMKPRVLG or DMKFPS obtains the address of the privileged instruction and determines the type of operation requested. If the virtual machine was running in problem state, the interrupt is reflected back to the virtual machine.

For I/O privileged instructions, DMKPRVLG transfers control to the virtual I/O executive program (DMKVSIEX).

Missing Interrupt Handler

A missing interrupt condition exists when an I/O device fails to return an interrupt to the control program after a specified time interval. The missing interrupt handler automatically monitors system I/O activity to detect this condition and to attempt to correct it.

To detect missing interrupts, at system initialization module DMKCPI schedules TRQBLOKs to enter module DMKDID. DMKDID tests the RDEVBUZY and RDEVSCHD flag bits in each RDEVBLOK. It sets RDEVMID to one when it scans the RDEVBLOKs and finds RDEVBUZY or RDEVSCHD on, indicating that a device interrupt is pending for this interval.

Then, the first-level interrupt handler, DMKIOT, resets the flags when the device returns an interrupt. If both flags are on when DMKDID again receives control, a missing interrupt condition exists. CP attempts to correct the condition.

If MIH is set on, CP attempts to simulate the interrupt. If CP cannot simulate the interrupt, the user receives an interface control check (IFCC). CP then writes a record to the recording area, sends a message to inform the system operator of the missing interrupt, and tells him whether or not the missing interrupt was cleared.

If MIH is set off, CP pursues no corrective action, but sends a message to the operator informing him that a missing interrupt was detected. CP also writes a record to the error recording area.

The user can set MIH on by specifying an option in the directory, or by issuing the class G SET command.

If you enable tracing activity, CP traces the missing interrupt and records it as trace table entry X'19'. See the Virtual Machine Diagnosis Guide for more information about tracing.

Interrupt timing varies widely among devices. Certain devices are more critical than others. To allow greater flexibility for monitoring I/O activity, CP specifies a different time interval for each class of device. You can change the IBM supplied defaults (provided in module DMKSYS) in two ways. You can change the values supplied in the SYSMIH macro statement in DMKSYS and reassemble DMKSYS. Or you can use the SET MITIME command.

The default intervals and the devices monitored are:

CLASDASD and CLASFBA	15 seconds
CLASGRAF	30 seconds (except TYP1053 and TYP328X)
CLASTAPE	10 minutes
CLASURI and CLASURO	1 minute (except TYP3800 and TYP3289E)
Miscellaneous devices	12 minutes

Note: Miscellaneous devices include MSS devices, TYP1053X and TYP328X graphic devices, and TYP3800 and TYP3289E unit record output devices.

The SET MITIME command dynamically changes the intervals specified in DMKSYS. Time intervals used in the SET MITIME command remain in effect until you issue another SET MITIME command, or until the system is reloaded (through IPL). SET MITIME is described in the VM/SP HPO Operator's Guide. See the VM/SP HPO Planning Guide and Reference for a description of the SYSMIH macro statement. For the method of operation refer to the topic "Monitoring I/O Operations" later in this publication.

I/O Interrupts

I/O interrupts from completed I/O operations initiate various completion routines and the scheduling of further I/O requests. The I/O interrupt handling routine also gathers device sense information.

Machine Check Interrupts

When a machine check occurs, CP Recovery Management Support (RMS) gains control to save data associated with the failure for FE maintenance. RMS analyzes the failure and determines the extent of damage.

Damage assessment results in one or more of the following actions being taken:

- System termination
- In attached processor or multiprocessor configurations, a processor or an attached processor is varied offline (system converts to uniprocessor mode)
- A Vector Facility is disabled
- One or more channels are marked offline
- Virtual user running at the time of error is terminated
- Refreshing of damaged information with no effect on system configuration
- Refreshing of damaged information with the defective storage page removed from further system use
- Error recording only for certain soft machine checks.

The system operator is informed of all actions taken by the RMS routines. When a machine check occurs during startup (before the system is set up well enough to permit RMS to operate successfully), the processor goes into a disabled wait state and places a completion code of X'00B' in the leftmost bytes of the current PSW.

SVC Interrupts

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When an SVC interrupt occurs, the SVC interrupt routine (DMKSVCIN) is entered. If the machine is in the problem state, DMKSVC branches to DMKSVDIN. DMKSVDIN takes the following action:

- If the interruption was the result of an ADSTOP (SVC code X'B3'), the message ADSTOP AT XXXXX is sent to the user's terminal, the overlaid instruction is replaced, and the virtual machine is placed in console function mode (CP mode) via DMKCFMBK.
- If the interruption was the result of an error recording interface (SVC 76), DMKSVD checks for valid parameters and passes control to DMKVER to convert virtual device addresses in the error record to real device addresses. The actual recording is accomplished in DMKIOE and DMKIOF. If recording is not possible, the interrupt is reflected back to the virtual machine.
- If the virtual machine's page 0 was not in real storage, then all general and floating-point registers are saved, the user's VMBLOK is flagged as being in an instruction wait, and control is transferred (via GOTO) to DMKPRGRF to reflect the interruption to the virtual machine.
- If the virtual machine's page 0 is in main storage, an appropriate SVC old PSW is stored in the user's page 0 and the interruption is reflected to the virtual machine, bypassing unnecessary register saving (fast reflection). If the new virtual PSW indicates a mode or enablement change, all registers are saved in the VMBLOK and control is transferred to DMKDSPB for PSW validation.

If the machine is in the supervisor state, then DMKSVC determines the SVC interruption code and branches to the appropriate SVC interruption handler.

SVC 0

Impossible condition or terminal error. The SVCDIE routine initiates an abnormal termination by using the DMKDMPDK routine.

SVC 4

Reserved for IBM use.

SVC 8

A link request that transfers control from the calling routine to the routine specified by register 15. The SVCLINK routine sets up a new save area, and then saves the caller's base register in register 12 and save area address in register 13, and the return address (from the SVCOPSW) in the new save area. If the called routine is within the resident CP nucleus, SVCLINK places its address in register 12 and branches directly to the called routine. If the called routine is in a pageable module, a TRANS macro is performed for register 12 to ensure that the page containing the called routine is in storage. Upon return from the TRANS execution, the real address of the pageable routine is placed in register 12 and SVCLINK branches to the called routine. The real storage location of DMKCPE is the end of the resident CP nucleus. Any modules loaded at a higher real storage address

are defined as pageable modules. If bit zero of register 15 is on when DMKSVC is entered, then the caller has requested AFFINITY. DMKSVC turns on a bit in the save area passed to the caller to indicate that control is to be returned to the caller on the same processor on which it was running before issuing the SVC. It is not ensured that control will be retained by the initiating processor throughout the called operation, but only that final return will occur on the initiating processor.

SVC 12 (X'0C')

A return request that transfers control from the called routine to the calling routine. The SVCRET routine is invoked. If the routine that issued the SVC 12 is pageable, then DMKPTRUL is called to unlock the page. SVCRET then restores registers 12 and 13 (addressability and save area address saved by SVCLINK), places the user's return address (also saved in this area) back into the SVCOPSW, and returns control to the calling routine by loading the SVCOPSW.

SVC 16 (X'10')

Releases current save area from the active chain (removes linkage pointers to the calling routine). The SVCRLSE routine releases the current save area by placing the address of the next higher save area in register 13 and returns control to the current routine by loading the SVCOPSW. This SVC is used by second level interrupt handlers to bypass returning the first-level handler under specific circumstances. The base address field (register 12) in the save area being released is examined to determine if the bypassed routine is in a pageable module. If so, DMKPTRUL is called to unlock the page.

SVC 20 (X'14')

Obtain a new save area. The SVCGET routine places the address of the next available save area in register 13 and the address of the previous save area in the save area pointer field of the current save area.

SVC 24 (X'18')

In attached processor mode, SVC 24 causes the instructions following the SVC to be executed by the main processor. This SVC is used only via the SWITCH macro to force processing to continue on the main processor (the processor capable of performing I/O). If the SWITCH macro determines that the code is currently running on the main processor then the SVC is not issued.

In multiprocessor mode, the SWITCH macro is coded with the 'PROC' operand. SVC 24 causes the instructions following the SVC to be executed on the processor specified by the "PROC = " operand.

Save areas are initially set up by DMKCPI for use by the SVC linkage handlers. There is one list of save areas per processor. The number of save areas to be pre-allocated is determined by DMKCPI based upon the model number of the processor and upon whether or not the system was AP or MP generated.

External Interrupts

Timer Interrupt

If DMKEXTIN is entered because of a timer interruption, the state of the machine must be determined. If the virtual machine was in wait state, control is transferred to DMKDSPCH, and the virtual machine stays idle until another interruption occurs. If the virtual machine is in problem state, the address of the current user's VMBLOK is obtained from RUNUSER. The user's current PSW (VMPSW) is updated from the external interruption old PSW, the address of the current VMBLOK is placed in register 11, and control is transferred to DMKDSPCH. For additional information about timers, see "Virtual Timer Maintenance."

External Interrupts

If DMKEXTIN is entered because the operator pressed the console interrupt button (INTERRUPT), a CPEXBLOK is stacked to do the following:

- If Multiprocessor mode (MP mode), continue processing on the processor which has an I/O path to the operator's console.
- Reference the current system operator's VMBLOK (DMKSYSOP).
- Disconnect this virtual machine.

The operator can now log on from another terminal. Pressing the console interrupt button activates an alternate operator's console.

Note: If this interrupt comes from the attached processor, it is ignored.

For a description of the processing of the external interruption command, refer to module DMKCPB in the Entry Point Directory.

For a discussion of external interrupts that occur in attached processor or multiprocessor mode, see "Multiprocessor External Interrupts" under "Machine Check Handler – Attached Processor and Multiprocessor Applications."

Extended Virtual External Interrupts

To reflect external interrupts to a virtual machine, DMKDSPE queues an XINTBLOK on a chain pointed to by VMPXINT in the VMBLOK. The XINTBLOKs are chained sequentially by the XINTSORT field that contains the collating number of the pending interruption. If more than one interruption has the same collating number, the interruption codes are ORed together in the XINTCODE field for possible simultaneous reflection. When a virtual machine is enabled for external interrupts, the XINTBLOK queue for that machine is searched for an eligible block. An XINTBLOK is eligible for reflection if one or more bits of the XINTMASK field match the bits in the rightmost halfword of control register 0. If the interruption was an interruption such as CPU timer or clock comparator, the block is left chained because reflection does not reset these interrupts. If the reflected interruption(s) does not represent all those coded in the XINTMASK field, the block is left chained and only the interrupts that were reflected are reset. In all other conditions, the XINTBLOK is unchained and returned to free storage.

A special external interrupt, code X'4001' notifies a virtual machine of a pending Virtual Machine Communication Facility request. The XINTBLOK for this interrupt is set up with an XINTSORT field of X'7FFFFFFF', the lowest priority.

System Support

Free Storage Management

During its execution, CP occasionally requires small blocks of storage that are used for the duration of a task. CP obtains this storage from the free storage area. The free storage area is divided into various size subpools for all requests that are smaller than or equal to 1,024 doublewords. Requests for 128 doublewords or fewer are rounded to 2 doubleword boundaries (the subpool width is 2 doublewords). Requests above 128 doublewords and fewer than 1024 doublewords are rounded to 32 doubleword boundaries (subpool width is 32 doublewords).

The requester informs the free storage manager of the size of the block required. Two stacks are maintained for each logical subpool so that extended items are isolated from nonextended items and only used if the nonextended stack is empty. All nonextended items are time stamped when they are pushed onto the subpool stack.

When a user logs off, for example, (DMKFRTRS is called) all extended items are returned to the free list, but only those nonextended items that haven't been used for some time interval (an algorithm parameter) are returned to the free list. This saves storage on large systems.

If the request for free storage cannot be fulfilled, the free storage manager requests the temporary use of a page of storage from the dynamic paging area. If a page is obtained, the page is chained to the free storage area and used for that purpose until it is no longer needed and subsequently returned to the dynamic paging area.

If the request for a page cannot be fulfilled, the requester waits until free storage becomes available.

Storage Protection

CP provides both fetch and store protection for real storage. The contents of real storage are protected from destruction or misuse caused by erroneous or unauthorized storing of fetching by the program. Storage is protected from improper storing or from both improper storing and fetching, but not from improper fetching alone.

When the processor accesses storage, and protection applies, the protection key of the current PSW is used as the comparand. The protection key of the processor is bit positions 8-11 of the PSW.

If the processor access is prohibited because of a protection violation, the operation is suppressed or terminated, and a program interruption for a protection exception takes place.

When the reference is made to a channel, and protection applies, the protection key associated with the I/O operation is used as the comparand. The protection key for an I/O operation is in bit positions 0-3 of the CAW and is recorded in bit positions 0-3 of the CSW stored as a result of an I/O operation. If channel access is prohibited, the CSW stored as a result of the operation indicates a protection-check condition.

When a storage access is prohibited because of a store protection violation, the contents of the protected location remain unchanged. If a fetch protection violation occurs, the protected information is not loaded into an addressable register, moved to another storage location, or provided to an I/O device.

To use fetch protection, a virtual machine must execute the set storage key (SSK) or set storage key extended (SSKE) instruction. The fetch protection bit in the storage key refers to the data area that is to be protected. CP subsequently:

- 1. Checks for a fetch protection violation when handling privileged and nonprivileged instructions.
- 2. Saves and restores the fetch protection bit (in the virtual storage key) when writing and recovering virtual machine pages from the paging device.
- 3. Checks for a fetch protection violation on a write CCW (except for spooling or console devices).

A special case of storage protection occurs when the CMS nucleus resides in a protected shared segment. The CMS nucleus may be protected and still be shared by many CMS users. After a virtual machine has used a protected shared segment, CP checks the pages of the segment for changes. If a virtual machine user has changed a page in a protected shared segment using an ADSTOP, STORE, or TRACE command, that virtual machine receives a private copy of the segment and virtual machine execution continues. If a virtual machine user has changed a shared page by any other means, CP issues an error message and places the virtual machine in console function mode. If the segment protection feature is active, the virtual machine user receives program check PROG04 when attempting to alter a shared segment page.

Storage Validation

At system load, the loader (DMKLD00E) uses the TB instruction as it relocates itself to the high-end of storage and as it loads the system modules into storage. The system nucleus must reside in contiguous storage. If an unusable or non-addressable frame is detected within the area reserved for the nucleus, the system load is terminated with a disabled wait state code X'AAAAAA'. There is one exception. Non-addressable frames and frames having errors encountered in the virtual=real area do not cause a disabled wait state at system load. Instead, informational messages are sent to the system operator. This presents a special consideration for virtual machine operating systems running V=R. The V=R guest should use appropriate storage techniques to validate V=R storage and avoid machine errors in real storage.

The system cannot be initialized on any supported processor when an unusable or non-addressable frame is found either at location 0 of main storage or from DMKSLC through the frame in main storage where DMKSAV ends. System initialization routines DMKCKP, DMKSAV, and DMKSTA issue the TB instruction to determine the status of every frame of real storage. System initialization will fail with unpredictable results if DMKCKP or DMKSAV cannot be loaded at their expected real storage locations. If a non-addressable frame or a frame containing errors is detected within the area reserved for the nucleus (excluding the V = R area), system initialization is terminated with a disabled wait state code X'14'. Storage frames reserved for the V = R area are not validated at system initialization. V = R area frames are validated only at system load time as described above. Non-addressable and invalid frames encountered outside the area occupied by the system modules are identified to the system operator by a series of informational messages.

Executing the Pageable Control Program

Calls to pageable routines are recognized at execution time by the SVC 8 linkage manager in DMKSVC. For every SVC 8, the called address (in the caller's GPR15) is tested to see if it is within the resident nucleus. If it is less than DMKCPEND and greater than DMKSLC, the called routine's base address is placed in GPR12 and control is passed to the called routine in the normal way. However, if the called address is above DMKCPEND or below DMKSLC, the linkage manager issues a TRANS macro, requesting the paging manager to locate and, if necessary, page-in the called routine. The TRANS is issued with LOCK option. Thus, the lock count associated with the called routine's real page indicates the responsibility count of the module.

- When the module is called, the count is incremented.
- When the routine exits via SVC 12, the count is decremented.

When the count reaches zero, the pageable routine is unlocked and is eligible to be paged out of the system. However, because all CP pageable modules are reenterable, the page is never swapped out, but when the page is stolen, it is placed directly on the free page list.

Because unlocked pageable routines participate in the paging process in a manner similar to user virtual storage pages, the least recently used approximation used by page selection tends to make highly used control program routines, even when not locked, remain resident. The called routine is locked into real storage until it exits. Thus, it can request asynchronously scheduled function, such as I/O or timer interrupts, as long as it dynamically establishes the interruption return address for the requested operation and does not give up control via an EXIT macro prior to receiving the requested interruption.

Addressability for the module, while it is executing, is guaranteed because the CALL linkage loads the real address of the paged module into GPR12 (the module base register) prior to passing control. If all addressing is done in a base/displacement form, the fact that the module is executing at an address different from that at which it was loaded is not apparent. Although part of CP is pageable, it never runs in relocate mode. Thus, the processor is not degraded by the DAT feature being active, and no problems occur because of handling disabled page faults.

System Support Modules

The system support modules provide CP with several common functions for data conversion and control block scanning and verification. Most of the routines are linked to via the BALR option of the CALL macro, and make use of the BALRSAVE and TEMPSAVE work areas in DMKPSA. Two exceptions are the virtual and real I/O control block scan routines DMKSCNVU and DMKSCNRU. These routines do not alter the contents of the BALRSAVE area, and hence may be called by another low-level BALR routine.

Control Register Usage

Every IBM System/370 processor provides the program with 16 logical control registers (logical registers since the number that are active depends on the features installed in the machine at any one time) that are addressable for loading and storing from basic control (BC) mode. CP provides only a single control register, control register zero, for normal virtual machines, and for processing systems that do not require the full set of registers (for example, CMS, or VSE).

Any user whose virtual machine operating system requires the use of control registers other than control register zero, can request the full set of 16 registers by specifying the ECMODE option in the directory entry for his virtual machine. A virtual machine, which utilizes any System/370 features that use the control registers, requires the ECMODE option. Some of these features are expanded timer support of the System/370 CPU timer, clock comparator, the virtual relocate mode and its instructions, RRB, LRA, PTLB, virtual monitor calls, virtual Program Event Recording (PER), etc.

Restrictions and Conventions for Pageable CP Modules

Pageable CP modules must observe the following restrictions and conventions when they are designed and coded:

- The module must be entered by the standard SVC 8 CALL linkage. Modules entered by BALR or GOTO cannot be pageable. The module must return to its caller by SVC also.
- The module cannot contain any A- or V-type address constants that point to locations within itself or within other pageable modules, and it cannot contain any CCWs that contain data addresses within themselves. The only exceptions are address constant literals generated as the result of calls to other modules (because these addresses are dynamically relocated at execution time, they must be resolved by the loader to the loaded address of the called module) and a pageable module that locks itself into storage. In practice, this restriction means that data or instructions within the pageable routine must be referenced via base/displacement addressing, and the address in register 15 for a CALL may not be generated by a LOAD ADDRESS instruction.
- The pageable module must be no more than 4096 bytes in length. (The one exception is DMKSTP.)

If the three above design and coding restrictions are adhered to, the CP module can be added to the existing pageable nucleus modules by utilizing the service routine, VMFLOAD, which is described in VM/SP HPO Service Routines Program Logic. Additional information can be found in the VM/SP HPO Installation Guide.

Figure 26 lists all the executable resident and pageable modules.

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Executable Resident Modules

DMKACR	DMKGRE	DMKPSA	DMKTTX
DMKACS	DMKGRF	DMKPTR	DMKTTY
DMKBSC	DMKGRG	DMKPTS	DMKUNT
DMKCCD	DMKGRH	DMKPTT	DMKVAT
DMKCCF	DMKGRI	DMKQCN	DMKVAU
DMKCCH	DMKGRT	DMKQCO	DMKVCN
DMKCCO	DMKHVC	DMKQCP	DMKVCP
DMKCCS	DMKIOC	DMKQCQ	DMKVCQ
DMKCCT	DMKIOE	DMKQVM	DMKVCR
DMKCCW	DMKIOF	DMKRET	DMKVCS
DMKCFM	DMKIOJ	DMKRGA	DMKVCT
DMKCNS	DMKIOQ	DMKRGB	DMKVCU
DMKCNT	DMKIOS	DMKRGC	DMKVCV
DMKCPX	DMKIOT	DMKRGD	DMKVCW
DMKCSC	DMKIUA	DMKRGE	DMKVCX
DMKCVT	DMKIUB	DMKRNH	DMKVFR
DMKCVU	DMKIUE	DMKRPA	DMKVIO
DMKDAD	DMKIUN	DMKRSP	DMKVMA
DMKDAS	DMKIUS	DMKRST	DMKVRR
DMKDAU	DMKLOC	DMKSCH	DMKVRS
DMKDEX	DMKLOK	DMKSCN	DMKVSC
DMKDGD	DMKMCH	DMKSCO	DMKVSD
DMKDGF	DMKMCT	DMKSEL	DMKVSE
DMKDID	DMKMHC	DMKSPK	DMKVSF
DMKDMP	DMKMPO	DMKSSS	DMKVSG
DMKDMQ	DMKMSW	DMKSST	DMKVSI
DMKDRD	DMKOPR	DMKSSU	DMKVSJ
DMKDSB	DMKPAG	DMKSSV	DMKVSP
DMKDSP	DMKPAH	DMKSTK	DMKVSQ
DMKENT	DMKPGS	DMKSVC	DMKVSR
DMKEXT	DMKPGT	DMKSVD	DMKVST
DMKFPS	DMKPGU	DMKSWA	DMKVSU
DMKFRE	DMKPMA	DMKTBN	DMKVSV
DMKFRT	DMKPRG	DMKTMR	DMKVSW
DMKGRC	DMKPRV	DMKTRK	DMKVSX
DMKGRD	DMKPRW	DMKTRQ	DMKWAI

Figure 26 (Part 1 of 2). Executable Modules

Executable Pageable Modules

DMKACO	DMKCLK	DMKEPS	DMKOPE	DMKTRP
DMKALG	DMKCMD	DMKERM	DMKPEI	DMKTRT
DMKALO	DMKCPB	DMKERP	DMKPEL	DMKTRU
DMKAPI	DMKCPI	DMKGIO	DMKPEN	DMKTRX
DMKAPS	DMKCPJ	DMKGRA	DMKPEQ	DMKUCC
DMKAPT	DMKCPM	DMKHPS	DMKPER	DMKUDR
DMKAPU	DMKCPN	DMKHPT	DMKPET	DMKUDU
DMKAPV	DMKCPO	DMKHPU	DMKPGM	DMKURS
DMKAPW	DMKCPP	DMKHVD	DMKPST	DMKUSO
DMKAPX	DMKCPS	DMKHVE	DMKREI	DMKUSP
DMKAPY	DMKCPT	DMKHVF	DMKRPD	DMKUSQ
DMKAPZ	DMKCPU	DMKIDR	DMKRPI	DMKVBM
DMKATS	DMKCPV	DMKIDU	DMKRPW	DMKVCA
DMKBIO	DMKCPW	DMKIOG	DMKRSE	DMKVCB
DMKBLD	DMKCPY	DMKIOH	DMKRSF	DMKVCH
DMKCAC	DMKCPZ	DMKISM	DMKRSQ	DMKVDA
DMKCAO	DMKCQC	DMKIUC	DMKSAV	DMKVDB
DMKCDB	DMKCQG	DMKIUG	DMKSBL	DMKVDC
DMKCDM	DMKCQH	DMKIUJ	DMKSEG	DMKVDD
DMKCDS	DMKCQI	DMKIUL	DMKSEP	DMKVDE
DMKCFC	DMKCQP	DMKIUP	DMKSEV	DMKVDF
DMKCFD	DMKCQQ	DMKJRL	DMKSFB	DMKVDG
DMKCFF	DMKCQR	DMKLNK	DMKSIX	DMKVDH
DMKCFG	DMKCQS	DMKLNM	DMKSNC	DMKVDR
DMKCFG	DMKCQT	DMKLOG	DMKSND	DMKVDS
DMKCFJ	DMKCQU	DMKLOH	DMKSPC	DMKVDT
DMKCFO	DMKCQY	DMKLOJ	DMKSPL	DMKVER
DMKCFP	DMKCRM	DMKLOM	DMKSPM	DMKVEC
DMKCFQ	DMKCSB	DMKMCC	DMKSPS	DMKVFD
DMKCFR	DMKCSF	DMKMCD	DMKSPT	DMKVFE
DMKCFS	DMKCSO	DMKMCI	DMKSRM	DMKVFI
DMKCFT	DMKCSP	DMKMHV	DMKSTA	DMKVFS
DMKCFU	DMKCSQ	DMKMIA	DMKSTA	DMKVMC
DMKCFV	DMKCSR	DMKMID	DMKSTR	DMKVMD
DMKCFW	DMKCST	DMKMNI	DMKSWM	DMKVME
DMKCFY	DMKCSU	DMKMNJ	DMKTAP	DMKVMG
DMKCKD	DMKCSV	DMKMNL	DMKTAQ	DMKVMI
DMKCKF	DMKCSW	DMKMNT	DMKTCS	DMKWRM
DMKCKH	DMKCSX	DMKMON	DMKTCT	DMKWRN
DMKCKM	DMKCSY	DMKMOO	DMKTDK	DMKXAB
DMKCKN	DMKDEF	DMKMSG	DMKTH	DMKXAD
DMKCKP	DMKDEG	DMKNEA	DMKTOD	DMKXST
DMKCKR	DMKDEG	DMKNEM	DMKTOD	DMKZTD
DMKCKS	DMKDIA	DMKNES	DMKTRA	DIVINEID
DMKCKS	DMKDIA	DMKNES	DMKTRA	
DMKCKV	DMKDIF	DMKNLD	DMKTRD	
DMKCKW	DMKEIG	DMKNLE	DMKTRD	
DIVINGEN	DIVINEIG	DWINNLE	DIVININI	

Figure 26 (Part 2 of 2). Executable Modules

Data Area Modules

In addition to the executable resident and pageable modules (see Figure 26), there are certain modules that only contain data areas and do not contain executable code. These modules are:

Resident Module	Contents
DMKCPE	Defines the end of the CP nucleus
DMKPXA	Extension to the PSA
DMKPXB	Extension to the PSA
DMKRIO	I/O device blocks
DMKSEQ	Printer separator logo
DMKSLC	Data to define the end of the CP nucleus
DMKSTD	Starting address of STDATA table
DMKSYS	System constants
DMKTBL	Terminal translate table
DMKTTZ	CCWs and data pointed to by certain CCWs for TTY terminals
Pageable Module	Contents
DMKBOX	Output separator table
DMKBTS	Bootstrap routines for 3705
DMKEMR	Response message data module
DMKFCB	3211-type Forms Control Buffer (FCB) load tables
DMKMES	CP message data module
DMKPIA	3289 Model 4 Font Offset Buffer (FOB) load tables
DMKPIB	3262 Universal Character Set Buffer (UCSB) load tables
DMKSNT	System name table (may exceed one page)
DMKSYM	System symbol table
DMKTBM	Terminal translate tables
DMKTBN	Terminal translate tables for APL/ASCII for TTY terminals.
DIMINOD	
DMKUCB	3211 Universal Character Set Buffer (UCSB) load tables
DMKUCB DMKUCS	3211 Universal Character Set Buffer (UCSB) load tables 1403 Universal Character Set (UCS) load tables

Virtual Timer Maintenance

The System/370 with EC mode provides the system user (both real and virtual) with four timing facilities. They are:

- The interval timer at main storage location X'50'
- The time-of-day clock
- The time-of-day clock comparator
- The CPU timer.

Real Timing Facilities

Before describing how CP maintains these timers for virtual machines, it is necessary to review how CP uses the timing facilities of the real machine.

1. The location X'50' interval timer is used only for time-slicing. The value placed in the timer is the maximum length of time that the dispatched virtual machine is allowed to execute.

Because the BLIP function of CMS uses the interval timer (location X'50'), the use of STIMER can cause extra blips at the user's terminal. To avoid extra blips, issue the CMS command SET BLIP OFF.

- 2. The time-of-day clock is used as a time stamp for messages and enables the scheduler to compute elapsed in-queue time for the dispatching priority calculation.
- 3. The time-of-day clock comparator facility is used by CP to schedule timer-driven events for both control program functions and for virtual machines. A stack of comparator requests is maintained and as clock comparator interrupts occur, the timer request blocks are stacked for the dispatcher via calls to DMKSTKIO.
- 4. The CPU timer facility performs three functions:
 - Accumulates CP overhead
 - Detects in-queue time slice end
 - Simulates virtual CPU timer.

Since VMTTIME only accounts for supervisor state overhead, detection of in-queue time slice end is performed by the processor timer when the virtual machine is dispatched in the problem state. The VMTMOUTQ field in the VMBLOK is initialized to the amount of problem state time that the virtual machine is allowed to accumulate before being dropped from a queue. This initial value is set by the scheduler (DMKSCH) when the virtual machine is added to a queue and its value depends on the queue entered (interactive or noninteractive) and on the processor model. For example, the initial value of VMTMOUTQ for a user entering Q1 (interactive) on a Model 145 is 300 milliseconds, while for the same user entering Q2 (noninteractive) it is 2 seconds. Each time the user is dispatched, the value in VMTMOUTQ is entered into the CPU timer. Whenever the user is interrupted, the decremented processor timer is stored into VMTMOUTQ prior to being set from the new VMTTIME. When the problem state time slice has been exhausted, a CPU timer interrupt occurs, the VMQSEND flag bit is set in the VMBLOK, and the scheduler drops the user from the queue. At each queue drop, the problem time used in-queue (the difference between VMTMOUTQ and the initial value) is added to the total problem time field (VMVTIME) in the VMBLOK.

Virtual CPU timer simulation is handled for EC mode virtual machines if the value in the virtual processor timer is less than that in VMTMOUTQ. In this case, the VMBLOK is flagged as "tracking processor timer" and a processor timer interrupt is interpreted as a virtual timer interrupt rather than as an in-queue time slice end.

Virtual Timing Facilities

Virtual location X'50' timers are updated by the elapsed processor time each time the dispatcher has been entered after a running user has been interrupted. The size of the update is the difference between the value of the timer at dispatch (saved in QUANTUM at location X'54') and the value of the timer at the time of the interruption (saved in QUANTUMR at location X'4C').

Virtual clock comparator requests are handled by the virtual timer maintenance routine, DMKTMR. They are inserted into the general comparator request stack and the virtual machine is posted when the interruption occurs.

Virtual clock comparator requests to set the virtual processor timer place the new value into the ECBLOK. Requests to store the new value update the ECBLOK field with the virtual processor time used since the last entry to dispatch and pass the value to the user. Requests to set the time-of-day clock are ignored.

A real interval timer or processor timer is one that runs when the virtual machine is executing or is in a self-imposed wait state (that is, the wait bit is on in the virtual PSW). A real timer does not run if the virtual machine is in a CP pseudo wait state (for example, page wait or I/O wait) or if the virtual machine can be run but is not being dispatched because of other user interaction. Real timers provide accurate interrupts to programs that depend on measurement of elapsed processor and/or wait time. They do not accurately measure wall time—the TOD clock must be used for this function.

An EC mode virtual machine with the real timer option has both a real interval timer and a real processor timer. Real timer requests for waiting machines are maintained in the clock comparator stack. CPU timer requests are added to TOD clock value at the time that they are issued. Interval timer requests must have their units converted. In addition, if the virtual CPU timer contains a large negative value, then a real timer request is scheduled to occur when the virtual CPU timer becomes positive, so that the pending timer interruption can be unflagged. Comparator requests for real timer interruptions are inserted into the stack whenever a virtual machine enters a self-imposed wait. They are removed either when the virtual machine resumes execution or when it is forced (or places itself) into a pseudo wait.

I/O Management

I/O Supervisor

The module DMKIOS handles the I/O requirements of all system devices except the following terminals: 1052, 3210, 3215, 2150, 2741, and compatible teletypewriter devices. Scheduling and interruption handling for these devices is essentially a synchronous process and does not require the queuing and restart services of DMKIOS. This is handled by the module DMKCNS. For handling the I/O requirements of 3270 remote equipment, refer to "Programming for 3270 Remote Terminals - an Introduction" in this section.

Real I/O Control Blocks

To schedule I/O requests and control the activity of the I/O devices of the system, I/O control uses several types of control blocks. These blocks are separated into two basic types.

- Static blocks that describe the components of the I/O system.
- The dynamic blocks that represent active and pending requests for I/O operations.

The I/O devices of the real system are described by one control block for each channel, control unit, and device available to the control program. For multiprocessor generated systems, two sets of real channel blocks are created. Units present but not represented by control blocks are not available for either user-initiated or CP-initiated operations.

Because all virtual machines are run in the problem state, any attempt to issue a SIO instruction results in a program interruption that indicates a privileged operation exception. This interruption is handled by CP's first level program interrupt handler, DMKPRGIN. It determines if the virtual machine was in virtual supervisor state (problem state bit in the virtual PSW is zero). If so, the instruction causing the interruption is saved in the VMBLOK for the virtual machine and control is transferred to the privileged instruction simulator, DMKPRVLG, via a GOTO.

DMKPRVLG determines if the privileged operation affects the virtual I/O configuration. DMKPRVLG simulates non-I/O privileged instructions (such as LPSW). If the instruction's operation code is from X'9C' to X'9F', control is transferred to DMKVSIEX.

After clearing the condition code in the user's VMBLOK, DMKSCNVU is then called to locate the virtual I/O blocks representing the I/O components (channel, control unit and device) addressed by the instruction.

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DMKVSIEX then branches to handle the request based on the operation requested.

In attached processor systems and multiprocessor systems, the I/O control blocks are protected by the I/O lock, a global spin lock.

Virtual I/O Requests

The virtual I/O interface maintained by CP provides to the software operating in the user's virtual machine, the condition codes, CSW status information, and interruptions necessary to make it appear to the user's virtual machine that it is in fact running on a real System/370. The virtual I/O interface consists of:

- A virtual I/O configuration for each active virtual machine that consists of a set of I/O control blocks that are maintained in the Control Program's free storage. This configuration is built at logon time from information contained in the user's directory file, and can be changed by the user or the system operator.
- A set of routines that maintain the status of the virtual I/O configuration.
- Other system routines that simulate or translate the channel programs provided by the user to initiate I/O on units in the real system's configuration.

With a SIO, the condition code returned from DMKSCNVU is tested to verify that all addressed components were located. If they were not, then a condition code of 3 (unit not available) is placed in the PSW and control returns to the dispatcher. Otherwise, the addresses of the appropriate virtual I/O control blocks are saved, and DMKVSIEX tests the status of the addressed I/O units by scanning the VCHBLOKs, VCUBLOKS, and VDEVBLOKs to locate the block that contains the status of the addressed subchannel. The subchannel status is indicated in:

- The VCHBLOK for a selector or block multiplexer channel.
- The VCUBLOK for a shared selector subchannel on a byte multiplexer channel.
- The VDEVBLOK for a nonshared subchannel on a byte multiplexer channel.

When the block containing the status is found, the status is tested. If the subchannel is busy or has an interruption pending, condition code 2 is placed in the virtual PSW. Otherwise, the subchannel is available and the device and the control unit are tested for interruption pending or busy. If either is found, condition code 1 is placed in the virtual PSW and the proper CSW status is stored in the virtual machine's page zero. If all components in the subchannel path are free, DMKVSIEX proceeds to

Virtual SIO

simulate the SIO by locating and loading the contents of the virtual machine's CAW from virtual location X'48' and testing the device type of the unit addressed.

The device type is in the VDEVBLOK. If the device class code indicates a terminal or console, control is passed to the module DMKVCNEX with a GOTO. DMKVCNEX interprets and simulates the entire channel program, moving the necessary data to or from virtual storage and reflecting the proper interruptions and status bytes. When DMKVCNEX has finished, it passes control directly to the dispatcher, DMKDSPCH.

If the referenced device is a spooled unit record device, DMKVSIEX passes control to DMKVSPEX for additional processing. When control returns to DMKVSIEX, it passes control to DMKDSPCH.

If the device is not a terminal or a spooling device, the SIO is translated and executed directly on the real system's I/O device. DMKVSIEX calls DMKFREE to obtain free storage and then it constructs an IOBLOK in the storage obtained. The IOBLOK serves as an identifier of the I/O task to be performed. It contains a pointer to the channel program to be executed and the address of the routine that is to handle any interruptions associated with the operation.

DMKVSIEX stores the contents of the user's CAW in IOBCAW and sets the interruption return address (IOBIRA) to be the same as the virtual interruption return address (DMKVIOIN) in DMKVIO. The CCW translation routine (DMKCCWTR) is then called to locate and bring into real main storage all user pages associated with the channel program, including those containing data and CCWs. The following occurs:

- The CCWs are translated.
- A corresponding real channel program is constructed.
- The data pages are locked into real storage.
- DMKCCWTR returns control to DMKVSIEX. DMKVSIEX places the user in a pseudo wait state, IOWAIT, and calls the real I/O scheduler DMKIOSQV to schedule the I/O on the real configuration.

DMKIOSQV queues the request for operation on the real channel, control unit, and device corresponding to the address used by the virtual machine. When the real SIO is issued, DMKIOS takes the user out of IOWAIT and reflects the condition code for the SIO if it is zero. If it is not zero, the operation is further analyzed by DMKVIOIN. In any case, DMKIOSQV returns control to DMKVSIEX, which passes control to DMKDSPCH.

Other Privileged I/O Instructions

Other privileged I/O instructions are handled directly by DMKVSIEX. DMKVSIEX scans the virtual channel, control unit, and device blocks in the same manner as for a SIO and reflects the proper status and condition to the virtual machine. In some cases (TIO), the status of the addressed devices is altered after the status is presented.

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If the operation active on the virtual device is actually in progress in the real equipment, the simulation of a HIO or HDV is somewhat more involved, since it requires the actual execution of the instruction. In this case, the active operation is halted and the resultant condition code/status is returned to the user.

Virtual Channel-to-Channel Support (CTCA and 3088)

Virtual channel-to-channel support simulates data transfer and control communication between two channel-to-channel devices, either on two distinct processors or two channels on a single processor. Data transfer is accomplished via synchronized complementary I/O commands (for example, read/write, write/read) issued to both parts of the CTC device. Each part of the CTC device is identical and the operation of the unit is completely symmetrical.

The VM/SP HPO control program support for virtual CTC devices (channel-to-channel adapter and 3088) includes all status data, sense data, and interrupt logic necessary to simulate the operation of the real CTC device. Data transfer, command byte exchange, sense data, and status data presentation for the virtual CTC device is accomplished via storage-to-storage operations (like MVCL). No real I/O operations (excluding paging I/O) or I/O interrupts are involved. Unit errors or control errors cannot occur.

Virtual Selector Channel I/O Requests

The CCW translator, DMKCCWTR, is called by the virtual machine I/O executive program (DMKVSIEX) when an I/O task block has been created and a list of virtual CCWs associated with a user's SIO request must be translated into real CCWs.

When the I/O operation from a self-modifying channel program is completed, DMKUNTIS is called by DMKIOS. When retranslation of OS ISAM CCWs is required, the self-modifying channel program checking portion of DMKCCWTR calls DMKISMTR.

DMKCCWTR operates in two phases:

- A scan and a translate phase
- A TIC-scan phase.

A self-modifying channel program checking function is also included.

The scan and translate phase analyzes the virtual CCW list. Some channel commands require additional doublewords for control information (for example, seek addresses). Additional control words are also allocated (in pairs) if the data area specified by a virtual CCW crosses 4096-byte page boundaries, or if the virtual CCW includes an IDA (indirect data address) flag.

Space is obtained from DMKFREE for the real CCW list, and the translation phase then translates the virtual CCW list into a real CCW list. TIC commands that cannot be immediately translated are flagged for later processing by the TIC-scan phase. A READ or WRITE command that specifies that data cross 4096-byte boundaries is revised to include an IDA flag that points to an indirect data address list (IDAL) and a pair of words for each 4096-byte page, in which each word handles a data transfer of 2048 bytes (or less). The real CCW is flagged as having a CP-generated IDA. DMKPTRAN is called (via the TRANS macro) to lock each 4096-byte page.

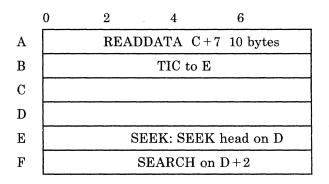
If the real CCW string does not fit in the allocated free storage block, a new block is obtained. The old block is transferred and adjusted before being released. The translation continues with the new block. The process is repeated, as needed, to contain the real CCW string.

Virtual CCWs having an IDA flag set are converted to user translated addresses for each IDAW (indirect data address word) in the virtual IDAL. DMKPTRAN is called for each IDAW. The CCW is flagged as having a user (but not CP) generated IDA.

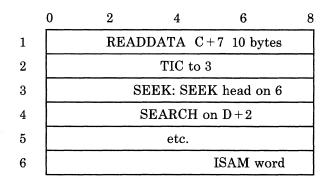
The TIC-scan phase scans the real CCW list for flagged (untranslated) TIC commands and creates a new virtual CCW list for the untranslated commands. Scan-translate phase processing is then repeated. When all virtual CCWs are translated, the virtual CAW in the IOBLOK task block is replaced by the real CAW (that is, a pointer to the real CCW list created by DMKCCWTR), and DMKCCWTR returns control to DMKVIOEX. The user protection key is saved.

OS ISAM Handling by DMKISMTR

Because many of the OS PCP, MFT, and MVT ISAM channel programs are self-modifying, special handling is required by CP to allow virtual machines to use this access method. The particular CCWs that require special handling have the following general format:



The CCW at A reads 10 bytes of data. The tenth byte forms the command code of the CCW at E. In addition, the data read in makes up the seek and search arguments for the CCWs at E and F. After the CCW string is translated by CP, it usually is in the following format:



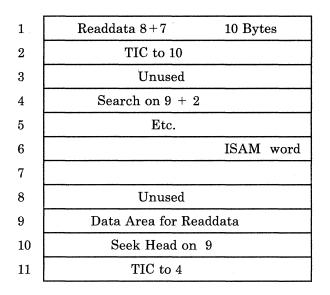
To accomplish an efficient and non-timing-dependent translated operation for OS ISAM, the virtual CCW string is modified in the following manner.

DMKISMTR is called by DMKCCWTR if, during normal translation, a CCW of the type at 1 is encountered. The scan program locates the TIC at 2 by searching the translated CCW strings. The TIC at 2 locates the SEEK at 3.

The virtual address of the virtual SEEK CCW at E is located from the RCWTASK header. Seven doublewords of free storage are obtained and the address of the block is saved in the ISAM control word at 5. The seven doublewords are used to save the following information from the translated CCW strings:

7	Address of Read at 1	Address of TIC at 2
8	Unused	Unused
9	Data area for READ at 1	
10	SEEK HEAD on 9	
11	TIC to 4	
12	Image of READ CCW at 1	
13	Image of TIC CCW at 2	

The translated read CCW (at 1) is moved to the save block at 12. The TIC CCW (at 2) is moved to the save block at 13, and the addresses of 1 and 2 are saved at 7. The read CCW at 1 is modified to point to a 10-byte data area at 8+7 in the save block. The seek head CCW at 3 is copied into the save block at 10, and the seek address is modified to point to the data area at 9. At 11, a TIC CCW is built to rejoin the translated CCW string at 4. The search at 4 (or any subsequent search referencing D+2) is modified to point to 9+2. The completed CCW string has the following format:



The interruption return address in the IOBLOK is set to DMKUNTIS. DMKUNTIS restores the CCWs to their original format from the seven doubleword extensions, moves the 10 bytes of data from 8+7 into virtual storage (at C+7), and releases the block. Normal I/O handling is resumed by DMKVIO and DMKUNT.

I/O Component States

The I/O components represented by the control blocks described in "Real I/O Control Blocks" are in one of four states and the state is indicated by the flag bits in the block status byte. If the component is not disabled, it is either busy, scheduled, or available.

If the disabled bit is on, the component has been taken offline by the operator or the system and is at least temporarily unavailable. A request to use a disabled component causes the IOBLOK to be stacked with an indication of condition code 3 on the SIO and the real SIO is not performed.

An I/O unit is busy if it is transferring data (in the case of a channel or control unit), or if it is in physical motion (in the case of a device). If an I/O unit is busy, the IOBLOK for the request is queued from the control block representing that I/O unit.

An I/O unit is scheduled if it is not busy but will become busy after a higher-level component in the subchannel path becomes available and an operation is started. For example, if a request is made to read from a tape

drive and the drive and control unit are available, but the channel is busy, the IOBLOK for that request is queued from the RCHBLOK for the busy channel and the RCUBLOK and RDEVBLOK of the drive and control unit are marked as scheduled. Future requests to that drive are queued from the RDEVBLOK for the scheduled device. When the channel completes the operation, the next pending operation is dequeued and started. The scheduled control unit and device are then marked as busy.

The IOBLOKs for various I/O requests indicate the status of that request by a combination of the status bits in the IOBLOK and the queue in which the block resides. In general, an IOBLOK is queued from the control block of the highest level I/O unit (taken from device up to channel) in the subchannel path that is not available. Once the I/O operation is started, the IOBLOK is chained from the active IOBLOK pointer (RDEVAIOB) in the real device control block. Flags in the IOBLOK status fields may also indicate that a unit check has occurred, that a sense is in progress, or that a fatal I/O error (unrecoverable) has been recognized by error recovery procedures. After I/O control releases control of the IOBLOK, it is stacked on the queue of IOBLOKS and CPEXBLOKs anchored at DMKDSPRQ in the dispatcher and control is passed to the second-level interruption handler whose address is stored in IOBIRA.

I/O Interrupts

I/O interrupts are either synchronous or asynchronous. Asynchronous interrupts indicate the change in status of an I/O unit from the not-ready to ready state or busy to not-busy state. In either case, if the affected component has any pending requests queued from its control block, they are restarted, and whether or not the given interrupt is processed any further depends upon the status of the interrupting component. Channel-available and control-unit-end interrupts restart the interrupting component. An asynchronous device end is passed to the user if the device is dedicated. Otherwise, the device is restarted.

An interrupt is considered to be synchronous if the interrupting device has a nonzero pointer to an active IOBLOK. In this case, the following processing occurs:

- If a unit check has occurred, a sense is scheduled, and when the sense is completed, the appropriate ERP is called.
- If an ERP is currently in control of the task (indicated by a flag in the IOBLOK), return the IOBLOK to the appropriate ERP.
- If the operation is incomplete (for example, channel end is received without device end), the IOBLOK is copied and the copy is stacked but the original IOBLOK remains attached to RDEVAIOB to receive the final interrupt. The control unit and channel are then restarted.
- If the operation is complete (that is, the device is available), the IOBLOK is detached from the device and stacked, and the device, control unit and channel are restarted.

The restart operation usually dequeues the next IOBLOK that is queued to the restarted component and queues it to the next higher component in the subchannel path. When the channel level is reached, a SIO is issued and exit is taken to the dispatcher after handling any nonzero condition codes as previously described.

Virtual I/O Interruptions

When an I/O interruption is received, the IOBLOK is stacked for dispatching and control is passed to the address specified in the IOBIRA (interrupt return address) field. For operations requested by DMKVIOEX, the return address is DMKVIOIN (virtual interrupt return address). When DMKVIOIN receives control from the dispatcher, it loads the virtual address of the unit with which the interruption is associated from the IOBLOK and calls DMKSCNVU to locate the virtual device control blocks. DMKVIOIN then tests the IOBLOK status field to determine the cause for the interruption. If the block has been unstacked because of an interruption, the field is zero. If the operation was not started, it contains the condition code from the real SIO.

Note: The VIRA should not see a real condition code 2 as the result of a SIO, since channel-busy conditions are detected and reflected before any real I/O operation is attempted.

A condition code of 3 is reflected to the virtual machine and exit is taken to the dispatcher. For a condition code of 1, the CSW status field in the IOBLOK is examined to determine the cause for the CSW stored condition. The status is reflected to the virtual machine and various components of the virtual configuration may be freed, if the status so indicates. For example, if the CSW status indicated both channel end and device end, the operation was immediate and has completed. Thus, the CCW string (real) may be released and all virtual components marked available.

The CSW status returned for a virtual interruption must be tested in the same manner, with the additional requirement that the status be saved in the affected virtual I/O control blocks and that the CSW be saved in the VDEVCSW field for the device causing the interruption. If the unit check bit is on in the status field, the sense information saved in the associated IOERBLOK (pointed to by the IOBLOK) must be retained so that a sense initiated by the virtual machine receives the proper information.

In any case, when an interruption is received for a virtual device, a bit in the interruption mask, VCUDVINT, for the device's control unit is set to 1. The bit that is set is the one corresponding to the relative address of the interrupting device on the control unit. For example, if device 235 interrupts, the fifth bit in the VCUDVINT mask in the VCUBLOK for control unit 30 on channel 2 is flagged. Similarly, the bit in the VCHCUINT in the affected VCHBLOK is also set. In this case, bit 3 in VCHBLOK for channel 2. If the interruption is a channel class interrupt (PCI or CE), the address of the interrupting unit (235) is stored in the VCHCEDEV field in the VCHBLOK. The final interruption flag is set in the VMPEND field in the VMBLOK for the interrupted virtual machine. The bit set corresponds to the address of the interrupting channel. The next time, the virtual machine is dispatched and becomes enabled for I/O.

Monitoring I/O Activity

The missing interrupt handler, module DMKDID, receives control from a timer interrupt (TRQBLOK). The timer is reset, and DMKDID scans all of the real device blocks. If the RDEVBUZY or RDEVSCHD indicator flag is on, active I/O or device busy conditions exist. A check is made to make certain that the RDEVBUZY flag and the timer interrupt are for the same class of device. If the class is valid, the RDEVMID bit indicator-flag is turned on. If the class is not valid, the scan continues.

RDEVMID on means that the device is active for the time interval and a device interrupt is pending. When the device causes an interrupt, the indicator flags are reset by the first level interrupt handler, DMKIOT. If both flags (RDEVMID and RDEVBUZY) are on at the end of the *next* interval, a missing interrupt condition exists.

When a missing interrupt condition is detected, a CPEXBLOK is set up to give control to module DMKDID. The action taken depends on whether the request was queued or active. If the request was queued, a control unit end or device end is simulated. If the request was active, an interface control check is simulated.

If a virtual machine initiated the I/O operation, the interface control check is returned to the virtual machine. If CP started the I/O operation, the interface control check is handled by CP's ERPs.

Before either action is taken, a ten second timer is scheduled to return control to module DMKDID. When control is received from the timer, the RDEVMID flag is examined. If it is off (indicating that some I/O was completed), DMKDID sends a message to the system operator and a record is written to the error recording area indicating that the condition is corrected. If RDEVMID is on, the operator message and error record indicate the condition was not cleared.

Scheduling I/O Requests

A task that requests an I/O operation must specify the device on which the operation is to take place and must provide an IOBLOK that describes the operation. Upon entry to DMKIOS, register 10 must point to the IOBLOK. The IOBLOK must contain at least a pointer to the channel program to be started in IOBCAW and the address to which the dispatcher is to pass control in IOBIRA. In addition, the flags and status fields should be set to zero. If the operation is a control program function such as for spooling or paging, the entry point DMKIOSQR is called. If the requester is the virtual I/O executive (DMKVIOEX) attempting to start a virtual machine operation, the entry point DMKIOSQV is called and some additional housekeeping is done. In either case, an attempt is made to find an available subchannel path from the device to its control unit and channel. If an I/O unit in the path is busy or scheduled, the IOBLOK for the request is queued to the control block of the I/O unit.

Requests are usually queued first-in-first-out (FIFO), except those requests:

- To fixed-head DASD primary paging areas that are queued first
- To movable-head DASDs that are queued in order of seek address
- That release the affected component after initiation (SEEKS and other control commands) which are queued last-in-first-out (LIFO) from the control block.

Whether or not the operation has been successfully started, the caller requesting the I/O operation receives control from DMKIOS. If a free path to the device is found, the unit address is constructed and an SIO is issued. If the resulting condition code is zero, control is returned to the caller. Otherwise, the code is stored in the requester's IOBLOK along with any pertinent CSW status, the IOBLOK is stacked, any components that become available are restarted, and control is returned to the caller.

In a multiprocessor environment, both processors have I/O capability. If either processor receives an I/O request, that processor attempts to initiate I/O operations.

At system generation time, when a channel path to a device is defined on one processor, an alternate logical path is automatically defined for the other processor. Thus, both processors *can* have access to any I/O device in the MP configuration.

If either processor receives an I/O request, that processor attempts to initiate the I/O operation on one of its own paths to the required device. If none of the online paths to the required device are available from the executing processor, that processor queues the I/O request on all busy and scheduled paths to the device, both its own and those of the other processor. If there is no online path from the executing processor, that processor queues the I/O request on the first online and available path for the second processor, as well as on all busy or scheduled paths for that processor.

While it is not required that both processors have access to all I/O devices, heavily used devices should be accessible by both processors to provide efficient system operation and to increase the possibility of system recovery following a processor or channel failure.

The I/O lock serializes access to the control blocks that represent I/O devices.

Alternate Path Scheduling

Alternate path I/O scheduling is performed according to the following scheme:

DMKIOQ, called by DMKIOS, searches for an available path beginning with the primary path to the device. If an available path to the device exists, the I/O request is started immediately on the first available path to the device. If the device is busy or scheduled, the IOBLOK is queued off the RDEVBLOK. No alternate path processing is performed at the device level.

If the device is not busy, not scheduled, nor offline, an IOBLOK for this I/O request is promoted upward to the RCUBLOK.

If the RCUBLOK is marked busy, the IOBLOK is queued on the RCUBLOK and a search is made for an alternate control unit path. If the RCUBLOK is marked scheduled and the present request will not release the control unit (as in TAPE FSF and TAPE BSF), then the IOBLOK is queued off the RCUBLOK and a search is made for an alternate control unit path. If the RCUBLOK is marked scheduled and the present request will release the control unit, the search continues for a channel path. If the RCUBLOK is not marked scheduled or busy but other I/O requests are queued on the RCUBLOK, the check is again made to see if the present request will release the control unit. If the present request will not release the control unit, the request is queued and a search is made for an alternate control unit, the request is queued and a search is made for an alternate control unit path. Otherwise, the search continues for a channel path.

The RCUBLOK busy and scheduled indicators are turned on only for shared control units. The busy and scheduled indicators are turned on in the RCUBLOK for tape control units. The nonshared DASD RCUBLOKs never have the busy and scheduled indicators turned on. For this reason, alternate control unit path selection rarely takes place for nonshared control units. The one exception occurs when the channel path through the first control appears busy (because a real-channel-busy condition was encountered). If an alternate path exists through a second control unit, the control blocks associated with the second control unit path are examined. Finding an available channel path is the final step before issuing the SIO. If the RCHBLOK is marked busy, a search is made for an alternate channel path. If the RCHBLOK has other requests queued on the RCHBLOK, a search for an alternate channel path is made. VM/370 never marks a byte multiplexer RCHBLOK busy. The only time a byte multiplexer is marked busy is after a condition code 2 has been encountered. The I/O load on byte multiplexer channels must be sufficient to cause channel-busy conditions before path selection on an alternate channel can take place.

If a busy or scheduled path is encountered, an IOBLOK is queued to the real block (RCUBLOK or RCHBLOK) and the search continues for an available path. If more than one busy path is encountered, multiple IOBLOKs are queued for the same I/O request. This is accomplished by creating mini IOBLOKs for each busy/scheduled path after the first. The primary IOBLOK is queued off the first busy path encountered. The mini IOBLOK is 16 bytes in length and consists of the first two doublewords of the IOBLOK, which is the same as the current IOBLOK structure. The IOBLOK and associated mini IOBLOKs are chained in a single-threaded queue by means of the IOBLINK field. The active IOBLOK pointer is not stored in the IOBLINK field until just prior to the SIO. Zeros are stored in IOBLINK at entry to DMKIOSQR to indicate no mini IOBLOKs have been queued as yet. See Figure 27 for an example of mini IOBLOK queuing.

The last two words of the mini IOBLOK (IOBFPNT and IOBBPNT) are used as the double-threaded queue pointers for the RCUBLOK/RCHBLOK from which it is queued. A flag is set in the mini IOBLOK to identify it as a mini IOBLOK. Figure 28 shows a sample control block structure when mini IOBLOKs are queued.

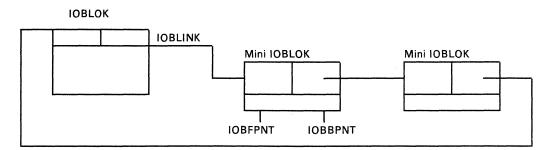


Figure 27. Mini IOBLOK Queuing

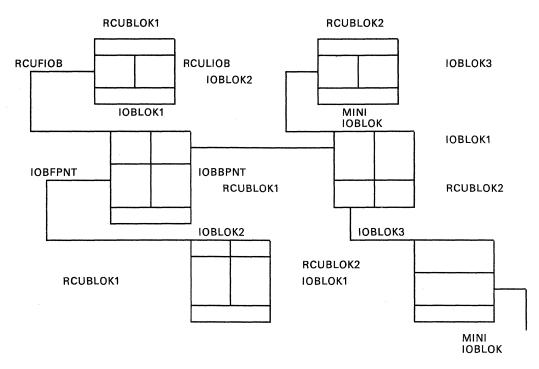


Figure 28. Control Block Structure for Alternate Path Request

Prior to starting an I/O operation associated with the request, a check is made to see if the IOBLOK is a mini IOBLOK and whether mini IOBLOKs are queued off this IOBLOK. All mini IOBLOKs associated with this request are dequeued from their respective queues by running the IOBLINK chain. The storage for the blocks is released. If the active IOBLOK is a mini IOBLOK, the IOBRADD from the mini IOBLOK is moved to the primary IOBLOK and the I/O started using the primary IOBLOK.

Reserve/Release

Reserve/release is supported for shared DASD as though each virtual machine has a separate channel path to a shared device. Reserve/release support prevents the occurrence of a channel lockout situation. This is accomplished by changing reserve CCWs to sense CCWs when a reserve is issued to a device that has alternate paths defined to it. In an MP configuration, an alternate path is defined if there is a channel path from each processor. This means that whenever alternate paths are defined to a device, the real reserve does not execute on the hardware.

Note: In an MP configuration, an alternate path is defined when there is a channel path from each processor.

When a reserve operation is changed into a sense before being executed on the hardware, the access to the device remains **unrestricted**. This can lead to an integrity exposure on the shared device. For devices being shared among virtual machines active on the same processor, reserve/release support is implemented on a virtual basis allowing the reserve/release operation codes to be simulated on a virtual basis for minidisks, including full-extent minidisks. When a reserve is issued against a minidisk, the reserve is accomplished by a locking mechanism. The status of the minidisk is maintained in the VRRBLOK that is chained from the VDEVBLOK.

The following matrix identifies how the reserve operation code is handled in the various situations.

	Defined Alternate Paths to Device ³	Will Reserve/ Release Execute on the Hardware	Virtual Reserve/ Release Requested for Minidisks	RESERVE ¹ —or— SENSE ²
Dedicated	NO	N/A	N/A	RESERVE
DASD or Tape	YES	N/A	N/A	SENSE
Minidisk	NO	NO	NO	RESERVE
	NO	NO	YES	SENSE
	NO	YES	NO	RESERVE
	NO	YES	YES	RESERVE
	YES	N/A	N/A	SENSE

¹The 'RESERVE' keyword in the chart indicates that the real reserve is allowed to execute on the hardware.

²The SENSE keyword indicates that the reserve CCW is changed to a sense CCW. Virtual Reserve/Release is requested by means of a new option on the MDISK directory control statement.

³In an MP system, an alternate path is defined when each processor has a channel path to the device.

DMKVIO performs the following steps when virtual reserve/release processing is requested:

1. DMKVSI calls DMKCCW to perform CCW translation. For DASD devices, DMKCCW checks if the virtual reserve/release feature bit is on in the VDEVBLOK. If virtual reserve/release processing has been requested and if the device is not reserved by anyone or it is reserved by this user, processing continues normally. If the device is reserved by another user, DMKCCW calls DMKUNTFR to restore the CCWs to their original state and returns to the caller, unless sense bytes have been transferred to the user's storage in which case CP enqueues on the minidisk and waits until it is no longer reserved at which time the I/O can proceed. If the I/O request can continue and the CCW chain contains a reserve command, the VDEVBLOK and the VRRBLOK are flagged as reserved. If the CCW chain also contains a release, the IOBLOK is flagged to indicate to DMKUNTFR to release the virtual disk. Control returns to DMKVSI.

2. DMKVSI reflects a device-busy condition to the virtual machine if the minidisk is currently reserved by another user.

3. DMKUNT reflects a device end interrupt to all virtual machine users who previously received a busy condition, when the device is released.

DMKRIO

The DMKRIO module implicitly defines alternate channels for all control units in a multiprocessor environment. Reserve operations are changed into sense operations for shared DASDs, and to ensure proper integrity of data in this case, following are some recommendations for several situations:

1. Two or more virtual machines share DASD devices on a single processor.

Use virtual reserve/release and define minidisks with the access mode "V" for all shared volumes. All virtual machines willing to share these volumes should have a link established to them.

2. One virtual machine sharing DASD devices with one or more real processors.

Only one path to the shared DASD devices should be on-line on the VM/SP HPO side. Dedicate the shared devices to the virtual machine. Or define the shared volumes as *full pack* minidisks, making sure that every real unit on which a shared volume is mounted has the Reserve/Release Feature available.

3. Two or more virtual machines sharing DASD between themselves and one or more real processors.

This is a combination of the two situations described above.

Ensure that only one path to the shared DASD devices is on-line on the VM/SP HPO side. Define all shared volumes as *full pack* minidisks for VM/SP HPO with access mode "V." All virtual machines willing to share those volumes should have a link established to them. Ensure that every real unit on which a shared volume is mounted has the Reserve/Release Feature available.

Ordered Seek Queuing: Requests to start I/O on system devices are normally handled first in first out. However, requests to movable-head DASD devices are queued on the device in ascending order by seek address. This ordered seek queuing is performed to minimize intercylinder seek times and to improve the overall throughput of the I/O system.

CP assumes that very few virtual machines perform chained SEEKs. Therefore, the first logical address represents the position of the arm upon completion of the I/O operation. Ordered SEEK queuing is based on the relocated real cylinder. DMKIOQ, which handles IOBLOK queuing, uses the cylinder location supplied in IOBCYL for ordered SEEK queuing. This field is initialized by the calling CP routine for paging and spooling or by the CCW translator for virtual I/O. The CCW translator, DMKCCW, supplies the IOBCYL value in the following manner:

- Reads the IPL record, relocates to virtual cylinder 0
- Recalibrates, issues a real calibrate, and then a SEEK to virtual cylinder 0
- Issues a channel SEEK, relocates to the virtual cylinder
- For FBA devices, converts the block number being located to the corresponding cylinder number.

The IOBLOK queuing subroutine of DMKIOQ recognizes that a request is being queued on a movable-head DASD by means of the device class and type fields of RDEVBLOK. Instead of adding the IOBLOK to the end of the queue on the RDEVBLOK, the queuing routine sorts the block into the queue based on the cylinder number for the request. The cylinder number for any request to DASD is recorded in the IOBCYL field. The queue of IOBLOKs on a real device block is sorted in ascending order by SEEK address, unless the entire device is dedicated to a given user. In this case, DMKIOQ does not automatically schedule the device, and no more than one request can be outstanding at any one time.

When an outstanding I/O request for a device has completed, DMKIOS attempts to restart the device by dequeuing and starting the next IOBLOK queued on the device. For non-DASD, this is the first IOBLOK queued. Fixed-head DASD paging requests are dequeued ahead of moveable-head requests. For movable-head DASD, the queued requests are dequeued in either ascending or descending order, depending upon the current position (recorded in RDEVCYL) and the direction of motion of the arm. If the arm is seeking up (that is, toward the higher cylinder numbers), the queue of IOBLOKs is scanned from the first block toward the last until an IOBLOK is found with an IOBCYL value equal to or greater than the value in RDEVCYL, or until the end of the queue is reached. At this point, the device is flagged as seeking down and the queue is scanned from last to first until an IOBLOK with an IOBCYL value equal to or less than RDEVCYL is found. When the IOBLOK is found, it is dequeued and started. The direction of motion is indicated by an RDEVFLAG bit and the next request is dequeued downward until the head of the queue is reached.

Because the queue itself is a two-way chained list, no special handling for null or unity set lists is required, and the ordered seek algorithm returns to first-in-first-out queuing.

Dedicated Channel Support: One of the facilities of the control program allows a virtual machine to control one or more channels on a dedicated basis. The channels are attached to the virtual machine by using the privileged ATTACH CHANNEL command. A virtual machine can have one or more dedicated channels. In addition, channels can be split between virtual machines but a dedicated channel cannot be shared between two virtual machines. For instance, channel 1 could be dedicated to virtual machine A, and channel 2 could be dedicated to virtual machine B, or both could be dedicated to virtual machine A or B.

With a dedicated channel, all virtual machine device addresses must be identical to the real machine device addresses. For instance, virtual device 130 must be real device 130, and virtual device 132 must be real device 132. With dedicated channels, CP does not perform any virtual device address mapping.

CP error recording and channel recovery procedures are still in effect for dedicated channels. The dedicated channel support can be used in conjunction with the virtual=real feature for any virtual machine that is occupying the virtual=real storage space.

Virtual Console Simulation

DMKVCN receives control from the virtual machine I/O executive, DMKVSI. When control is received, the device is available with no interruptions pending. A console control block, VCONCTL, that is obtained from storage and chained from the virtual device control block, VDEVBLOK, by DMKLOG is accessed for use during the interpretation of the virtual console I/O sequence. The user's CAW is examined for validity. If it is valid, the TRANS macro is issued to fetch the first user CCW. This CCW is moved to the VCONCTL block for analysis.

The CCW is analyzed to determine if it is a read, a write, a control, a sense, a TIC, or an invalid operation. Based upon the analysis, the appropriate processing routine in DMKVCN is invoked.

The Read Simulation Routine: Obtains a buffer for input data from free storage. The location of the buffer is set in the VCONCTL block. The DMKQCNRD routine is called to schedule and perform an actual read to the corresponding real device representing the user's virtual console. If SET LINEDIT ON is specified, the buffer data is edited and translated to EBCDIC. When the read is completed, the data is moved to the specified user address obtained from the address portion of the virtual CCW. If command chaining is specified, processing returns to fetch and analyze the next CCW. If command chaining is not specified, the virtual CSW is constructed in the VDEVBLOK and an interrupt is flagged as pending in the VMBLOK.

The Write Simulation Routine: Obtains a buffer for the construction of the output message from free storage. The virtual machine data is located from the virtual CCW address in the VCONCTL block and moved to the data buffer. The DMKQCNWT routine is called to write the data in the buffer and provide the necessary length, translation, and format functions. Control is received at the DMKVCN module upon completion of the writing. At this point, the virtual CCW is re-examined. If command chaining is specified, processing continues to fetch and analyze the next CCW. If command chaining is not specified, the virtual CSW is constructed in the VDEVBLOK and an interruption is flagged as pending in the VMBLOK.

The Control Simulation Routine: Is used for the NOP and ALARM operations. A NOP operation requires no data transfer or I/O operation. An ALARM operation has no equivalent on low-speed teleprocessing equipment. Thus, a message indicating the ALARM operation is constructed. DMKQCNWT is called to output the constructed message. If the command is chained, processing continues (for NOP or ALARM) to fetch the next CCW and analyze it. If command chaining is not specified and this is not the first CCW, a virtual CSW is constructed in the VDEVBLOK and an interruption is flagged as pending in the VMBLOK. If this is the first (and only) CCW, then a condition code of 1 is presented with channel end and device end in the virtual CSW.

A Virtual Sense Operation: Is similar to a control operation, because no actual I/O operation is performed. However, there is data transfer. The sense data from the VDEVBLOK is moved to the virtual storage location specified in the virtual CCW address. If the command is chained, processing continues to fetch the next CCW and analyze it. Otherwise, an interruption is flagged as pending in the VMBLOK.

A Virtual TIC Operation: Fetches the virtual CCW addressed by the TIC address and analyzes the fetched CCW. If the fetched CCW is itself a TIC, or if the TIC is the first CCW, a channel program check condition is reflected to the virtual machine as an interruption or as a CSW-stored condition, respectively.

Invalid Operation: Any other operation is considered invalid. Command reject status is posted in the virtual sense byte and the operation is terminated with unit check status presented in the virtual CSW.

Remote 3270 Programming

For a basic understanding of CP processing of data relating to 3270 devices on binary synchronous lines, the information and terminology contained in IBM 3270 Information Display System Component Description, and General Information - Binary Synchronous Communications is required. A digest of some of this essential information as it applies to VM/SP HPO follows:

- Text messages to and from remote terminals and printers can only be achieved when the bisync line is in text mode.
- Text messages from a remote device can be the result of a general poll or specific poll operation to the related device or devices on the bisync line. This polling communication interface is accomplished by each line-connected control unit having unique specific poll and general poll recognition circuitry and by the CP terminal list of valid bisync lines and 3270 remote control unit addresses. This list, the terminal list, is generated by system generation procedures employing TERMINAL and CLUSTER macros. For more details about terminal list generation, see the VM/SP HPO Planning Guide and Reference.
- Reliability and dependability of line operation is achieved by the use of: a double addressing scheme, control characters with a rigid message protocol, and complex redundancy-check characters appended to transmission messages. Examples of these techniques are shown in the formats that follow.
- Every message (text or control) that is issued by CP may or may not be responded to by the remote station or control unit. The type of response (or absence of response) that CP receives depends on the receptiveness of that device or control unit to the previously sent message (is the device ready and enabled and accurately addressed) and the content and correctness of the message (no line errors).
- To establish the relationship of the line of terminal response to a particular line or device write or read operation, CP employs an operation "tracking" facility (TP op code) imbedded in the issued CCWs. The function performed by the CP op code is described in the following CCW formats.

Format of the 3270 Remote CCW

Operat Code 1 byte	ion	Address Field 3 bytes	Flags 1 byte	TP Op Code 1 byte	Count 2 bytes
0	78	31 3	2 39 4	0 474	8 63

where:

Operation Code

contains the hexadecimal value of the type of operation performed by the command.

Valid operation codes are:

X'01' WRITE X'02' READ X'03' NO-OP X'09' POLL X'23' SET MODE X'27' ENABLE X'2F' DISABLE

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Address Field

Depending on CCW usage, this field may address:

Data

Output data contained in the CONTASK.

Area

The address of the data area (read buffer) located in the BSCBLOK at BSCREAD.

Control Characters

A data-link control character such as EOT or ENQ that is defined in DMKRGA or DMKRGB.

Response

(BSCRESP). The address location of the response buffer in the BSCBLOK.

Addressing Characters

Data characters that indicate which device is to send or receive data. The entry for WRITE operation is at location BSCSEL. The entry for the READ operation is at location BSCPOLL.

Note: To see how the key words DATA, Control Characters, Response and addressing Characters are used, refer to the CCW sequences described in "I/O Program Routines for BSC Lines and 3270 Remote Devices" in this section.

Flags

The flag bits turned on in the CCW: CC (channel commands), CD (chained data), SILI (suppress incorrect length indication), skip (suppress data transfer to main storage).

TP Op Code

An imbedded teleprocessing operation code in the CCWs used in bisync line communications. This code is inspected by the secondary interruption handler, DMKRGAIN, when channel end and device end are received. The code is also used by the error processing module, DMKBSC. The code indicates the function being performed by the associated command. For use of the TP op codes, refer to the formatted CCWs that follow.

Count

Refers to the byte length of the CCW READ or WRITE operation.

I/O Programs for Bisynchronous Lines and Remote 3270s

Before data communication to remote 3270 equipment can take place, the remote teleprocessing line, the control unit and the device(s) must be enabled for communication. This occurs when a special sequence of channel commands is executed. Disabling a line occurs in a similar manner. The following is the format of the CCWs used in the enabling/disabling operation.

Enable a Line

Operation	Command Code	Address	Flags	TP Op Code	Count
Disable Line	X′2F′	0	CC, SILI	01	1
Set Mode	X'23'	Control Character	CC, SILI	01	1
Enable Line	X'27'	0	SILI	01	1

Disable a Line

Operation	Command Code	Address	Flags	TP Op Code	Count
Disable Line	X′2F′	0	SILI	01	1

After a line is enabled, communication can then be directed to a particular resource. The sequence of events is as follows:

Send a data link control character on the line that places the control unit in control mode. This mode makes the control unit receptive to the specific address indicated by the second CCW. The third CCW is a read CCW that is needed for the acknowledgement response from the addressed control unit. Normally, in response, CP transmits a block of data to that device with a write text CCW. Acknowledgement of receipt of this data is contained by the read response (text) CCW. The format of the CCW select and write text operation follows.

Write Select

Operation	Command Code	Address	Flags `	TP Op Code	Count
Write an EOT	01	Control Character	CC, SILI	02	1
Write addressing char.	01	List Address Character	CC, SILI	03	5
Read Response	02	Response	SILI	05	2

When the control unit recognizes the addressing characters, it transmits an acknowledgement and enters text mode. CP then writes one or more screen lines in a Write Text operation. Multiple lines are written if more than one CONTASK is queued or if the screen must be updated.

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Write Text (One Line)

Operation	Command Code	Address	Flags	TP Op Code	Count
Write text	01	Data	CD, SILI	03	vari- able
Write ETX	01	Control Character	CC, SILI	03	1
Read Response	02	Response	SILI	11	2

Write Text (Multiple Lines)

	Operation	Command Code	Address	Flags	TP Op Code	Count
(1)	Write text	01	Data	CD, SILI	03	vari- able
	TIC	08	To next write*	CD, SILI	03	1

^{*} Format (1) or (2)

(2)	Write text	01	Data	CD, SILI	03	vari- able
	Write ETX	01	Control Character	CC, SILI	03	1
	Read response	02	Response	SILI	11	2

In situations where the line is found to be in text mode, CP can issue a write reset sequence to put the binary synchronous line in control mode. The following format illustrates the write reset CCW.

Write Reset

Operation	Command Code	Address	Flags	TP Op Code	Count
Write EOT	01	Control Character	SILI	09	1

In situations where the expected response from a remote station was not received or was invalid, the channel program may request the remote station to retransmit the response. The following write ENQ format shows this sequence. The remote station, upon receipt of the ENQ message, responds by transmitting the expected or valid response to the response area indicated by the original read response CCW.

Write ENQ

Operation	Command Code	Address	Flags	TP Op Code	Count
Write ENQ	01		CC, SILI	06	1
TIC	02	Original Read Response CCW	SILI	0	1

Read operations occur following a general poll or a specific poll for text messages. In a general poll sequence, CP transmits the general poll characters to the attached control unit on the bisync line. The control unit recognizes the polling request, and transmits any pending data, causing the second (NOP) CCW to be skipped. The last CCW provides the read buffer and the count necessary for the incoming data block from the first remote station on the list that had a message queued for transmission. If, however, there is no pending data, then the channel program ends with the third CCW. The following read initial format shows the initial read CCW sequence.

Read Initial

Operation	Command Code	Address	Flags	TP Op Code	Count
Write EOT	01	Control Character	CC, SILI	02	1
Poll	09	Control Character	CC, SILI	03	7
I/O No operation	03	0	SILI	07	1
Read Text	02	Area	SILI	10	263

After CP receives a message from a remote station, it transmits any outbound data, then repeats the general poll. When a poll completes with no inbound data, CP starts the poll delay, then allows the poll delay interval to expire before starting another poll to the line (assuming CP has no higher line priority tasks to process). If, in the process of receiving messages from remote stations, CP receives a message block that is invalid or its beginning or ending bisync control characters are not recognized, CP can elect to send a negative response back to the remote station. This negative response, the NAK control character, causes the remote station to retransmit the previous message to CP. This incoming message is processed by the second CCW of the read repeat sequence as shown in the format below.

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Read Repeat

Operation	Command Code	Address	Flags	TP Op Code	Count
Write NAK	01	Control Character	CC, SILI	06	1
TIC	08	Original Read Text CCW	SILI	00	1

Once an error-free message is received from the remote station, CP proceeds with one of two actions:

- 1. If there is data to be transmitted, CP sends it immediately, using the Write Text format. This is known as binary synchronous "limited conversational mode."
- 2. If there is no data to be transmitted to the remote device that generated the message, CP sends an RVI control character to suspend the transmission of additional data. The remote 3270 responds to the RVI with EOT instead of sending another message.

Read Interruption

Operation	Command Code	Address	Flags	TP Op Code	Count
Write RVI	01	Control Character	CC, ;SILI	06	2
Read Response	02	Response	SILI	11	2

Data Formats - Remote 3270s

CP, in conjunction with remote 3270 support, uses the following formats for its text messages. For a detailed explanation of the abbreviations used, see the *IBM 3270 Information Display System Component Description*.

Write Text Data Message Format

Display commands use this message format for the placement or erasure of data anywhere on the display screen. The display commands that implement this function are: WRITE (X'F1'), ERASE/WRITE (X'F5'), ERASE WRITE ALTERNATE (X'X7E'), and COPY (X'F7').

Write Data Stream

SI	X ESC	CMD	WCC	SBA	Buffer Address	Orders & Text	SBA	Buffer Address	ETX
1	1	1	1	1	2	variable	1	2	1

Write Structured Field Data Stream

CP uses the Write Structured Field data stream when the display is enabled by the system operator using the READ PARTITION (QUERY) function. CP determines the device features of 3278 and 3279 display stations. This data stream is not used if the display station type is 3277.

DLE	STX	ESC	WSF CMD	length	RD. PART.	X'FF'	QUERY
1	1	1	1	2	1	1	1

If the remote control unit supports the 3270 extended data stream, then CP transmits all text messages in binary synchronous *transparent text* mode. Transparent text mode is used so that all 256 possible character codes may be transmitted. These character codes are required for certain 3270 extended functions such as color and extended highlight features.

The format of the Write Data Stream in transparent text mode is similar to the normal format except that the leading STX and trailing ETX are modified:

DLE	STX	ESC	CMD	wcc	SBA	Buffer Address	Orders & Text	SBA	Buffer Address	DLE	ETX
1	1	1	1	1	1	2	variable	1	2	1	1

The DLE-STX header indicates the start of a transparent text block and the trailing DLE-ETX indicates the end of the block. The DLE-ETX is written by a special *command-chained* CCW to differentiate it from the data contained in the text.

The transmission control unit (TCU) hardware provides special support for transparent text mode. The TCU checks each text character and inserts a DLE character before any DLE text character, thus turning a single DLE into a double DLE. These DLEs are automatically removed by the remote control unit. See *General Information-Binary Synchronous Communications* for details.

Write Text Messages for the Copy Command

The COPY command is limited to compatible printers located on the same control unit. Action starts by pressing a PF key designated for the COPY function. CP responds by sending a message to the control unit that contains both the designated printer and the displays station that requested the action and directs the control unit to print the designated display buffer to the printer specified.

The format of the COPY messages follows.

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3271 Copy Data Stream

STX ESC CMD C X'F7'	C From Address	ETX
------------------------	-------------------	-----

3275 Copy Data Stream

STX	ESC	CMD X'F1'	WCC	SBA Adr	Buff (4040)	IC	ETX
-----	-----	--------------	-----	------------	----------------	----	-----

Read Text and Read Header Message Formats

The following is representative of input message formats. The format of a CP-generated read operation follows.

Read Text Data Stream

Index Byte	STX	CU Adr	Dev Adr	AID	Cursor Addr	SBA	Buff Addr	Text	SBA	Buff Addr	Text		ETX	
---------------	-----	-----------	------------	-----	----------------	-----	--------------	------	-----	--------------	------	--	-----	--

The Read Text Data Stream is in the *transparent text* format if there are binary synchronous control codes in the data.

Inde Byte		STX	CU Adr	Dev Adr	AID	Cursor Addr	SBA	Buff Adr	Text	SBA	Buff Addr	Text	DLE	ETX	
--------------	--	-----	-----------	------------	-----	----------------	-----	-------------	------	-----	--------------	------	-----	-----	--

Error Status Data Stream

Another form of input message is the error status message. Error status is processed by the DMKRGA module. The characters, %R, following the SOH signify that this message contains sense and status data. The format of this message follows.

Byte A Bit Bit <th>Index Byte</th> <th>SOH</th> <th>% R</th> <th>STX</th> <th></th> <th></th> <th></th> <th>ETX</th>	Index Byte	SOH	% R	STX				ETX
--	---------------	-----	-----	-----	--	--	--	-----

Test Request Data Stream

The test request message, upon receipt from display terminals, is ignored by CP. The input inhibit mode that the display terminal enters upon pressing the test request key can be reset only if the terminal user presses the RESET key. The characters, %/, following SOH indicate the test request function. The format of this message follows.

Index	SOH	%	1	STX	Text	ETX
Byte						

Allocation Management

Real storage space above the nucleus and below the 16 Mb line is made up of the dynamic paging area, the free storage area, and the trace table area. The dynamic paging area consists of page frames that are allocated to virtual machines and CP to satisfy paging requests. The free storage area consists of page frames that are allocated to virtual machines and CP for working storage. The trace table area consists of page frames into which trace information is placed.

Real storage space above the 16 Mb line consists of a dynamic paging area for virtual machines only, and can include a V = R area for a PMA guest, depending on the way the system programmer generated the system. The page frames in the greater than 16 Mb dynamic paging area are used to satisfy virtual machine page faults, but are not used for pages that CP wants to directly address. CP must move a page referenced in this area to the less than 16 Mb dynamic paging area before CP can use the page.

The size of the free storage area, the trace table area, the two dynamic paging areas, and the V=R area for the PMA guest are defined by the system programmer at system generation time.

Normal Paging Requests

If a program interruption is caused by a normal paging request (not from a virtual machine that is running in EC mode with translation on), DMKPRGIN determines whether a segment or page translation error has occurred. If one of these errors occurred, an invalid address interruption code is set, and the interruption is reflected to the virtual machine supervisor. If a segment or page translation error has not occurred, the virtual machine's current PSW is updated from the program old PSW (PROPSW), the address of the current VMBLOK is placed in register 11, and DMKPTRAN is called to obtain the required page. When the paging operation is completed, control is returned to DMKDSPCH.

Virtual Storage Management

When operating in the CP relocate environment, each virtual machine's virtual storage space is described by two sets of tables.

- One set, the segment and page tables, describes the location and availability of any of the virtual machine's virtual pages that may be resident in real storage. Locations in these tables are indexable by virtual address, and the entries contain index values that reference corresponding real storage addresses. In addition, each table entry contains an indication of whether the corresponding virtual page is available to the user in real storage. These tables are referenced directly by the DAT feature when the virtual machine's program is running.
- The second set of tables, called swap tables, is a map of the locations of the virtual machine's pages on the DASD devices that comprise the system's paging or auxiliary storage. The DASD addresses in these

tables can either represent where the page is on DASD or zero, indicating that the given page has not yet been referenced and, thus, has a value of binary zeros.

The swap tables are arranged in a format indexable by virtual storage address. In addition to containing the address of a page, each entry contains flags and status bytes that indicate such information as:

- The storage protection keys to be assigned to the page when it is made resident.
- Whether the page is currently on its way into or out of the system (in transit), etc.

These tables are not referenced directly by the hardware as are the page and segment tables, but are used by paging management to locate user pages that are needed to execute a program.

A demand for a page on DASD can be made implicitly by a virtual machine or explicitly by CP.

- An implicit demand for a page is made when a program attempts to reference a page that is not available in real main storage. This attempt causes a program interruption with the interruption code indicating a page or segment exception. Upon recognition of this condition, control is passed to the paging manager to obtain a page frame of real main storage and to bring in the desired page.
- An explicit demand for a page can be made by CP (for example, in the course of translating a user's channel program). If, in the process of translation, CP encounters a CCW that addresses a page that is not resident in real storage, a call is made to the paging manager to make the referenced page resident.

While the requested page is being fetched, the requesting virtual machine is unable to continue execution. It may be possible, however, to run other tasks in the system, and CP runs these while the needed page is being paged in. When the requested page is resident, the virtual machine can be run and is dispatched in its turn.

In addition to demanding pages, virtual machines implicitly or explicitly release page frames of their virtual storage space. Part of the space may be explicitly released from both real and virtual storage via a DIAGNOSE instruction that indicates to the control program those page frames that are to be released. An entire virtual storage is released when a user loads (via IPL) a new operating system or logs off from the system.

CP also has virtual storage associated with it. This space contains CP (some parts of which need not always be resident in real storage), and virtual storage buffers for spooling and system directory operations. Although CP makes use of virtual storage space for its execution, it does not run in relocate mode. Thus, nonresident modules must be completely relocatable. To improve performance by eliminating unnecessary LCTL and LRA instructions, CP keeps track of where each virtual machine's page zero resides. CP does this by checking an in-storage pointer in the VMBLOK—the pointer contains the address of the virtual machine's page zero if the page is resident. If it is not resident, CP issues a TRANS macro, which checks for page residency and demands a page-in if the page is not in real storage. If the page is resident, CP bypasses issuing the TRANS macro.

Virtual Buffer Manager

CP can dynamically increase the amount of virtual storage it can use by using a virtual buffer manager. This virtual buffer manager also allows CP to allocate and deallocate this virtual storage.

The virtual buffer manager maintains a chain of nondispatchable VMBLOKs, similar to the SYSTEM VMBLOK. CP modules can invoke the virtual buffer manager to create these VMBLOKs dynamically. These VMBLOKs, which have userid defined by the calling CP module, are maintained on a chain that is anchored off the SYSTEM VMBLOK and not associated with the user VMBLOK chain.

As with the SYSTEM VMBLOK, each of these VMBLOKs is associated with segment, page, and swap tables for virtual storage. CP maintains an allocation table so it can control the allocation and deallocation of the virtual storage associated with the VMBLOK. The system function using the virtual buffer manager must be sensitive to the VMBLOK associated with the virtual storage it uses. The paging operation must be done using the specific VMBLOK associated with the virtual storage obtained to get access to the correct segment, page, and swap tables needed to access that storage.

Real Storage Management

Real storage management allocates the system's page frames of real storage to satisfy the demands for virtual pages made by the system's virtual machines. Efficiency of allocation involves a trade-off—the paging manager uses only enough processor time to ensure that:

- The set of virtual storage pages that are resident represent those pages that are most likely to be used.
- A sufficient number of cycles is available to execute virtual machine programs.

Inefficiency in the first area causes a condition known as thrashing, which means that frequently used pages are not allowed to remain resident long enough for useful work to be performed by or on them. Thrashing could be aggravated by the paging manager's page frame selection algorithm or by a scheduler that attempts to run more tasks than the system can handle (the sum of their storage requirements exceeds the real paging space available in the system). Thus, the paging manager must keep statistics on system and virtual machine paging activity and make these statistics available to the scheduler to detect and prevent a potential thrashing condition. Inefficiency in the second area causes an unacceptable ratio of CP overhead to virtual machine program time, and in extreme cases may cause CP to use excessive processor time. To understand how allocation is determined by CP, the way in which the inventory of real storage page frames is described to the system must be understood.

Each page frame (4096-byte block) of real storage in the system is in one of two basic states: nonpageable or pageable. A nonpageable page must remain resident in real storage for some period of time. Thus, the page frame cannot be taken from its current owner to be given to someone else. Pages can be either permanently or temporarily nonpageable, depending upon their use.

Temporary locks usually occur when an I/O operation has been initiated that is moving data either to or from the page, and the page must be kept in real storage until the operation has completed.

A page can also be temporarily nonpageable if it contains an active nonresident CP routine.

In addition, a page can be nonpageable through use of the LOCK command. Pages locked this way are permanently resident until they are explicitly unlocked by the UNLOCK command. Pages that are usually considered permanently nonpageable are those that contain the resident portion of CP and those that contain the system's free storage area in which control blocks, I/O buffers, and the like, are built.

The data area that page management routines use to control and allocate real storage is the CORTABLE. Each page frame of real storage has a corresponding entry in the CORTABLE, and because the table entries are fixed in length and contiguous, the entry for any given real page frame may be located directly by indexing into the table. Each entry contains pointers that indicate both the status and ownership of the real page that it represents. Some pointers link page table and swap table entries to the real page (and thus establish ownership), while others link the entry into one of several lists that real storage management uses to indicate the page frame's status and availability for paging. A given CORTABLE entry may appear on one of three lists. The lists are known as the free list (FREELIST), the flush list (FLUSHLST), and the swap list and they represent various levels of page frame availability.

The free list contains page frames that are immediately available for assignment to a requesting virtual machine. The virtual storage pages for which they were last used have either been released by their owners or they have been paged out to auxiliary storage. Requests for real storage are always satisfied from the free list. If the list has been depleted, the requestor waits until a new page frame becomes available as the result of a virtual storage release or a page out.

The flush list gains pages when special conditions occur during physical and logical swapping. See "Flush List Management" on page 145 for more information. The flush list is one of the places that the page frame selection routine looks to find a page frame to page out or to assign to the free list for a virtual machine that requires real storage space. A swap list contains pages that have been logically but not physically swapped out. There is one swap list for each user. Swap lists are another place the page selection routine looks to find pages that can be written to DASD.

Requests for Real Storage Page Frames

Requests for real storage fall into two general categories: those that are requesting space for virtual storage, and those (such as requests for CP work space) that need page frames for their own use. The former, more general case is discussed first, because the latter case is a subset of the first.

The main page manager routine, DMKPTRAN, maps a request for a specific virtual storage address into a page frame of real storage. This requires that the virtual page be read in and the necessary tables be updated to show the proper status of the page frame.

DMKPTRAN requires that the caller supply the virtual address to be translated, the VMBLOK address, and any options that apply to the page to be located. Most calls are made via the TRANS macro, which sets up the necessary parameters, determines whether or not the required page is resident, and calls DMKPTRAN if it is not.

When DMKPTRAN receives control, it uses the LRA instruction to see if the requested page is marked valid. If the page is valid, the routine locks the page if requested and exits to the caller. If the page is nonvalid, it may still be in real storage. Nonvalidity means that:

- The page table or segment table entry associated with the page has been flagged as invalid or
- The page table or segment table length has been exceeded.

Note: Pages above the 16 Mb line are exceptions. They are considered nonresident by CP although an LRA on them returns a condition code of zero.

If this is not the first reference to the page, it may be in transit or exist in one of the following areas:

- The less than 16 Mb free list
- The greater than 16 Mb free list
- The flush list
- The swap list
- A paging area
- A swap area.

Note: Swapping, in general, is the paging in or out of groups of pages. Logical swapping is the creation of lists of pages that can be written to DASD. Physical swapping provides for better DASD use by writing and reading pages in groups. Real storage management initiates swapping, but the I/O operations are done by the auxiliary storage manager.

When the referenced page is on a swap list, it means that the page has been logically but not physically swapped out. The routine RECLWSPG in DMKPTR reclaims the page without an I/O operation by removing it from the swap list and updating its page table entry, core table entry, and swap table entry.

When the referenced page is in a swap set that has been written to a swap area, the routine SWAPFLT swaps in the entire swap set. This situation is indicated by a swap fault, which is an address translation exception that occurs when CP or a virtual machine references a page that has been physically swapped out. Pages referenced by CP are always swapped in below the 16 Mb line. Other pages in the swap set, except page zero, can be swapped in anywhere.

If a page exists on the free list or on the flush list, DMKPTR reclaims it by the following process: If the LRA indicates that the page is invalid, it is still possible that the required page is resident. This occurs if the page frame has been placed on the FREELIST but has not yet been assigned to another virtual machine. When the page out routine removes a page frame from a virtual machine, the invalid bit is set in the corresponding page table entry. However, the real main storage index for the page frame is left unchanged. The page table entry is set to zero only when the corresponding page is actually assigned to another virtual machine. Thus, if DMKPTRAN finds the page to be invalid, a further test is made on the page table entry to see if the page can be reclaimed. If the entry is not zero (aside from the unavailable bit), the CORTABLE entry for the page frame is removed from the free or flush list and the page frame is returned to the calling virtual machine.

If the page table entry corresponding to the requested virtual page is zero, the required page is not in real storage and must be paged in. However, it is possible that the page is already on its way into main storage (intransit). This condition is indicated by a flag in the SWPTABLE entry for the virtual page. The DMKPAGIO routine maintains a queue of CPEXBLOKs to be dispatched when the pending page I/O is complete. The CPEXBLOK for the page in transit is located and a new CPEXBLOK, representing the current request, is chained to it.

Before exiting to wait for the paging operation to complete, DMKPTRAN checks to see if the deferred return (DEFER option) has been specified. If it has not, DMKPTRAN returns to the caller. If the DEFER option has been requested, DMKPTRAN exits to the dispatcher to wait for page I/O completion. When the requested page has been read into real storage, the list of CPEXBLOKs are unstacked first in first out to satisfy all requests for the page that arrived while it was in transit.

If a page is not in transit, a page frame of real storage must be allocated to fill the request. Before the allocation routine is called, a test is made to see if the caller wishes the return to his routine or to be delayed until after the requested page is available. If the DEFER option is not requested, DMKPTRAN returns to the caller after first building and stacking a CPEXBLOK that allows processing of the page request to be continued the next time the dispatcher (DMKDSPCH) is entered.

DMKPTRAN next calls the FREELIST manager (DMKPTRFR) to obtain the address of the next available CORTABLE entry. DMKPTRFR maintains a list of the CORTABLE entries for those page frames that are immediately available for assignment. As DMKPTRFR releases these page frames, a check is made to see if the number of entries on the FREELIST has fallen below a dynamically maintained minimum value. If it has, DMKSELCT is called to find page frames for placement on the free list.

Once a page frame has been assigned, DMKPTRAN checks to see if a page-in is required. It usually is, and the DASD address of the virtual storage page must be obtained from the user's swap table entry and the I/O operation scheduled. However, if the page frame has not yet been referenced (as indicated by a DASD address of zero), the real main storage page frame is set to zero, and no page-in is required. After the page-in operation has been queued, DMKPTRAN exits to the paging I/O scheduler (DMKPAGIO), which initiates the paging operation and exits to the dispatcher (DMKDSPCH) to await the interruption.

Some requests for main storage page frames are handled differently from general virtual-to-real storage mapping. In particular, it may be necessary for CP to obtain additional free storage for control blocks, I/O lists, buffers, etc. This is handled by the free storage manager, which makes a direct call to DMKPTRFR to obtain the needed storage. Usually, this storage is immediately available (due to free list replenishment). However, if the FREELIST is exhausted, the request for free storage is recognized as a high-priority call and queued first on the list of those waiting for free page frames.

Free List Replenishment

The free list contains page frames to satisfy virtual machine requests for real storage. DMKSELCT replenishes the free list in such a way that enough page frames are available to satisfy a certain number of swap in operations. The threshold value for free list replenishment can be set by the class A and class E SET SRM MINNUMSS command, and is equal to:

(SS * M)+1+Q1+Q2

where:

SS is the size of a swap set.

Μ

is the value specified by the SET SRM MINNUMSS command, or a default value of six. This value is the number of swap in operations that can be started without having to defer the request until pages become free.

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- Q1 is the number of Q1 virtual machines.
- Q2 is the number of Q2 virtual machines.

The free list is replenished directly when users release virtual storage space. DMKPGSPO, the page release routine, calls DMKPTTFT to place released frames directly on the free list. However, the free list frequently has to be replenished in other ways.

When the free list has to be replenished, DMKSEL obtains page frames in the following order:

- 1. Takes pages from the flush list.
- 2. Performs a core table scan.

Flush List Management

Pages are placed on the flush list when any of the following conditions are met:

- An I/O error occurs during physical swap-out, and no more swap space is available.
- When a virtual machine is dropped from queue, there are no valid swappable pages, and some pages are logically swapped for that virtual machine. These logically swapped pages are moved to the flush list.
- During core table scan, the number of resident pages becomes zero and some pages are logically swapped for that virtual machine.

DMKSELCT takes pages from the top of the flush list first.

Core Table Scan

DMKSEL performs a four-pass core table scan to replenish the free list. These passes are organized into two phases with two passes in each:

- Phase 1, Pass 1—Pages that have not been physically referenced since DMKSEL last examined them are selected for free list replenishment. This excludes reserved pages, shared pages owned by another processor, and pages owned by a virtual machine locked by another processor.
- Phase 1, Pass 2—Any of the pages not selected in the first pass because they were recently referenced are now selected.
- Phase 2, Pass 1—This pass is similar to Pass 1 of Phase 1. Now, however, the exclusions in Pass 1 of Phase 1 do not apply.
- Phase 2, Pass 2—As in Pass 2 of Phase 1, all pages are now eligible.

Note: The above sequence occurs in a general core table scan. In a preferred core table scan, in which a page below the 16Mb line is exclusively sought, the following distinction is made: During Phase 1 (both passes), logically swapped pages below the 16Mb line are moved above the line IF free pages are available there. In the case of a logically swapped page, the copy above the line remains logically swapped—the copy below the line is moved to the free list.

Once a page has been selected for free list replenishment, DMKSEL decides whether to page out or logically swap out that page. If the page was referenced at least once while resident, it is logically swapped out. If it was never referenced while resident, it is paged out, UNLESS that page can be used to satisfy a minimum working set. If this is the case, that page is logically swapped out. See VM/SP HPO CP for System Programming for a discussion of the minimum working set and the SET MINWS command.

When enough pages have been logically swapped out to form a complete swap set, that set is physically swapped out. Physical swap-out will also occur if the user has no more resident swappable pages or if the core table scan is protecting reserved pages.

Physical Swap-Out

DMKSWAPO is called by DMKSEL to initiate a physical swap-out. First, DMKSWAPO creates a swap set block (SSBLOK) for the swap set. A swap set block contains the number of pages in the swap set and the Paging Storage or DASD address where the pages are to be written.

After DMKSWAPO initializes the SSBLOK, it calls DMKPGTSW to allocate Paging Storage or DASD space for the swap set. If DMKPGTSW can successfully allocate space in a swap area (area defined by SYSPAG TYPE = SW), DMKPGUPP is called to deallocate the page area slots. This prevents the same page from existing in different Paging Storage or DASD areas at the same time. The swap set is moved to Paging Storage, if possible. Otherwise, all pages in the swap set are written to the DASD swap area with one SIO.

At the end of physical swap-out, DMKSWAPO places the page frames formerly occupied by the written-out pages on the free list.

If DMKPGTSW cannot allocate space in a swap area, return is made to DMKSELCT, which writes only changed pages to Paging Storage or DASD, and writes them to a paging area instead of a swap area. The number of SIOs required for writing these pages to DASD will generally be greater than one.

Swap Fault

A swap fault is an address translation exception that occurs when CP or a virtual machine references a page which is physically swapped out to a swap area. When a swap fault occurs, DMKPTRAN physically swaps in the entire swap set in which the page resides.

When a page in a swap area is referenced by a virtual machine, the page is placed wherever page frames are available, either above or below the 16 Mb line. If the page was referenced by CP, however, or if it is virtual page zero, then it must be swapped into the less than 16 Mb area. The other pages in the swap set can be placed anywhere. During a physical swap-in, all the pages in the swap set are flagged as in transit to prevent other I/O operations from being started if CP makes subsequent references to pages in the swap set.

When DMKPTRAN physically swaps in pages, it sets the change and recompute flags in the page and swap tables to show that these pages are to be paged out again and will require new DASD slots. Their old slots are deallocated by DMKPGUSW when the pages are physically swapped in.

For a deferred request (DEFER option of the TRANS macro specified with the call to DMKPTRAN), the virtual machine is placed in a swap wait state and does not execute until the physical swap-in is completed.

Prepaging

Prepaging is a physical swap-in that occurs when a virtual machine is added to queue. Prepaging tries to reduce the number of page faults and their subsequent delays by performing in advance some paging that may be required by the virtual machine. DMKSWAPI reads in from auxiliary storage the number of swap sets necessary to meet the prepaging requirement. The system programmer can use the SET SRM PREPAGE command to specify the number of swap sets of resident pages that DMKSWAPI should achieve via prepaging. When the SET SRM PREPAGE command has not been issued for a virtual machine, DMKSWAPI achieves a default value of two swap sets for Q1 virtual machines and does not swap in any swap sets for Q2 virtual machines.

Paging Statistics

The real storage manager, DMKPTR, accumulates paging statistics that the scheduler, DMKSCH, uses to anticipate user storage requirements. It keeps these statistics individually for each virtual machine in its VMBLOK. Cumulative statistics reflecting total paging activity for the system are kept in DMKPSA.

Among the kinds of page counts kept in each VMBLOK are the number of page-reads and page-writes for each virtual machine, and the number of times a virtual machine enters page wait when a page frame has been stolen from it. The VMBLOK also keeps a running total of the number of pages a virtual machine has resident at each page-read.

On systems running with more than 16 Mb of storage online, the VMBLOK contains two additional fullwords that reflect the number of times a page is moved from the greater than 16 Mb dynamic paging area to the less than 16 Mb dynamic paging area, and vice-versa.

Virtual = Real Option: The virtual = real option involves the mapping in a one-to-one correspondence of a virtual machine storage area with an equivalent real storage area. For instance, virtual page 1 is in real page frame 1 and virtual page 20 is in real page frame 20. Virtual page 0 is relocated at the end of the virtual storage space because it cannot occupy real page frame 0.

Note: There are times, such as when preferred machine assist is active, when the V=R guest gets absolute page 0.

The CP nucleus is altered at system generation to support the virtual = real option. Virtual machines with virtual = real (specially identified in the directory) can then logon and use the space reserved for this option. That space can be used by only one virtual machine at a time. Two virtual machines with the virtual = real capability cannot occupy the same space at the same time.

The reliability and availability of an operating system running virtual=real on a 308x, 3090, or 4381 processor can be enhanced when the TEST BLOCK instruction (TB) is used to validate the V=R storage area.

The virtual = real option allows the virtual machine to bypass the control program's CCW translation. This is possible because I/O from a virtual machine occupying a virtual = real space contains a list of CCWs whose data addresses reflect the real storage addresses. The restriction in this situation is that the virtual machine does not perform I/O into page frame 0 because this would perform a data transfer into real page frame 0. At the same time, it is assumed, and cannot be checked, that the virtual machine also does not attempt to do I/O beyond the bounds of its virtual addressing space. To do so would cause the destruction of either the CP nucleus, which resides beyond the virtual machine space, or another user's page.

Virtual 270X lines and sense operations from the virtual machine do not use the virtual=real function. These invoke CCW translation for the virtual enable/disable lines and the transfer of the sense bytes.

If the real I/O device is an MSS 3330V, then CCW translation is not bypassed since CP must still be able to recognize an MSS cylinder fault. See Appendix B for details.

The bypassing of CCW translation for the virtual machine occupying the virtual = real space is only invoked after the virtual machine has executed the SET NOTRANS ON command. This command can only be issued by the virtual machine occupying the virtual = real space. The command initiates the bypass of CCW translation. This option is automatically turned off if the virtual machine performs an explicit reset or an implied reset by performing a virtual IPL. During virtual machine IPL, I/O must be performed into page frame 0. For this reason, normal virtual IPL simulation assumes CCW translation in effect to accomplish the full simulation. Once the IPL sequence has completed, CCW translation can be bypassed by issuing the SET NOTRANS ON command.

When the virtual machine demands a page frame through normal use of CP's page tables, the paging routine recognizes the virtual = real capability. It then assigns the virtual page to the equivalent real page frame and does not perform a paging operation, because all these pages are resident and are never swapped out.

Note: The virtual machine running with virtual=real is still run in System/370 relocate mode. Shadow table bypass, invoked by the SET STBYPASS command, allows CP to eliminate the shadow tables for an operating system running in the V=R area. When CP runs a V=R user, the shadow table for the V=R user is identical to the virtual system's own page and segment tables, with the exception of page zero. CP relocates the virtual machine's page zero (via the shadow table) to the highest real address within the V=R area. When STBYPASS is turned on, CP modifies the virtual operating system's page table to relocate virtual page zero to the highest real address. It is then possible to dispatch the virtual machine with control register 1 pointing to the virtual page and segment tables.

The UNLOCK command has a VIRT=REAL operand that essentially releases the virtual=real area for normal system paging use. Once the area has been released, it can only be reclaimed for additional virtual=real operations only by an IPL of the system. The size of the virtual=real area is an installation specification that is part of the special nucleus generation procedure that is outlined in the VM/SP HPO Installation Guide. The size of the area must be large enough to contain the entire addressing space of whatever V=R machine wishes to occupy that space. A V=R machine can use a smaller space than is provided but cannot use a larger space without regenerating the CP nucleus.

Extended Storage Support

Extended storage support is a software feature that allows CP to utilize greater than 16 Mb of real storage. It can be used only on processors that have the hardware for greater than 16 Mb of real storage and 4K storage keys. This support will only be active if greater than 16 Mb of storage is configured and on-line. Extended storage support improves system performance because virtual machines can use the storage above the 16 Mb line as additional dynamic paging area, freeing CP from contending as often with virtual machines for page frames below the 16 Mb line. Thus, CP has more work space available when it needs it.

Real storage above the 16 Mb line is known as the greater than 16 Mb area. The greater than 16 Mb area below the RMSIZE value specified by the system programmer is a dynamic paging area used to satisfy virtual machine page faults, but is not used for CP pageable pages, virtual page 0, or virtual machine pages that CP wants to directly address. Before CP can work with a virtual machine page resident in the greater than 16 Mb area, that page is moved to the less than 16 Mb area. CP needs to work with virtual machine pages for such functions as privileged operation simulation, channel program translation, interrupt reflection, and console functions.

Extended Real Storage Management

DMKPTR manages real storage somewhat differently when more than 16 megabytes of real storage is online.

- It manages two FREELISTs instead of one
- It uses DMKPTTPM to move pages referenced by CP to below the 16 Mb line
- It includes page frames above the 16 Mb line when it is managing CORTABLE entries in the page selection/replenishment functions.

The only modules that can use the greater than 16 Mb area are DMKPTR, DMKPTS, DMKPTT, and DMKSEL. When CP wants to work with a page that resides in the greater than 16 Mb area, DMKPTRFR allocates a page frame in the less than 16 Mb area (as if it were satisfying a page fault) and DMKPTTPM then moves the page to that frame.

Under extended storage support, DMKPTRAN is responsible for distinguishing virtual machine page fault calls from other calls. DMKPTRAN is called when an LRA instruction indicates to CP that the page CP wants to reference is not resident or is in the greater than 16 Mb area. Anytime the LRA instruction returns a condition code of 0, indicating that the page is resident, DMKPTRAN checks to see if the VFAULT option is specified in the caller's register 2.

If VFAULT is specified, DMKPTRAN knows it is being asked to satisfy a virtual page fault. DMKPTRAN sets the real translated address into the caller's register 2, makes the appropriate page table entry valid, sets a condition code of 0, and returns to the caller. If the VFAULT option is not specified when calling DMKPTRAN, then DMKPTRAN calls DMKPTRFR to allocate a page frame in the less than 16 Mb area, and either calls DMKPTTPM to move the page to below the 16 Mb line or pages it in from DASD.

Note: If an IX/370 virtual machine page zero resides above the 16Mb line, DMKVAURN will call DMKPTRAN to bring the page below the line. (In this case, handshaking in DMKSVC will take place via the long path.)

When DMKPTRFR is called to allocate a page frame anywhere in storage, it compares the number of pages currently available in the less than 16 Mb area with the number of pages currently available in the greater than 16 Mb area. DMKPTRFR will take a page frame from whichever area has more page frames on its FREELIST.

DMKPTTFT is called to return a page frame of real storage to a FREELIST. It checks to see if the page frame to be returned resides above or below the 16 Mb line. For a less than 16 Mb page frame, DMKPTTFT will check the less than 16 Mb deferred request queue for a page request. If there are no page requests there, DMKPTTFT will satisfy a request on the greater than 16 Mb deferred request queue, if one exists. If there are no deferred requests, DMKPTTFT will queue the free page frame off the less than 16 Mb FREELIST. If the page frame being returned resides in the greater than 16 Mb area, DMKPTTFT will satisfy requests on the greater than 16 Mb deferred request queue only, or return the free page frame to the greater than 16 Mb FREELIST.

Page Interchange Processing

DMKPTT contains the subroutines necessary to move pages between the greater than 16 Mb area and the less than 16 Mb area. DMKPTTAL is first called at IPL time by DMKSEG if extended storage support is generated by the system programmer. At that time, DMKPTTAL allocates two virtual address slots in the system address space. These slots are needed later when CP wants to reference a virtual machine page that is stored in the greater than 16 Mb area, and needs to move the page.

DMKPTTPM is the subroutine that DMKPTR calls to move a page from one page frame to another. It uses the virtual address slots allocated at IPL time to store the old and new address of the page that is being moved. DMKPTTPM then moves the page using the move long instruction (MVCL) after DAT is turned on. The old and new address of the page are the operands of the instruction.

DASD and Paging Storage Management

Any virtual machine's virtual storage pages that have been referenced but are not resident in real storage must be kept in slots on the DASD devices or Paging Storage. DASD page space is assigned only when the page is selected for a page-out. Certain DASD pages may also be marked read-only. Thus, the DASD address slot initially associated with the page should be considered to be the source of the page only. If the page is changed after it has been read into real storage, a new slot must be obtained when it is paged out. Examples of read-only pages are those which contain portions of pageable saved systems and pages which are part of a system spool file. Slots can be reassigned when DMKPTRAN finds that it must move a page out to a movable-head DASD device. In this case, the old slot is released and the new slot is obtained.

CP-owned DASD volumes are used by CP for swapping, paging, spooling, page migration, and dump spool file allocation. Paging Storage areas can be used by CP for swapping or primary paging. The system programmer uses the SYSPAG macro to define how the CP-owned DASDs and Paging Storage are to be allocated and used. The number and order of the SYSPAG macros, which are included and assembled in DMKSYS, establish an allocation hierarchy which tells CP how to use specified areas of DASD and Paging Storage.

By specifying ORDER = SYSTEM (the default) on the SYSPAG macro, a routine will scan the ALOCBLOK chain and attempt to improve upon the ordering by spreading out the ALOCBLOKs over the control units. (This will be done only if at least four ALOCBLOKs are defined.) If ORDER = USER is specified (and if at least three ALOCBLOKs are defined), the areas will be ordered to match the order in which you specified them on the SYSPAG macro.

The types of areas that can be defined on the SYSPAG macro for CP-owned volumes are:

- SW An area defined for swapping
- PP An area defined for primary paging
- PG An area defined for general paging
- PM An area defined for page migration
- PS An area defined for spooling
- DU An area defined for dump spool files

Paging Storage areas may be used only for types SW (and then only on the first TYPE=SW level) and PP.

The 3880 Model 13 or Model 23 storage subsystem should be used only for **nonpaging** applications. Nonpaging refers to any data on DASD that is not defined as paging, swapping, spooling, or dump area. (Actually, these subsystems may be used for paging applications, but only if the caching function is turned off via the CACHE command.) See VM/SP HPO CP for System Programming for more information on this subsystem. See the VM/SP HPO Planning Guide and Reference for complete information on the format, restrictions, and recommendations for the usage of the SYSPAG macro.

Note: You can alternately use the SYSXSTOR macro to define Paging Storage on the 3090 processor. See VM/SP HPO CP for System Programming or the VM/SP HPO Planning Guide and Reference for more information on this support.

The SYSPAG and SYSXSTOR macros generate control blocks called SYSPLIST blocks. See Figure 29 for the SYSPLIST/ALOCBLOK Generation and Relationship. At least one SYSPLIST block is built for each SYSPAG macro. CP uses these SYSPLIST control blocks as anchor points, from which it chains ALOCBLOKs during CP initialization. Each ALOCBLOK (DASD cylinder or Paging Storage allocation block) describes a group of contiguous cylinders on a CP-owned volume or a group of contiguous Paging Storage increments.

Each such group is used for the specified type of pages. Therefore, each ALOCBLOK appears on only one allocation chain, although more than one ALOCBLOK may exist for each CP-owned volume and for Paging Storage.

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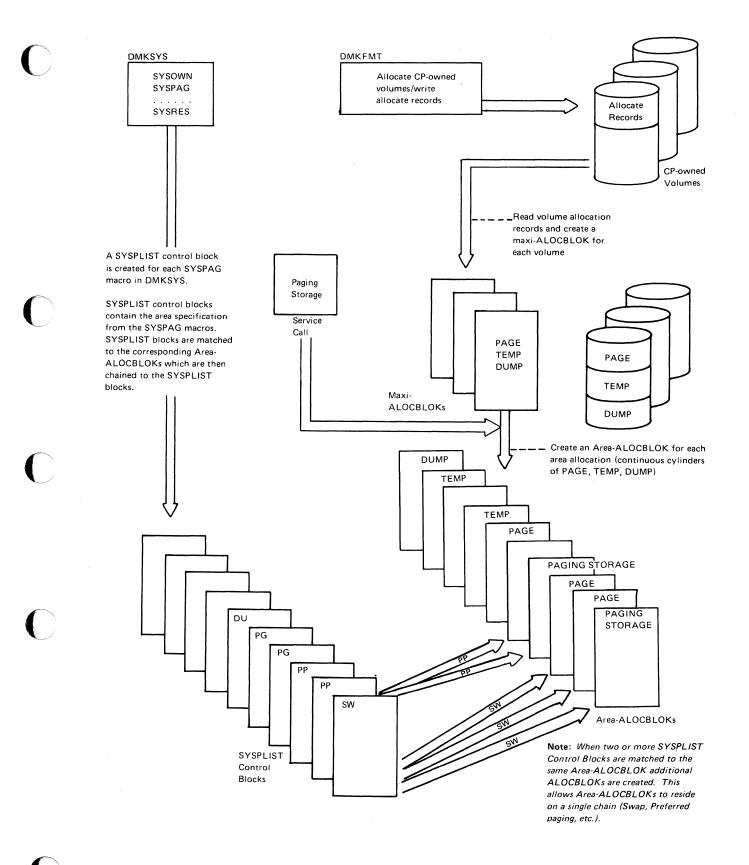


Figure 29. SYSPLIST/ALOCBLOK Generation and Relationship

Creation of ALOCBLOK Structure

DMKVDG is responsible for building ALOCBLOKs and chaining them to the appropriate anchors. DMKVDGAL is called by two modules:

- DMKIDU, when a CP-owned volume is online at initialization.
- DMKVDA, when a CP-owned volume is attached to the system.

When DMKVDGAL is called by DMKIDU, it receives the address of the maxi-ALOCBLOK for the volume. The maxi-ALOCBLOK describes all the space on the volume as specified in DMKFMT. DMKVDGAL then builds area-ALOCBLOKs, which define contiguous cylinders of the same allocation type (e.g., PAGE, TEMP). Lastly, DMKVDGAL matches the area-ALOCBLOKs to the SYSPAG macro specifications, and makes the necessary adjustments to create the final ALOCBLOKs that are chained to the appropriate SYSPLIST anchors.

Note: Before DMKVDGAL creates area-ALOCBLOKs for FBA volumes, it rounds the starting page number for each area of the volume up to the next pseudo cylinder value. Failure to match pseudo cylinder boundaries with allocation boundaries will result in the loss of use of one access position. It rounds the ending page number for each area down to the previous pseudo cylinder boundary. A pseudo cylinder is one access position on a physical FBA device.

When DMKVDGAL is called by DMKVDA, it must first build a maxi-ALOCBLOK before creating area-ALOCBLOKs and completing the process just described.

Area-ALOCBLOKs that are created for TDISK DASD space are not chained off a SYSPLIST anchor, but are chained by device type from anchors defined in DMKPGT.

The RECBLOKs (DASD page allocation blocks) for a given ALOCBLOK will appear on one of two chains. DMKVDGAL rechains existing RECBLOKs for recovered spool files to the appropriate area-ALOCBLOKs, in ascending order from low to high cylinder, from the field ALOCRECS. It chains RECBLOKs for PAGE and SWAP area cylinders in the same way but from the field ALOCPAGE, after it preallocates and initializes them.

Page RECBLOKs for TYPE = SW, PP, and PG are obtained as a group and are contiguous to allow quick allocation of a particular type for deallocation. The paging RECBLOKs for cylinders of these types that contain spool files will be marked "full," and will not be used for page allocation, even if the corresponding spool RECBLOKs are completely deallocated and FRETted.

Paging Storage Allocation

DMKPST builds the ALOCBLOKs for Paging Storage areas defined by the SYSPAG macro. It is called by DMKIDU during initialization, immediately after DMKIDU calls DMKVDG.

Finally, DMKIDU calls DMKVDH to order the ALOCBLOK chains. If ORDER = USER was specified (and at least three ALOCBLOKs are defined), the ALOCBLOKs are ordered according to the order specified on the SYSPAG macro.

If the SYSPAG macro was coded with the ORDER = SYSTEM option (the default), and if at least four ALOCBLOKs are defined, DMKVDH will scan the ALOCBLOK chain and attempt to improve upon the ordering. The goal will be to spread out the ALOCBLOKs over the control units as much as possible in an attempt to avoid excessive I/O contention.

If you use the SYSXSTOR macro to define Paging Storage, DMKPST calls DMKXST to build the ALOCBLOKs and RECBLOKs.

Pages are moved to and from main and Paging Storage via the Real Storage Manager (RSM). As in DASD allocation, the Auxiliary Storage Manager (ASM) controls the ALOCBLOK and RECBLOK structures, which map the pages of Paging Storage. A page, the smallest unit of allocation, is 4K bytes in length. For TYPE = PP, each bit in the RECMAP represents a single page. For TYPE = SW, each bit in the RECMAP represents a swap set which contains multiple pages.

For allocation, the ALOCBLOKs are chained to the SYSPLISTs. When the ASM determines that a page is being allocated in Paging Storage, it places an X'FF' in the device code byte (fourth byte) of the slot address, to indicate that the page is in Paging Storage.

There is no RDEVBLOK for Paging Storage. Instead, ALOCBLOKs are chained to a special anchor (APSTALOC) in the PSA. When the ASM determines that a page in Paging Storage is being de-allocated, (via the X'FF' in the device code byte), this anchor is used as the starting point to find the correct ALOCBLOK and RECBLOK.

Slot Allocation

DMKPGT is called to supply the address of an available slot or slots when they are required. When CP needs DASD space for system paging, system spooling, or page migration, DMKPGT allocates one DASD slot. When CP needs DASD space for swapping or dumps, DMKPGT allocates contiguous DASD slots.

DMKPGT maintains the chains of ALOCBLOKs normally created during CP initialization. It has several entry points, which correspond to the type of DASD space the caller requires. When DMKPGT cannot allocate space from the requested area, it allocates space from a lower level, unless the request is for swap space. Unfulfilled swap requests cause a non-zero return code to be returned to the caller. Dump requests that cannot be satisfied from TYPE = DU areas will be satisfied from TYPE = PS areas.

DMKPGT uses an algorithm called **N-select** to select multiple (N) slots on the same cylinder of the same device before proceeding to the next defined paging area as described by the next ALOCBLOK on a chain. This enables CP to write multiple pages to the same device with a single SIO.

When DMKPGT receives a request for DASD space, it examines the "current" ALOCBLOK, the one that it used to satisfy the last request from the given area. The current ALOCBLOK is pointed to by the anchor in the SYSPLIST block and contains a count of consecutive allocations to the given area. This count is the N-select count. Each time DMKPGT allocates DASD space using the current ALOCBLOK, it decreases the N-select count until the cylinder is fully allocated or the area has been used for "N" consecutive allocations. Then the N-select count goes to zero. (The N-select count is set to zero when paging I/O is started to this area.) The initial value of "N" for a given ALOCBLOK is determined at system initialization and depends on the device type and the use for a particular DASD area as defined by the SYSPAG macro.

When the count for the current ALOCBLOK is zero, DMKPGT uses the next non-full ALOCBLOK on a particular SYSPLIST level. When all the ALOCBLOKs on a particular SYSPLIST level are full, DMKPGT goes to the next (lower) SYSPLIST level (except, as mentioned, for dump requests).

Note: Spooling (PS) and page migration (PM) areas have an N-select value of 1. Consecutive requests for these slots are not related to any one user, and the requests are not grouped by the caller, so there is no reason to group these pages together on DASD. Swap (SW) areas also have an N-select value of 1, where one set of swap set pages will be allocated per request, rather than one page. Allocation for swap sets tends to be on different devices to enable more than one swap set to be in transit at the same time.

Cylinder Allocation

DMKPGT controls the swapping, paging, and spooling I/O load of the system by allocating cylinders evenly across all available channels and devices. In order for a device to be considered available for the allocation of paging and spooling space:

- Its volume serial number must appear in the system's owned list
- For CKD DASD, it must have at least one cylinder of PAGE or TEMP allocated space marked as available in the cylinder allocation block which is located on cylinder 0, head 0, record 4
- For FBA DASD, it must have at least one page of PAGE or TEMP space marked as available in the allocation extent map located in blocks 3 and 4
- It must not be an MSS 3330V volume.

DMKPGT allocates DASD slots by using the chains of ALOCBLOKs and RECBLOKS that are created at system initialization. It tries to select sparsely populated cylinders so that it will frequently be able to allocate many slots from the same cylinder. When the current cylinder is full, DMKPGT uses an algorithm called the moving cursor.

The moving cursor is actually a pointer which steadily moves through the chain of RECBLOKs. When a RECBLOK indicates that the last cylinder in the extent, as defined by the ALOCBLOK, is full, the cursor changes direction and moves back through the chain of RECBLOKs. Every time the moving cursor reaches the end of an extent and changes direction, it will usually skip over the first few cylinders it examines because they are the ones that were most recently allocated and are probably full, or nearly so. As the moving cursor progresses, however, it is more and more likely to find cylinders that have much unallocated space. This algorithm thus helps DMKPGT cut down on the time spent seeking an available cylinder.

In some cases, DMKPGT uses a modified moving cursor. The modified moving cursor is used for cylinder allocation for the 3880-11/3350 or the 3880-21/3350, and for page migration and spooling areas (TYPE = PM or TYPE = PS). The 3880-11 and 3880-21 uses a storage-like cache and works best by using recently deallocated slots instead of allocating from another cylinder. Page migration and spooling areas, as mentioned earlier, are not requested for contiguous slots, so there is no reason to have the moving cursor waste time examining full cylinders when it reverses direction.

The modified moving cursor starts at the first non-full cylinder of the extent. It moves in only one direction and starts over frequently. The cursor is thereby reset and enables DMKPGT to ensure that deallocated DASD slots will be reused in a minimum amount of time. DMKPGU, the slot deallocation routine, resets the cursor if the deallocated slot is on a lower-numbered cylinder.

If paging and swapping areas (TYPE=PP/PG and TYPE=SW) are allocated on the same 3880-11/3350 or 3880-21/3350, a reverse modified moving cursor algorithm is used for the first area and a modified moving cursor algorithm is used for the second area.

Page/Swapset Allocation Summary

Syspag Type	Moving Cursor	"N" Select (Fixed #'s)
Type = PP, SW All devices	Yes	1 Swapset
Type = PP, PG 3880 Models 11, 21	Modified*	1 Page
3380	Yes	10 Pages
3330, 3350, 3375	Yes	8 Pages
3310, 3370	Yes	3 "Pages"
2305, 3340	Yes	3 Pages
Type = PM, PS, DU		
All devices	Modified	1 Page

- Devices supported for Swapping: 3330, 3350, 3375, 3380, 2305.

- Extended CKD devices are not supported for swapping for speed matching buffers.

Paging I/O

DMKPAGIO handles all I/O requests for virtual storage and spooling pages. It builds the necessary IOBLOKs and channel programs, expands compressed slot addresses, and maintains two I/O request queues of CPEXBLOKs (one dummy queue and one in-transit queue) for pages to be moved. When possible, DMKPAGIO chains requests together to move several pages with one SIO.

DMKPAGIO maintains two stacks of preformatted paging IOBLOKs. One stack contains preformatted IOBLOKs for extended CKD devices, and is anchored from DMKPAGEX. The other stack contains preformatted IOBLOKs for regular FBA/CKD devices and is anchored from DMKPAGSK. When I/O operations complete, their IOBLOKs are added to a list of available blocks. When DMKPAGIO needs new IOBLOKs, it takes them from the appropriate list. When either list is empty, DMKPAGIO calls DMKFREE as needed to get storage for a new block.

DMKPAG also maintains a stack of IOBLOK extensions anchored from DMKPAGXS. These are used for swap requests, and are pointed to by the IOBLOK for the swap request.

^{*} Reverse modified moving cursor is used if paging area (TYPE = PP or PG) and swapping area (TYPE = SW) are allocated on same 3880-11/3350 or 3880-21/3350.

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DMKPAGIO is entered by a GOTO from:

- DMKPTR, to read virtual storage pages or swap sets
- DMKSEL, to write virtual storage pages
- DMKSWA, to write swap sets
- DMKRPA, to read and write virtual storage spool buffers.

When DMKPAGIO is called for paging (not swapping), it receives the CORTABLE entry for the page to be moved, the address of a swap table entry for the slot, a read or write operation code, and the address of a CPEXBLOK that will be stacked for dispatching after the I/O for the page has completed. When DMKPAGIO is called for swapping, it receives a swap set block (SSBLOK) address instead of a CORTABLE entry.

DMKPAGIO indexes into the system OWNDLIST by using the device code that is part of the page address. It finds a device to which to direct the I/O request. Then it looks for a preformatted IOBLOK on the proper stack.

As DMKPAGIO creates IOBLOKS, it slot-sorts them and queues them to dummy RDEVBLOKs that were created at system initialization. The pointer to the dummy RDEVBLOKs is stored at DMKPAGRD. This enables DMKPAGIO to chain the IOBLOKs for several pages together, so they can be paged with one SIO. When the I/O request queues are empty, DMKPAGIO unstacks the IOBLOKS, unstacks the corresponding CPEXBLOKs from the dummy queue and adds them to the in-transit queue, and calls DMKIOS to start the I/O operation.

Note: Swap sets are already grouped together, and their requests are started immediately. I/O done on a 3880-11 is also started immediately.

DMKPAGIO also periodically calculates system paging overhead. After 200 pages have been moved (read or written), the elapsed time for the 200 page moves is computed, and the paging rate is calculated in page moves per second. The recent paging load, expressed as the percentage of time that more than one half of the system's pages were idle due to page-wait, is averaged with the previous load and re-projected as the expected load for the next interval.

Preferred System Paging

Preferred system paging is high speed paging support that is carried out by the 3880 storage control system. The 3880 Model 11 storage control system consists of two levels of storage (an electronic storage array and variable numbers of 3350 DASDs), and two paging storage directors, only one of which can be used to access the storage array.

The 3880 Model 21 storage control system also consists of two levels of storage, (a cache and variable numbers of 3350 DASDs), and two paging storage directors, both of which can be used to access the cache.

Page I/O Request Queuing Algorithm

The ordering of page I/O requests that are chained together for initiation with one SIO is done on a priority ordering basis. The priority is:

- 1. In-queue requests
- 2. Not-in-queue requests
- 3. Reads
- 4. Writes
- 5. Q1 requests
- 6. Q2 requests.

Page Migration and Swap Table Migration

Both page and swap table migration are invoked through the performance monitoring routine (DMKSTP) or, for page migration only, via the migrate command. It has the responsibility of determining whether conditions warrant invoking page migration and/or swap table migration.

Page Migration

The purpose of page migration is to promote efficient usage of the preferred devices in a storage hierarchy. The specific goal is to keep the primary paging (PP) area available for truly active pages, and also the general paging (PG) area if necessary. Swap sets in the swapping (SW) area (Paging Storage only) are also migrated. The page migration routine (DMKPGM) is invoked via a CP request block by DMKSTP. It operates according to Figure 30 on page 161.

Note: Swap sets in Paging Storage are migrated to the next TYPE = SWlevel, if one exists. Otherwise, they are broken apart and each page is migrated separately to the first available non-Paging Storage level of TYPE = PP.

DMKSTP invokes page migration if the percentage of occupied storage on the PP, PG or SW level reaches the value set by the SET SRM PGFULL command. (The default value is 99 percent.)

To migrate pages, DMKPGM scans each virtual machine's segment and page tables. If a page is selected for migration, DMKPGM checks the level on which the page is located. If the page is on a PP or PG level, DMKPGM checks to see if enough pages of that type have been migrated. If so, the page is not migrated. Otherwise, the address of the first SYSPLIST of the next level type is saved for use when calling DMKPGTPM for a DASD slot for migration.

You can monitor page migration performance with the QUERY SRM PGMACT command. For more information, see VM/SP HPO CP for System Programming.

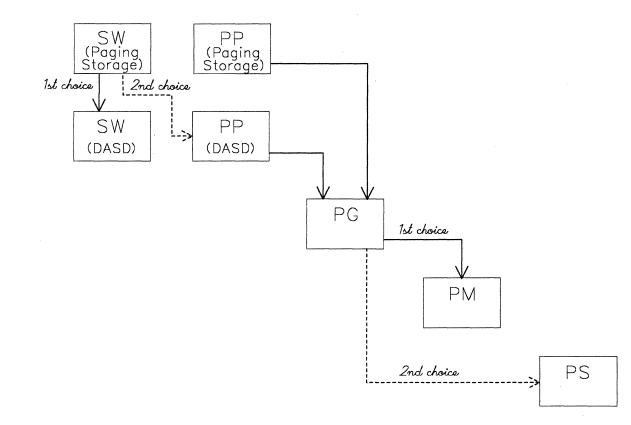


Figure 30. Page Migration Hierarchy

Swap Table Migration

DMKSTP invokes swap table migration if the following condition is met: the space occupied by page tables and swap tables is equivalent to at least 12% of nonextended free storage.

DMKSTR scans the VMBLOK chain for virtual machines with swap tables that are eligible for migration. DMKSTR migrates the swap table, setting bits to mark where the migratable pages in the segment reside on DASD (TYPE = PP or PG). This information is used by DMKPGM to determine if a swap table should be restored, in order to migrate more of the pages in that segment.

The swap table is brought back into storage whenever a reference is made to that segment. The page translation routine (DMKPTR) always calls the swap table migration routine (DMKSTR) whenever a reference is made to a segment table entry with a zero page table pointer. If appropriate, DMKSTR calls DMKBLD to have a page table and a swap table built for that segment. If the swap table has been migrated, the 4K buffer page is brought into storage and the swap table information is copied into the swap table.

Virtual Storage Paging Error Recovery

Errors encountered during virtual storage (as opposed to spooling) paging operations can generally be classified as either soft or hard errors. Soft errors allow the system to continue operation without delay or degradation. Hard errors can cause noticeable effects such as the abnormal termination of user tasks (abend) and response degradation. Errors that are successfully retried or corrected are known only to the I/O supervisor and the I/O error retry and recording routines—they appear to the second level interruption handlers (such as WAITPAGE) as if the original operation completed normally.

Soft Error Recovery: An I/O error that occurs on a page swap-out is considered to be a soft error. DMKPTRAN calls DMKPGTPG to assign a different DASD page slot and the page is re-queued for output. The slot that caused the error is not de-allocated, and thus is not assigned to another virtual machine. All other uncorrectable paging errors are hard because they more drastically affect system performance.

Hard Error Recovery: Hard paging errors occur on either I/O errors for page reads or upon exhausting the system's spooling and paging space. Recovery attempted on hard errors depends upon the nature of the task for which the read was being done. If the operation was an attempt to place a page of a virtual machine's virtual storage into real storage, the operation of that particular virtual machine is terminated by setting the page frame in error to zero and placing the virtual machine in console function mode. The user and operator are informed of the condition, and the page frame causing the error is not de-allocated, thereby ensuring that it is not allocated to another user.

The control program functions that call DMKPTRAN (such as spooling, pageable control program calls, and system directory management) have the option of requesting that unrecoverable errors be returned to the caller. In this case, the CP task may attempt some recovery to keep the entire system from terminating (abend). In general, every attempt is made to at least allow the operator to bring the system to orderly shutdown if continued operation is impossible.

Proper installation planning should make the occurrence of a space exhaustion error an exception. An unusually heavy user load and a backed-up spooling file could cause this to happen. The operator is warned when 90% of the temporary (paging/spooling) space in the system is exhausted. He should take immediate steps to alleviate the shortage. Possible remedies that exist include preventing more users from logging on and requesting users to stop output spooling operations. More drastic measures might include the purging of low-priority spool files. If the system's paging space is completely exhausted, the operation of virtual machines progressively slows as more and more users have paging requests that cannot be satisfied and operator intervention is required.

Virtual Relocation

CP provides the virtual machine the capability of using the DAT feature of the real System/370. Programming simulation and hardware features are combined to allow usage of all of the available features in the real hardware, (that is, 2K or 4K pages, 64K or 1M segments).

Note: When greater than 16 megabytes of storage are configured and online, any virtual machine running with DAT turned on must use 4K page size. Virtual machines that have 2K DAT tables do not have enough bits to store the page frame address in the page frame table entry for pages resident above the 16 Mb line—this is because of the S/370 architecture for extended addressing. A virtual machine that attempts to use DAT with 2K page size will receive a program check.

For clarification, some term definitions follow:

First-level storage: The physical storage of the real CPU, in which CP resides.

Second-level storage: The virtual storage available to any virtual machine, maintained by CP.

Third-level storage: The virtual storage space defined by the system operating in second-level storage, under control of page and segment tables which reside in second-level storage.

Page and segment tables: Logical mapping between first-level and second-level storage.

Virtual page and segment tables: Logical mapping between second-level and third-level storage.

Shadow page and segment tables: Logical mapping between first-level storage and third-level storage.

A standard, nonrelocating virtual machine in CP is provided with a single control register, control register zero, that can be used for:

- Extended masking of external interruptions
- Special interruption traps for SSM
- Enabling of virtual block multiplexing.

A virtual machine that is allowed to use the extended control feature of System/370 is provided with a full complement of 16 control registers, allowing virtual monitor calls, PER, extended channel masking, and dynamic address translation.

An extension to the normal virtual-machine VMBLOK is built at the time that an extended control virtual machine logs onto CP. This ECBLOK contains the 16 virtual control registers, 2 shadow control registers, and several words of information for maintenance of the shadow tables, virtual CPU timer, virtual TOD clock comparator, and virtual PER event data. The majority of the processing for virtual address translation is performed by the module DMKVAT, with additional routines in DMKPRG, DMKPRV, DMKDSP, DMKCDB, DMKLOG, DMKUSO, and DMKPTR. The simulation of the relocation-control instructions (that is, LCTL, STCTL, PTLB, RRB, and LRA) is performed by DMKPRV or DMKFPS. These instructions, with the exception of LCTL and STCTL, are not available to virtual machines which are not allowed the extended control mode.

When an extended-control virtual machine is first active, it has only the real page and segment tables provided for it by CP and operates entirely in second-level storage. DMKPRV examines each PSW loaded via LPSW to determine when the virtual machine enters or leaves extended control or translate mode, setting the appropriate flag bits in the VMBLOK. Flag bits are also set whenever the virtual machine modifies control registers 0 or 1, the registers that control the dynamic address translation feature. DMKDSP also examines PSWs that are loaded as the result of interruptions to determine any changes in the virtual machine's operating mode. The virtual machine can load or store any of the control registers, enter or leave extended control mode, take interruptions, etc., without invoking the address translation feature.

If the virtual machine, already in extended control mode, turns on the translate bit in the EC mode PSW, then the DMKVATMD routine is called to examine the virtual control registers and build the required shadow tables. (Shadow tables are required because the real DAT hardware is capable of only a first-level storage mapping.) DMKVATMD examines virtual control registers 0 and 1 to determine if they contain valid information for use in constructing the shadow tables. Control register zero specifies the size of the page and segment the virtual machine is using in the virtual page and segment tables. The shadow tables constructed by DMKVATMD are always in the same format as the virtual tables.

The shadow segment table is constructed in first-level storage and initialized to indicate that all segments are unavailable. Flags are maintained in the VMBLOK to indicate that the shadow tables exist. DMKVATMD also constructs the shadow control registers 0 and 1. Shadow control register 0 contains the external interruption mask bits used by CP, mixed with the hardware controls and enabling bits from virtual control register 0. Shadow control register 1 contains the segment table origin address of the shadow segment table.

When the virtual machine is operating in virtual translate mode, CP loads the shadow control registers into the real control registers and dispatches the user. The immediate result of attempting to execute an instruction is a segment exception, intercepted by DMKPRG and passed to DMKVATSX. DMKVATSX examines the virtual segment table in second-level storage.

If the virtual segment is not available, the segment exception interruption is reflected to the virtual machine. If the virtual segment is marked available, then DMKVATSX:

- Allocates one full segment of shadow page table, in the format specified by virtual control register 0.
- Sets all of the page table entries to indicate page not in storage.
- Marks the segment available in the shadow segment table.
- Redispatches the virtual machine via DMKDSP.

Once again, the immediate result is an interruption, which is a paging exception and control is passed to DMKVATPX. DMKVATPX references the virtual page table in second-level storage to determine if the virtual page is available. If the virtual page is not available, the paging interruption is reflected to the virtual machine. However, if the virtual page is marked in storage, the virtual page table entry determines which page of second-level storage is being referenced by the third-level storage address provided. DMKVATPX next determines if that page of second-level storage is resident in first-level storage at that time. If so, the appropriate entry in the shadow page table is filled in and marked in storage. If not, the required page is brought into first-level storage via DMKPTRAN and the shadow page table filled in as above.

As the virtual machine continues execution, more shadow tables are filled in or allocated as the third-level storage locations are referenced. Whenever a new segment is referenced, another segment of shadow page tables is allocated. Whenever a new page is referenced, the appropriate shadow page table entry is validated, etc. No changes are made in the shadow tables if the virtual machine leaves translate mode (usually via an interruption), unless it also leaves extended control mode. Dropping out of EC mode is the signal for CP to release all of the shadow page and segment tables and the copy of the virtual segment table.

There are some situations that require invalidating the shadow tables constructed by CP or even releasing and reallocating them. Whenever DMKPTR pages out a page that belongs to a virtual relocating machine, DMKVATSI is called to selectively invalidate the shadow page tables. If the stolen page is below the high-water mark, the shadow page table entry for the stolen page is invalidated. If the stolen page is above the high-water mark and virtual machine assist is on, a bit is set in the VMBLOK to indicate that all of the shadow page tables above the high-water mark must be invalidated. The actual invalidation is handled by DMKVATAB, called from DMKDSP when the virtual machine is about to be dispatched. If the stolen page is above the high-water mark and virtual machine assist is off, the shadow page tables are scanned to selectively invalidate shadow page table entries that map to the real page being stolen.

The other situations which cause shadow table invalidation arise from the simulation of privileged instructions in DMKPRV or DMKFPS. Flags are set in the VMBLOK whenever the virtual machine loads either control register 0 or 1, and DMKPRV calls DMKVATAB to perform whatever maintenance is required. When control register 1 is loaded by the virtual

machine, DMKVATAB scans the chain of STOBLOKs to see if shadow tables are already allocated for the value in virtual control register 1. If a matching STOBLOK is found, it is requeued as the first in the STOBLOK chain and the virtual machine can be redispatched. If a matching STOBLOK is not found, and the number of STOBLOKs is equal to the maximum STOBLOK count, the last STOBLOK in the chain is reused by invalidating the entire shadow table and then queuing it first on the STOBLOK chain. If the number of STOBLOKs is less than the maximum STOBLOK count, a new STOBLOK is acquired and initialized, and placed first in the STOBLOK chain. When control register 0 is loaded, DMKVATAB examines the relocation-architecture control bits to determine if they have changed, (such that the format of the virtual page and segment tables no longer matches that of the shadow tables). If the format has not changed, the shadow tables are left intact-otherwise, all of the shadow tables must be returned to free storage and another set, in the new format, must be allocated and initialized. The same actions can result from modifying the control registers via the CP console functions, in which case DMKVATAB is called from DMKCDB. The privileged operation, PTLB, causes the shadow page tables to be invalidated above the high-water mark because the shadow tables are the logical equivalent of the translation look-aside buffer.

The privileged instruction LRA is simulated via DMKVATLA, which searches the virtual page and segment tables to translate a third-level storage address to a second-level storage address, returning a condition code indicator to DMKPRV, or forcing an interruption if the tables are incorrectly formatted.

Most error situations that occur in the virtual machine are handled by means of the extended program interruptions associated with the real address translation hardware. Whenever a virtual relocating machine loads control registers 0 or 1 with an invalid value, DMKVAT releases all of the shadow tables exactly as if the hardware controls had changed. The shadow control registers are set valid, with the shadow segment table re-allocated at a minimum size and all segments marked unavailable. Flag bits are set in the VMBLOK to indicate that the shadow tables are artificially valid, and DMKVATSX reflects a translation specification exception to the virtual machine as soon as it is dispatched. While it is possible for the virtual machine to enter an interruption loop (if the new PSW is also a translate mode PSW), the cited process prevents the occurrence of a disabled loop within CP, which would result if the virtual machine is never dispatched.

Extended Storage Key Support

DMKPRV checks the status of the reference and change bits in the virtual storage keys, which involve the privileged instructions ISK, ISKE, RRB, RRBE, SSK, and SSKE.

For SSK, CP increases simulation counter DMKPRVEK, sets general purpose register 0 to X'0C' and branches to subroutine CKCR0B7 to simulate the instruction.

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For ISK, CP increases the simulation counter DMKPRVIK and sets to X'04' general purpose register 0. DMKPRV uses BAL to invoke subroutine CKCR0B7.

For RRB, CP increases simulation counter DMKPRVRR and sets to X'08' general purpose register 0. DMKPRV uses BAL to invoke subroutine CKCR0B7.

Subroutine CKCR0B7 examines the XKEYMODE field in the PSA to determine the type of real storage frames that are installed on the processor. If XKEYMODE indicates that the storage for this virtual machine consists entirely of frames that are protected by single storage protection keys, CP does the following:

- Examines bit 7 of control register 0 (VMVCR0 if in BC mode, or EXTCR0 if in EC mode)
- If control register 0 bit 7 is B'1', control passes to DMKPRWXK to simulate the proper extended key operation for the virtual machine
- If control register 0 bit 7 is B'0', CP returns a special operation exception to the virtual machine.

If XKEYMODE indicates that the storage for this virtual machine consists entirely of frames that are protected by two storage protection keys, control returns to DMKPRV to simulate the key operation.

The extended key instructions ISKE, SSKE, and RRBE are used for manipulating the storage keys of single key real storage frames. CP simulates the extended key instructions only when they are installed on the processor. To use the extended key instructions, the virtual machine must be operating in extended control mode or an operation exception results.

Module DMKPRV validates the ISKE, SSKE, and RRBE instructions and increases the simulation counters DMKPRVXI, DMKPRVXR, and DMKPRVXS respectively, to keep track of the number of times that the instructions are simulated. Control passes to DMKPRW at entry point DMKPRWXK for simulation of the instruction.

Before simulating an extended key operation, CP examines the XKEYMODE field in the PSA. XKEYMODE contains the value B'1' if one or more single key real storage frames were found during system initialization. CP simulates extended key operations for guest virtual machines only when the XKEYMODE field is set to B'1'.

If the processor has single-key storage frames and virtual machine assist can process the extended key instructions, it does so, provided the assist is there and enabled. If the assist is not there, disabled, or cannot handle the instruction, it is passed to CP for simulation.

Virtual machine assist processes extended key instructions for processors that do not have single key real storage frames. That is, if the extended key instructions are installed on the processor, and if virtual machine assist can process the instruction, the presence of single key real storage frames is irrelevant. If single processor mode is active, virtual machine assist will not simulate ISK, ISKE, SSK, or SSKE instructions.

Free Storage Management

DMKFRE and DMKFRT are responsible for the management of free storage. CP uses them to obtain free storage for I/O tasks, CCW strings, I/O buffers, and almost all other related applications. However, CP does not use them for real channel control blocks, real control unit blocks, real device blocks, or the CORTABLE.

The way storage is allocated depends on the amount of storage requested. Block sizes of 1024 doublewords or fewer are grouped into 92 subpool sizes. The subpool sizes range from 2 doublewords to 1024 doublewords.

Requests for 128 doublewords or fewer are rounded to the next highest boundary that is a multiple of two (2,4,6,...128). For example, DMKFREE would try to honor a request for seven doublewords by searching for a subpool size of eight doublewords. Requests for greater than 128 doublewords are rounded to the next highest boundary that is a multiple of 32 (160,192,224,...) to a maximum subpool size of 1024 doublewords. Block sizes of 1024 doublewords or fewer are handled by LIFO (push-down stack) logic. Block sizes of greater than 1024 doublewords are strung off a chained list.

When subpools are exhausted, small blocks are generally obtained from the first larger block at the end of available free storage. Large blocks, on the other hand, are obtained from the high-numbered end of the last larger block. This procedure tends to keep the volatile small subpool blocks separated from the large blocks, some of which stay in storage for much longer periods of time. Thus, undue fragmenting of available storage is avoided.

DMKFRE initially starts without any subpool blocks. They are obtained from DMKFREE and returned to DMKFRET on a demand basis.

The various cases of calls to DMKFREE for obtaining free storage, or to DMKFRET for returning it, for subpool sizes and large sizes, are handled as follows.

Calling DMKFREE for a Subpool

The subpool algorithm maintains two entire sets of subpools. Each processor maintains its own subpool table when it requires subpool-sized blocks.

Subpool Available: When there is a call to DMKFREE for a subpool, the appropriate subpool for the requested size is checked to see if there is at least one suitable block available. If one is available, the first one found is detached from the chain, the chain is pointed to the next subpool block of the same size (if any), and the detached block is returned to the caller.

Subpool Not Available: In an AP or MP configured system, if "NUMSTEAL + MINLEAVE" (2+3) blocks exist on the other processor's subpool of the the desired size, then NUMSTEAL (2) blocks will be stolen. One is added to this processor's subpool and the other is used to satisfy the caller's request. If no block of suitable size is available when a call to DMKFREE is made, the way the request is then handled depends on the size of the request. Requests for more than 28 doublewords are handled in the same manner as requests for greater than 1024 doublewords (see "Calling DMKFREE for a Large Block").

If the request is for 28 doublewords or less, the chained list of free storage is searched for a block of equal or larger size. The first block of equal or larger storage outside the dynamic paging area is used to satisfy the call, with a block of equal size taking priority. If an equal block is found, it is detached from the chain and returned to the caller. If a larger block must be used, the low-numbered end is split off and returned to the caller.

If there is still no block large enough to satisfy the request, then DMKPTRFR is called to obtain another page from the dynamic paging area. This page is merged into the chain of free storage, and the above process is repeated until DMKFREE obtains the needed block.

Calling DMKFREE for a Large Block

If there is a call to DMKFREE for a block larger than 1024 doublewords, or if no subpool is available for a request of greater than 28 doublewords, the chained list of free storage is searched for a block of equal or larger size outside the dynamic paging area, with a block of equal size taking priority. If such an equal size block is found, it is detached from the chain and given to the caller. If at least one such larger block is found, the desired block size is split off the high-numbered end of the last larger block found and given to the caller.

If a block of suitable size is not found outside the dynamic paging area, a block within the dynamic paging area may be used to satisfy the request. If no such block exists, DMKPTRFR is called to obtain another page of storage from the dynamic paging area. This page is merged into the chain of free storage. The above process is repeated (as necessary) until DMKFREE obtains the needed block.

Calling DMKFREE for a Prime Storage Block

A special call can be made to DMKFREEP for a 16 doubleword prime storage block (equivalent in size to one cache-line). There exists one prime list for each processor in the AP or MP configured system. Typically, these blocks will be used frequently for short periods of time (for example, CPEXBLOKs, IOBLOKs, SVC SAVEAREAS).

Calling DMKFREE for an Align Pool Block

A special call can be made to DMKFREEA for a block size of 16, 32, 48, 64, 80, 96, 112, or 128 doublewords (cache-aligned). There exists only one align pool for use by both processors in the AP or MP configured system. These blocks are used for long periods of time and are referenced frequently (for example, VMBLOKs).

Calling DMKFRET for a Subpool

DMKFRET processes the CP Assist Fret instruction (E601) to return free storage. If the microcode cannot return the block to an appropriate subpool, control is passed to DMKFRTT to handle the request.

If a subpool sized block is given back via a call to DMKFRET, the block is attached to the appropriate subpool chain on a LIFO (push-down stack) basis, and return is made to the caller. If the block was in a page within the dynamic paging area, it is also placed in a subpool chain.

(Subpool storage is returned, however, via a call to DMKFRTRS, to the regular free storage chain once every hour or when a user logs off. DMKFRTRS then calls DMKFRTSN to search the free storage chain for page frame sized blocks needing to be returned to the dynamic paging area.)

Calling DMKFRET for a Large Block

If a block larger than 1024 doublewords is returned via DMKFRET, control is passed to DMKFRTT where the block is merged appropriately into the regular free storage chain. Then a check is made to see if the area given back (after all merging has been done) is a page frame within the dynamic paging area. If so, DMKPTTFT is called to return the block to the dynamic paging area for subsequent use. If the block is returned by specific modules known to use large blocks frequently for very short periods of time, DMKPTTFT is purposely not called, to avoid continuous extending and unextending of free storage over very short time intervals.

Calling DMKFRET for a Prime Storage Block

When a prime storage block is returned via DMKFRET, it is attached to the appropriate processor's prime list on a LIFO (push-down stack) basis.

Calling DMKFRET for an Align Pool Block

When an align pool block is returned via DMKFRET, it is inserted in the appropriate list in the align pool (32 doubleword blocks are attached to the 32 doubleword list, etc.). Blocks are chained together in ascending order according to starting address.

Align Pool Storage Collector

DMKFRTRA is the align pool storage collector. It tries to return pages to the dynamic paging area when they are no longer needed in an aligned storage pool. DMKFRTRA looks for such pages whenever a user logs off and every hour after system IPL. It is invoked as part of a call to DMKFRTRS, the regular subpool collector.

DMKFRTRA scans the subpools looking for contiguous cache-aligned blocks. When it finds enough such blocks to make up one or more complete pages, it unchains these blocks from the appropriate list. It then calls DMKPTTFT to return the page or pages to the dynamic paging area.

Free Storage Page Frame Allocation

The number of page frames allocated to free storage depends upon:

- 1. The real machine storage size
- 2. The RMSIZE operand specified in the SYSCOR macro at system generation time
- 3. The PRIME operand in the SYSCOR macro
- 4. The FREE operand in the SYSCOR macro
- 5. The number of unusable or inaccessible storage frames detected during CP initialization (308x processors only).

The storage size used by VM/SP HPO is the smaller of the real machine storage size and the RMSIZE value.

If the FREE operand was not included in the SYSCOR macro statement for DMKSYS, the default amount of free storage DMKSTA allocates at IPL time is three page frames for the first 256K of real storage (not including a V=R area, if any), and one page frame for each additional 64K below the 16 Mb line. If more than 16 Mb of storage is online, then one additional page frame will be allocated for each 256K above the 16 Mb line.

If the PRIME operand was not included in the SYSCOR macro statement for DMKSYS, the default amount of prime storage that DMKSTA allocates at IPL time is 10% of free storage.

DMKSTA will allocate an additional 25% of free storage in AP and MP environments.

If the FREE operand was included in the SYSCOR macro statement for DMKSYS, that value is the number of fixed free storage page frames allocated at IPL time.

When the values for free storage and trace area storage are too large, they are decreased proportionally to guarantee that a predetermined number of DPA pages exist. Prime storage is also adjusted by this percentage.

CP FRET Trap

The CP FRET Trap detects the release of areas of free storage that were not assigned, previously released, or outside the boundaries of the storage given. Based on the value of the option &FRETRAP, the trap code is conditionally assembled in modules DMKCPI, DMKFRE, DMKFRT, and DMKSVC. &FRETRAP can be found in OPTIONS COPY and has a default value of 0 for normal operations without the trap. The trap may be installed at system generation time. Refer to the VM/SP HPO Installation Guide for the installation instructions.

When the trap is installed, DMKCPI disables CP Assists FREEP, FREE, FRET, DSP1, DSP2, DSP3, DSP4, UNTFR, and Diagnose '18' during system initialization. DMKFRE adds two doublewords to each free storage request, creating a trap extension area. The extension area contains:

- The status of the request. The status consists of the tag ALLO when the storage is allocated by DMKFRE or the tag FRET when the storage is released by DMKFRT.
- The saved size, in doublewords, of the requested free storage area.
- The starting address of the assigned free storage block.
- The return address of the module requesting the storage.
- The last three bytes of the calling module's name, if it is pageable.

For the format of the extension area, refer to the FREEXT control block in the VM/SP HPO Data Areas and Control Block Logic Volume 1 (CP).

DMKFRT checks each request to release free storage for an ALLO tag. It checks at the address calculated by adding the size in bytes of the storage block being released to the address of the block. If the ALLO tag is found, the size of the free storage block being released is checked against the saved size in the extension area. If the sizes are equal, the ALLO tag is changed to FRET. The requested free storage block, including the extension area, is then released.

For prime storage requests, the extension area contains:

- The last three bytes of the calling module's name (if it is pageable)
- The low order byte of the size request
- The return address of module requesting the storage
- The saved size (in doublewords and bytes).

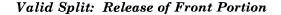
Trap Error Detection

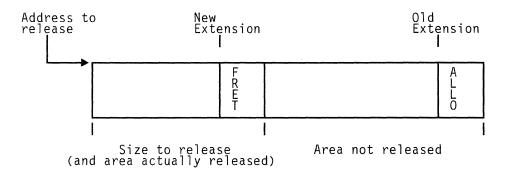
When storage is released, for systems generated in uniprocessor mode (or in AP/MP mode and the second processor is not operating), the trap code in DMKFRT detects three types of errors. If the extension area cannot be located, the system abends with code FRT013. If the tag in the extension area is FRET instead of ALLO, indicating that the storage has already been released, the system abends with code FRT016. If the size of the free storage block being released does not match the saved size in the extension area, the system abends with code FRT015.

With the prime storage extension, two other types of errors may be detected. If the prime block is not marked prime, the system abends with either code FRT20 or SVC06. If the prime block is not on a cache boundary, the system abends with code FRT21.

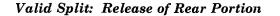
AP/MP Differences

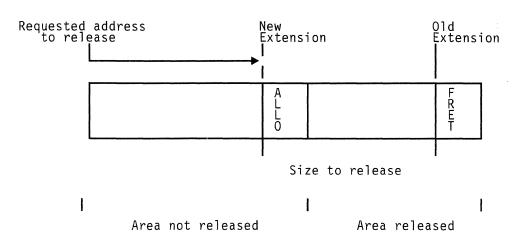
For systems generated in AP/MP mode, and the second processor is operating, the area released can be a split of the storage area given. Either the front or the rear portion of the free storage area may be released. Storage splits are allowed due to the way CP handles page and swap tables for shared systems in the AP/MP environment. With one call to DMKFREE, two sets of contiguous page and swap tables are obtained (one for each processor). Under some conditions, the page and swap tables for only one processor are released. The trap code allows only "valid" splits. A split is considered "valid" if both parts of the split free storage area are each the size of a contiguous page and swap table, and the saved size in the extension area is the size of two page and swap tables.





When the front portion of the free storage block is being released, the extension area is updated: the saved size is adjusted to the size of a page and swap table, and the new address of the area not being released is stored. The information in the extension area is copied to create a new extension area. The new extension area is constructed within the front portion being released, using the last two doublewords. The new extension area is updated with the address of the front portion and the FRET tag. The front portion of the storage area is returned to the DMKFRELS chain.





When the rear portion of the free storage block is being released, the extension area is copied to create a new extension. The new extension area is constructed at the end of the front portion that was not requested to be released. The saved size, in both the old and the new extension areas, is adjusted to the size of a page and swap table. The old extension is updated with the new address of the rear portion actually released, information about the module releasing the storage, and the FRET tag. The new address to release to the size in bytes of the new extension area. At this new address, the rear portion of the free storage block is returned to the DMKFRELS chain (including the old extension).

Trap Error Detection in AP/MP Mode

For systems generated in AP/MP mode when the second processor is operating, the trap code takes into account the possibility of storage splits while checking for errors. The trap code checks for the ALLO tag at the address calculated by adding the address of the free storage block to the size in bytes of the returned free storage area. If at this address a FRET tag is found, the system abends with code FRT016. If neither the ALLO or FRET tag is found, the trap code checks for an ALLO tag at that address plus the size in bytes of a page and swap table. The system abends with code FRT013 if the ALLO tag is still not found. If the ALLO tag is found at one of the two addresses checked, but the size of the free storage area to be released does not match the saved size in the extension area, then the trap checks for a "valid" split. Abend FRT015 occurs if the split is not "valid."

CP Initialization

System initialization starts when the operator selects the DASD address of the CP system residence volume (SYSRES) and IPL's the sytem. The System/370 hardware reads 24 bytes from the SYSRES (record 1 of cylinder 0) into location 0 of main storage. This record consists of an initial PSW and a channel program. The channel program reads the module DMKCKP from the SYSRES (record 2 of cylinder 0) into location X'1000' of main storage. (The load point for a V=R system is the V=R size plus X'2000', or

DMKSLC plus X'1000'.) The hardware then loads the initial PSW from location 0 of main storage. This PSW causes execution to begin at the entry point DMKCKPT.

A VM/SP HPO system cannot be initialized on any supported processor when an unusable or nonaddressable frame is found either at location 0 of main storage or from DMKSLC through the frame in main storage where DMKSAV ends. The system initialization process will fail with unpredictable results if DMKCKP or DMKSAV cannot be loaded at their expected real storage locations.

DMKCKP will load DMKSAV from the SYSRES and then pass control to DMKSAVRS. DMKSAVRS loads the CP nucleus from the nucleus area of the SYSRES into main storage starting at page 0 and ending with the pageable module immediately preceding DMKSAV in the CP load list. Next DMKSAV passes control to DMKCPINT.

DMKCPINT performs the main initialization function. This includes:

- Calling DMKSTANT to initialize main storage
- Calling DMKMNTIO to mount the I/O devices that were defined in DMKRIO and are available
- Calling DMKSEGCP to set up CP's segment page, core, and swap tables
- Building a LANGBLOK for the installation default language
- Calling DMKOPERC to locate the operator's console
- Calling DMKTODIN to initialize the time-of-day clock
- Calling DMKCPJNT to continue system initialization
- Calling DMKUDROV to read any command class override records stored on the primary override cylinders.

When a processor is running with more than 16 megabytes of online storage, DMKSTA allocates all page frames above the 16 Mb line as a dynamic paging area anchored by DMKPTRF2, and allocates a separate dynamic paging area below the 16 Mb line that is anchored by DMKPTRF1.

For each valid CP-owned DASD volume which is online at initialization, DMKALO builds a maxi-ALOCBLOK. (DMKCPI calls DMKMNT to mount and initialize CP-owned devices. DMKMNT then calls DMKALO to build the maxi-ALOCBLOKs.) Each maxi-ALOCBLOK contains the DMKFMT allocation information from the allocation record on the DASD volume, and describes all the space on that particular volume. DMKIDU then calls DMKVDG to finish creating an ALOCBLOK chaining structure for DASD allocation.

DMKCPI then calls DMKPSTIN to initialize Paging Storage. The READ SCP INFORMATION Service Call is issued to obtain the largest valid Paging Storage increment number, as well as the number of Paging Storage pages in each increment. For each Paging Storage area specified on the SYSPAG macros, RECBLOKs and an ALOCBLOK are then permanently allocated from free storage to map the pages in the specified increments.

The installation may issue the QUERY PSTOR command to display the amount of Paging Storage available for swapping and primary paging, and the percentages of these amounts that are not usable. See the VM/SP HPO Operator's Guide for more information on the QUERY PSTOR command.

After DMKCPI calls DMKPST to initialize use of Paging Storage, DMKPST calls DMKVFI to initialize use of the Vector Facility.

You can use the QUERY VECTOR command to obtain information about the availability and current use of the Vector Facility. See the VM/SP HPO Operator's Guide or VM/SP HPO CP for System Programming for more information on this command.

When CP initialization occurs on a processor that has more than 16 megabytes of online storage, DMKSEG calls DMKPTSAL. DMKPTSAL allocates two system virtual address slots that CP later uses for page interchanges necessary for CP to accommodate real storage above the 16 Mb line.

Some models of the 308x, 3090, and 3033 processors are equipped with real storage frames that are protected by one storage protection key per frame, rather than two storage protection keys. At system initialization, CP assumes that all storage frames are protected by two storage keys. CP initializes the storage protection keys by issuing SSK instructions.

If during real storage initialization a special operation exception occurs, DMKSTA initializes to one the field XKEYMODE in the PSA and initializes to B'1' bit 7 of control register 0. CP initializes these areas to indicate the presence of one or more single key storage frames. CP then examines the level of the virtual machine assist feature to determine whether virtual machine assist is capable of simulating the extended key instructions (ISKE, RRBE, and SSKE). The correct level of virtual machine assist must be present on both processors of an AP or MP configured system. If virtual machine assist can process the extended key instructions, DMKSTA initializes to B'1' XKEYASST in the PSA. Control is returned to DMKCPI.

DMKCPJNT calls DMKWRMST to perform a WARM, CKPT, FORCE or COLD start, DMKIDUMP to locate DASD space for system dumps, and DMKOPELO to LOGON the system operator. When control is returned from DMKCPJ, DMKCPI goes to the dispatcher, DMKDSPCH, to begin dispatching virtual machines.

See Figure 4 on page 26 for a more detailed description of CP initialization.

Warm Start

Following an orderly system shutdown where warm start data has been saved by the checkpoint programs (DMKCKP, DMKCKD, DMKCKF, DMKCKH, DMKCKM, DMKERP, DMKCKW, and DMKCKN) it is possible to start the system with a warm start. DMKWRMST is called by DMKCPJ during system initialization to perform a warm start. DMKWRMST reconstructs the system log message, the saved accounting ACNTBLOKs, the saved printer, punch, open reader, and delete SPOOL file blocks (SFBLOKs), and the Reader Hash Table (plus extension pages), the saved ALOCBLOK and RECBLOK data, and the saved SPOOL hold queue blocks (SHQBLOKs). In addition, if this is an automatic system re-IPL following a dump to DASD, DMKWRMST reenables the terminals that were enabled when the system abended. Control passes back to DMKCPJ (which calls

DMKIDU to reinitialize the paging function). Control then goes to DMKWRMSY to reconstruct the closed reader SFBLOKs (in SYSSPOOL's virtual storage) and the SPUBLOKs (in SYSSPOOL's virtual storage). DMKWRM then calls DMKWRNWM to initialize the checkpoint area cylinders and also to checkpoint any SHQBLOKs or open SFBLOKs. Finally DMKWRMST invalidates the first record of the warm start area so that another warm start cannot occur until the system undergoes an orderly shutdown.

Checkpoint Start

If the system is unable to perform a warm start because of I/O errors or because no warm start data was saved from the last session, then a checkpoint (CKPT) start can be attempted. This option attempts to initialize the system using the information that has been dynamically checkpointed during system operation and stored on the checkpoint area. DMKWRMST is called by DMKCPJ for a CKPT start. DMKWRM calls DMKCKVWM to handle the CKPT start. DMKCKVWM restores the checkpointed real device information for system printers, punches, and readers, and reconstructs the SPOOL hold queue blocks (SHQBLOKs). DMKCKVWM also reconstructs the allocation maps for the SPOOL file blocks (SFBLOKs) which were dynamically checkpointed during system operation. This is done via a multitasking approach, in which a task is built to allocate each spool file. DMKWRMST then invalidates the first record of the warm start area so that a warm start cannot occur until the system undergoes an orderly shutdown. DMKCKRSY rebuilds SYSSPOOL's virtual storage and restores all the spool file chains (printer, punch, and reader).

The system log message is not reconstructed as for a WARM start. Also, since ALOCBLOK and RECBLOK information is not saved on the checkpoint area, the pages used by the reconstructed SPOOL files must be allocated by following the SPOOL file links (SPLINKs) for each reconstructed SPOOL file. Because of this, a checkpoint start takes longer than a warm start. For a CKPT start, DMKCKVWM loads a disabled wait state PSW (code 00E) if an invalid checkpoint record is encountered.

Force Start

If the system is unable to perform a checkpoint start because of I/O errors or invalid data on the checkpoint area, then a FORCE start can be attempted. A FORCE start works in the same way as a CKPT start, except DMKCKVWM truncates or erases any data that cannot be read.

Cold Start Overview

A COLD start is usually performed only on the initial loading operation of a new version of the VM/SP HPO system or when a hardware error prevented valid system checkpointing and shutdown. DMKWRMST is called by DMKCPJ during system initialization to perform a COLD start. No data is recovered from the checkpoint or warm start area. DMKWRMST calls DMKCKSIN to initialize the checkpoint area. Next DMKWRMST invalidates the first record of the warm start area so that a warm start cannot occur until the system undergoes an orderly shutdown.

Shutdown Start

A SHUTDOWN start is performed in order to halt the initialization process. For example, if vital CP owned volumes are not mounted, then it might be desirable to mount those devices and then re-IPL without first doing a WARM, CHECKPOINT, FORCE or COLD start. For a SHUTDOWN start no processing of system warm start or checkpoint data is done. A disabled wait state PSW (code 006) is loaded.

System Shutdown

When the operator or other authorized user issues the SHUTDOWN command without the REIPL option, DMKCPSSH moves "CPCP" into the CPID field of the PSA. "CPCP" tells the checkpoint program that a system shutdown is to be performed.

When the operator or other authorized user issues the SHUTDOWN command with the REIPL option, DMKCPSSH moves "WARM" into the CPID field of the PSA. "WARM" tells the checkpoint program that a system shutdown is to be performed and then an automatic warm start is to be performed.

DMKCPSSH then goes to DMKDMPRS. DMKDMPRS issues an IPL CCW to read the IPL sequence and, subsequently, DMKCKP from the SYSRES just as during system initialization. DMKDMPRS then loads the initial PSW from location 0 of main storage and DMKCKPT gets control.

DMKCKPT reads in the rest of the checkpoint programs (DMKCKD, DMKCKF, DMKCKH, DMKCKM, DMKERP, DMKCKW, and DMKCKN). DMKCKPT calls DMKCKDEV to drain pending I/O interrupts and issue an HIO to all available devices. DMKCKPT calls DMKCKFIL to save the addresses of enabled terminals, the status of the system operator, device and user accounting cards, the system log message, printer, punch, open reader, and delete SPOOL file blocks (SFBLOKs). DMKCKFIL saves this information on the warm start area for recovery by DMKWRM during a warm start. DMKCKWSP is called to save the closed reader SFBLOKs and SPUBLOKs (in SYSSPOOL's virtual storage) and DMKCKWHT is called to save the Reader Hash Table (plus extension pages), open CPTRAP and monitor SPOOL files, the allocation RECBLOKs, and the SPOOL hold queue blocks (SHQBLOKs). DMKCKP calls DMKCKMSV to save virtual machines which were enabled for VMSAVE. DMKCKMSV saves the virtual machines on the CP owned DASD as specified in the system name table, DMKSNT. If CPID = "CPCP" (i.e. SHUTDOWN was requested), then DMKCKPT loads a disabled wait state PSW (code 008) and the system shutdown is complete. If CPID = "WARM" (i.e. SHUTDOWN REIPL was requested), then DMKCKPT will load DMKSAV, set CPID to "WARM" and transfer control to DMKSAVRS in the same way that a normal system initialization is performed. DMKCPINT will get control from DMKSAV and perform the same functions as for normal system initialization, but since CPID is "WARM", the operator will not be requested to change the time of day clock, nor choose the type of start to perform. In fact, it is not necessary for an operator to be present during this automatic re-IPL. The system will automatically perform a WARM start (see Figure 5 on page 27).

Dumping the System and Automatic Re-IPL

When a system abend occurs or when the system restarts, the module DMKDMPDK dumps all of main storage or just the CP portion of main storage (plus SYSSPOOL's virtual pages that are resident at the time of the dump) to the indicated dump device. After the dump is completed, DMKDMPRS issues an IPL CCW to read the IPL sequence and, subsequently, DMKCKP from the SYSRES just as during system shutdown. DMKDMPRS then loads the initial PSW from location 0 of main storage and DMKCKPT gets control. If the dump was to a printer or to a tape, then DMKDMP leaves CPID set to "CPCP" and DMKCKPT will conduct an orderly system shutdown just as if the SHUTDOWN command had been issued. If the dump was to a DASD, then DMKDMP sets CPID to "WARM" before loading DMKCKP. DMKCKPT will still perform an orderly system shutdown and then an automatic warm start will be performed just as if the SHUTDOWN REIPL command had been issued.

Initialization and Termination

Attaching a Virtual Machine to the System

Note: The process described here ignores VCNA SNA terminal attachment support.

After CP has been initialized, DMKCPVEN enables the communication lines in response to the ENABLE command. Then an individual virtual machine is attached to the system, using the following steps:

1. Terminal Identification

When the CP receives the initial interrupt from a terminal on an enabled line (normally initiated by a user dialing in on a data-set), the DMKCNSIN routine is entered. DMKCNSIN determines the terminal device type, stores this information in the terminal device block, writes the online message and puts the terminal line in a state to receive an attention interruption.

2. Attention from User

After the online message has been displayed at the user's terminal, and he has pressed the ATTENTION key, DMKCNSIN (the console interruption routine) calls DMKBLDVM to build a skeleton VMBLOK for the user. At this time, the userid is LOGONxxx, where xxx is the terminal real device address, and a flag is set to indicate that the user has not yet completed the logon process.

Then DMKCNSIN calls DMKCFMBK, which types a single blank at the terminal, and issues a read to the terminal for the user to enter his first command (normally LOGON or DIAL).

3. First Command from User

After the first command has been entered by the user, DMKCNSIN further determines the type of terminal. If the terminal is a 2741, DMKTRMID is called to identify it as either a 2741P (PTTC/EBCD) or a 2741C (Correspondence) terminal. If successful, the correct device type and translate tables for input and output are set. If not, flags are set to indicate that the terminal is not yet identified.

Then control is returned to DMKCFMBK, which determines if the first command is valid (for example, LOGON, MSG, or DIAL). If the first command is not valid, a restart message is given, and the read to the terminal occurs again for the first command. If the first command was LOGON (or its abbreviation), DMKLOGON is called to complete the process of attaching the virtual machine to the system.

The operations performed by DMKLOGON include the following:

- Obtains the userid from the command line, and checks for a possible password and other optional operands.
- Checks the userid and password against entries in CP's directory of users.
- Ensures that the user is not logged on at another terminal (an error condition), or reconnects the user if he was running in disconnect mode.
- Obtains pertinent information on the user's virtual machine from the user machine block portion of the directory.
- Stores the correct userid (replacing the LOGONxxx userid used until now), virtual storage size, and other vital information in the virtual machine's VMBLOK.
- Allocates and initializes segment, page, and swap tables (necessary for handling of the virtual machine's virtual storage).
- Obtains free storage for the virtual machine's swap control block (SCBLOK).
- Schedules MSS volume mounts for any required MSS volumes if the MSS is available and the volume is not already mounted.
- Allocates an extended VMBLOK (ECBLOK) if the user's virtual machine has the ability to run in the extended control mode.
- Allocates and initializes virtual device blocks, control unit blocks, and channel blocks, using information from the user device blocks portion of the directory.
- Establishes links (as feasible) to all DASD included in the directory, the accessibility of any disk being determined by the user access mode in the directory, and whether any other users are presently linked to the disk, in read mode and/or write mode.
- Initializes all other virtual device blocks as appropriate, such as reader, punch, printer, and terminal.

- Maps all virtual devices to real devices.
- Performs appropriate accounting.
- Informs the user of the date and time of the most recent revision to the system log message (LOGMSG), and of the presence of any outstanding spooled files in his virtual reader, printer, or punch.
- Sends a ready message to the user with the date and time (and weekday), and a message to the system operator indicating that the user has logged on.

If the virtual machine has a device address or a named system in the directory and the initialization was not suppressed via an option on the LOGON command line, then that device or named system is then loaded (via IPL) at the conclusion of the logon process. Otherwise, when the logon functions are complete, the user's terminal is placed in CP read mode ready for the entry of his first desired command.

Under the latter condition of no automatic IPL, the user can IPL an alternate nucleus by using the STOP option in the IPL command. This option causes the normal IPL procedure to halt execution prior to loading the initial PSW, and issues a DIAGNOSE code 8 that places the user's terminal in CP read mode. A hexadecimal character entered in location X'08' changes the nucleus name. A hexadecimal character entered in location X'09' changes the apparent storage size. The BEGIN command allows the IPL procedure to continue.

System Reconfiguration

308x Processor Complex

The 308x Processor Complex uses the 3082 Processor Controller to coordinate central communications for the processor complex. The monitoring and service support facility (MSSF) is a hardware component of the processor controller. MSSF supplies I/O configuration and storage information for the 308x Processor Complex and executes commands that modify the real system configuration.

CP uses the MSSFCALL instruction to communicate with the MSSF. MSSFCALL is a diagnose instruction with a function code of X'80'. A command request to the MSSF requires a hardware call control block (HCBLOK). CP modules use the HCBLOK to issue an MSSFCALL instruction. The HCBLOK is a 48 byte storage area that is used to order requests to the MSSF and provide status of the requests to the caller.

After executing a command, MSSF uses the MSSF data block (MSFBLOK) to return information to the requester. MSFBLOK is a 2K byte storage area that MSSF uses to return header information and other command dependent information. The maximum length of the MSFBLOK is 2K. However, CP always obtains a page of storage to ensure that the control block is on a 2K storage boundary. The page of storage is locked to prevent page faults during processing of the request.

MSSF provides support for VARY PROCESSOR and SCPINFO commands. If the VLOG option of the VARY OFFLINE PROC command is used, the processor is logically switched offline—MSSF is not called for this process. For real MSSF processing, the SCPINFO command must originate from a V=R virtual machine.

3090 Processor Complex

The 3090 Processor Complex uses the Service Processor to obtain I/O configuration and storage information and to execute commands that modify the real system configuration. VM/SP HPO uses the Service Call together with the Service Call Control Block and the HCBLOK when invoking the Service Processor. The interface for MSSF support also processes Service Call requests, (they are mutually exclusive).

Real MSSF or Service Call Processing - VARY PROCESSOR

Module DMKCPU receives control to execute a VARY PROC command. If the VARY PROCESSOR command is issued on a 308x or 3090 processor (CPUID = 308x or 3090 in PSA), DMKCPU calls DMKPTR to obtain a page of storage for the HCBLOK and the MSFBLOK or SCCBLOK—the MSFBLOK or SCCBLOK uses the first 2K of storage while the HCBLOK uses the remaining 2K. DMKCPU sets the HCBLOK fields to contain the address of the MSFBLOK or SCCBLOK, the command, and the return address for MSSF or Service Call interruptions. DMKCPU passes control to DMKMHC (at entry point MDKMHCCP) to schedule the MSSF or Service Call request.

Note: When VM/SP HPO is running second level as a virtual MP guest on VM/XA, MSSF and Service calls are bypassed.

Real MSSF or Service Call Processing – SCPINFO and IOCP

Module DMKHVC receives control to validate the diagnose instruction produced by an IOCP or SCPINFO command. DMKHVC calls DMKMHV to process the diagnose instruction. DMKPRV validates the Service Call instruction (SERVC) and calls DMKMHV to simulate the privileged operation. DMKMHV examines the MSSF external interruption pending field (VMMSSFXP) in the virtual machine's VMBLOK to determine whether the virtual machine has an outstanding request. If an MSSF or Service Call external interruption is pending, the virtual machine's PSW (VMPSW) is set to condition code two (busy), and control returns to the caller. If a request is not pending, DMKMHV does the following:

- Sets VMPSW to indicate condition code zero
- Sets VMMSSFXP in the VMBLOK for the virtual machine

Note: If the preferred machine assist guest issues a command to vary the channel offline, the hardware receives the instruction directly and the channel is varied offline. VM has no way to intercept the instruction when the preferred machine assist guest is running.

- Builds an HCBLOK
- Sets the HCVMEEQ field to indicate a virtual machine generated MSSFCALL or Service Call operation
- Passes control to DMKMHC (at entry point DMKMHCVM) to schedule the request.

If DMKMHV is processing the IOCP command, before passing control to DMKMHC, DMKMHV verifies that the processor is a 308x or 3090 and examines the virtual machine user's privilege class. The virtual machine user must have privilege class C or E to read from an input/output configuration data set (IOCDS) and privilege class C in order to write to an IOCDS. If the user does not have the appropriate privilege class for a MSSFCALL, DMKMHV sets an invalid command response code and returns control to DMKHVC. If the user does not have the appropriate class for a Service Call request, an invalid function code is set by DMKMHV and control returns to DMKPRV.

For more information on the IOCP command, see the VM/SP HPO Planning Guide and Reference and the IOCP User's Guide and Reference for either the 308x Processor Complex (GC28-1027) or the 3090 Processor Complex (GC38-0039).

For an IOCP write request, DMKMHV limits access to an IOCDS to one user at a time. DMKMHV obtains a lock to serialize access to an IOCDS. Because DMKMHV is pageable, the IOCP write lock (ICPWLOK) is defined in module DMKMHC at entry point DMKMHCLK. DMKMHV uses an external reference to DMKMHC to examine the lock.

- If the lock is free, DMKMHV ensures that the IOCDS is open and sets the lock for the virtual machine user.
- If the lock byte is not set up properly, an error response code is returned.
- If the lock is held, DMKMHV determines whether the lock is held by this user. If the lock is not held by this user, DMKMHV sets condition code two (busy) and returns control to the caller.
- If DMKMHV determines that the lock is held by this virtual machine user, DMKMHV prepares to perform a real MSSF or Service Call open for the IOCDS.

After obtaining the lock, DMKMHV builds an HCBLOK to contain the address of the data block and command word, and calls DMKMHC to schedule the request. The lock is not needed to process an IOCP read request. DMKMHCVM is the entry point for MSSFCALL or Service Call instructions generated by a virtual machine.

Scheduling and Executing the Real MSSF or Service Call Request

Module DMKMHC schedules all CP and virtual machine requests for MSSF or Service Call operations. DMKMHC contains several entry points. DMKMHCCP is the entry point for CP generated MSSFCALL or Service Call operations while DMKMHCVM is the entry point for virtual machine MSSFCALL or Service Call requests. Though the entry points are different for the operations, processing is similar. On entry, DMKMHC obtains the address of the HCBLOK and performs the following functions:

- Verifies that the processor-id is that of a 308x or 3090 processor.
- Sets the HCBLOK pointer, flag bytes, and priority fields to zeros.
- Sets the HCMSFSYS field to indicate a CP or virtual machine generated MSSFCALL or Service Call operation.
- Saves the system VMBLOK address in the HCUSER field of the HCBLOK.
- Adds the address of the HCBLOK to the internal control block pointer chain (HCANCHOR).
- Examines the active pointer (HCACTIVE) to determine whether MSSF or Service Processor is busy with a previous request.

HCACTIVE contains the address of the HCBLOK that MSSF or Service Processor is currently processing. The HCACTIVE field is zero if the MSSF or Service Processor is available. If the MSSF or Service Processor is available, DMKMHC passes control to subroutine MSFCALL (using BALR) to issue the diagnose instruction for a 308x processor or the Service Call instruction for a 3090 processor. Subroutine MSFCALL performs the following:

- Obtains the address of the HCBLOK from the queue
- Issues the MSSFCALL for a 308x processor or issues the Service Call for a 3090 processor
- Traces the or Service Call (if internal tracing is active)
- Validates the condition code returned by MSSF or the Service Processor.

If MSSF or the Service Processor returns condition code zero, the address of the HCBLOK is placed in HCACTIVE and HCGLAG is set to indicate that MSSF is processing the diagnose instruction or the Service Processor is processing the Service Call instruction. The HCBLOK is deleted from the queue of pending requests and control is returned to the caller.

The MSSFCALL or Service Call instruction and response are traced if the internal trace facility is active. For further information on the format of the trace table entries see the *Virtual Machine Diagnosis Guide*.

Processing the MSSF or Service Processor Interrupt

MSSF and the Service Processor use an external interruption to signal CP that processing is complete. DMKEXT passes control to DMKMHC. DNMMHC processes external interruptions resulting from the MSSFCALL or Service Call at entry point DMKMHCIN.

At entry point DMKMHCIN processing is as follows:

- Obtains the global system lock
- Verifies that the interruption from DMKPSA is associated with the HCBLOK address in the HCACTIVE field
- Examines the interruption to determine whether CP or a virtual machine initiated the request.

For a CP request, DMKMHC obtains a CPEXBLOK and initializes it to contain the interruption return address (DMKCPUMI) and the address of the MSFBLOK or SCCBLOK. DMKMHC places the CPEXBLOK on the dispatcher queue and control returns to the caller, which examines the completion code and issues appropriate messages.

For a virtual machine request (HCVMREQ=1), processing is as follows:

- Resets the interruption pending field (VMMSSFXP) in the virtual machine's VMBLOK
- Resets information on Service Call facilities, Paging Storage for the 3090, and storage information for both the 3090 and 308x
- Obtains an XINTBLOK for the virtual machine
- Determines whether the virtual machine is logging off or performing a system reset
- Releases the HCBLOK
- Places the XINTBLOK on the virtual machine's external interruption queue
- Exits to the dispatcher to return control to the caller.

DMKMHC compares the HCACTIVE field with the address of the data block. If the addresses do not match, ABEND MHC02 is generated. If they match, DMKMHC uses zeros to clear HCACTIVE and examines the queue or pending MSSF requests. If another request is in the queue, DMKMHC sets HCACTIVE to the addresss of the new HCBLOK, and the new request is executed. When a virtual machine logs off or performs a system reset, DMKCFP receives control. DMKCFP (at entry point DMKCFPRR), calls DMKMHC to determine whether the virtual machine has an outstanding MSSF request. At entry point DMKMHCRE, processing is as follows:

- Clears VMMSSFXP in the virtual machine's VMBLOK
- Examines the queue of pending requests
- Deletes the HCBLOK from the queue if the request is not active
- Releases storage for the MSFBLOK or the SCCBLOK and the HCBLOK
- Examines the IOCP write lock
- Clears the lock if it sets for this virtual machine
- Returns control to DMKCFP through the dispatcher.

If the virtual machine has an active operation, DMKMHC waits for the external interruption before releasing the MSFBLOK or SCCBLOK and HCBLOK.

Virtual MSSF or Service Call Processing – SCPINFO

On a 308x processor, module DMKHVC receives control to validate the diagnose instruction produced by a virtual machine SCPINFO command. DMKHVC calls DMKMHV to process the diagnose instruction. DMKMHV simulates the MSSFCALL instruction by setting the virtual machine's PSW to contain a condition code and scheduling external interruptions in the same sequence as would be presented on the real processor.

On a 3090 processor, module DMKPRV receives control to validate the Service Call instruction. DMKPRV calls DMKMHV to simulate a virtual Service Call.

DMKMHV examines the XINTBLOKs for the virtual machine to determine whether the virtual machine has an outstanding request. If an external interruption is pending, the virtual machine's PSW (VMPSW) is set to condition code two, and control returns to the caller. If there is no request pending for the virtual machine, VMPSW is set to condition code one. DMKMHV then verifies the MSFBLOK or SCCBLOK. If there are no violations, the MSFBLOK or SCCBLOK is set up to contain predefined response codes.

After setting the MSFBLOK or SCCBLOK, DMKMHV obtains an XINTBLOK for the virtual machine and sets it to the address of the MSFBLOK or SCCBLOK. The XINTBLOK is placed on the virtual machine's external interruption queue and control returns to the caller through the dispatcher.

If an error condition exists for violation of boundary, block length, or command, the data block response field is set to the appropriate error response code. Control returns to DMKHVC or DMKPRV.

Figure 31 shows a real MSSFCALL control block structure. Figure 32 shows a virtual MSSFCALL control block structure.

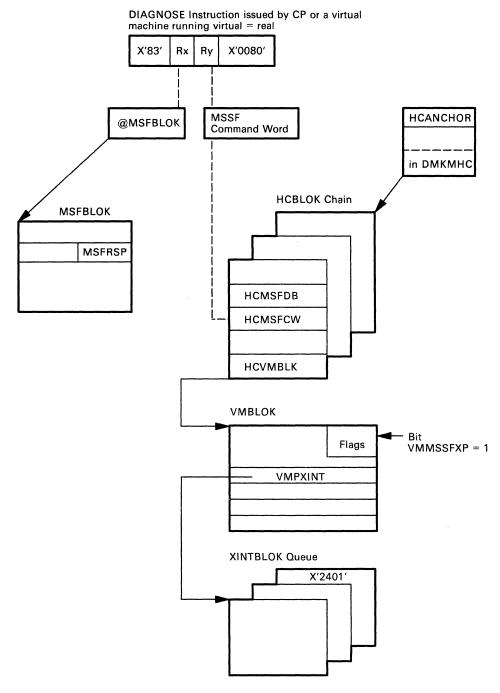


Figure 31. Real MSSFCALL Control Block Structure

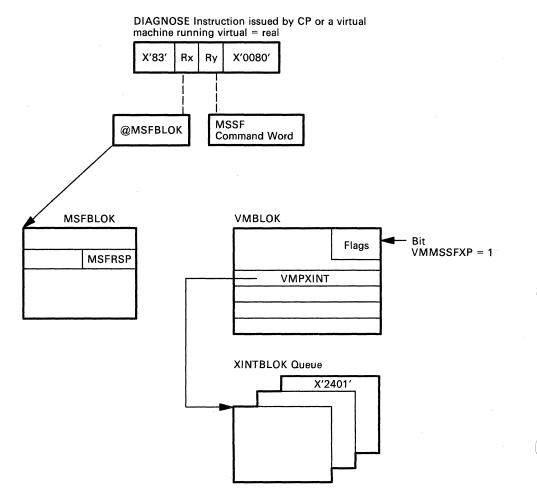


Figure 32. Virtual MSSFCALL Control Block Structure

I/O Reconfiguration

Three commands alter the I/O configuration of a user's virtual machine after he has logged on. Two are user commands, while the third is a system operator command, because it affects the status of real devices attached to the system. The ATTACH and DETACH commands are contained in DMKVDA, DMKVDC, DMKVDD, and DMKVDE and the DEFINE command in DMKDEF. The system command scanner (DMKCFM) calls both pageable modules after their format and privilege classes have been validated. These commands access the same control-block building subroutines in the module DMKVDS that DMKLOG, the LOGON processor, uses.

Note: The authorized user will be able to dynamically redefine the virtual machine's device configuration during the terminal session (using the ATTACH, DETACH, and DEFINE commands), but only if there is a sufficient amount of available contiguous space. See "Virtual Machine I/O Management" on page 8 to determine the maximum device limit.

Attaching a Real Device: The system operator can dedicate any real device to a single virtual machine by issuing the ATTACH command. The device attached is available only to the given virtual machine, and all I/O requests to it are handled by CCW translation. If the device is a DASD, cylinder relocation does not occur when SEEK addresses or home addresses are referenced. The I/O supervisor does not queue operations on the device, nor does it automatically restart it or do ordered seek queuing. Nonsharable devices such as tape drives must be attached to a virtual machine to be accessed by the virtual machine. A virtual machine can also have a dedicated card reader/punch or printer. However, this is usually not necessary because of the unit record spooling facilities of CP. Unit record input or output on a dedicated (attached) device is not spooled by CP. The unit attached may be given a virtual address different from its real address. However, the virtual machine may not already have a virtual device at the attached address. A real device cannot be attached (1) if it is currently dedicated to another virtual machine, (2) if it contains minidisks that are in use by other virtual machines, or (3) if it is a system-owned volume that is in use for spooling or paging.

The system operator can dedicate a remote 3270 Information Display System Printer (3284, 3286, 3287, 3288, or 3289) to a single virtual machine by issuing the NETWORK ATTACH command. The printer attached is available only to the given virtual machine, and all I/O requests to it are handled by Start I/O.

Defining a Virtual Device: A system user can define a new virtual device with the DEFINE command that does not require the dedication of a corresponding real device. Devices that can be defined are consoles, spooled readers, punches and printers, dialable TP lines, virtual channel-to-channel devices, pseudo timers, and temporary disks. With the DEFINE command, the user can change any existing virtual device address whether it corresponds to a shared or dedicated real device or no real device unit.

Care must be taken when using the DEFINE statement to create (or move) a virtual device. CP checks for a potential subchannel protocol conflict on the virtual control unit that would support the new device. If a conflict is detected, the operation is not performed. Instead, CP sends an error message to the user who issued the command.

The DEFINE command can also describe the virtual machine channel mode of operation, that is, either selector or block multiplexer. The default mode, selector channel mode, reflects a channel busy to any SIO operation attempted on the same channel path that has not completed the previous channel SIO operation. Block multiplexer mode allows the successful initiation of different devices on the same channel path. Channel 0, a byte-multiplexer channel, is unaffected by the DEFINE command. Use of the DEFINE command with the CHANNELS operand generates a virtual machine reset. Therefore, it should be invoked prior to the virtual machine IPL operation.

Note: The channel mode selected has no bearing on the types of channels that are attached to the real system.

Temporary disks are dynamically obtained cylinders or blocks of DASD storage space. They are available to the user for as long as they are part of his virtual machine configuration, but the data on them is destroyed after the user detaches the area. For all other purposes, however, they appear to be a standard disk.

Detaching a Virtual Device: A virtual device can be removed from a virtual machine configuration prior to logging off with the DETACH command. A user can detach any of his own devices, and the system operator can detach a real device from a virtual machine. If the operator detaches the device, the user is informed of the operator's action. A real device can be detached only if it is dedicated to a single virtual machine or is attached to the system and is not in use when the DETACH is issued.

The system operator can detach a remote 3270 Information Display System Printer (3284, 3286, 3287, 3288, or 3289) from a virtual machine with the NETWORK DETACH command. If the operator detaches a 3270 Information Display System Printer, the user is informed of the operator's action. A 3270 Information Display System Printer can be detached only if it is dedicated to a single virtual machine or is attached to the system and is not in use when the NETWORK DETACH command is issued.

Disconnecting a Terminal or Virtual Machine

A user may permanently or temporarily disconnect his terminal or virtual machine from the system by a console command, or the terminal or virtual machine may be forcibly disconnected by the operator. The system can also log off the virtual machine. Reconfiguration routines that handle the termination process are in the pageable module, DMKUSO.

Permanent Disconnect: The user may voluntarily remove his virtual machine from the system via the LOGOFF command. This command terminates all virtual machine operation, releases all storage occupied by control blocks and virtual storage pages, and disconnects the teleprocessing line connection to the user's terminal. If the user specifies the HOLD option with LOGOFF, all of the above occurs, except that the teleprocessing line remains enabled. This option is especially useful for dialed connections that are reused immediately by another user.

The virtual machine can be forced off the system by the system operator via the FORCE command. This has the same effect as a user-initiated logoff, except that the user is informed that the operator has logged off his machine. A virtual machine may also be logged off the system if the virtual machine is running disconnected (without an active terminal) and the virtual machine attempts a terminal read or enters a disabled wait state.

The DMKUSOLG and DMKUSOFF subroutines process the LOGOFF command. DMKDSP calls DMKUSOFF to force the logoff of a virtual machine.

Temporary Disconnect: A user may temporarily disconnect his terminal from his virtual machine by using the DISCONN command, while allowing the virtual machine to continue to run. This command flags the virtual machine as being disconnected and releases the user's terminal and teleprocessing line. If the HOLD option was specified in the DISCONN command, CP allows the line to remain enabled, and another user can use the terminal to log on. The disconnected virtual machine continues to be dispatched until it either attempts to execute a terminal read to the disconnected console or it enters a disabled wait state. At this time, the dispatcher (DMKDSP) calls the routine DMKUSOFF directly to force the machine out of the system. While the machine is disconnected from its virtual console (real terminal) any terminal output is lost. In addition, CP may apply a disconnected penalty to the machines scheduling priority, to bias the system in favor of interactive users.

A user's virtual machine may also be disconnected by the system. If the disconnected user logs on to the system while the disconnected machine is still running, it is reconnected and can continue to interact with the system in the usual manner.

The DMKUSO subroutine processes the DISCONN command.

Console Functions

DMKCFM analyzes CP commands and passes control to the appropriate routine to handle the command. DMKCFM can be entered by the Attention key (or equivalent) at the user's terminal or directly from a virtual machine.

When a console interruption occurs by the Attention key at the user's terminal, DMKIOSIN calls DMKCNSIN to handle the unsolicited interruption, then DMKCNSIN calls DMKCFMBK.

DMKCFMBK first calls DMKFREE to obtain storage for an 18-doubleword input buffer. Next, DMKQCNWT is called to send the CP message to the terminal to inform the user that he has entered console function mode. DMKQCORD is then called to read the command line entered at the console.

DMKCFMEN is the entry point for commands coming directly from the virtual machine. DMKPRGIN enters at DMKCFMEN here when a DIAGNOSE instruction with a code of 8 is detected. The address of an 18-doubleword input buffer is passed in register 1. Therefore, a read to the terminal is not needed.

After either the read to the terminal or entry from the virtual machine, DMKSCNFD is called to find the command type. On return from DMKSCNFD, register 1 points to the start of the command and register 0 contains the length of the command. DMKCFM then calls DMKCFCMD to scan and validate the command. DMKCFCMD contains a command table which it searches until it finds a command name match. It compares the user classes in the field CTCLASS. If the user is authorized to issue the command, DMKCFCMD will pass the appropriate module entry point from the command table to DMKCFM. The command is then processed. For the commands ATTACH, DETACH, INDICATE, NETWORK, QUERY, and SET, DMKCFCMD uses subcommand tables in DMKCMD to locate the appropriate module entry points.

If DMKCFCMD cannot find a command name match, the user receives a message informing him that the command or subcommand table search has failed.

After the command has been processed, control is returned to DMKCFM. There are three possible returns. (1) On a normal return, the input buffer is scanned to see if there are any more commands. If none exist, DMKCFM returns to the virtual machine (if entered via DIAGNOSE) or calls DMKQCNRD to read the next command from the terminal. (2) On a return plus 4, the VMCFWAIT bit is turned off to allow the virtual machine to run. DMKFRET is called to return the input buffer storage. Then control returns to either the virtual machine, if entered via a DIAGNOSE, or to DMKDSPCH if entered via the Attention key. (3) On a return plus 8, the operation is the same as plus 4 except that the VMCFWAIT bit is left on.

Dispatching and Scheduling

The dispatcher, DMKDSP, gives the processor resource to a virtual machine. It is called after an interrupt handler has finished processing, and after each stacked CPEXBLOK, I/O request, deferred input task, and external interrupt has been taken care of. The dispatcher updates the virtual timers and CPU timer of the virtual machine just serviced, and also reflects any pending interrupts for that virtual machine. Then the dispatcher either stacks the next available stacked request, or places the highest priority dispatchable user into execution.

The scheduler, DMKSCH, decides how often and for how long virtual machines will be given processor resources. The scheduler keeps statistics to make dispatching decisions, and maintains queues for runnable, dispatchable, and eligible users.

The auxiliary routine that assists the dispatcher and scheduler is the request stack maintenance routine, DMKSTK. Other auxiliary routines: DMKTRQ, which handles timer requests, and DMKSTP, which periodically recalculates feedback variables. (Also, DMKSTP uses data areas in DMKSTD.)

To make decisions on dispatching and scheduling, the control program places all virtual machines into various categories, and recognizes user machines as being in one of several states. The virtual machine categories either interactive or noninteractive virtual machine, are defined in the following way:

• An interactive virtual machine is one whose use of the system is punctuated by long idle waits, and does not have long processor execution times. A virtual machine becomes eligible to enter interactive status whenever it remains idle for more than 300 milliseconds. It remains interactive until it uses one Q1 time slice and one Q2 time slice. In addition, the scheduler identifies virtual machines that use CPU in frequent, short bursts. If the average length of these bursts is less than one fourth of the Q1 time slice, the scheduler forces that virtual machine into Q1 and treats it as if it were interactive.

- A non-interactive virtual machine is one that has violated an interactive criterion: it has used more than one Q1 time slice and one Q2 time slice without entering a long idle wait (remaining idle for more than 300 milliseconds.). CP schedules interactive users ahead of non-interactive users. Non-interactive users are subdivided into several classes. Normal non-interactive virtual machines are scheduled by a priority scheme described below. A virtual machine is allowed to execute for a specified time period and then it is placed in a list of those machines that are waiting.
- Note: You can alter the queue scheme by using the NOQ2 or NOQ3 option on the SET QDROP OFF command. Specifying NOQ3 will force a virtual machine to be kept in Q1 or Q2. Specifying NOQ2 will force a virtual machine to be kept only in Q1. Furthermore, virtual machines are categorized on a sliding scale by their levels of resource utilization. The actual scheduling sequence depends on both a virtual machine's interactive/noninteractive characteristics and its current resource utilization level.

To give preference to certain classes of virtual machines, a priority scheduling scheme allows virtual machines to be scheduled with a priority class. The priority is a number assigned by the directory. However, the number may be altered by the system operator. This priority is referred to as the "user priority" to distinguish it from the priority used by the scheduler for ordering virtual machines for service.

The run list is a list of all virtual machines, runnable and nonrunnable, that are competing for processor resources. The scheduler places a virtual machine in the run list if its projected working set size is less than or equal to the number of page frames available for allocation, AND if processor time is available. If processor time is not available, the scheduler decides if the virtual machine should go on the run list anyway by checking to see if the virtual machine's deadline priority is earlier (better) than that of the last virtual machine already on the run list. If this is true, then the virtual machine is placed on the run list.

Note: The INDICATE POSITION and INDICATE QUEUES commands show you the specific list on which a user is waiting. If the user is on the eligible list, the response will show you that the user is either interactive or noninteractive, and that the user is waiting for either storage or processor time. See VM/SP HPO CP for System Programming for more information on these commands.

The interactive buffer is a pool of page frames which is set aside for the exclusive use of interactive virtual machines. The size of the interactive buffer can be set with the class E SET SRM IBUFF command. Pages in the interactive buffer can only be used by interactive virtual machines (Q1 or first Q2 time slice).

Run List

If space is not available for a virtual machine, the scheduler:

- 1. Checks the minimum MPL levels
- 2. Attempts to preempt virtual machines of lower priority
- 3. Blocks addition of this virtual machine and others of the same class to the run list.

A virtual machine that completes its time slice is dropped from queue and placed in the eligible list according to its deadline priority. A virtual machine in queue that enters a long idle wait is removed from queue if it remains idle for at least 300 milliseconds.

Dispatch List

The dispatch lists, or true run lists (TRLs), are subsets of the run list, anchored in the PSA extensions as doubly-linked lists of VMBLOKs of the virtual machines that are currently dispatchable. One dispatch list exists for each processor on an AP/MP system. The dispatcher scans the processor's dispatch list when it wants to give processor time to a user. The dispatch list is maintained by the scheduler, who considers a user to be dispatchable when the VMRUN bit in the VMDSTAT field of a VMBLOK is on. The VMRUN bit is a summary of the bits in the VMRSTAT field, each of which indicates various conditions that keep a runnable user from being dispatchable. If any bit in VMRSTAT is on, the VMRUN bit is off.

The scheduler maintains the dispatch list by adding and removing the VMBLOKs of users as their current status dictates. The four basic actions the scheduler must perform to do this are:

- 1. Adding a VMBLOK already in the run list to a dispatch list when the user becomes dispatchable
- 2. Adding a VMBLOK to a dispatch list when a user, who is currently dispatchable, is added to the run list
- 3. Removing a VMBLOK from a dispatch list when the user is no longer dispatchable
- 4. Removing a VMBLOK from a dispatch list when the user is dropped from the run list.

To add a VMBLOK to a dispatch list AND to the run list, the scheduler follows this procedure:

- 1. Call DMKLOK to lock the run list
- 2. Add user to the run list
- 3. Call DMKLOK to unlock the run list
- 4. Call DMKLOK to lock the dispatch list

- 5. Add user to the dispatch list by using the anchor point of the dispatch list (TRLANCHR) and the priority of the user (VMTPRIOR) to scan the dispatch list for the proper place to insert the new VMBLOK. The forward pointer (VMFTRL) of the new VMBLOK will be a nonzero value to indicate that the VMBLOK is in the dispatch list.
- 6. Call DMKLOK to unlock the dispatch list.

To add a VMBLOK to a dispatch list, when it is already in the run list, the scheduler follows this procedure:

- 1. Call DMKLOK to lock the dispatch list
- 2. Add user to the dispatch list as in step 5 above
- 3. Call DMKLOK to unlock the dispatch list.

To drop a VMBLOK from a dispatch list AND from the run list, the scheduler follows this procedure:

- 1. Call DMKLOK to lock the run list
- 2. Drop user from the run list
- 3. Call DMKLOK to unlock the run list
- 4. Call DMKLOK to lock the dispatch list
- 5. Drop user from the dispatch list by linking the VMBLOKs of the users that come before and after the VMBLOK that is about to be deleted. It does this by updating their backward and forward pointers (VMBTRL and VMFTRL). The scheduler then sets to zero the deleted VMBLOK's forward pointer, to mark it as no longer in the dispatch list.
- 6. Call DMKLOK to unlock the dispatch list.

To drop a VMBLOK from a dispatch list but NOT from the the run list, the scheduler follows this procedure:

- 1. Call DMKLOK to lock the dispatch list
- 2. Drop user from the dispatch list as in step 5 above
- 3. Call DMKLOK to unlock the dispatch list.

Occasionally a dispatch list maintenance problem will occur. The scheduler will realize this when it attempts to add a VMBLOK to a dispatch list, only to find that it is already there, or when it attempts to remove a VMBLOK from a dispatch list when the VMBLOK is not there. In either of these instances, CP will abend. When the dispatcher wants to put a virtual machine into execution, it uses the anchor of the processor's dispatch list (TRLANCHR) and the dispatch list forward pointer (VMFTRL) to find the address of a VMBLOK reflecting a dispatchable user.

Dispatch Request Queues

The dispatch request queues contain pointers to CPEXBLOKs, IOBLOKs, and TRQBLOKs. CPEXBLOKs are control blocks that designate CP tasks to run. IOBLOKs contain information on imminent I/O operations, and TRQBLOKs are used to manage system timing facilities. The scheduler gives priority to these blocks over VMBLOKs.

For attached processor and multiprocessor systems, the scheduler maintains two dispatch request queues and two dispatch lists (one for each processor). When the scheduler adds a virtual machine to a dispatch list, the virtual machine is queued for the local processor unless:

- The other processor has fewer virtual machines on its dispatch list, or
- A virtual machine has affinity specified for the other processor, or
- A virtual machine's virtual Vector Facility status is in the real Vector Facility on the other processor.

It stays assigned to that processor until it is stolen, becomes nondispatchable, or drops from queue.

Active Wait

In MP systems work can become available without an accompanying interrupt. When one processor becomes idle, it goes into an active wait. During active wait, the otherwise idle processor scans certain queues looking for work. The first queue it checks is the local DSPRQ. If the dispatcher request queue is empty or the system lock is unavailable, the header of the local dispatch list is examined. If this queue is empty or a runnable user is not found, the dispatch list of the other processor is examined. If the idle processor finds more than one runnable virtual machine on that dispatch list, it dequeues the second virtual machine, adds it to its local dispatch list, and dispatches it. If it does not find more than one VMBLOK, it will return to the dispatcher (if trying to steal) or check the local dispatch list again.

Note: When VM/SP HPO is running second level as a virtual MP guest on VM/XA SF, active wait will load an enabled wait PSW if it has no work on its queues. It will issue DIAGNOSE X'44' if it has work that cannot be unstacked. When work is stacked for a waiting processor, that processor will be signaled.

Note: When a system is running in AP/MP mode, the dispatcher will call the scheduler as needed to update the dispatch list after a CPU timer interrupt. This occurs whenever the system lock is needed for the scheduler to update the current VMRSTAT.

Queue Drop Elimination

When a virtual machine is not dispatchable (a bit is on in VMRSTAT) and in queue (VMINQ is on), the scheduler must decide whether to drop the virtual machine from queue. When the virtual machine enters a potential long wait, the scheduler calls DMKFREXX for a TRQBLOK to create a 300-millisecond queue drop delay. The TRQBLOK for the queue drop delay is anchored in the field VMDRPTRQ in the VMBLOK.

After the scheduler invokes a 300-millisecond delay, it returns to the caller because it can do no more useful work until after the virtual machine is dropped from queue. If the virtual machine becomes runnable before the 300-millisecond delay is completed, the delay is canceled and the virtual machine continues in its current queue stay. If the delay expires, the virtual machine is dropped from the run list.

When the scheduler is called on behalf of a virtual machine, and the virtual machine is dispatchable, the scheduler ignores the call. This prevents a noninteractive virtual machine from periodically becoming interactive and hindering response times for truly interactive machines.

Virtual Machine Run Lists and States

To efficiently manage the large inventory of potential virtual machines that are logged on to the system, CP defines several states that a virtual machine may occupy. The scheduler can move a virtual machine from one state to another. However, a virtual machine may exist in only one state at any given instant. CP can then make scheduling and dispatching decisions by looking only at the subset of virtual machines that are in the appropriate state. To do this search, it also maintains lists of virtual machines in certain executable states.

A user's virtual machine may be in one of the following states:

State	Meaning
1	Interactive and dispatchable (in run list, in dispatch list)
2	Interactive and not dispatchable (in run list, not in dispatch list)
3a	Interactive and runnable, but no available real storage (waiting, in eligible list)
3b	Interactive and runnable, but no available real storage or processor time (waiting, in eligible list)
4	In wait state with queue-drop delay timer active (in run list)
5	Noninteractive and dispatchable (in run list, in dispatch list)
6	Noninteractive and not dispatchable (in run list, not in dispatch list)
7a	Noninteractive and runnable but no available real storage (waiting, in eligible list)
7b	Noninteractive and runnable but no available real storage or processor time (waiting, in eligible list)
8	In long idle wait (idle for more than 300 ms.)

Entries on the run list and the dispatch list are the VMBLOKs for those virtual machines in states 1 and 5, and represent the virtual machines that can be run at any given time. (States 2, 4 and 6 remain in the run list even though they are not dispatchable.) The dispatch list is sorted by deadline priority, which is the time of day (TOD) by which the virtual machine should have used its allocated processor time and been dropped from queue. A task is defined as one processor time slice or execution between long idle waits (movement from state 8 to state 1), whichever is less. It is re-projected for a virtual machine each time it is dropped from a queue.

The deadline priority is recalculated each time a virtual machine is dropped from queue and represents the next expected time that the virtual machine should again be dropped from queue.

The eligible list contains virtual machines in states 3 and 7. These virtual machines are potentially executable, but they are not allowed to compete for the processor because of the current demand for main (real) storage. The list is ordered by deadline priority, which is determined at the time the virtual machine was previously dropped from queue, as follows:

- 1. Paging ratio The virtual machine's projected working set size, calculated the last time it was dropped from a queue, is expressed as a ratio to the overall system average working set size.
- 2. Processor resource ratio The virtual machine's processor resource utilization the last time it was dropped from a queue, is expressed as a ratio to the overall system average processor resource utilization.
- 3. Average queue delay The sum of the average time spent in Q1, the average time in E1, the average time in Q2, and the average time in E2.
- 4. Paging bias The total eligible list time divided by the total list time (sum of the total eligible and dispatch list times) for all virtual machines.
- 5. User bias ratio A user bias ratio is formed by multiplying the paging ratio by the paging bias and adding that to the result of multiplying the processor resource ratio by 100 minus the paging bias. The resulting sum is divided by 100 to arrive at a user bias. In effect, this is a weighted average of the paging and processor resource ratios, where the weights are determined by the paging bias. The higher (lower) the real storage contention, the larger (smaller) the weight given to the paging ratio.
- 6. Prioritized average queue delay The average queue delay, which is the sum of the average times a virtual machine spends on the eligible list and on the dispatch list, is multiplied by the function $(64^{**}(UP/64))/64$, where UP is the user priority. For those priorities equal to 64, the function is 1, for those greater than 64, the function is greater than 1, and for those less than 64, the function is less than 1.
- 7. Virtual machine queue delay factor The user bias ratio is multiplied by the user-prioritized average queue delay to arrive at the virtual machine's queue delay factor. The queue delay factor is stored in the VMBLOK in the VMQPRIOR field. The units of the queue delay factor are equal to the high-order word of the time-of-day clock multiplied by 32 (approximately 1/32 of a second).
- 8. Q3 If a given virtual machine is (a) a Q2 virtual machine, and (b) it is dropping from Q2 because it has used up its allocated processor in-queue time, and (c) it has dropped from Q2 at least 6 consecutive times because of using all allocated processor resources (without entering a true long idle wait), then the virtual machine is marked as Q3 and the queue delay factor is multiplied by 8. A Q3 virtual machine is treated as a normal Q2 virtual machine, except for differences in deadline priority calculation and the amount of processor time the virtual machine is allowed in queue.

- 9. Virtual machine deadline priority The virtual machine's queue delay factor from Step 6 is added to the time of day when the virtual machine drops from queue. This value becomes the virtual machine's deadline priority and represents the expected time of day that the virtual machine should be next dropping from queue. The deadline priority is stored in the VMBLOK in the VMEPRIOR and VMTPRIOR fields.
- 10. Q1 deadline priority If the virtual machine is being added to the Q1 eligible list, then the deadline priority is adjusted to account for the shorter processor time allowed in Q1. The queue delay factor (from VMQPRIOR) is subtracted from the deadline priority (from VMEPRIOR and VMTPRIOR). The queue delay factor is divided by 8 (shifted right 3) and stored back in VMQPRIOR as the new queue delay factor. In addition, if the virtual machine is currently using less than its allocated processor resource, the queue delay factor is shifted right additionally the number of places determined by the interactive bias. In the standard system the interactive bias is set to 2. The new queue delay factor is then added back into the result of the subtraction to form the Q1 deadline priority, which is then stored in VMEPRIOR and VMTPRIOR.

The VMBLOK is then sorted into the eligible list in ascending value of VMTPRIOR. The units used in the deadline priority calculation are obtained by taking the high-order word of the time-of-day clock and shifting it left 5 bit positions. The low-order bit position is approximately equal to 1/32 of a second. To handle the effects of losing the high 5 bits of the time-of-day clock, the current time-of-day value first has the time of day at system IPL subtracted from it before shifting. The effects of the individual virtual machine's resource utilization and user priority are illustrated by the following examples.

Example 1

Assume that two virtual machines are to be added to the eligible list for Q2. The current paging bias percentage is 0, both user priorities are 64, and the current average queue delay is 10 seconds. Virtual machine A has a current resource utilization (user bias) of 1/2 the system average. Virtual machine B has a resource utilization equal to the system average. The queue delay factors are obtained as follows:

Step 1:

Virtual Machine A B	Paging Ratio 0 x ? 0 x ?	+ +	Processor Ratio (100-0) x 1/2 (100-0) x 1	=	Combined Ratio 50/100 = 1/2 100/100 = 1
Step 2:					
Virtual Machine A B	Average delay 10 10	e queue x x	Virtual Machine Priority ratio 1		Queue delay 10 10

Step 3:

Virtual					
Virtual	Combined		Queue		Machine
Machine	ratio		delay		delay
Α	1/2	x	10	=	5
в	1	x	10		10

If both virtual machines A and B dropped from queue at time-of-day 0, then virtual machine A has a deadline priority of 5 seconds and virtual machine B has a deadline priority of 10 seconds. Assuming virtual machine B continues to run, it should drop from queue once every 10 seconds, which is the system average. By cycling through queue at the average system rate, virtual machine B should maintain its processor resource utilization at the system average level (which will maintain its virtual machine delay at 10 seconds). Virtual machine A, whose current processor resource is 1/2 the system average, is allowed to cycle through queue in 1/2 the normal time. If virtual machine A continues to run at this rate, its processor resource utilization is accumulating at twice the system average. As virtual machine A's processor resource utilization approaches the system average, its combined ratio approaches 1. This increases its delay factor to 10 seconds, and its and virtual machine B running at the same rate.

Example 2

The conditions are the same as example 1, except both virtual machine A and B have the same processor resource utilization. Virtual machine A has a virtual machine priority of 64 and user B has a user priority of 54.

Step 1:			-			
Virtual Machine	Paging Ratio		Process Ratio			ombined Ratio
A	0 x ?	+	(100-0) x			/100 = 1
В	0 x ?	+	(100-0) x	1	= 100	/100 = 1
Step 2:						
Virtual	Average	queue	Virtual M		-	ueue
Machine	delay		Priority ra	atio =		elay
A B	10 10	x	1			10 5
Б	10	x	1/2			0
Step 3:						
Virtual						
Virtual	Combine	d.	Que			lachine
Machine	ratio		del	•		elay
A	1		x 10			10
В	1	2	x 5		:	5

Virtual machine B continues to have queue delays calculated smaller than virtual machine A (or the system average) as long as its processor resource utilization is less than twice the system average.

The above examples illustrate the following general points about the deadline priority scheduling:

- 1. The purpose of the calculations is to effectively control each virtual machine's resource utilization rate by controlling how fast each machine is able to move through queue each time.
- 2. Each virtual machine's default allowed utilization is the same, namely the system "fair share" value, that is, the total system resources divided by the number of active virtual machines.
- 3. Deviations from a virtual machine's specified resource utilization (because of any particular advantageous or disadvantageous stay in queue) are corrected for successive stays in queue.
- 4. The system's response to any virtual machine's transaction is proportional to that virtual machine's current resource utilization and the total system load. Very trivial, interactive virtual machines receive the best service. Service for others degrades proportionally to their current level of utilization.

Paging Percentage Bias

The paging percentage bias is a dynamically calculated value that is proportional to the percentage of time spent in the eligible list. The upper limit of the paging percentage bias is 40 percent. The paging percentage bias represents the portion of the utilization ratio due to paging, rather than processor utilization. The maximum paging percentage bias can be queried and set with the QUERY SRM and SET SRM commands.

Interactive Bias

The interactive bias shift is set to 2. Normally when a virtual machine enters the eligible list as Q1, its priority delay factor is shifted right 3 (divided by 8). In addition, if the virtual machine's current resource utilization is less than its allocated amount (or fair share), the priority delay factor is shifted right an additional amount specified by the interactive bias (2). This allows much faster response for trivial interactive virtual machines, but still prevents the nontrivial interactive virtual machines from obtaining more than their fair share of the system resources. The interactive bias can be queried and set with the QUERY SRM and SET SRM commands.

User Priority Function

The user priority function is a nonlinear function, and changes to user priorities effect performance differently, depending on the original priority. The priority ratio is used to pro-rate a virtual machine's queue delay. The smaller the ratio (and the smaller the priority), the shorter the queue delay and therefore the better the service for that virtual machine. The following is a table of user priorities and the priority ratio calculated from the user priority function.

User Priority	Priority Ratio
0	1/64
11	1/32
21	1/16
32	1/8
43	1/4
54	1/2
64	1
75	2
85	4
96	8

A user priority of 0 provides a virtual machine with a "fair share" 64 times as large as the average (by multiplying its actual use by 1/64). A user priority of 99 provides a virtual machine with a "fair share" 1/8 of the average (by multiplying its actual use by 8). The normal priority is assumed to be 64 (which results in a multiplier of 1).

Virtual Machines in Idle Wait

Virtual machines that use the Diagnose X'58' instruction to communicate with 327x terminals are in an idle wait state when they are waiting for full-screen output. To prevent the scheduler from dropping these virtual machines from queue and adding their page frames to the FLUSHLST, CP keeps track of virtual machines that are currently waiting for high-speed I/O by using the counter VMDVBSY in each user's VMBLOK. Whenever VMDVBSY contains a positive value, indicating that the user is in idle wait and waiting for the high-speed I/O to complete, the scheduler will start the 300 millisecond queue drop delay timer and return to the caller when it would otherwise drop the virtual machine from queue.

VMDVBSY is increased whenever a virtual machine issues a Diagnose X'58' instruction, and decreased when the I/O service is finished. DMKGRF, which supports graphic display devices, uses the status of the bit RDEVPS located in the field RDEVSTA3 to control VMDVBSY. RDEVPS tells DMKGRF whether it is dealing with a logical device (RDEVPS on) or a local device (RDEVPS off). The VMDVBSY field is not maintained for logical devices.

Controlling Multiprogramming

To control the number of virtual machines allowed in queue, the scheduler monitors the paging activity of all virtual machines and of the total system. A decision as to whether or not to move a potential virtual machine from the eligible to the dispatch list is based upon whether or not its projected working set exceeds the system's remaining capacity. A virtual machine's projected working set is calculated at queue drop time.

Average Resident Pages - The basic mechanism used in predicting working set size is dependent upon the virtual machine's average resident pages while in queue. When a page is acquired, the current number of resident pages for the virtual machine is added to the resident page sum (VMPGRINQ). At queue drop, the resident page sum and the number of pages read are corrected to allow for initialization of the virtual machine's working set, and the corrected resident page sum is divided by the corrected number of pages read and swapped in to approximate the average number of resident pages.

Projected Working Set Size - Before using as a working set size estimate, the average resident page value is adjusted upward or downward dependent upon how well the virtual machine (and overall system) ran while it was in queue.

There is a tuning parameter in the system, which is changeable by the CP SET PAGING command, that provides a "norm" with which to compare paging performance. This value is set at a default value of 4. It is used in two ways to adjust the average resident page value for use as the working set size estimate.

- 1. The average resident page value is increased by 1 for each page steal in excess of 4 percent of the total number of pages read for that virtual machine while in queue.
- 2. The average resident page value is multiplied by the square root of the virtual machine's paging performance ratio, which is defined as follows:

At queue drop time, the estimated productive processor time per page read is calculated. The total processor time used by the virtual machine while in queue is divided by the number of pages read while in queue. From this processor time per page read value is subtracted the estimate processor time per page read. The scheduler maintains an "ideal" system-wide productive processor time per page read value. Dividing the virtual machine's calculated productive processor time per page read by the "ideal" system value gives the virtual machine's paging performance ratio for that time in-queue.

The adjustment formula for average resident pages is:

ARP' = (ARP + S) * (ICPU/PCPU) - reserved pages

where:

ARP'	is the adjusted average resident page value.
ARP	is the calculated average resident page value.
S	is the number of page steals in excess of 4 percent of the number of pages read.
PCPU	is the average productive processor time per page read.
ICPU	is the system's ideal productive processor time per page read.

The projected working set size estimate is set equal to the adjusted average resident pages (ARP') and stored in the VMWSPROJ field in the VMBLOK. There are four constraints on the projected working set size estimate:

- 1. The minimum value for the projected working set size is 2 (except for a V=R user or a user with reserved pages, in which case it may be 0).
- 2. The number of pages read while in queue.

- 3. The maximum number of pages owned plus the number of pages stolen while in queue.
- 4. The maximum value of a size is set by the SET SRM MAXWS command. Ordinarily this maximum is not active.

Assuming that page contention is being adequately controlled and the system resources have not been severely overcommitted, the projected working set size would ordinarily be close to the number of average resident pages. However, this estimate is sensitive to the overall system paging activity for the following reasons:

- 1. If there is no paging load on the system, the "ideal" productive processor time per page fault is set to its minimum value, and the projected working set size is continually underestimated.
- 2. As paging activity increases, the ideal productive processor time is calculated larger and causes the square root calculation to approach 1.
- 3. If the paging activity becomes excessive, and the system is unable to control it by reducing the multiprogramming level, the ideal productive processor time figure approaches its maximum value. This normally happens when there is insufficient real storage or paging I/O capacity to meet the interactive requirements.

In summary, the scheduler selects the subset of logged-on virtual machines that are allowed to compete for the resources of the processor. The scheduler prevents a virtual machine from being added to the active subset if the virtual machine's projected main storage requirements, added to those of the other active virtual machines, and the larger size of the interactive buffer (for noninteractive virtual machines), or the size of the largest user left in the eligible list cause the current capacity of the system to be exceeded. Once the active subset (the set of in-queue virtual machines) has been selected, the dispatcher allocates resources of the processor among them.

The list of executable virtual machines in a queue (on the dispatch list) is sorted by deadline priority (the same as for the eligible list). The only exceptions to this rule are machines identified as being compute-bound, that is, which use at least the dispatching time-slice amount of processor time without becoming nonrunnable. The dispatching time-slice is set at 50 ms for a System/370 Model 145 and is adjusted for other models based on the ratio of their speed to the Model 145 speed. In the case of a compute-bound virtual machine, the following value is added to the deadline priority for use in ordering the virtual machine on the dispatch list:

The current TOD is taken as the time it was first determined that the virtual machine was compute-bound.

Virtual machines identified as interactive are not necessarily placed at the top of the dispatch list, although you can ordinarily expect to find them there. The virtual machine that is found at the top of the list is ordinarily one that: 1) has been waiting the longest since it was dropped from queue or 2) has the smallest current resource utilization. Normally, interactive virtual machines satisfy both of these requirements.

Projected Working Set Size Estimate Feedback Control

A performance monitoring routine (DMKSTP) is invoked at intervals between 15 and 60 seconds (the interval depends on the processor speed and the multiprogramming level). This routine calculates among other things: a) the smoothed activity values used in the INDICATE LOAD command response, b) the average queue delay, c) the average processor resource utilization, and d) the "ideal" processor per page read.

Part of the response to the INDICATE LOAD command is a value that estimates the paging load on the system due to page contention, expressed as a percent of total system resources. The difference between this percent number and the SET PAGING value is divided by the number of pages read during the interval. This percent (positive or negative) is multiplied by the measurement interval and the resulting product is added to the "ideal" processor time per page fault.

The "ideal" processor time per page read should control page contention by controlling the working set size estimates. Page contention can be reduced by reducing the level of multiprogramming. High levels of page contention (with respect to the SET PAGING value) cause the "ideal" processor time per page read to increase. Eventually, increases in this "ideal" value cause all working set size estimates to increase, which should lead to decreases in the average multiprogramming level. The reverse situation, where page contention is lower than the SET PAGING value, causes a reduction in the "ideal" value, leading to smaller working set size estimates and possibly higher levels of multiprogramming.

Dispatcher Fast Path

The dispatcher fast path is software support that allows certain users running in the appropriate real and virtual machine states to take a shortcut through the code in DMKDSP. It eliminates unnecessary interrogation of users by the dispatcher before they are run, thus reducing the average time a user spends in DMKDSP. A user can take the fast path when the dispatcher determines, from an inspection of summary bits, that the user belongs to a class of virtual machines that do not need much checking before they are run.

The flags that indicate if a user is in the appropriate real machine environment to take the fast path are located in the byte DPSTAT in the PSA. If virtual machine assist is on the machine, this byte is initialized at IPL time to X'0F' plus the bits that reflect the actual machine state. If virtual machine assist is not on the machine, the state of the low order four bits is unpredictable, but since the DPMICON bit will be zero, the dispatcher fast path will not be activated anyway. The dispatcher fast path requires that each of the four bits are turned on. This indicates that the user is in a physical AP/MP environment with virtual machine assist on and the segment protection feature available, and that the TLB has not been used by a STBYPASS VR user since the last PTLB (purge translation lookaside buffer) instruction.

Bits Defined in DPSTAT:

DPMICON DPAPUOP DPSEGPRT DPOKTLB	X'80' X'40' X'20' X'10'	Indicates virtual machine assist is on. Indicates system is in AP/MP mode. Indicates system has segment protect feature. Indicates TLB is or will be OK when a virtual machine is dispatched. It is turned off when a STBYPASS VR user
		dispatched. It is turned off when a STBYPASS VR user is dispatched and turned back on when a non-STBYPASS VR user is dispatched.

The flags that tell the dispatcher if a user is in the appropriate virtual machine state to take the fast path are located in a byte in the VMBLOK called VMPATH. This byte contains three bits that reflect the user state, the interrupt history, and the locking history of the virtual machine. A user can take the fast path when all seven bits in DPSTAT and VMPATH are on. As the table below shows, a virtual machine is eligible for the fast path if it is in BC mode and if it is resuming execution after a simple interrupt such as an I/O interrupt, a disk diagnose, or a page fault.

Bits Defined in VMPATH:

VMVBCM	X'04'	Turned on to indicate a simple BC mode machine.
VMIHIST	X'02'	Turned on when the virtual machine is in a simple interrupt that leaves it eligible for the fast path. Turned off when the VMBLOK is locked more than once before the event that caused the interrupt has been completed.
VMLKHIST	X'01'	Turned on when the virtual machine has been locked. Turned off when an event which makes the virtual machine eligible for the fast path (such as a paging operation) has completed.

One action the dispatcher must perform to prepare a virtual machine to run is to acquire the VMBLOK lock. The logic for interfacing with the lock manager, DMKLOK, is contained in the subroutine LOCKVM.

When the lock manager is called, if the VMBLOK lock is obtained, DMKLOK maintains the VMLKHIST and VMIHIST bits as follows:

- If VMLKHIST is already on, turn VMIHIST off
- If VMLKHIST is off, turn VMLKHIST on.

Since both VMIHIST and VMLKHIST must be on in order for the virtual machine to take advantage of the dispatcher fast path, the virtual machine will be disqualified from the dispatcher fast path for its next dispatch if DMKLOK turns off the VMIHIST bit.

Every virtual machine has its VMIHIST and VMLKHIST set to B'00' at dispatch time, regardless of the previous settings.

When the subroutine LOCKVM is called and general register 1 is equal to LASTUSER, the lock manager is not called. Therefore, code equivalent to the maintenance of the history bits in the lock manager is included in the subroutine LOCKVM.

Another way the dispatcher fast path saves time spent in DMKDSP is by preserving the interrupt old PSWs in a field called VMECPSW after a program interrupt or an I/O interrupt. This support also saves real control register 6 in a field called VMC6SAVE after a program interrupt or an I/O interrupt.

Thus, if the virtual machine qualifies for the dispatcher fast path after it was interrupted by DIAGNOSE X'18', a page fault, or an I/O interrupt, the dispatcher does not have to reconstruct the appropriate PSW and control register 6 in order to run the guest. The correct data is available in VMECPSW and VMC6SAVE except for reflection of the correct condition code from the DIAGNOSE X'18' completion.

Fast Redispatch

DMKDSP also provides a fast dispatch path for virtual machines that have issued specific privileged instructions that are not handled by virtual machine assist.

These virtual machines can be dispatched very rapidly because the virtual machine's program old PSW needs very little reconstruction to redispatch the virtual machine, hence use of full PSW reconstruction path is not required. The decision for using the fast dispatch path (DMKDSPA) is accomplished by the module that handles privileged operation, DMKPRV or DMKVIO. A fast redispatch path is also available after I/O interrupts. If DMKDSP can determine that the I/O interrupt processing had no effect on the running virtual machine's status and it caused no higher-priority virtual machine to become runnable, then the virtual PSW stored at the I/O old PSW location will be used to redispatch the virtual machine.

Enable Window

The CP supervisor runs disabled for all I/O and external interrupts. The dispatcher, in order to alleviate part of this problem, will temporarily enable for interrupts and then disable. There are three occasions when the dispatcher enables for interruptions (enable windows):

- 1. When an enabled wait state is entered.
- 2. When an enabled problem state is entered to run a virtual machine.
- 3. When another part of the supervisor is to be entered via the unstacking of an CPEXBLOK.

On occasion 3, if the dispatcher finds a CP request block to unstack, it first enables then disables for I/O and external interruptions before unstacking the request.

Favored Execution Options

When the resources of the processor (and real storage) are being allocated, the dispatching and scheduling functions are implemented in such a manner that options exist which allow an installation to designate that certain virtual machines are to receive preferential treatment.

The favored execution options allow an installation to modify the algorithms described above and force the system to devote more of (or a specific portion of) its resources to a given virtual machine than would ordinarily be the case. The options provided are:

1. The favored execution option.

2. The favored execution percentage.

The favored execution option means that the virtual machine so designated is never to be dropped from the active (in-queue) subset by the scheduler as long as it remains runnable. When the virtual machine is executable, it is to be placed in the dispatchable list at its normal priority position. However, any active virtual machine represents either an explicit or implicit commitment of main storage. An explicit storage commitment can be specified by either the virtual = real option or the reserved page option. An implicit commitment exists if neither of these options are specified, and the scheduler recomputes the virtual machine's projected working set at what it would normally have been at queue-drop time. Multiple virtual machines can have the basic favored execution option set. However, if the combined main storage requirements of the favored virtual machines and the other runnable virtual machines exceed the system's capacity, performance can suffer due to thrashing.

The basic favored execution option removes the primary source of elapsed time stretch-out in a loaded time-sharing environment. However, if the favored task is highly compute-bound and must compete for the processor with many other tasks of the same type, an installation can define the processor allocation to be made. In this case, the favored execution percentage option can be selected for the virtual machine. This option specifies that the selected virtual machines are to receive a given minimum percentage of the total processor time, if it can use it. It modifies the deadline priority to specify a fixed interval between queue drops instead of one proportional to system load, number of active virtual machines, current resource utilization, etc. The deadline priority is modified in the following way:

- 1. The in-queue time slice is divided by the requested percentage to arrive at the fixed stretch-out interval.
- 2. The calculated interval is added to the TOD at queue drop to arrive at the next expected queue drop TOD (or deadline).

These options can impact the response time of other virtual machines. Both favored options can be applied to any number of virtual machines, either in conjunction with one another or separately.

Dispatching and Scheduling Support Routines

Most of the routines in the CP nucleus are reenterable and multiple control program or virtual machine tasks can make use of one routine at the same time. However, there are certain areas where requests for a resource must be serialized (as in paging) or delayed while previous requests are serviced (as in requests to schedule I/O).

The CP Request Stack

The routine handling the request obtains a CPEXBLOK from free storage and stores the caller's registers in it; when the requested resource is free, the CPEXBLOK is stacked for the dispatcher via a call to the request stack manager (DMKSTK). The dispatcher unstacks the block and exits to the requesting routine the next time it is entered. I/O requests are stacked in the same manner, except that the stacking vehicle is the IOBLOK, and return is passed to the address specified in the interrupt return address (IOBIRA). In either case, it should be noted that the dispatcher always unstacks and gives control to any stacked IOBLOKs and CPEXBLOKs prior to dispatching a user. This guarantees that CP information needed by a virtual machine (such as page availability) is always as up to date as possible.

CP Spooling

The spooling support in CP performs three functions.

- Simulates the operation of the virtual unit record devices that are attached to each user's virtual machine configuration. The simulation is done in such a way that it appears to the program in the virtual machine that it is controlling a real unit record device. This support involves the interception and interpretation of virtual machine SIOs, the movement of data to and from the virtual machine's virtual storage space, and the reflection of the necessary interruption codes and ending conditions in PSWs, CSWs, and sense bytes. This support is provided by the virtual spooling executive.
- Operates the real unit record equipment, attached to the system, that transcribes virtual machine output spool files to the real printer or punch and input from the real card reader to DASD storage. This function is provided by the real spooling executive.
- Provides an interface among the virtual machines, the system operator, and the spooling system so that the location, format, priority and utilization of the systems spooling data and resources can be controlled.

Spool Data and File Format

Data Format

The buffers that collect and write spool data are all one page (4096 bytes) in length, and contain the data to be transcribed and all CCWs necessary for operating the unit record devices that perform the transcription. The data is provided in the exact format required with no compression except that trailing blanks are suppressed. A 2-byte field at the end of the data CCW contains the length of the original data (including the truncated blanks). The first two doublewords of each buffer contain linkage information described below, followed by the data and CCWs, except for the first spool buffer which contains 3800-related information.

Spool files created on virtual spooled 3211, 3203, 3262, or 3289E, or 4245 printers may contain the following CCWs:

Load FCB (X'63') Fold (X'43') Unfold (X'23').

Spool files created on a virtual 3800 may contain the following CCW's:

Load Forms Control Buffer (X'63') Load Translate Table (X'83') Load WCGM (X'53') Load Copy Number (X'23') Load Copy Number (X'23') Load Graphic Char. Mod. (X'25') Load Copy Modification (X'35') Initialize Printer (X'37') Clear Printer (X'87') Select Translate Table 0 (X'47') Select Translate Table 1 (X'57') Select Translate Table 1 (X'57') Select Translate Table 2 (X'67') Select Translate Table 3 (X'77') End of Transmission (X'07') Mark Form (X'17').

In addition, since the data associated with the Load Graphic Modification (X'25') and Load Copy Modification (X'35') CCW's may be longer than 4080 bytes. This data may appear in successive DASD buffers. This is accomplished by setting the "Data Chaining" bit in the CCW associated with each section of data except the last one.

Each spool logical record (card or print line) is stored as one CCW that moves data (READ or WRITE), a TIC to the following CCW, and the full data record. Space is left at the end of each buffer so that a SENSE

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command can be inserted to force concurrent channel end and device end. For card punch channel programs there is an additional back chain field that points to the card previously punched so that error recovery for punch equipment checks can back up one card. The only exception to the format of READ/WRITE-TIC-Data is in buffers of files directed to the printer. In this case, immediate operation code CCWs (skips and spaces) are followed by the next CCW.

File Format

In addition to the data and CCWs contained in each spool buffer, the first two doublewords contain forward and backward links to the next and previous buffers in the file. This two-way linkage allows the file to be backspaced or restarted from any point at any time. The exception to this are spool files that contain 3800-related CCW's. These files can only be restarted from the beginning of the file. Also, it means that if I/O errors are encountered while reading one buffer, the file is put in system hold status. If purged, all buffers except those in error are released. The two-way chain allows this control of the file while preventing fragmentation by allowing pages to be assigned and released individually regardless of their ownership.

The first spool buffer of an output spool file contains a special data record called the tag record. This record immediately follows the two doublewords containing the forward and backward buffer linkage pointers. The tag record allows VM/SP HPO users to specify information to be associated with spool files that they generate. The information is entered via the CP TAG command, although the tag record is not considered a spool file data record and is not printed or punched as part of the spool file. However, the contents may be interrogated via the CP TAG QUERY command. There are two fields at the end of the buffer to identify the number of buffers in the file and the size of the largest data written in the file.

The format of the tag record is an NOP CCW, followed by a TIC to the next CCW and a 136-byte data field. Any blanks in the NOP CCW will not be truncated. Therefore, the 2-byte length field at the end of the CCW will always contain X'0088' (decimal 136). To differentiate the tag record from an immediate NOP CCW (no TIC-data sequence) independently of the command code, the "skip" bit (bit 35) in the CCW has the following convention:

Bit 35 = 0 for NOP CCW, TIC, data (tag record)

= 1 for NOP CCW (immediate NOP command)

Each spool file in the system is controlled by a spool file control block (SFBLOK). Printer and punch SFBLOKS are resident in free storage. Closed reader SFBLOKs are found in a virtual address space, SYSSPOOL, created specifically for them. While the file is open, these blocks are chained from the devices (either real or virtual) that are processing the file. When a printer or punch file is closed, it is chained from the device type file anchor (DMKRSPPR for printer files, DMKRSPPU for punch files). When a reader file is closed, it is moved from real storage to a user-unique spool file chain in SYSSPOOL's virtual storage, anchored in the Reader Hash Table. This table contains pointers to a user's first reader file. When a user is logged on, the VMBLOK points to the user's Reader Hash Table entry. Each SFBLOK contains information about the file that describes its owner and originator (these can be different for transferred files), the filename and filetype, and the class and number of copies for output files. All of these attributes can be examined and most can be changed by the file's owner or the system operator. The SFBLOK also contains information such as the starting and ending buffer addresses for the file, the starting address and the length of external attributes buffer (if it exists), the record size, certain file status flags, destination, and so on.

Spool Buffer Management

Real/Virtual Storage Management

Buffers that temporarily store spool data on its way between DASD secondary storage and the user's machine are allocated from a pool of virtual storage space that belongs to CP. The size of this pool varies with the real storage available to VM/SP HPO (the storage specified at system generation or actual real storage, whichever is less). Allocation is as follows:

Storage Size Available	Virtual Buffer Allocated
384K to 655,360 bytes	128
655,361 bytes to 1.1 megabytes	320
1.1 megabytes to 3 megabytes	640
over 3 megabytes	1280

Virtual storage buffers are allocated in 1-page increments by DMKPGT at the time the spool file is opened for either input or output. If no virtual storage space is available, the virtual machine is terminated with an abend. This places limits on the number of concurrent spooling operations permitted by the system because spooling operates as a high-priority task.

Real storage is not allocated for a spooling buffer until a virtual machine actually issues a SIO that attempts to transfer data between the buffer and the user's virtual storage space. At this time, a page of real storage is allocated to the buffer via the real storage paging manager. The buffer is locked in main storage (that is, is unavailable to be paged out) only for the amount of time necessary to transfer the data. After the data transfer is complete, the buffer is treated as a normal page of virtual storage, and can be selected to be paged out. This ensures that low-usage spool files do not have buffers in real storage, while the buffers for high-usage files should remain resident. (Two spool file buffers are maintained for printing on a real 3800 printer.)

DASD Space Allocation

While a spool buffer is inactive, it resides in real storage or on the paging device. After it has been filled with data from the virtual machine or a real input reader, it is written to a page of secondary DASD storage. The allocation of pages on the spooling disk(s) is managed by DMKPGT, which handles requests for both pages of virtual storage and semipermanent spool file residence. DMKPGT maintains a separate allocation block chain for spooling pages. Each block contains control information and a bit map that allocates pages on a single cylinder. If none of the cylinders allocated have any available pages, DMKPGT enters its cylinder allocation routine. See the section "Cylinder Allocation" for more information.

Paging Device Support: All actual I/O for the page buffers on any device is controlled by the paging I/O executive DMKPAGIO.

Virtual Spooling Manager (DMKVSP)

The two functions of the virtual spooling manager are (1) to simulate the operation of all spooled unit-record devices attached to the user's virtual machine, and (2) to read and write the spool files associated with those devices. The following virtual devices are supported for spooling, with the exceptions noted:

- IBM 2540 Card Reader/Punch, except for punch feed read and column binary
- IBM 3203 Printer Model 4 and Model 5 (132 positions)
- IBM 3262 Printer Models 1, 5 (in 3262-1 Emulator Mode), and 11 (132 positions)
- IBM 1403 Printer Models 2 and N1 (132 positions)
- IBM 3211 Printer (150 print positions)
- IBM 3505 Card Reader (except for mark senses reading)
- IBM 3525 Punch (except for the card read, print, and data protect features)
- IBM 3800 Printing Subsystem (204 print positions)
- IBM 4245 Printer Model 1 (132 positions)
- IBM 4248 Printer Model 1 (168 positions).

The following consoles are supported for spooling when entered into the directory as the virtual system console:

- IBM 1052 Printer-Keyboard Model 7 (via the 2150 Console)
- IBM 3210 Console Printer-Keyboard Models 1 and 2
- IBM 3215 Console Printer-Keyboard Model 1.

All virtual printers, except 3800, must have the universal character set feature. No checking is done on the spooled printer data. There is, however, an exception to this. On a virtual 3800 all Load Checks and Data Checks are reflected to the virtual machine unless

- Explicitly disabled via the NODATCK parameter of the DEFINE command or
- Explicitly disabled via a 'Block Data Check' CCW or if
- A 'Select Translate' CCW has associated with it a specified character arrangement table in the spool command.

However, any UCS buffer commands issued by the virtual machine (load UCS buffer, block data checks, etc.) are ignored. It is up to the user and the installation to ensure that the output is directed to the proper real printer via use of the output CLASS and FORMS described below. For the 3203, 3211, 3262, 3289E, 4245, or 4248 printer, forms control buffer (FCB) commands are accepted and simulated by means of a virtual FCB maintained by the executive. For the 4248 printer, the extended FCB is also accepted. The use of the virtual FCB is the only way to simulate end-of-form conditions reflected by the detection of a channel 9 or 12 punch. The LOAD FCB command and its associated data (FCB image) are also captured in the spool file. When the file prints on a real 3203, 3211, 3262, 3289E, 4245, or 4248 printer, according to the file FCB, CFILEFCB, or DEFFCB option on the START command, the FCB image is sent to the real printer and controls channel skipping. If the file prints on a real 1403, the FCB is ignored. Since there is no provision for FCB loading, the device is carriage tape controlled.

The FOLD and UNFOLD commands are also captured in the spool file for a virtual spooled 3203, 3211, 3262, 3263, 3289E, 4245 or 4248, and sent to the real printer when the file is processed (unless it is a real 1403 printer).

For a virtual 3800, all Load and Control CCWs are included in the spool file and issued when printed on a real 3800. When that file prints on another device (such as 1403, 3211, 3262, 3289E, 4245 or 4248) these CCWs are changed to NO-OPs and therefore ignored.

If any of the unsupported unit record features are required, the real device must be attached directly to the user's virtual machine. Thus, a 3505 reader could be a spooling input reader, but attached directly to a batch virtual machine when it is necessary to read mark sense cards.

If a 3211-type printer is started with the DEFFCB option, all LOAD FCB commands imbedded in a spool file are NO-OPed (that is, they are not sent to the printer).

Note: If a spool file contains an extended FCB, it can only be printed on a real printer that supports the extended format (the 4248) or on a real printer that NO-OPs the LOADFCB command.

Output File Processing

DMKVSP receives control from the virtual I/O executive, DMKVIO, when the user's machine issues a SIO to a spooled unit record device. DMKVIO does not pass control until it has been determined that the device is available (that is, it is not busy and has no interruptions pending). DMKVSP first determines if the device is currently processing a file. If it is, processing continues. If this is the first command issued by the given device, a new output file must be opened. An open subroutine is called to build the control blocks necessary to manage the file and to obtain virtual storage and DASD buffer space. Control is then returned to DMKVSP.

Before the first record of an output spool file is written, DMKVSP writes a tag record (NOP CCW, TIC, data sequence) and initializes the 136-byte data area to blanks. It then sets the spool buffer displacement pointer to the first doubleword in the buffer beyond the tag record. DMSVSP then analyzes and interprets the channel program associated with the virtual machine's SIO. Each CCW is tested for validity of command, address, flags, alignment, protection, etc., and if the CCW is valid, the virtual machine's data is moved from his own virtual storage space to the buffer in the spooling virtual storage. When this buffer is full, it is written to a page of DASD secondary storage and a new buffer is obtained. The interpretation of the virtual machine's channel program continues until there are no more CCWs or until an error condition is detected that prohibits further processing. In either case, the device is marked as having the proper interruptions pending, a CSW is constructed, and DMKVSP exits to the main dispatcher. In contrast to nonspooled I/O, the virtual machine has remained in a pseudo-wait (IOWAIT) for the time it took to interpret the entire channel program.

The output file can be logically closed by the virtual machine either by issuing an invalid CCW command code, or by the CP CLOSE command. In either case, DMKSPL checks for tag record information and 3800-related information in the VSPXBLOK. (The VSPXBLOK, pointed to by the VDEVEXTN field of the VDEVBLOK for the output spool device, contains the tag information entered via the CP TAG command.) If tag data exists, the first spool buffer for the file is read in, the tag data is inserted in the tag record, and the buffer is rewritten to DASD storage. If no tag data exists, the tag record data field is left blank. The first buffer is read in, and the number of buffers and the size of the largest CCW are put into the first SPLINK. The device is then cleared of pending interruptions, the file chains are completed, and the file either is queued for output on a real device of the proper type (printer or punch) or, if TRANSFER is in effect, is queued for input to another virtual machine.

The 3800-related information includes:

CHARS - character arrangement table MODIFY - copy modification name FCB - file control block FLASH - flash count overlay use.

This information is contained in the VSPXBLOK for a virtual printer. When the file is closed, the information is contained in the first DASD buffer.

Input File Processing

Input file processing is similar to output file processing, except for the open and close functions, and the analysis of CCW commands and the direction of data movement. Many common routines are utilized to locate and verify CCWs, obtain buffer space, and to move the spooling data.

The difference in the open function is that, instead of creating a new file, it is necessary to locate a reader file that already exists in the system. To do this, the open subroutine scans the user's reader SFBLOK chain, anchored in the Reader Hash Table. If a file is not found, a unit-check or intervention-required condition is reflected to the virtual machine; otherwise, its SFBLOK is chained to the control block for the reader and the channel program is interpreted in the same manner as for an output file.

After the input file is exhausted, a unit exception is reflected to the user machine, unless the user has requested either continuous spooling or that an EOF not be reflected. With continuous spooling, the unit exception is not reflected until the last file for that virtual machine is processed. If NOEOF is specified, the simulation terminates with a unit check or intervention-required condition (similar to what happens if the EOF button on a real reader is not pushed).

In either case, the input file is then deleted from the system, unless the user has specifically requested that his input files be saved. If the file is saved, it can be re-read any number of times.

Virtual Console Spooling

Support of virtual console I/O for both the virtual machine and VM/SP HPO is provided as an option for the spooling capabilities. This support fulfills the following requirements:

- Provides hardcopy support for CMS Batch Facility virtual machines.
- Provides hardcopy support for display devices used as system or virtual machine consoles.
- Allows disconnected virtual machines to spool virtual console output, CP commands and system resources to disk instead of losing the output.
- Improves the performance of virtual machines that currently produce a large amount of console output.

Whenever a SIO is issued to a virtual machine console, the virtual console manager (DMKVCN) determines if the spooling option is active. If it is, control is passed to DMKVSTVP (via DMKQCN and DMKQCO) to insert the data into a spool file buffer. While console spooling utilizes, basically, the same code as printer spooling, the following exceptions are made:

- A skip to channel 1 CCW is inserted after every 60 lines of output.
- The operator's virtual console spool buffer is written out after every 16 lines of output.

• The virtual spool buffer is written out to the allocated spool device when the first CCW is placed in that virtual buffer. The linkage area of the virtual spool buffer takes the form of a CLOSE file to allow checkpoint (DMKCKP) to recover the active spool file in the event of a shutdown because of system failure. If data in the virtual buffer has not yet been written to the spool device, it will not be recovered.

To maintain a pseudo closed file status for console spool files, DMKSPL now assigns spool identifications to all output spool files where they are first queued.

A virtual system reset, device reset, or IPL *does not* close the virtual console spool file. The LOGOFF, FORCE, or DETACH of virtual console commands *does* close the virtual console spool file. The SHUTDOWN command does close the operator's console spool file. If the SHUTDOWN command is issued by a Class A user other than the operator, the console spool file for both the user and operator is closed.

The inclusion of the spool file tag record in a virtual console spool file is processed by DMKVSP and DMKSPL as described for printer spool files in "Output File Processing" under "Virtual Spooling Manager." Virtual console I/O is not spooled when the TERMINAL CONMODE 3270 function is used and when virtual channel command codes X'19', X'29', and X'2A' are used by full screen applications.

Real Spooling Manager (DMKRSP)

Command chaining is used for all unit record channel programs so that the devices are running at their maximum speed with a minimum of interruptions. In addition, because of the high speed of the 3800 Printing Subsystem, a double-buffering arrangement is used to write output to it. All other real spooling devices utilize a single output buffer.

Output File Processing

Both the input and output operations of DMKRSP are interruption driven. Thus, DMKRSP does not process unless an internally or externally generated not-ready to ready device end interruption occurs. External interruptions are generated by the hardware in the normal manner, while internal, "pseudo interruptions," are generated by the software when an output file has been queued on the real printer or punch file chain, or when the operator issues a START command to a drained device.

Upon receipt of the initial device end for a printer or punch, DMKRSP searches the appropriate file chain for the SFBLOK of a file whose class, form, and destination match those of the device that was made ready. If FLASH is specified for a 3800 printer, the flash overlay name must also match. In addition, the location of 3800 Load CCWs within the spool file will determine whether it is eligible for printing on the given output device. If the device is in AUTO or SETUP mode, first preference is given to the current form. If no file with the current form exists, a file with a different form is selected, and a MOUNT REQ message is sent to the operator. On the next device end, processing will continue. When the SFBLOK is located (provided the file is not in a hold status), it is unchained from the

output queue and chained to the real device block that services the file. A page of real main storage (two pages for a 3800 printer) is then obtained for use as a buffer, and the output separator routine (DMKSEP) is called to print output identifier pages. DMKTCS and DMKTCT are then called to set up the 3800 for printing that file. When DMKSEP returns control to DMKRSP, the first buffer of the file is paged into real main storage, and the CCWs in the channel program that it contains are adjusted so that their data addresses correspond to the real addresses at which the data resides. To reduce the number of channel commands that cause movement of the printer carriage, DMKRSP also performs CCW optimization. Whenever a sequence of two carriage control commands can be replaced by a single equivalent command, the first CCW is replaced by the equivalent command and the second is changed to a no-op. For example, if a "Write and Space 1 line" command is followed by an "Immediate Space 2 lines" command, DMKRSP will replace them with a single "Write and Space 3 lines" command. The real SIO supervisor (DMKIOSQR) is then called to start the channel program, and DMKRSP exits to the dispatcher (DMKDSPCH) to await the interruption.

In SETUP mode, only a page of data at a time is printed until the START command is entered again. Then the entire file is printed.

When the channel end/device end interruption for the completed buffer is unstacked to DMKRSP, the forward chain file link field locates the next buffer. This buffer is paged-in, and the process is repeated until the final buffer is processed. At this point, the number of copies requested for the file is decremented. If the number of copies is 0, processing is terminated and the file is deleted from the system; otherwise, the process is repeated as many times as necessary. For a 3800 printer, double buffering is maintained so that the second buffer is filled while the first buffer is being printed.

When file processing is completed, a scan of the appropriate output queue is again made, and if a file is found it is processed. If the queue is empty, or if a file with a matching class, form, or destination is not found, an exit is taken to DMKDSPCH to wait for another ready interruption. If a 3800 device is used, the file is placed on the 3800 delayed purge queue. If this queue reaches maximum size, the oldest file in the queue is deleted from the system.

Output file processing can be modified by either the system operator, by a spooling support command or as a result of system errors. The operator commands allow a given file to be backspaced or restarted, and the files of individual users or the whole system to be held and released for output. I/O errors also affect the spooling system, and a description of how they are processed is in the section "Spool File Error Recovery."

Input File Processing

Reader file processing is initiated by the receipt of a device end interruption from a spooling card reader. No explicit operator command is required to start the processing of an input file. When the device end is unstacked to DMKRSP, a call is made to DMKRSTIN to handle the input process. DMKRST contains an open subroutine that is called to build the necessary control blocks and to obtain the virtual, real, and DASD buffer space required for the file. A channel program to read 41 cards is built in the buffer, and DMKIOSQR is called to start the reader.

When the interruption for the first buffer is unstacked, the first card is checked for its validity as a userid card. The minimum information that this card must contain is the userid of the owner of the input file. It may appear anywhere on the card, with the restriction that it must be the first information punched. Optional information on the userid card can include a filename and type and/or the class of the virtual card reader to which the file is to be directed. If the userid is valid, the file processing continues; otherwise, the operator receives an error message and processing is terminated.

After each file buffer is read, it is written onto disk by the paging I/O routines in the same way that virtual output files are handled. When a unit exception signaling physical end of file is received from the reader, the file is closed by writing the final buffer to disk and completing and queuing the SFBLOK to the user's reader file chain. If the owner of the file is currently logged on, he is given a message indicating that a file has been read and if he has an available card reader, it is posted with a device end interruption. An available reader is one of the correct class which is ready, is not busy, has no active file, and has no pending interruptions.

As DMKRST completes each phase, it returns to DMKRSP for an exit to the dispatcher (DMKDSP).

Accounting Card Processing

1

Various routines in CP accumulate, format, and store account records that contain system usage information for certain users. These routines format the information into an 80-column card image record.

In addition to the records generated by CP to account for a virtual machine's use of system resources, the user may request records in order to account for the use of virtual machine resources by jobs running under his userid. In order to do so, the user must have the account option (ACCT) entered into the directory.

The user can issue a code X'004C' DIAGNOSE instruction with a pointer to either a parameter list containing user-specified "charge to" information, or a data area containing up to 70 bytes of user-specified information to be included in the accounting record. DMKHVC validates the instruction operands, builds an account buffer (ACNTBLOK), and DMKACOQU is called to put the records in spool format. For additional information about this user option, see "DIAGNOSE Interface (DMKHVC)" under "Privileged Instructions."

Spooling Commands

The spooling commands provide an interface between the user, the system operator, and the spooling system. There are three types of spooling commands:

- Those that affect virtual devices
- Those that affect real devices
- Those that affect spool files that are queued within the system.

The commands that affect virtual devices are generally available to all system users, and a user can only affect the status of devices that are attached to his own virtual machine. Commands that affect the status of the real system's spooling devices can be used by the system operator only. Commands that affect closed spool files that are awaiting processing are generally available to all users, with some additional capabilities assigned to the system operator. For example, a user may alter the characteristics only of those files that have an owner's userid that matches his own, whereas the system operator may change any spool file in the system.

File States and Attributes

Each spool file in the system has a number of attributes that are assigned to it, either explicitly or by default, at the time that it is created. These attributes and their values are as follows:

- The filename and filetype can be either a 24-character field or two 8-character fields (one each for filename and filetype). Any of these fields can be replaced by a user-supplied value.
- The spool ID is a user-unique number between 1 and 9900. It is automatically assigned when the file is created (opened). The file's owner, the device type, and the ID number are specified. Usually, the userid defaults to the identification of the user issuing the given command. (When a class G user refers to a spool ID, that user's userid is the default. Privileged users, however, must specify a userid and a spool ID when referring to a specific spool file.)
- The number of logical records (cards or print lines) in the file is an integer between 1 and 16 million. For printer files, the record count also includes any immediate operation code space or skip CCWs.
- The originating user is the identification of the file's creator, if the file has been internally transferred from the originator's printer or punch to the new owner's card reader.
- The number of copies requested for an output file is between 1 and 255. Unless altered by the user or operator, it defaults to 1.
- The device type is used by DIAGNOSE for a file transferred to a reader to determine the virtual type of output device.

- CHARS for 3800 printer.
- FCB for 3800 printer.
- MODIFY for 3800 printer.
- FLASH for 3800 printer.

In addition to those attributes, a file that is queued for real output or virtual input always has a class associated with it. A class is a single alphanumeric character from A through Z or from 0 to 9. It controls both the real or virtual device on which the file will be printed, punched, or read, and the relative priority and sequence of output on the device. While each file is assigned a single class, each real spooling output device can be assigned from one to four classes. The device then processes only files that have a class attribute that corresponds to one of its own, and processes these files in the order that its own classes are specified.

For example, if a printer is assigned the classes A, D, 2, it processes any printer file with a class of A before it searches the printer output queue for a file with class D. All class D files are printed before class 2 files.

The output class for a file is assigned at the time the file is created and is the class that is associated with the virtual device that created it. While each real spooling device can have up to four classes, each virtual spooling device can have only one. When a user logs on to the system, the class associated with a device is the one defined in his directory entry for that device. However, he can alter this class at any time by the SPOOL command. As files are created and closed by a device, they take on the device's output class.

Each file also has an operator form and a user form. These are one-to eight-characters each. The user form is defined by the user. There is a system default user form for each spool file type (printer, punch, or console). The operator form is determined from the user form by a table lookup. The table is defined by the SYSFORM macro at CP system generation. The SYSFORM macro also specifies the default form.

The operator may start a spool device in MANUAL mode for a certain form. The system will then process only spool files with that operator form. AUTO mode is also available. In this mode, all spool files are grouped by form, and each group is processed with the operator being prompted each time a new group is processed.

Each printer, punch, and console file has a destination (DEST is the keyword) assigned to it. The default is OFF, unless a DESTination is set via the SPOOL, CHANGE, or CLOSE command. The DESTination value is a 1- to 8-character alphanumeric name that your installation assigns.

Individual spool files and virtual printers and punches have one DESTination. Real printer and punch spooling devices can have as many as four DESTinations. For example, a real printer can be started such that it handles files for DESTinations OFF, PRT1, PRT2, and PRT3. The same operator and user commands that use CLASS and FORM can be used to control DESTination.

After they are closed and are awaiting output, their class, form, and destination can be changed by a CHANGE command issued either by the file's owner or by the system operator. The system operator can alter the system-generated output class(es) of a real output device by the START command.

Output files transferred to a user's virtual reader can also be controlled by class. If the receiving user has several readers, the input to each can be limited to files of a certain class. In addition, the ORDER command allows sequencing of input files by class as well as spoolid number.

Output priorities can also be managed by altering the hold status of a file. Individual users can alter the hold status with the CHANGE command, while the system operator can change (hold or free) the files of specific individual users.

SPOOL and CHANGE commands can be used to modify the CHARS, FCB, MODIFY, and FLASH attributes of a file or a virtual printer.

Imbedded LOAD FCB commands also affect the printer selection. A spool file with an imbedded LOAD FCB command is eligible only for output to a printer that can handle the FCB length, or a printer that will NOOP the LOAD FCB command (like the 1403).

The operator may start a 3211-type printer in DEFFCB mode to remove the FCB restriction. DEFFCB causes DMKRSP to NOOP all LOAD FCB commands that are found in the spool file. A file that would normally be incompatible can then be printed.

Virtual Device Spooling Commands

These commands affect the status of a user's virtual spooling devices:

Command Meaning

CLOSE Terminates spooling operations on a specified device. It clears the device of any pending interrupt conditions, and for output files, updates the tag record, completes and queues the file for real output. Optional operands allow the user to specify a filename and filetype, and to override for the given file any standard FORM, HOLD/NOHOLD or COPY operands set into the output device by the SPOOL command.

SPOOL Establishes the file attributes that apply to files created on, or read by, the given device. It establishes the class and form that will be in effect, whether: files are to be automatically held, input files are to be saved or purged after reading, and output files are to be directed to the real system printers and punches or are to be transferred to a user's virtual reader. The SPOOL command also specifies 3800 attributes. Note: The SPOOL command invokes the access control verification routine. See the VM/SP HPO Planning Guide and Reference or VM/SP HPO CP for System Programming for more information. See also Appendix D for further information on the access control verification modules.

Real Device Spooling Commands

The operator can use these commands to control the activity of the real spooling devices:

Command	Meaning
BACKSPAC	Backspaces an active spooling device for either a specified number of pages (printers only) or to the beginning of the file (printers or punches).
DRAIN	Stops the operation of a specified output or input device after it has finished processing the file on which it is currently working. A printer must be drained prior to the issuance of the LOADBUF command. Unit record devices are normally drained prior to system shutdown.
START	Restart a device after it has been drained. Options allow the operator to specify the spooling output class, form, destination, and mode for the output device type of virtual 3800 files to be printed and output separator records. For a 3800 printer, the IMAGE CHAR, FCB, and PURGE options may also be specified.
FLUSH	Immediately halts the output on the specified device and either flushes that copy of the file from the system, or puts it into the system hold status for future processing.
REPEAT	Supplements the number of copies requested by the user for the file when it was created. The operator can specify a number from 1 to 255 that is added to the number specified by the user.
LOADBUF	Loads the universal character set buffer of the FCB of the specified printer with the specified image. If requested, the system verifies the loading by printing its contents on the affected printer.
SPACE	Forces the output on the specified printer to be single spaced, regardless of the skipping or spacing commands specified by the file's creator.
0	

Spool File Management Commands: The spooling commands alter the attributes and status of closed spool files that are queued and awaiting processing. When a command applies to an individual file, the device type (RDR, PUN, PRT) and the spoolid number must be provided to identify the file. In most commands requiring a spoolid, the keyword CLASS, FORM, or DEST followed by a valid spool class or form or the keyword ALL is an acceptable substitute for the spoolid number. This causes the command to

be executed for all files of the given class, form, or device type. The userid is the identification of the user issuing the command, except that the system operator must explicitly supply the identification of the user whose files he wishes to affect or he must specify the keyword SYSTEM, which gives access to all files (valid for CHANGE, PURGE, ORDER, and TRANSFER commands also).

Command Meaning

CHANGE Changes the filename and filetype, the number of copies, the form, the destination, or the class of the specified file. The CHANGE command also specifies 3800 attributes. Any of the above attributes of a file can be determined via the QUERY command.

- HOLD Places, via the system operator, the specified file in a hold status. The file is not printed or punched and is released by the system operator. The operator can hold any user files by device type.
- FREE Opposite of the HOLD command. Allows a file or group of files that were previously held to become available for processing. However, the user cannot reset a hold that was set by the operator with the HOLD command.
- PURGE Removes unwanted spool files from the system before they are printed or punched.
- ORDER Reorders the input files in a virtual card reader. It can order files by identification number, by class, by form, or by any combination of the three.
- SPTAPE Dumps output spool files to tape or loads output spool files from tape.
- TRANSFER Transfers a closed spool file to a different queue (reader/printer/punch), to a different user, or both simultaneously. Also optionally reclaims spool files for the file's originator.

Notes:

- 1. The (user-unique) spool ID will change when a file is transferred from one user to another.
- 2. The TRANSFER command invokes the access control verification routine. See the VM/SP HPO Planning Guide and Reference or VM/SP HPO CP for System Programming for more information. See also Appendix D for further information on the access control verification modules.

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Spool File Error Recovery

Unit Record I/O Errors

I/O errors on real spooling unit record devices are handled by a transient routine that is called by DMKIOS after it has sensed the unit check associated with the error on a spooling device. If appropriate, a restart CAW is calculated and DMKIOS is requested to retry the operation, in some cases waiting for a device end that signals that the failing device has been made ready after manual corrective measures have been taken. If, after retrying the operation, the error is unrecoverable, DMKIOS is informed that a fatal error has occurred. DMKIOS then unstacks the interruption, flagged as a fatal error, and passes control to real spooling executive. The routines that handle unstacked interruptions in real spooling execute only module operations that have been completed correctly or those that are fatal errors. If a fatal error is unstacked, the recovery mechanism depends on the operation in progress.

For fatal reader errors, processing of the current file is terminated and any portion of the file that has been read and stored on disk is purged. The owner of the file is not informed of the presence of a fractional part of the file in the system.

For fatal printer or punch errors, the SFBLOK for the partially completed file is re-queued to the appropriate output list and processing can be resumed by another available printer or punch, or can be deferred until the failing device is repaired.

In any case, the failing device is marked logically offline, and no attempt is made by the system to use it until the operator varies it back online via the VARY command.

If an invalid load module is specified for a 3800 printer (refer to DIAGNOSE code X'74'), the file involved is held or purged, and the printer queue is searched for the next file to print. In addition, the user and operator are sent a message describing the action.

DASD Errors during Spooling

DASD I/O errors for page writes are transparent to the user. A new page for the buffer is assigned, the file linkage pointers are adjusted, and the buffer is rewritten. The failing page is not de-allocated and no subsequent request for page space is granted access to the failing page. If an unrecoverable error is encountered while reading a page, processing depends on the routine that is reading the file. If the processing is being done for a virtual reader, the user is informed of the error and a unit check/intervention required condition is reflected to the reader. If the processing is being done for a real printer or punch, the failing buffer is put into the system hold status, and processing continues with the next file. In either case, the DASD page is not de-allocated and it is not available for the use of other tasks.

DASD Spool Space Exhausted

If the space allocated for paging and spooling on the system's DASD volumes is exhausted and more is requested by a virtual spooling function, the user receives a message and a unit check intervention required condition is reflected to the virtual output device that is requesting the space, the output file is automatically closed and it is available for future processing. The user can clear the unit check and periodically retry the operation which will start when space is free or completely restart later from the beginning of the job. If the task requesting the space is the real spooling reader task, the operator receives an error message and the partially complete file is purged. Any time the spooling space is exhausted, the operator is warned by a console message and alarm. However, the system attempts to continue normal operation.

Recovery from System Failure

Should the system suffer an abnormal termination, CP attempts to perform a warm start. Spool file and device data, as well as other system information is copied from real storage to warm start cylinders on DASD storage. If any virtual machines were enabled for VMSAVE, they are stored on DASD as specified in the DMKSNT module. When the system is reinitialized, the spool data and other system data is retrieved from the warm start cylinders and operation continues.

If the warm start data in real storage was damaged by the abnormal termination, the warm start procedure recognizes the situation and notifies the operator that a warm start cannot be performed. Another recovery method would be to attempt a checkpoint start.

The spool file recovery routines (DMKCKS, DMKCKT, DMKCKV) dynamically checkpoint on DASD storage; the status of all open reader files, the status of all closed output files, real spooling device data, and system hold queue information. This information is stored on checkpoint cylinders that are allocated, along with warm start cylinders, at system generation.

When a checkpoint (CKPT) start is requested, spool file and spooling device information is retrieved from the checkpoint cylinders. Spool file blocks are chained to their appropriate printer, punch, or individual reader chains, record allocation blocks are reconstructed, spooling device status is restored, and system hold queues are chained to the proper devices. System operation then continues.

If the checkpoint start procedure encounters I/O errors or invalid DASD data on the checkpoint cylinders, the operator is notified. The FORCE option of the checkpoint start performs all the checkpoint start functions except that invalid or unreadable files are bypassed. While this is at best a partial recovery, the only other alternative is a cold (COLD) start, where all spool file data is lost.

Recovery Management Support (RMS)

The machine check handler (MCH) minimizes lost computing time caused by machine malfunction. MCH does this by attempting to correct the malfunction immediately, and by producing machine check records and messages to assist the service representatives in determining the cause of the problem.

The channel check handler (CCH) aids the I/O supervisor (DMKIOS) to recover from channel errors. CCH provides the device-dependent error recovery programs (ERPs) with the information needed to retry a channel operation that has failed.

This support is standard and model-independent on the external level (from the user's point of view there are no considerations, at system generation time, for model dependencies).

System Initialization for RMS

DMKCPJ calls DMKIOEFL to initialize the error recording at cold start and warm start. DMKIOEFL gives control to DMKIOG to initialize the MCH area. A store CPU ID (STIDP) instruction is performed to determine if VM/SP HPO is running in a virtual machine environment, or running stand-alone on the real machine. If the system is running in a virtual machine, the version code is set to X'FF' by DMKPRV. If the version code returned is X'FF', the RMS functions are not initialized beyond setting the wait bit on in the machine check new PSW (virtual). This occurs because machine check interruptions are not reflected to any virtual machine. VM/SP HPO, running on the real machine, determines whether the virtual machine should be terminated.

If the version code is not X'FF', DMKIOG determines what channels are online by performing a Store Channel ID (STIDC) instruction and saves the channel type for each channel that is online. The maximum machine check extended logout length (MCEL) indicated by the Store CPU ID (STIDP) instruction is added to the length of the MCH record header, fixed logout length and damage assessment data field. DMKIOG then calls DMKFRE to obtain the necessary storage to be allocated for the MCH record area (MCRECORD), the CP execution block (CPEXBLOK), MCHAREA, and MCEL. The address of MCHAREA is put in the PSA (AMCHAREA). Pointers to MCRECORD and the CPEXBLOK are put in MCHAREA. DMKIOG puts the address of MCEL in control register 15. DMKIOG obtains the storage for the I/O extended logout area and initializes the logout area and the ECSW to ones. The I/O extended logout pointer is saved at location 172 and control register 15 is initialized with the address of the extended logout area. The length of the CCH record and the online channel types are saved in DMKCCH. It should be noted that the ability of a processor to produce an extended logout or I/O extended logout and the length of the logouts are both model- and channel-dependent. 308x processors do not store a machine check fixed logout, machine check extended logout, or a region code. If VM/SP HPO is being initialized on a Model 165 II or 168, the 2860, 2870, and 2880 standalone channel modules are loaded and locked by the paging supervisor and the pointers are saved in DMKCCH. If VM/SP HPO is being initialized on any other model, the

integrated channel support is assumed; this support is part of the channel control subroutine of DMKCCH. Before returning to DMKIOE, the VM/SP HPO error recording cylinders are initialized. DMKIOE passes control back to DMKCPJ and control register 14 is initialized with the proper mask to record machine checks.

Overview of Machine Check Handler

A machine malfunction can originate from the processor, Vector Facility, real storage, or control storage. When any of these fails to work properly, the processor attempts to correct the malfunction.

When the malfunction is corrected, the machine check handler (MCH) is notified by a machine check interruption and the processor logs out fields of information in real storage, detailing the cause and nature of the error. The model-independent data is stored in the fixed logout area and the model-dependent data is stored in the extended logout area. The machine check handler uses these fields to analyze the error, format an error record, and write the record out on the error recording cylinder of SYSRES.

If the machine fails to recover from the malfunction through its own recovery facilities, the machine check handler is notified by a machine check interruption. An interruption code, noting that the recovery attempt was unsuccessful, is inserted in the fixed logout area. The machine check handler then analyzes the data and attempts to keep the system as fully operational as possible.

Recovery from machine malfunctions can be divided into the following categories: functional recovery, system recovery, operator-initiated restart, and system repair. These levels of error recovery are discussed in their order of acceptability, functional recovery being most acceptable and system repair being least acceptable.

Functional Recovery

Functional recovery is recovery from a machine check without adverse effect on the system or the interrupted user. This type of recovery can be made by processor retry, the ECC facility, or the machine check handler. Processor retry and ECC error correcting facilities are discussed separately in this section because they are significant in the total error recovery scheme. Functional recovery by MCH is made by correcting storage protect feature (SPF) keys and intermittent errors in real storage.

System Recovery

System recovery is attempted when functional recovery is impossible. System recovery is the continuation of system operations at the expense of the interrupted user, whose virtual machine operation is terminated. System recovery can only take place if the user in question is not critical to continued system operation. An error in a system routine that is considered to be critical to system operation precludes functional recovery and would require logout and a system dump followed by reloading the system.

Operator-Initiated Restart

When the errors may have caused a loss of supervisor or system integrity, the system is put into a disabled wait state. The operator is instructed to run the standalone error recovery (SEREP) program and then manually restart the system.

System Repair

System repair is recovery that requires the services of maintenance personnel and takes place at the discretion of the operator. Usually, the operator has tried to recover by system-supported restart one or more times with no success.

System/370 Recovery Features

The operation of the Machine Check Handler depends on certain automatic recovery actions taken by the hardware and on logout information given to it by the hardware.

Processor Retry

Processor errors are automatically retried by microprogram routines. These routines save source data before it is altered by the operation. When the error is detected, a microprogram returns the processor to the beginning of the operation, or to a point where the operation was executing correctly, and the operation is repeated. After several unsuccessful retries, the error is considered permanent.

ECC Validity Checking

ECC checks the validity of data from real and control storage, automatically correcting single-bit errors. It also detects multiple-bit errors but does not correct them. Data enters and leaves storage through a storage adapter unit. This unit checks each doubleword for correct parity in each byte. If a single-bit error is detected, it is corrected. The corrected doubleword is then sent back into real or control storage and on to the processor. When a multiple-bit error is detected, a machine check interruption occurs, and the error location is placed in the fixed logout area. MCH gains control and attempts to recover from the error.

Control Registers

Two control registers are used by MCH for loading and storing control information (see Figure 33). Control register 14 contains mask bits which specify whether certain conditions can cause machine check interruptions and mask bits which control conditions under which an extended logout can occur. Control register 15 contains the address of the extended logout area.

Word	Bits	Name of Field	Associated with				
14	0	Check-stop control	Mch-Chk handling				
14	1	Synchronous MCEL control	Mch-Chk handling				
14	2	I/O extended logout control	Chan-Chk handling				
14	4	Recovery report mask	Mch-Chk handling				
14	5	Degradation report mask	Mch-Chk handling				
14	6	External damage report mask	Mch-Chk handling				
14	7	Warning mask	Mch-Chk handling				
14	8	Asynchronous MCEL control	Mch-Chk handling				
14	9	Asynchronous fixed log control	Mch-Chk handling				
15	8-28	MCEL address	Mch-Chk handling				

Machine Check Handler Subroutines

VM/SP HPO Machine Check Handler module (DMKMCH) consists of the following functions:

- Initial analysis subroutine
- Main storage analysis subroutine
- Storage protect feature analysis subroutine
- Recovery facility mode switching
- Operator communication subroutine
- Virtual user termination subroutine
- Soft recording subroutine
- Buffer error subroutine
- Termination subroutine.

Initial Analysis Subroutine

The initial analysis subroutine of DMKMCH receives control by a machine check interruption. To minimize the possibility of losing logout information by recursive machine check interruptions, the machine check new PSW gives control to DMKMCH with the system disabled for further interruptions. There is always a danger that a machine malfunction may occur immediately after DMKMCH is entered and the system is disabled for interruption. Disabling all interruptions is only a temporary measure to give the initial analysis subroutine time to make the following emergency provisions:

- It disables for soft machine check interruptions. Soft recording is not enabled until the error is recorded.
- It saves the contents of the fixed and extended logout areas in the machine check record.

- It alters the machine check new PSW to point to the term subroutine. The term subroutine handles second machine check errors.
- It enables the machine for hard machine check interruption.
- If a virtual user was running when the interruption occurred, the running status (GPRs, FPRs, PSW, M.C. old PSW, CRs, etc.) is saved in the user's VMBLOK.
- It initially examines the machine check data for the following error types:

MCIC = ZERO PSW invalid System damage Timing facilities damage

The occurrence of any of these errors is considered uncorrectable by DMKMCH; the primary system operator is informed, the error is formatted and recorded, and the system enters a wait state, code 001 or 013.

- If none of the above errors are present, it checks for a channel inoperative error on a 303x processor. If such an error is detected, DMKACRCT will be called to attempt (a) to recover the failing channel(s) and (b) if the channel(s) is still not operational mark the channel(s) offline and attempt to continue system operation.
- If the instruction processing damage bit is on, it tests for the following types of malfunctions:
 - Multiple-Bit Error in Main Storage Control is given to the main storage analysis subroutine.
 - SPF Key Error Control is given to the SPF analysis subroutine.
 - Retry failed If the processor was in supervisor state the error is considered uncorrectable and the system is terminated. If the processor was in problem state, the virtual machine is reset or terminated and the system continues operation.
- If processor retry or ECC was successful on a soft error, control is given to the soft recording subroutine to format the record, write it out on the error recording cylinder, and update the count of soft error occurrences.
- If external damage was reported, control is given to the soft recording subroutine to format the record and write it out on the error recording cylinder.

Main Storage Analysis Subroutine

The main storage analysis subroutine is given control when the machine check interruption was caused by a multiple-bit storage error. An initial function points the machine check new PSW to an internal subroutine to indicate a solid machine check, in case a machine check interruption occurs while exercising main storage.

Damaged storage areas associated with any portion of the CP nucleus itself cannot be refreshed; multiple-bit storage errors in CP cause the system to be terminated. An automatic restart reinitializes the system.

If the damage is not in the CP nucleus, main storage is exercised to determine if the failure is solid or intermittent. Multiple-bit ECC storage errors on a 303x processor are always treated as solid errors. If the failure is solid, the 4K page frame is marked unavailable for use by the system. If the failure is intermittent, the page frame is marked invalid. The change bits associated with the damaged page frame are checked to determine if the page had been altered, by the virtual machine. If no alteration had occurred, CP assigns a new page frame to the virtual machine and a backup copy of the page is brought into storage the next time the page is referenced. If the page had been altered VM/SP HPO resets or terminates the virtual machine, clears its virtual storage, and sends an appropriate message to the user. Normal system operation continues for all other users.

Storage Protect Feature Analysis Subroutine

The storage protect feature analysis subroutine gets control when the machine check interruption was caused by a storage protect feature error. An initial function points the machine check new PSW to an internal subroutine if a machine check interruption occurs during testing and validation. The storage protect feature analysis routine then determines if the error was associated with a failure in virtual machine storage or in the storage associated with the control program.

A storage protect feature error associated with VM/SP HPO is a potentially catastrophic failure. Namely, VM/SP HPO always runs with a PSW key of zero, which means that the storage protect feature key in main storage is not checked for an out-of-parity condition. The storage protect feature analysis subroutine exercises all 16 keys in the failing storage 2K page frame. If a storage protect feature machine check occurs in exercising the 16 keys 5 times each, the error is considered solid and the operating system is terminated with a system shutdown. If a storage protect feature machine check does not occur, the machine check is considered intermittent. The zero key is restored to the failing 2K page frame and this is transparent to the virtual machine.

If a storage protect feature machine check occurs that is associated with a virtual machine, the storage protect feature analysis subroutine exercises all 16 keys in the failing storage 2K page frame. If a storage protect feature machine check does not occur, the machine check is intermittent and the SWPTABLE for the page associated with the failing storage address is located. The storage key for the failing 2K storage page frame is retrieved from the SWPTABLE and the change and reference bits are set on in the

storage key. The storage key is then stored into the affected failing storage 2K page frame. If a storage protect feature machine check occurs in exercising the 16 keys 5 times each, then the machine check is considered solid and the following actions are taken. (1) The virtual machine is selectively reset or terminated by the virtual machine termination subroutine; (2) The 4K page frame associated with the failing address is removed as an available system resource. This is accomplished by locating the CORTABLE for the defective page and altering the CORFPNT and CORPBPNT pointers to make the page unavailable to the system. The CORDISA bit in this CORTABLE is set on to identify the reason for the status of this page in a system dump.

Recovery Facility Mode Switching

The recovery facility mode switching subroutine (DMKMCIMS) allows the service representative to change the mode that processor retry and ECC recording are operating in. This subroutine receives control when a user with privilege class F issues some form of the SET command with the MODE operand. A check is initially made to determine if this is VM running under VM. If this is the case, the request is ignored and control is returned to the calling routine. For the format and usage of the SET command with the MODE operand, refer to the VM/SP HPO Operator's Guide.

Operator Communication Subroutine

The operator communication subroutine is invoked when the integrity of the system has degraded to a point where automatic shutdown and reload of the system has been tried and was unsuccessful, or could not be attempted due to the severity of the hardware failure. A check is first made to determine if the system operator is logged on as a user, then a check is made to determine if the system operator is disconnected. If either of these checks is not affirmative a message cannot be issued directly to the system operator. A LPSW is performed to place the processor in a disabled wait state with a recognizable wait state code in the processor instruction counter.

Virtual User Termination Subroutine

The virtual machine termination subroutine selectively resets or terminates a virtual user whose operation has been interrupted by an uncorrectable machine check. First, the machine is marked nondispatchable to prevent the damaged machine from running before reset or termination is performed. The machine check record is formatted and DMKIOEMC is called to record the error. Then the user is notified by a call to DMKQCNWT that a machine check has occurred and that his operation is terminated. The primary system operator is notified of the virtual user termination by a message issued by a call to DMKQCNWT. If the virtual machine is running in the virtual=real area, DMKUSO is called to log the virtual machine off the system and to return the storage previously allocated to the virtual machine and to clear any outstanding virtual machine I/O requests. The HOLD option of LOGOFF is invoked to allow a user on a dial facility to retain the connection and thus permit LOGON without re-establishing the line connection. However, if the virtual machine is running in the virtual area, and DMKCFM is then called to put the virtual machine in console function mode, the user must re-initialize the system to commence operation.

Soft Recording Subroutine

The soft recording subroutine performs two basic functions:

- Formats a machine check record and calls DMKIOEMC to record the error on the error recording cylinder.
- Maintains the threshold for processor retry and ECC errors and switches from recording to quiet mode when the threshold value is exceeded. To accomplish this, a counter is maintained by DMKMCH for successful processor retry and corrected ECC events.

Processor Retry Recording Mode: Recording mode (bit 4 of control register 14 set to one) is the initialized state, and normal CP operating state for processor retry errors. Recording mode may also be entered by use of the CP SET command. When 12 soft machine checks have occurred, the soft recording subroutine switches the processor from recording mode to quiet mode. For the purpose of model-independent implementation this is accomplished by setting bit 4 of control register 14 to zero. Because in quiet mode no soft machine check interruptions occur, a switch from quiet mode to recording mode can be made by issuing the SET MODE RETRY|MAIN RECORD command. While in recording mode, corrected CPU RETRY|MAIN reports are formatted and recorded on the error recording cylinders, but the primary systems operator is not informed of these occurrences.

Processor Retry Quiet Mode: Quiet mode (bit 4 of control register 14 set to 0) can be entered in one of two ways: (1) when 12 soft machine checks have occurred, or (2) when the SET MODE RETRY QUIET command is executed by a class F user. In this mode, both processor retry and ECC reporting are disabled. The processor remains in quiet mode until the next system IPL (warm start or cold start) occurs or a SET MODE RETRY MAIN RECORD command is executed by a class F user. SET MODE MAIN is treated as invalid on 303x, 308x and 3090 processors.

ECC Recording Modes: To achieve model-independent support, RMS does not set a specific mode for ECC recording. The mode in which ECC recording is initialized depends upon the hardware design for each specific processor model. For the IBM System/370 Models 158, 168, 303x, 308x, and 3090, the hardware-initialized state (therefore the normal operational state for CP) is quiet mode. For the IBM System/370 Models 155II and 165II, the hardware initialized state (the normal operational state for CP) is record mode. An automatic restart incident due to a system failure does not reset the ECC recording mode in effect at the time of failure. The change from record to quiet mode for ECC recording can be initiated in either of the following ways: (1) by issuing the SET MODE {MAIN|RETRY} QUIET command, or (2) automatically whenever 12 soft machine checks have occurred. For the purpose of model-independent implementation, this occurs by setting bit 4 of control register 14 to zero.

The change from quiet to record mode for ECC recording can be accomplished by use of the SET MODE MAIN RECORD command. This recording mode option is for use by maintenance personnel only. It should be noted that processor retry is placed in recording mode if it is not in that state when the SET MODE MAIN RECORD command is issued.

While in recording mode, corrected ECC reports are formatted and recorded on the error recording cylinder, but the primary systems operator is not informed of these incidents.

Buffer Error Subroutine

On processor models equipped with a high-speed buffer (155-II, 158, 165-II, 168, 303x, 308x) or a data lookaside table (DLAT) (165-II, 168, 303x, 308x, 3090) the deletion of buffer blocks because of hardware failure is reported via a degradation report machine check interruption. MCH enables itself for degradation report machine check interruptions at system initialization by setting bit 5 of control register 14 to 1. If a machine check interruption occurs that indicates high-speed buffer or DLAT damage, MCH formats the record and calls DMKIOEMC to record it on the error recording cylinder, informs the primary systems operator of the failure, and returns control to the system to continue normal operation.

Termination Subroutine

The termination subroutine is given control if a hard machine check interruption occurs while DMKMCH is in the process of handling a machine check interruption. Note that soft error reporting is disabled for the entire time that MCH is processing an error.

An analysis is performed of the machine check interruption code of the first error to determine if it was a soft error. If it was, the first error is recorded, the system status is restored and control is restored to the point where the first error occurred. If the first error was a hard error, the operator communication subroutine is given control to issue a message directly to the system operator, and to terminate CP operation.

Overview of Channel Check Handler

The channel check handler (CCH) aids the I/O supervisor in recovering from channel errors and informs the operator or service representative of the occurrence of channel errors.

CCH receives control from the I/O supervisor when a channel data check, channel control check, or interface control check occurs. CCH produces an I/O error block (IOERBLOK) for the error recovery program and a record to be written on the error recording cylinder for the system operator or service representative. The operator or service representative may obtain a copy of the record by using the CMS CPEREP command. A message about the channel error is issued to the system operator each time a record is written on the error recording cylinder.

When the I/O supervisor program detects a channel error during routine status examination following an SIO, TIO, HIO, or an I/O interruption, it passes control to the channel check handler (DMKCCH). DMKCCH analyzes the channel logout information and constructs an IOERBLOK and, if the error is a channel control or interface control check, an ECSW is constructed and placed in the IOERBLOK. The IOERBLOK provides information for the device-dependent error recovery procedures. DMKCCH also constructs a record to be recorded on the error recording cylinder. Normally, DMKCCH returns control to the I/O supervisor after constructing an IOERBLOK and a record. However, if DMKCCH determines that system integrity has been damaged (system reset or invalid unit address, etc.), then CP operation is terminated. CP termination causes DMKCCH to issue a message directly to the system operator and place the processor in a disabled wait state with a recognizable wait code in the processor instruction counter.

Normally, when DMKCCH returns control to the I/O supervisor, the error recovery program for the device which experienced the error is scheduled. When the ERP receives control, it prepares to retry the operation if analysis of the IOERBLOK indicates that retry is possible. Depending on the device type and error condition, the ERP either effects recovery or marks the event fatal and returns control to the I/O supervisor. The I/O supervisor calls the recording routine DMKIOE to record the channel error.

The primary system operator is notified of the failure, and DMKIOE returns control to the system and normal processing continues.

If the channel check is associated with an I/O event initiated by a SIO in a virtual machine, the logout is reflected to the virtual machine in one of two ways, depending upon whether the channel check occurred at SIO time or later in an interrupt. If it occurred at SIO time, then DMKVSI (or occasionally DMKVIO) calls upon DMKCCHRF to reflect the logout. If it occurred in an I/O interrupt, the dispatcher notices the channel check as it is reflecting the I/O interrupt to the virtual machine, and so, at that time, DMKDSP calls upon DMKCCHRF to reflect the logout.

Channel Control Subroutine

Control is passed to the channel control subroutine of DMKCCH after a SIO with failing status stored, or an I/O interrupt because of a channel control check, interface control check, or channel data check.

If "logout pending" is indicated in the CSW, the CP termination flag is set. The existence of real device blocks (RCHBLOK, RCUBLOK, RDEVBLOK), for the failing device address, is determined by a call to DMKSCNRU and an indicator is set if they do exist. An indicator is also set if the IOBLOK for the failing device address exists. A call to DMKFREE obtains storage space for the channel check record and the channel control subroutine builds the record. If the indicators show that the real device blocks and the IOBLOK exist, a call to DMKFREE obtains storage space and the channel control subroutine builds the I/O error block (IOERBLOK); if these blocks do not exist, the IOERBLOK is not built. The IOERBLOK is used for two purposes:

- 1. The device-dependent error recording program (ERP) uses the IOERBLOK to attempt recovery on CP-initiated I/O events. If the I/O events that resulted in a channel check are associated with a virtual machine, the I/O fatal flag is set in the IOBLOK and the virtual machine is reset, cleared, and put into CP read status. The length and address of the channel check record is placed in the IOERBLOK and the IOERBLOK is chained off the IOBLOK.
- 2. DMKIOECC uses the IOERBLOK to record the channel check record on the error recording cylinders.

The channel control subroutine gives control to a channel-dependent error analysis routine to build or save the extended channel status word (ECSW). When the channel control subroutine regains control, eight active addresses are saved in the channel check record.

If the CP termination flag is set, the I/O extended logout data from the channel check record is restored to main storage for use by SEREP. If the system operator is both logged on as a user and connected to the system, a message is sent to him advising him of the channel error. A LPSW is then executed to place the processor in a disabled wait state with a wait state code of 002 in the processor instruction counter.

If the CP termination flag is not set, a check is made to determine if an IOERBLOK was built by the channel control subroutine.

If an IOERBLOK was not built, a CPEXBLOK is stacked to call DMKIOECC to record the channel check record on the error recording cylinders and send the system operator a message informing him of the error. If channel termination is set, DMKACRCT is called to attempt to recover the failing channel with a CLRCH operation. If the CLRCH recovers, the failing channel, or if the system can continue operation with the failing channel marked offline, DMKACR returns to DMKCCH. Otherwise a wait state X'002' is loaded.

If an IOERBLOK was built, control is returned to DMKIOS, which calls the appropriate ERP. Whether or not recovery is successful, DMKIOS eventually calls DMKIOE to record the channel check record. DMKIOE examines the status of the in CSW error in the IOERBLOK to determine if it was a channel error; if so, it finds the length and pointer to the channel check record and records the error on the error recording cylinder. If this was not a channel error, DMKIOE continues normal processing.

Individual Routines

A separate channel error analysis routine is provided for each type of channel for which DMKCCH can be used. The purpose of these routines and the channel control subroutine is to analyze the channel logout to determine the extent of damage and to create a sequence and termination code to be placed in the ECSW in the IOERBLOK. At system initialization, the correct model dependent channel recovery routine is loaded and the storage necessary to support the routine is allocated. The model-dependent error analysis subroutines and routines and their functions are as follows:

Integrated Channels (Models 135, 135-3, 138, 145, 145-3, 148, 155II, 158, 3031, 3032, 3033, 3081, 3090 and 43xx Processors)

Since all of these systems have integrated channels one common subroutine is used to handle all of these processor types. This subroutine:

- Indicates CP termination if the ECSW is not complete or the reset codes are invalid
- If the channel has been reset or if the error was an I/O interface inoperative condition on a 303x processor, indicate channel termination
- Moves the ECSW to the IOERBLOK
- Moves the hardware stored unit address and the I/O extended logout to the channel check record
- Sets the I/O extended logout area and ECSW area to ones
- Returns control to the channel control subroutine.

2860 Channel (Models 165 II and 168)

The 2860 logout area is checked to determine if a complete logout exists; if not, CP termination is necessary.

A check is made in the logout area for validity of the CSW fields and bits are set in the channel check record's ECSW field to indicate bad fields.

The channel logout is then checked and sequence codes are set based on the presence of a channel control check, or an interface control check. If a channel control check is present, the codes set are determined through parity. The count determines if parity is good and sets a resultant condition code.

The logout area is examined to ensure that the unit address has valid parity and is the same address passed by DMKIOS. If so, the unit-address-valid bit in the ECSW is set. If the unit address is not valid, the unit-address-valid bit is reset to indicate the invalid condition.

The ECSW field in the channel check record is moved to the IOERBLOK, if one exists.

After completing the ECSW the 2860 routine moves the 2860 I/O extended logout into the channel check record, set the I/O extended logout area to ones, and returns to the channel control subroutine.

2870 Channel (Models 165 II and 168)

If the channel failed to log out completely, at least part of the logout area is all ones. If a fullword of ones is found, a CP termination condition exists.

A check is made in the logout area for valid CSW fields, and bits are set in the channel check record's ECSW field to indicate bad fields.

The termination and sequence codes are set depending on the presence of an interface control check or channel control check. If a channel control check is present, the codes set are determined through parity, count, and/or data transfer checks. For the 2870, parity can be determined directly from the channel logout.

The logout area is also examined to ensure valid parity in the unit address and to ensure that the address is the same as that passed to DMKCCH by DMKIOS. If so, the unit-address-valid bit in the ECSW is set.

The third word of the logout area is also analyzed for type II errors. If a type II error is found, a CP termination condition exists.

The ECSW field in the channel check record is moved to the IOERBLOK, if one exists.

Before returning to the channel control subroutine, the 2870 routine moves the 2870 I/O extended logout into the channel check record and sets the I/O extended logout area to ones.

2880 Channel (Models 165 II and 168)

This routine analyzes 9 words of the 28-word logout.

The 2880 analysis routine handles channel data checks, interface control checks, and channel control checks.

Termination code 3 (system reset) is not set in the ECSW because the 2880 channel does not issue system reset to the devices. Retry codes of 0 to 5 are possible.

Note: There are several catastrophic conditions under which the CP termination flag can be set, in the 2880 analysis routine. They are:

- The channel did not complete the logout.
- The CSW is not reliable.
- The unit address in the I/O interruption device address field is not correct.

Only a channel check record is needed if the channel has recognized an internal error and has recovered from it without any damage. No recovery action is necessary in these cases.

If the channel address in the I/O interruption device address field does not match the channel address in the logout, a CP termination condition exists.

If the channel was doing a scan and the unit control word had a parity check a CP termination condition exists. If there was no parity check, there was no damage during the scan and only a channel check record is required.

Depending on the sequence the channel has entered, the termination and sequence codes are set; command address, unit address, and unit status validity is determined; and the sequence code is set valid. The ECSW field in the channel check record is moved into the IOERBLOK, if one exists.

Before returning to the channel control subroutine, the 2880 routine moves the I/O extended logout into the channel check record and sets the I/O extended logout area to ones.

Error Recording Interface for Virtual Machines

The error recording interface provides a means of recording errors encountered by operating systems running in a virtual machine under VM/SP HPO. An SVC 76 issued by a virtual machine is used to signal CP that error recording is required. The SVC interruption handler in DMKSVD examines general registers 0 and 1 to determine if valid parameters have been passed. If valid parameters are not found, the SVC is reflected back to the virtual machine and no recording takes place. If valid parameters are passed, a pageable routine (DMKVER) processes the error record.

DMKVER validates the record passed by the virtual machine. If invalid conditions are found, no recording takes place. Control is returned to the SVC interruption routine in DMKPSA to reflect the SVC to the virtual machine as an SVC interruption. The action taken by the virtual machine is dependent on the operating system running in the virtual machine, not VM/SP HPO. If the record is valid, it is modified by changing virtual information to real. The actual recording is accomplished by using the modules DMKIOE and DMKIOF.

Control is then returned to the instruction following the SVC 76 rather than reflecting the SVC. This eliminates the duplication of error recording in VM/SP HPO and the operating system in the virtual machine. If DMKVER determines that the recording represented a permanent I/O error, a message is sent to the primary system operator.

Error Recording and Recovery

The error recording facility is made up of six modules. Four modules (DMKIOE, DMKIOF, DMKIOC, and DMKIOJ) are resident and the other two (DMKIOG and DMKERP) are pageable.

The error recording modules record temporary errors (statistical data recording) for CP generated I/O except for DASDs with a buffered log.

The error recording routines record: unit checks, statistical data counter overflow records, selected temporary DASD errors, machine checks, channel checks, and hardware environmental counter sense data on the error recording cylinders of the system resident device in a format suitable for subsequent processing by the CPEREP command (DMSIFC). The recorder asynchronously updates the statistical data counters for supported devices. The recorder also initializes the error recording cylinders at IPL if they are in an unrecognizable format.

To prevent the loss of error information in the event of a CP abend or a SHUTDOWN command, DMKCKP calls DMKERP to record OBR, MDR, machine check, and channel check errors that are queued to be processed.

Note: You should run CPEREP when the error cylinders are 90% full. If the cylinder becomes full, errors will be lost and error records that are kept will use free storage below the 16Mb line. If CP abends with a DAS001, and CPEREP has not been run, error information for the 3880 Model 11 or Model 21 can still be retrieved using the DAS001 technique described in the "3880 Model 11 and Model 21 Storage Subsystem" section of the VM/SP HPO OLTSEP and Error Recording Guide.

When the recorder is entered from DMKIOS, it is entered at DMKIOERR. This entry is used for unit checks and channel data checks. A test is made of the failing CSW (located in the IOERBLOK) to see if the error was a channel error. If it was, control is passed to the routine for recording channel checks.

The IOERBLOK sense data, IOBLOK flags, and VMBLOK privilege class are examined to determine if the error should be recorded.

Error Record Writing

After an error record is formatted, it is added to the error recording area using DMKRPAGT and DMKRPAPT. The error recording area has page-sized records (4096 bytes). Each page contains a header (8 bytes) which signifies: the cylinder and page number of the page on a CKD DASD, or page number of the page on an FBA DASD (4 bytes), the next available space for recording within page (2 bytes), a page-in-use indicator (1 byte), and a flag byte. Each record within the page is recorded with a 4-byte prefix.

If an error record is too large to be added into a page, a new page is retrieved, updated with record, and placed back on the error recording cylinder with the paging routines. The area to be used for error recording is specified by the installation or system programmer at system generation time. For CKD DASDs, an integral number of cylinders (from two to nine) is specified. For FBA DASDs any number of pages can be specified. Generally, fifty pages or more is an appropriate amount. Errors are recorded in the order in which they occur. If the error recording area become 90 percent full, a message is issued to the operator using DMKQCNWT to warn him of the condition. If the area becomes full, another message is issued to inform the operator and recording is stopped.

On the 303x processors, frame records are read from all accessible SRF devices and written on the error recording area during initialization if no records exist (as after a CPEREP CLEARF operation).

If a channel check error is to be recorded, the recorder is entered at DMKIOERR or DMKIOECC. The channel check handler determines the entry. A channel check error record is formatted.

A machine check enters at DMKIOEMC. Pointers are passed from the machine check handler in registers 6 and 7 to locate a buffer where the machine check record and length are saved. A machine check error record is recorded with the saved machine check logout and additional information. The machine check error record is written onto the error recording area by using the paging routines.

Hardware environmental counter records are formed using routine DMKIOEEV. This routine is scheduled by DMKIOS after control is returned from the ERP. Sense data information is stored in the IOERBLOK by the ERP. The record formed is called a nonstandard record.

Clear and Format Recording Area

DMKIOEFM is called by DMSIFC (CPEREP command) via a DIAGNOSE instruction. DMKIOEFM is invoked to reset the specified error recording cylinders (if CLEAR, CLEARF, or ZERO = Y was specified). The clear is performed by resetting each page-header, space-available field. Pointers in storage are then updated to address the first available page in the error recording area. Control is then returned to the calling routine. For details on the CPEREP command and EREP execution, refer to the VM/SP HPO OLTSEP and Error Recording Guide and EREP publications.

CLEARF on a 303x processor clears the area, then causes the frame records to be read from each SRF device specified at system generation on the RIOGEN macro.

Find First Recording Cylinder at IPL

DMKIOEFL is called by DMKCPI to find the first available page that can be used for error recording. The paging routines, DMKRPAPT and DMKRPAGT, are used to read the error recording pages (4096-byte records).

As each page record is read, it is examined to see if this record is the last recorded. If so, a pointer in storage is saved so recording can continue on that page record. Control is then returned to the caller. If any error recording page is in an unrecognizable format, the error recording area is automatically reformatted by CP.

DASD Error Recovery, ERP (DMKDAS or DMKDAD for CKD or DMKDAU for FBA)

Error recovery is attempted for CP-initiated I/O operations to its supported devices and for user-initiated operations to CP-supported devices that use a DIAGNOSE interface. The primary control blocks used for error recovery are the RDEVBLOK, the IOBLOK and the IOERBLOK. In addition, auxiliary storage is sometimes used for recovery channel programs and sense buffers.

The initial error is first detected by the I/O interruption handler which performs a SENSE operation if a unit check occurs. Unit check errors are then passed to an appropriate ERP. If a channel check is encountered, the channel check interruption handler determines whether or not retry is possible and passes control to an ERP through the I/O interruption handler. DASD errors are processed as described below.

Channel Errors

- I/O interface inoperative on a 303x processor is reflected to the virtual machine if the channel is dedicated. Otherwise, a call is made to DMKACR to attempt to recover the failing channel. If recovery from the channel check is not possible, a wait state X'002' occurs.
- Channel control check is treated as seek check. It is retried 10 times.
- Interface control check is treated as seek check. It is retried 10 times.
- Channel data check is treated as data check. It is retried 10 times.

Unit Check Errors

Equipment check: Retry the operation 10 times for 3330, 3340, 3375, 3380, 3350, 2305, and FBA devices. If Alternate Interface Disabled is also on, retry the operation one time. For FBA devices, if the error is also permanent, the command is not retried.

No record found (CKD devices only): Execute a READ HOME ADDRESS and check home address against seek address. If they are the same, consider the error permanent. If not, return to caller.

Seek check (CKD devices only): Retry the operation 10 times except that 3375/3380/3330/3350 seek checks are retried by hardware.

Intervention required: Issue a message to console and wait for solicited device end. This procedure is repeated once.

Bus out check: One retry of the operation.

Data checks: For the 2305/3340, retry the operation 10 times. For the 3375/3380/3330/3350/FBA, the operation is retried by hardware.

Overrun: Retry the operation 10 times. For FBA devices, the operation is retried 10 times, unless the error is also permanent.

Missing address marker: Retry the operation 10 times.

Command reject: The command is not retried.

File Protect: The command is not retried.

Chaining check: Test for command reject. If not present, retry the operation 10 times.

Environmental data present: Issue a BUFFER UNLOAD command and retry the operation.

Track condition check (CKD devices only): On CP I/O and Diagnose I/O, when a track condition check is received from a device for which CP does not provide alternate track software recovery, the condition is treated as a permanent error. CP does provide alternate track support for other devices; this support is described in the section "Alternate Track Recovery, ERP (DMKTRK)."

Check Data (FBA devices only): The command is not retried.

The error recovery routine keeps track of the number of retries in the IOBRCNT field of the IOBLOK. This count determines if a retry limit has been exceeded for a particular error. On initial entry from DMKIOS for an error condition, the count is zero. Each time a retry is attempted, the count is increased by one.

The ERP preserves the original error CSW and sense information by placing a pointer to the original IOERBLOK in the RDEVBLOK. Additional IOERBLOKs, which are received from DMKIOS on failing restart attempts, are discarded. The original IOERBLOK is thus preserved for recording purposes.

If after a specified number of retries, DMKDAS or DMKDAD (for CKD devices) or DMKDAU (for FBA devices) fails to correct the error, the operator may or may not be notified of the error. Control is returned to DMKIOS. DMKIOS is notified of the permanent error by posting the IOBLOK (IOBSTAT = IOBFATAL). The error is recorded via DMKIOS by DMKIOERR, if DMKDAS or DMKDAU and DMKIOE determine that the error warrants recording.

If the error is corrected by a restart, the temporary or transient error is not recorded. Control is returned to DMKIOS with the error flag off.

Before returning control to DMKIOS on either a permanent error or a successful recovery, the ERP frees all auxiliary storage gotten for recovery CCWs, buffers, and IOERBLOKs.

The DMKIOS interface with the ERP uses the IOBSTAT and IOBFLAG fields of the IOBLOK to determine the action required when the ERP returns to DMKIOS.

When retry is to be attempted, the ERP turns on the restart bit of the IOBFLAG field. The ERP bit of the IOBFLAG field is also turned on to indicate to DMKIOS that the ERP wants control back when the task has finished. This enables the ERP to receive control even if the retry was successful and allows the freeing of all storage gotten for CCWs and temporary buffers. The IOBRCAW is set to the recovery CCW string address.

In handling an intervention-required situation, the ERP sends a message to the operator and then waits for the device end to arrive. This is accomplished by a return to DMKIOS with the ERP bit in the IOBFLAG field set on and the IOBSTRT bit in the IOBFLAG field set off. When the device end interruption arrives, the original channel program which was interrupted is then started.

The ERP flags of the IOERBLOK are also used to indicate when special recovery is being attempted. For example, a READ HOME ADDRESS command when a no record found error occurs.

The other two indicators are self-explanatory and are explained in Figure 34.

Field			
IOBFLAG IOBERP	IOBFLAG IOBRSTRT	IOBSTAT IOBFATAL	Action To Be performed by DMKIOS
1	0	0	Return control when solicited device end arrives
1	1	0	Restart using IOBRCAW
0	0	1	Permanent I/O error
0	0	0	Retry successful

Figure 34. Summary of IOB Indicators

If the error is uncorrectable or intervention is required, the ERP calls DMKMSW to notify operator. The specific message is identified in the MSGPARM field of the IOERBLOK.

Alternate Track Recovery, ERP (DMKTRK)

The software alternate track recovery support described in the following paragraphs applies only to the 3340/3344 disk. For 3380, 3330, and 3350 disks no software support is needed since the hardware performs alternate track recovery. No support is needed for the 2305 drum since the CE is able to rewire the device to use spare tracks in place of defective tracks. Track condition checks from any device type are reflected back to the virtual machine.

Overview of 3340 Alternate Track Support

The 3340 alternate track support applies to CP I/O, to Diagnose I/O (thereby giving alternate track support to CMS), and to SIO executed in a virtual machine. For CP I/O and Diagnose I/O, the alternate track recovery support essentially consists of directing (seeking) an interrupted channel program to an alternate track and restarting it. Later, in some cases, the interrupted channel program is directed back to the original cylinder and restarted there. For SIO in a virtual machine, the operating system in the virtual machine provides its own error recovery when CP reflects a track condition check to the virtual machine.

On the 3340 disk, alternate tracks are assigned in the conventional alternate tracks cylinders at the high end of the real disk, not in the last cylinder of each minidisk. Therefore a virtual machine may need to seek outside of its minidisk extent. This occurs when an operating system in a virtual machine performs its own error recovery following a track condition check. So for SIO issued from a virtual machine, CP's alternate track support must permit the virtual machine to escape from the confines of its minidisk to get to the alternate tracks assigned to the defective tracks of that minidisk. Yet at the same time CP must still prevent the virtual machine from accessing other tracks that it does not own.

Since alternate tracks are assigned only in the conventional alternate tracks cylinders at the high end of the real disk, CP does not apply minidisk cylinder relocation values to a virtual machine's channel commands that reference alternate tracks. Similarly, CP does not unrelocate alternate track CCHH addresses returned by read home address, by read record zero, in sense information, or for error recording.

Alternate Track Hardware Operation and Implications

The home address record (HA) on any track contains a flag byte with two bits that are involved in alternate track assignments. One bit, when set to one, indicates that the track is defective and that the track should have (and ordinarily does have) an alternate track assigned. The count field of record zero of a track with this bit set should point to (have the CCHH address of) the assigned alternate track. The second bit in the flag byte, when set to one, indicates that the track in which it appears is an assigned alternate track. The count field of record zero of an assigned alternate track should point back to (have the CCHH address of) the flagged defective track that it is assigned to.

Before using the pointer in record zero of a flagged track to get to the corresponding alternate, it is considered good form for an operating system to check the pointers both ways to see that each points to the other. CP performs two-way checks of the pointers for seeks to an alternate track initiated by Diagnose or by SIO in a virtual machine. For its own I/O, CP uses the forward record zero pointer without performing a two-way check. Performing a two-way check would decrease performance and should not be necessary since all of the record zero pointers were checked in both directions by the Format/Allocate program (DMKFMT) when the CP-owned disk was originally formatted.

Note: The DASD Dump/Restore (DDR) program also checks the record zero pointers both ways when a tape is restored to a disk.

Except for those channel commands that deal specifically with the home address and record zero, any attempt to search or read or write on a track that is flagged as defective results in a unit check with "track condition check" indicated in the sense data.

Operations on an assigned alternate track can also result in a unit check with "track condition check" indicated in the sense data. But in this case it occurs when an attempt is made to *leave* the assigned alternate track, not when the operation is reading or writing on the track. The situations where trying to leave the alternate track results in a track condition check are:

- Any multi-track operation
- A record overflow operation.

The hardware does *not* generate a track condition check when a seek is used to leave the track. This applies to any kind of seek, including seek head.

When a channel program from a virtual machine SIO (or from a Diagnose) is allowed to access an alternate track, subsequent CCWs in the channel program must be prevented from accessing adjacent tracks in the alternate track cylinder since these may belong to other virtual machines. A channel program may attempt a transition from one track to the next by any of the following:

- Seek
- Seek head
- Multi-track search or read
- Record overflow.

The full seek causes no problem: since it specifies the cylinder as well as the track, it causes the channel program to leave the alternate track and to return to a cylinder within the minidisk extent. It is certain to go back to the minidisk because the seek address was verified when the virtual CCWs were translated to real.

The seek head is dealt with as follows. When a seek to an alternate track is encountered in a virtual channel program by CP during the CCW translation process, CP converts all seek head commands (in the real, translated CCWs) to an invalid CCW opcode (X'FF'). Then when the translated channel program is executed, it is interrupted (with a command reject) at each seek head CCW so that the track to which the channel program is seeking can be checked to see that it really belongs to the virtual machine that requested the I/O. Note that this only happens to channel programs that seek out of the minidisk to an alternate track. The multi-track operations and record overflow operations also cause no problem, because, as explained above, these are caught by the hardware and result in a track condition check.

VM/SP HPO does not provide alternate track recovery for overflow records on a 3340/3344. Therefore, a record overflow operation that is indicated to be incomplete will cause a fatal error.

Module Function and Control Flow

DMKTRKVA: When DMKCCWTR finds a virtual machine seeking out of its minidisk extent to what should be an assigned alternate track, it has to do a check of the backward record zero pointer to verify that the alternate belongs to that minidisk. So DMKCCWTR calls DMKTRKVA, passing the CCHH address of the alternate as input, and DMKTRKVA performs CP I/O to read record zero of the alternate and then returns the pointer found in record zero to DMKCCWTR.

DMKTRKFP: This is called by both DMKUNT and DMKVIO. Its function is to handle command rejects in channel programs initiated by virtual machine SIO when the channel program was found (by DMKCCWTR) to be seeking to an alternate track outside the minidisk extent. The command rejects result because, for these channel programs, any seek head commands have been invalidated (opcode changed to X'FF') in order to trap seek heads that might switch to another minidisk's track in the alternate track cylinder.

Note: Even though DMKCCWTR may also find Diagnose I/O channel programs that seek directly to an alternate track and invalidate the seek head opcodes on these channel programs, the command rejects resulting from these channel programs are handled by DMKTRKIN, not by DMKTRKFP.

DMKTRKIN: This routine performs alternate track recovery for CP I/O and for Diagnose I/O both when the Diagnose channel program results in a track condition check and when a command reject results from a seek head whose opcode DMKCCWTR made invalid. The routine has nothing to do with alternate track recovery for SIO issued by a virtual machine. But it does share a few small subroutines with DMKTRKFP.

DMKTRKIN is called only by DMKDASER, which in turn is called only by DMKIOS. These three routines work closely together during alternate track error recovery and the control flow back and forth between these routines is controlled to a great degree by flags in the IOBLOK and the IOERBLOK.

The control blocks of major concern in this area are the RDEVBLOK, the IOBLOK, and the IOERBLOK. When an error occurs and DMKIOS makes the initial call to DMKDASER (at the time of the first error associated with this IOBLOK), an IOERBLOK containing sense data has already been created; the IOBIOER field of the IOBLOK points to it. When DMKDASER gets control, it notices that this is a first call and it moves the pointer out of IOBIOER into RDEVIOER so that this first IOERBLOK, associated with the original error, can be kept over a period of time during which attempts

may be made to retry the I/O operation. During these retries, further errors may cause new IOERBLOKs, pointed to by IOBIOER, to be sent back from DMKIOS. Generally speaking, RDEVIOER continues to point to the original IOERBLOK and new IOERBLOKs are created and sent back from DMKIOS after each retry that ends with an error. Generally, the new IOERBLOK from the failed retry is discarded before the next retry. But occasionally a new IOERBLOK is used by DMKDASER or DMKTRKIN to replace the original IOERBLOK, so it is pointed to by RDEVIOER and the first original IOERBLOK is discarded before the next retry. This happens when the new error is deemed to be more severe than the original (DMKDASER gives priority to channel checks) or when the original error gets corrected by a retry, but then the channel program fails on a later CCW (DMKTRKIN does this).

Control flow back and forth between DMKIOS and DMKDASER is controlled by the setting of the flags IOBERP, IOBRSTRT, and IOBFATAL, and has been described earlier in the section "DASD Error Recovery, ERP (DMKDAS)."

The control flow back and forth between DMKDASER and DMKTRKIN is controlled by the flags IOERRDR0 and IOERALTR and by a return code that DMKTRKIN passes back in register 1. Whenever either of the two flags is set, they cause DMKDASER to call DMKTRKIN whenever DMKDASER gets control (which in this case happens after a retry), even though there is no track condition check indicated in the new IOERBLOK. The IOERRDR0 flag indicates to DMKTRKIN that the retry being returned from was used to execute a channel program to read record zero. The IOERALTR flag indicates to DMKTRKIN that the retry being returned from is a restart of a user channel program (not strictly error recovery CCWs) that had a track condition check earlier. This means that invalidated seek head opcodes can be expected.

Details of Alternate Track Recovery for CP I/O and Diagnose I/O

Once a CP I/O or Diagnose I/O channel program has to be restarted because of a track condition check, the error recovery procedure invalidates (for Diagnose I/O only) all seek head opcodes in the channel program and sets the IOERALTR flag (indicating that alternate track error recovery is in progress) before proceeding. The IOERALTR flag remains set whenever any portion of the users channel program is being retried, until the channel program either ends successfully or ends with a permanent error.

Note: The flag does not remain set continuously; there are breaks while the error recovery procedure takes time out to use its own channel program to read record zero (the channel program is passed back to IOS as a "retry"). At these times the IOERRDR0 flag is set instead of the IOERALTR flag.

During the further execution of a Diagnose Channel program, invalidated seek head opcodes may be encountered once the IOERALTR flag is turned on. CP channel programs do not use seek head. The number of these opcodes encountered may be several, or none at all, depending on the user's channel program. Also, these invalidated seek heads may be trying to seek off of an assigned alternate track (usually to the next logical track) or they may have no involvement with flagged tracks at all, again depending on the nature of the user's channel program. Whenever the channel program is stopped by an invalidated seek head, a determination is made of whether or not it is trying to get off of an alternate track. This determination is made by looking at the current cylinder number (available in sense data taken at the time of the command reject) and seeing whether or not it falls within the alternate track cylinder area at the high end of the disk. If the seek head was not trying to get off of an alternate track, there is no problem and the subject channel program is restarted with a seek to the current cylinder and to the track specified by the invalidated seek head. If the seek head was trying to get off of an alternate track, record zero of the alternate track is read first to get the cylinder number of the defective track. Then the subject channel program is restarted with a seek to the cylinder of the defective track, but to the track specified by the invalidated seek head.

Tape Error Recovery, ERP (DMKTAP and DMKTPE)

Error recovery is attempted for user-initiated tape I/O operations to CP-supported devices that use the DIAGNOSE interface. The primary control blocks used for error recovery are the RDEVBLOK, the IOBLOK, and the IOERBLOK. In addition, auxiliary storage is used for recovery channel programs (repositioning and erase).

The interruption handler, DMKIOS, performs a SENSE operation when a unit check occurs. If the tape device is a 3480, tape errors are passed to DMKTPE; for all other tape devices, tape errors are passed to DMKTAP. The sense information associated with a unit check is contained in the IOERBLOK. If a channel check is encountered, the channel check interruption handler determines if retry is possible and passes control to the ERP through the I/O interruption handler.

When an error is encountered and ERP receives control, the tape error recovery module determines if this is the first entry into the ERP for this task. The IOBRCNT (IOB error count) field of the IOB is zero on initial entry. On this first entry, the pointer to the IOERBLOK is placed in the RDEVIOER field of the RDEVBLOK. This preserves the original error CSW and sense information for recording. Thereafter, IOERBLOKS are discarded before a retry is attempted or a permanent error is passed to IOS.

The ERP looks for two other specific conditions. If the error count field is not zero, entry must be due to a recovery attempt. Thus, it may be a solicited device end to correct an intervention-required condition or a retry attempt for either tape repositioning or channel program re-execution.

The ERP keeps track of the number of retries in the IOBRCNT field of the IOBLOK to determine if a retry limit has been exceeded for a particular error. If the specified number of retries fails to correct the error, the error is recorded and DMKIOS is notified of the permanent error by turning on a status flag in the IOBLOK (IOBSTAT=IOBFATAL).

If the error is corrected, the temporary error is not recorded and control is returned to DMKIOS with error flags all off. When repositioning is required in order to attempt recovery, additional ERP flags are contained in the IOERBLOK to indicate paths for specific errors (that is, data check on write must reposition, erase, and then reissue original channel program).

All error recovery is started the same except for intervention-required errors. The IOBFLAG is turned on to indicate RESTART (IOBFLAG=IOBRSTRT), and the IOBRCAW (IOBLOK Restart CAW) is filled with the restart channel address word. In addition, an IOBFLAG flag is turned on to indicate that the ERP is in control so that control can be returned to ERP during all tape error recovery (IOBFLAG=IOBERP). In the case of an intervention required error, the ERP sends a message to the operator, and then returns to DMKIOS with indications that tell DMKIOS the ERP is waiting for a device end on this device. This is done by clearing the restart flag and returning to DMKIOS with only the IOBERP flag on.

When ERP has determined a permanent error situation or successfully recovered from an error, all auxiliary storage obtained for recovery CCWs, buffers, and IOERBLOKs is freed before a return is made to DMKIOS (see Figure 34 for a summary of the IOB indicators), also, the statistical counters for 2400, 3410, and 3420 devices are updated.

If the error is uncorrectable or operator intervention is necessary, ERP calls the message writer to write the specific message.

3270 Remote Support Error Recovery

Recovery from errors associated with binary synchronous lines, and the related channel and transmission control unit hardware is processed by DMKBSC. Recovery from errors associated with data and control processing by the remote station (the device) as defined by remote status and sense byte definition (see *IBM 3270 Information Display Component Description*) is processed by DMKRGA. Control blocks associated with these errors are the CONTASK, the RDEVBLOK, the BSCBLOK, the NICBLOK, the IOBLOK, and the IOERBLOK.

The interruption handler, DMKIOS, performs a SENSE operation upon detection of a unit check condition (IOERBLOK). The related sense data is analyzed as it relates to the previous operation (CONTASK or BSCBLOK, whichever is applicable). If a channel check is encountered by the channel check interruption handler, the channel check interruption (DMKBSC) procedures determine if recovery can be attempted. If it cannot be retried, that operation is aborted and an appropriate message is sent to the system operator.

DMKRGB sends a Write Structured Field command with a Read Partition (QUERY) data stream to all 3278 and 3279 display stations during ENABLE processing. The purpose of this operation is to test for the presence of color, extended highlighting and programmed symbol set features on the enabled displays. A returned unit check with command reject is indicative of a device(s) without these features. For other errors, ERP receives control and either DMKBSC or DMKRGA determines if this is the first entry into the ERP for this task. The IOBRCNT (IOB error count) field of the IOB is zero on initial entry. On this first entry, the pointer to the IOERBLOK is placed in the RDEVIOER field of the RDEVBLOK. This preserves the original error CSW and sense information for recording. Thereafter, IOERBLOKs are discarded before a retry is attempted or a permanent error is passed to IOS. The ERP looks for two other specific conditions. If the error count field is not zero, entry must be due to a recovery attempt. Thus, it may be a solicited device end to correct an intervention-required condition or a retry of channel program execution.

The ERP keeps track of the number of retries in the IOBRCNT field of the IOBLOK to determine if a retry limit has been exceeded for a particular error. If the specified number of retries fails to correct the error, the error is recorded and DMKIOS is notified of the permanent error by turning on a status flag in the IOBLOK (IOBSTAT = IOBFATAL).

If an apparent fatal error occurs during a Write Text operation, DMKRGA makes an attempt to recover by reselecting the remote 3270, using the standard SELECT channel commands. If the SELECT CCW's succeed, DMKRGA restarts the Write Text operation. If the SELECT channel command fails, DMKRGA treats it as a permanent TP failure.

If the error is corrected, the temporary error is not recorded and control is returned to DMKIOS with all error flags off.

When ERP has determined a permanent error situation or successfully recovered from an error, all auxiliary storage obtained for recovery CCWs, buffers, and IOERBLOKs is freed before a return is made to DMKIOS (see Figure 34 for a summary of the IOB indicators). Also, the statistical counters for 3270 are updated.

The Attached Processor and Multiprocessor Environments

Attached processor support is requested by specifying AP = YES on the SYSCOR macro. Multiprocessor support is requested by specifying MP = YES. For a complete description of system generation considerations, see the VM/SP HPO Installation Guide.

CP Initialization for the Attached Processor or Multiprocessor

IBM System/370 Principles of Operation has a detailed discussion of prefixing that is necessary for understanding the initialization done for the attached processor and multiprocessor systems.

Processor Addresses

The CP initialization routine, DMKCPI, begins normal processing by storing the physical address of the initialized processor in the PSA at location absolute zero (field IPUADDR). (Prefixing has not yet been established.) The logical processor address is computed by doing a logical OR of the physical address with X'40' and is stored in the PSA in LPUADDR. The logical value is used by the CP LOCK manager to avoid using a zero value. The physical value is used for signaling between the two processors.

If AP = YES or MP = YES was coded on the SYSCOR macro, DMKCPI uses the SIGP function to see if another processor or noninitialized processor is available. If so, its physical and logical addresses are stored in the PSA in IPUADDRX and LPUADDRX, respectively. If another processor or noninitialized processor is not available, APUNONLN is set to 1. If the multi-processing option is installed, a message is sent to the operator.

PSA Setup

The top two 4K pages of storage are marked (in the CORTABLE) as being CP-owned and are used as the PSAs for the two processors. The addresses of these two pages are stored at PREFIXA and PREFIXB in the PSA at location absolute zero. DMKAPI copies the information from the PSA at location absolute zero to the new PSA locations. In the PSA designated for the attached processor, PREFIXA and PREFIXB are switched. Thus, on either processor PREFIXA always represents the current processor and PREFIXB the other processor. The values of IPUADDR, LPUADDR, IPUADDRX, and LPUADDRX are also switched so that IPUADDR and LPUADDR always contain the processor addresses of the current processor and IPUADDRX and LPUADDRX contain the other processor addresses.

DMKPXA and DMKPXB are extensions to the PSA. These extensions contain headers for the processor-local queues and associated locks and counters. DMKAPI initializes the field PSAEXT in the PSA of the processor being initialized with the PSA extension of that processor. It also initializes the field PSAEXTX in the same PSA to point to the PSA extension of the other processor. The PSA extension for the IPL processor is DMKPXA and the PSA extension for the non-IPL processor is DMKPXAB.

Locking

To provide system integrity, VM/SP HPO attached processor and multiprocessor support is designed around one global system lock, a VMBLOK local lock, and several system local locks for specifically identified queues or modules.

Note: When VM/SP HPO is running second level as a virtual MP guest on VM/XA, DIAGNOSE X'44' will be issued when a spin lock is entered.

Global System Lock

Much of CP runs under the global system lock. For example, all command processing requires the global system lock. Also, all code executed via an IOBLOK, TRQBLOK, or CPEXBLOK is protected by the global system lock. Certain basic system functions, however, are able to execute without the global system lock on the mainline, nonerror paths. These functions include virtual page fault processing, the simulation of virtual I/O requests and other privileged operations, and the processing of a real I/O interrupt. If the global system lock is needed and cannot be obtained, the function must be deferred until the global system lock is available. Deferral of the function is accomplished by either stacking the VMBLOK appendage (called the deferred interrupt block) or a CPEXBLOK for later processing. The processor that could not obtain the global system lock will then use the unlock dispatcher entry to dispatch a new virtual machine.

In some situations, a function cannot be deferred even though the global system lock is not available. In these cases, a processor will spin on the global system lock until it becomes available.

To ensure system integrity along the paths that do not require the global system lock, other local locks have been defined. With the exception of the VMBLOK lock, these locks are all spin locks and are held for relatively short periods of time.

Dispatch List Locks

Each processor has its own dispatch list queue, also call the true run list (TRL), of runnable users and each queue has its own dispatch list lock. The dispatcher adds the user to the dispatch list queue of the processor for which the user has affinity. This affinity lasts until the next queue-drop or until the VMBLOK is stolen by the other processor. If a user has no affinity, the user is added to the shorter of the two TRLs. If the to TRLs are equal in length, the user is added to the current processor.

Note: If the system operator issued the SET AFFINITY command for this user, the scheduler stacks the VMBLOK for the appropriate processor.

When a VMBLOK's status changes to not runnable, the scheduler removes the VMBLOK from the dispatch list.

Dispatcher Request Queue Locks

Each processor has its own dispatcher request queue (DSPRQ) and each queue has its own lock. These locks can be set by either processor. The dispatcher request queue locks control all additions to or deletions from the IOBLOK/TRQBLOK queue or the CPEXBLOK queue (collectively called the dispatch request queue) for each processor. The dispatcher determines the dispatcher request queue for a given VMBLOK by examining the field VMSTKCPU. If this field is zero, the dispatcher selects the local processor unless CPEXPROC indicates that the block is processor specific for the other processor.

I/O Lock

The I/O lock is a spin lock that serializes access to I/O devices by serializing access to fields in the real I/O control blocks: RCHBLOK, RCUBLOK, and RDEVBLOK.

Real Storage Management Lock (RM Lock)

The real storage management lock (called the RM lock) is a spin lock that controls access to certain real storage management functions and queues.

Scheduler Run List Lock

The scheduler run list (SRL) spin lock controls all additions to and deletions from the run list.

VMBLOK Lock

Each VMBLOK contains one lock, called VMLOCK, which is used by routines that need to serialize certain virtual machine related resources. These resources include the following:

- 1. Any unlocked or unshared pages belonging to the virtual machine.
- 2. Any of the unshared translation or backing store tables defining the address space of the virtual machine.
- 3. Certain fields of the VMBLOK that are modified by routines that do not hold the system lock. Some of these fields are VMPSW, VMGPRS, and VMRSTAT.

The dispatcher obtains the VMBLOK lock before a virtual machine is dispatched and also before a CP request or an I/O request is unstacked. When a virtual machine is dispatched, the VMBLOK address of this virtual machine is saved in the processor's PSA in the field RUNUSER. Normally this virtual machine is also unlocked by the dispatcher when it is entered after an interrupt handler has finished processing. When RUNUSER is still locked, the PSA field LASTUSER is equal to RUNUSER. When RUNUSER is unlocked, LASTUSER is set to ASYSVM.

When a CP request or an I/O request is unstacked, the associated virtual machine is locked and the VMBLOK address is placed in register 11. When the dispatcher is entered after a CP request or an I/O request has been serviced, the virtual machine whose VMBLOK address is in register 11 is locked and will be unlocked by the dispatcher. This virtual machine may not be the same virtual machine that was locked when the CP request or the I/O request was unstacked.

A CP routine must lock another virtual machine for any of the following reasons:

- 1. The routine, or a routine it calls, accesses any unshared page of the virtual machine.
- 2. The routine, or a routine it calls, alters any field of the VMBLOK that is serialized only by the VMBLOK lock.
- 3. The routine, or a routine it calls, could be interrupted and an exit taken to the dispatcher.

The original VMBLOK lock must be released before gaining the new lock.

Figure 35 shows the modules that obtain the VMBLOK lock for a virtual machine other than the one requesting the service.

Module	Action
DMKAPI	Locks the virtual machine that was last dispatched.
DMKBLDVM	Locks the virtual machine just built.
DMKCFO	Locks the virtual machine being set as favored.
DMKCNS	Locks the virtual machine associated with a real device block.
DMKCPS	Locks the virtual machine whose virtual device is being reset when a real device is halted.
DMKCPP	Locks each virtual machine in order to prepare the VMBLOK for uniprocessor mode.
DMKCPV	Locks the virtual machine whose storage is being locked or unlocked, or for whom accounting is being done.
DMKCSU	Locks the virtual machine receiving transferred spool files.
DMKDIA	Locks the virtual machine of the dialed system, the virtual machine of the line being dropped (DMKDIADR), or the virtual machine that owns the channel-to-channel device being coupled.
DMKGRF	Locks the virtual machine associated with a real device block.
DMKLOG	Locks the virtual machine being reconnected or the virtual machine being autologged.
DMKMID	Locks the virtual machines receiving messages at midnight.
DMKMSG	Locks the virtual machine receiving a message.
DMKMSW	Locks the system operator.
DMKNES	Locks each virtual machine active when the NETWORK SHUTDOWN command is processed.
DMKNLD	Locks the virtual machine associated with a real device block.
DMKPAG	Locks the virtual machine associated with a queued I/O request.
DMKPTR	Locks the virtual machine from which a page will be stolen.
DMKQCN	Locks the system operator.
DMKRGA	Locks the virtual machine associated with a NICBLOK.
DMKRGB	Locks the virtual machine associated with a NICBLOK.
DMKRNH	Locks the virtual machine of the destination user for a console task or the virtual machine associated with a remote teleprocessing line.
DMKSPL	Locks the virtual machine receiving a transferred spool file or the virtual machine owning a spooled reader file.
DMKVCA	Locks the virtual machine of the coupled-to CTC device.
DMKVCH	Locks the virtual machine to which the channel is being attached, or the the virtual machine from which the channel is being detached.
DMKVDA	Locks the virtual machine involved in attaching or detaching a real device.
DMKVDD	Locks the virtual machine involved in detaching a real device.
DMKVMC	Locks the virtual machine to which the caller is communicating.

Figure 35. Modules That Obtain Additional VMBLOK Lock

There are situations when a CP routine may access a virtual machine without locking it. If the CP routine, or any routine it calls, is only altering VMBLOK fields that are serialized by the system lock, locking the virtual machine is not necessary. For example, to process the SET PRIORITY command for a virtual machine, locking the virtual machine is not necessary since the altered VMBLOK field, VMUPRIOR, is serialized by the system lock. But to process the SET FAVORED command, locking the virtual machine is necessary since some of the VMBLOK fields altered, such as VMRSTAT, are only serialized by the VMBLOK lock.

DMKLOKFR	Free Storage Lock
DMKPXA, TRLLOCK DMKPXB, TRLLOCK	Dispatch List (True Run List) Locks (one per processor)
DMKLOKTR	Timer Request Queue Lock
DMKPXA, RQLOCK DMKPXB, RQLOCK	Dispatcher Request Queue Locks (one per processor)
CPEXBLOK IOBLOK, TRQBLOK	Queue Lock Deferred execution blocks Processor related blocks

These are system spin locks that are held for very short periods of time. The control program code that runs without the global system lock must manipulate these queues and these locks to ensure system integrity along the unlocked paths.

User-Defined Locks

If you have user-defined areas that are used by more than one virtual machine and you need to serialize their use, you will need to define your own locking conventions. You can use the LOCK macro to obtain and release a PRIVATE lock. VM/SP HPO CP for System Programming has details on how to code the LOCK macro.

Machine Check Handler – Attached Processor and Multiprocessor Applications

A machine check interrupt is initially handled without the global system lock. DMKMCH determines if the error requires system termination, virtual machine termination, or simply recording and continuation. If the system was in a wait state or a virtual machine was in control and the system is not to be terminated, the machine check handler requests the global system lock with the defer option. If the lock can be obtained, normal DMKMCH processing continues. If the lock cannot be obtained, DMKMCH stacks a CPEXBLOK with CPMCHSE set and exits to DMKDSPRU. This CPEXBLOK causes processing to resume at DMKMCHSE with the global system lock held. Any machine checks that occur before the CPEXBLOK processing has completed are considered recursive machine checks and handled appropriately. If the control program was in control and the system is not to be terminated, the machine check handler saves status in the CPEXBLOK, sets CPMCHSE and reloads MCOPSW. CPMCHSE is set to prevent the dispatcher from starting any new work on this processor until the machine check processing has completed.

DMKMCH passes control to DMKACRCT if channel termination has been set. DMKACR will attempt to recover the failing channel or channels by issuing CLRCH to each affected channel. If CLRCH does not restore a failing channel to an operational state, and if the system can continue operation without the failing channel, DMKACR will mark all paths through the channel offline and return to DMKMCH.

For machine checks on a Vector Facility, DMKMCH will take one of two actions: For a Vector source machine check, DMKMCH will reset the current user (if any) and place that user in console function mode. For a Vector failure machine check, (or for the 12th Vector source machine check since initialization), DMKMCH will disable the Vector Facility. If there is more than one Vector Facility connected to the complex, normal operation will continue, except for the user (if any) whose integrity is exposed because of the error. If no other Vector Facility is available, current and future users will be informed of the facility's status when they attempt to use it. A VARY-ON of the facility may allow re-access.

DMKMCH passes control to DMKMCTPT if the system is running in attached processor or multiprocessor mode and a decision has been made to terminate the system. In general, if a virtual machine was running when the machine check occurred, only that virtual machine is terminated.

DMKMCTPT determines if the system can continue and if the processor can continue. If the machine check was not a clock error or the control program was in control on either processor, the other processor is signalled to stop and store status and a wait state PSW is loaded on the failing processor. An attempt is made to issue message 610W to the operator before the main processor is stopped. If the machine check was a clock error on a non-I/O processor and the control program was not in control, the main processor is signalled via an external call to initiate automatic processor recovery with an indicator to continue processing. If the error was a clock error on an I/O processor, and (a) the system is an MP system or (b) the channel-set switching facility is installed, and if the control program was not in control, automatic processor recovery will be initiated on the nonfailing processor.

The malfunction alert interrupt handler (DMKMCTMA) receives control from the external second level interrupt handler. If the malfunction alert came from the main processor in an AP system, and if the channel-set switching facility is not installed, a X'001' wait state disabled PSW is loaded. If the malfunction alert came from an attached processor and a virtual machine was in control, an indication is set to terminate the virtual user and CPAPRPND is set for processor recovery. If the attached processor was in supervisor state, message 610W is sent to the operator and a 013 wait state PSW is loaded. If the attached processor was in a wait state, CPAPRPND is set for processor recovery. If the malfunction alert occurs in a MP system and if a virtual machine was in control on the failing processor, an indication is set to terminate the virtual machine and automatic processor recovery is initiated.

The automatic processor recovery routine (DMKMCTPR) receives control from the external SLIH or the dispatcher. If the system is to continue processing, the vary processor offline routine (DMKCPPUP) is called. DMKCPPUP examines the chain of virtual machines for affinity to the failing processor and shared segment pointers. Any shared segment pointers for the failing processor are switched to point to the recovery processor's shared segments. All the system control blocks and save areas necessary to run in attached processor or multiprocessor mode are also freed. The time from the first timer request queue element is placed into the clock comparator for the main processor.

While preserving the maintained fields in the absolute zero area, the recovery processor's prefix storage area is copied to the absolute zero area and prefixing is stopped. The APUOPER (or MPUOPER) in the PSA is turned off in the absolute zero area, and the prefix storage areas for both processors are freed. The pages and DASD slots held by the failing processor for shared segments are freed by DMKPGT and DMKPTR. A message (194I) is issued, and return is made to DMKMCTPR. For any virtual machines with affinity to the failing processor, DMKMCTPR resets the affinity for each, issues message 621I, and puts the machine in console function mode (if the virtual machine is not disconnected). If a virtual machine is to be terminated, the virtual machine is reset, messages 616I and 619I issued. Normal return causes the system to continue processing in uniprocessor mode.

The action that the machine check handler takes for a given situation is determined by the error itself, the operating environment of VM/SP HPO, and whether the system was performing a CP function or a virtual machine function—the system was not performing at all (a loaded wait state condition when the error occurred). Figure 36 clarifies the action the system takes for the given situations.

	VM/SP HPO Processing				Virtual	Virtual Machine Processing			
Error Condition	Uni- Attached Proc. Processor			Multiproc. Operation	Uni- Attac Proc. Proce			Multiproc. Operation	
		Main	Attach.	Either Processor		Main	Attach.	Either Processor	
Invalid machine check interrupt code	1	1	1	1		1	1	1	
Invalid PSW data	1	1	1	1	1	3	3	3	
Register, Program mask instruction address invalid	1	1	· 1	1	1	3	3	3	
System damages	1	1	1	1	1	3	3	3	
TOD or CPU Clock Errors	1	1		1	1	1	1	3,4	
Multibit (solid) Storage error	1	1	1	1	3,2	3,2	3,2	3,2	
Multibit (intermittent) storage error	1	1	1	1	3,2	3,2	3,2	3,2	
Storage Protect Key (solid) failure	1	1	1	1	3	3	3	3	
Storage Protect (intermittent) failure	2	2	2	2	2	2	2	2	
Malfunction alert	5	1	1	1	5	1	3,4	3,4	
Channel inoperative	6	6	5	6	6	6	5	6	
Vector Facility Failure (or 12th Source Error)	7	7	7	7	3	3	3	3	
Legend: 1 = Load wait state I 2 = Refresh for retry 3 = Terminate the vin 4 = Automatic proce 5 = Not applicable 6 = Channel recover 7 = Vector Facility d	operation rtual machi ssor recove y								

Figure 36. Condition/Action Table for Uncorrectable Errors

Multiprocessor External Interrupts

For external interrupts that can occur in attached processor/multiprocessor mode (time-of-day sync check, malfunction alert, external call, and emergency signal), DMKPSAEX gives control to DMKEXTSL. DMKEXTSL does the following for each kind of interrupt:

Malfunction alert

• Call DMKMCTMA, which will either load a disabled wait state on the appropriate processor or initiate automatic processor recovery, to allow the system to run in uniprocessor mode. If a user was running at the time of the malfunction alert he is terminated.

SHUTDOWN Emergency Signal

Issued prior to shutting the system down.

- Turn off APUOPER in each PSA to indicate that the system is in uniprocessor mode.
- Load a 008 disabled wait PSW on the receiving processor.

EXTEND Emergency Signal

- Disable channel zero.
- Pass control to the dispatcher at DMKDSPRU.

EXTEND EXIT Emergency Signal

• Enable channel zero in control register 2.

QUIESCE Emergency Signal

• Give control to the dispatcher at DMKDSPRU, which will load a wait PSW that is enabled for external calls only.

SYNC Emergency Signal

Issued by DMKCLKMP when the clocks are no longer synchronized (low order synchronization).

• Give control to DMKCLKAP to synchronize the clock on the attached processor. If the set clock fails, the non-IPL'ed processor is terminated with a CLK003 abend.

CLKCHK Emergency Signal

• Give control to DMKCLKCC. If the clock on the non-IPL'ed processor is not synchronized with the IPL'ed processor (high order synchronization) or is not set, then a flag is set to cause DMKCLKMP on the main processor to synchronize the clocks. The non-IPL'ed processor is then put in a wait state enabled for external interrupts. If the clock is not working, the non-IPL'ed processor is terminated with a CLK003 abend.

APR External Call

• Give control to DMKMCTPR to allow the system to run in uniprocessor mode.

RESUME External Call

Cancels a previous QUIESCE.

• Give control to the dispatcher at DMKDSPRU.

WAKEUP External Call

"Wake-up" an idle processor.

- If the system was running a user, reload the external old PSW.
- If the system was not running a user, then try to obtain the SYSTEM lock.
- If the SYSTEM lock is obtained, give control to the dispatcher at DMKDSPCH.
- If the lock is not obtained, give control to the dispatcher at DMKDSPRU.

DISPATCH External Call

Inform the other processor of a processor related CPEXBLOK.

- Try to obtain the global system lock.
- If the system lock is obtained, go to the dispatcher at DMKDSPCH.
- If the lock is not obtained and the system was in a wait state, go to DMKDSPRU.
- If the lock was not obtained and the system was not in a wait state, reload the external old PSW.

Time-of-Day SYNC Check

- Call DMKCLKSC. DMKCLKSC signals the non-IPL processor to quiesce. It then sends message DMKCLK970W to the operator and calls DMKCLKMP. DMKCLKMP issues a SYNC emergency signal to synchronize the clocks. DMKCLKSC issues a RESUME signal to allow the quiesced processor to continue.
- If the SYSTEM lock is held, go to the dispatcher at DMKDSPCH.
- If the SYSTEM lock is not held, go to the dispatcher at DMKDSPRU.

I/O Subsystem

Mainline, nonerror processing in the I/O subsystem runs without the global system lock. Access to fields in the real I/O control blocks (RCHBLOK, RCUBLOK, and RDEVBLOK) is serialized by the I/O lock, a global spin lock. In an attached processor environment, only the main processor is capable of initiating I/O requests and receiving I/O interrupts. If the I/O subsystem running on the attached processor requires that I/O be started, a SWITCH will be issued to resume processing on the main processor.

In a multiprocessor environment, both processors have I/O capability. If either processor receives an I/O request, that processor attempts to initiate I/O operations.

At system generation time, when a channel path to a device is defined on one processor, an alternate logical path is automatically defined for the other processor. Thus, both processors *can* have access to any I/O device in the MP configuration.

If either processor receives an I/O request, that processor attempts to initiate the I/O operation on one of its own paths to the required device. If none of the online paths to the required device is available from the executing processor, that processor queues the I/O request on all busy and scheduled paths to the device, both its own and those of the other processor. If there is no online path from the executing processor, that processor queues the I/O request on the first online and available path for the second processor, as well as on all busy or scheduled paths for that processor.

While it is not required that both processors have access to all I/O devices, heavily used devices should be accessible by both processors to provide efficient system operation and to increase the possibility of system recovery following a processor or channel failure.

Shared Segment

The shared segment subfunction of VM/SP HPO (DMKATS, DMKCFG, DMKCFH, DMKPGS, and DMKVMA) runs under the global system lock on either processor. All protected shared segments are duplicated in a system that is generated for attached processor or multiprocessor mode and that is initialized on a machine with the multiprocessing feature. DMKCFG obtains sufficient storage to construct the duplicate page and swap tables in contiguous storage. The SHRTABLE SHRPAGE pointer points to the page and swap tables for the main processor, and the page and swap tables for the attached processor are at a fixed displacement from the page and swap tables for the main processor. DMKCFG initializes both sets of page and swap tables. Initially, the two swap tables point to the DASD locations specified in DMKSNT. However, as the pages are read into storage and then stolen, each shared page is allocated its own DASD slot and is pointed to by only one swap table entry.

The last user to purge a shared system causes both sets of page and swap tables to be released.

One shared page table is reserved for use by each processor. This includes both problem state and supervisor state execution on behalf of a virtual machine. To accomplish this, each time a virtual machine running a shared system is locked, a test is made to determine whether or not the virtual machine was last serviced on this processor. If it was last serviced on the other processor, all of its shared page table pointers in its segment tables are switched to this processor's shared pages.

DMKPTR is able to steal a shared page from a shared page table reserved for the processor it is running on without notifying the other processor. The virtual page could not appear in the look-aside buffer of the other processor.

The dispatcher releases the VMBLOK lock on LASTUSER following the check for pending interrupts (assuming no fast redispatch possible) unless the virtual machine was running one or more shared systems. In the latter case the VMBLOK lock is not released until the DMKVMA scan for a changed page is completed.

DMKVMA scans all protected shared segments that the virtual machine used. For every changed page that it finds, DMKVMA checks whether or not the system lock is held. If the system lock is held, the changed page is returned to CP free storage. If the system lock is not held, DMKVMA marks the page table entry as invalid, marks the swap table entry as in transit, and indicates that the core table entry is on the free and flush lists. The other virtual machines can continue to use the shared segments. The changed pages are replaced when the next reference to the changed page is made.

If the shared segment is violated, an error message (DMKVMA456) is sent to the violator, and he is placed in console function mode. The user may examine his PSW and registers to determine what caused the violation. The user enters the BEGIN command to resume execution at the point of interruption.

Segment Protection Extension

Segment protection extension is a microcode assist hardware feature that prevents virtual machine users from changing shared system segments. It is an enhancement to virtual machine assist that provides the same level of segment protection currently handled by the control program. The segment protection extension is available on the 308x, 3090, and dual 4381 processors. If the feature is not present and you desire shared-segment protection, CP continues to handle shared-segment protection.

At system initialization, DMKCPI determines whether the segment protection extension is available. DMKCPI initializes the segment protection status field (CPSEGPRT) to zero. The segment protection bit (bit 29) is turned on in the segment table entry and a store is attempted into the location in DMKCPI. If no protection exception occurs, the segment protection extension is not available. If a protection exception occurs, the segment protection extension is available, and CPSEGPRT is set to one. In both cases, the segment protection bit in the segment table entry is turned off. System initialization continues.

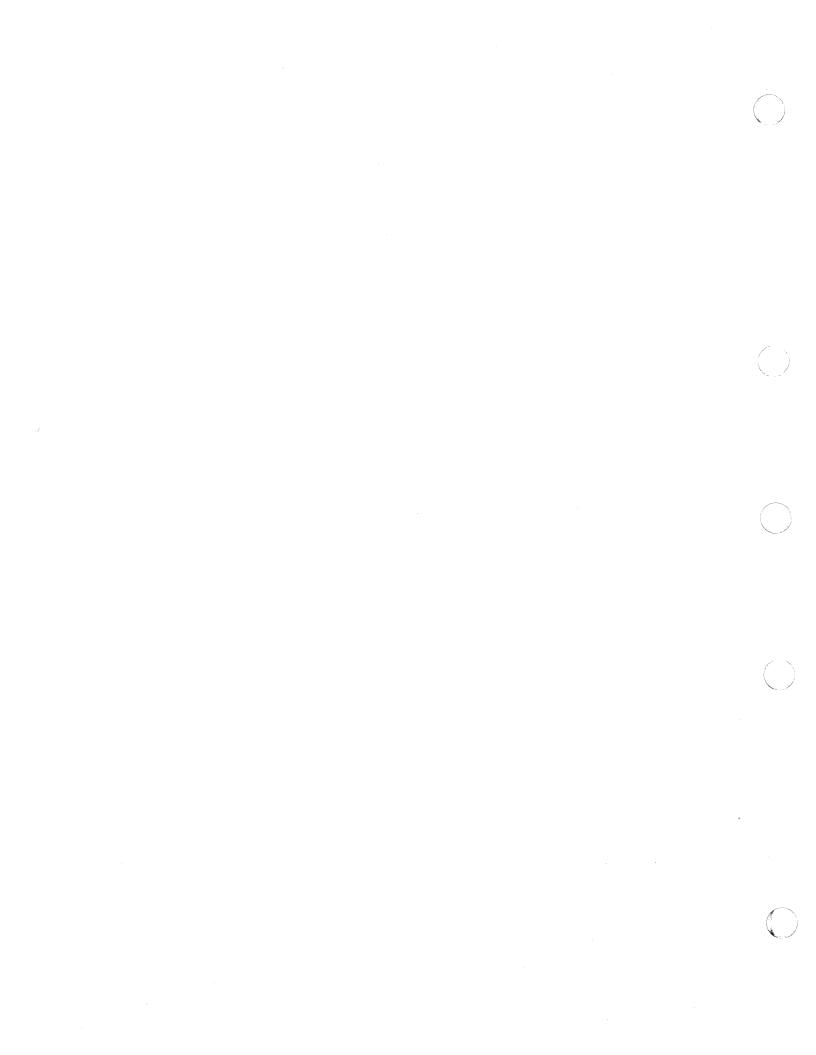
The first time a shared segment is used, a share table (SHRTABLE) and the appropriate number of page and swap tables are built. The status field CPSEGPRT is examined to determine whether the segment protection extension is available. If the segment protection extension is available, the SHRSGPRT field in the SHRFLAG byte of the SHRTABLE is turned on. When SHRSGPRT is on, only one set of page and swap tables are built for the segment. No VMABLOK is built for the segment. With the segment protection feature providing protection at the segment level, the scan for a shared page is eliminated. Additionally, the segment protection extension eliminates the need for the control program to maintain duplicate page tables and swap tables in attached processor or multiprocessor systems with protected shared segments.

You can operate without segment protection by specifying PROTECT = OFF in the NAMESYS macro statement. By specifying PROTECT = OFF, you do not enable the assist for the particular segment. The segment protection extension will still protect segments for which PROTECT = OFF was not specified. But this will also disable the (software) segment protection currently handled by CP and hence there will be no protection at the segment level at all.

CP Method of Operation and Program Organization

This part contains the following information:

- CP Program Organization
- Use of the Annotated Flow Diagram
- Virtual I/O Operations and Interruption Processes.



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CP Program Organization

Use of the Annotated Flow Diagram

The following text sections, which describe each major CP function, are annotated flow diagrams. These diagrams, consisting of logic labels and commentary, describe the general flow and use of CP logic modules and their relationship to other modules while performing a specific function or task. The annotated flow diagrams do not contain references to error messages, abnormal termination conditions, or most control block field labels. This avoids complexity and makes the general logic of CP and its related tasks more understandable to the user. With "understandability" as the key, obtuse and complex logic that is used for obscure and seldom used functions is not described. Also the flow diagram does not indicate nor describe every entry point encountered in a function. Nor do the diagrams illustrate the innumerable times that commonly used modules are utilized. DMKFRE and DMKCVT, the obtaining and returning of free storage and the number base conversion modules are such examples. Annotated flow diagrams are arranged by function and subfunction. Titles for these functions and subfunctions also precede annotated flow text and labels. The text in the charts is prefixed by underscored and capitalized entry points and labels. Entry points are indicated by seven or eight characters; the first three characters are DMK. Labels are indicated by prefixing with a comma and the six-character module identification.

The annotated flow diagrams in this section do not reflect use of the MSS. If there is an MSS attached to your system, consult Appendix B in this volume for flow diagrams of those functions that utilize the MSS (such as logging on a virtual machine that has a minidisk defined on an MSS 3330V volume).

Note: Annotated flow diagrams are not to be construed as trace material. The dynamics of CP operations preclude the use of the annotated flow diagrams, as they are shown in this manual, as traces of CP functions.

CP Interrupt Processing

SVC Interrupts – Problem State

DMKSVDIN

Entry for SVC interrupts from problem state. For problem mode and ADSTOP (SVC X'B3'), the overlaid instruction is replaced.

DMKCFMBK

Console function mode is entered.

DMKSVDIN

For problem state SVC 76 (X'4C'), check for valid parameter passing.

DMKVERD, DMKVERO

Determine the operating SCP used in the virtual machine by examining passed parameters in R0 and R1.

DMKSVD, SVCVER

For invalid parameter passing, error recording is not performed.

DMKIOEVR

The SVC is reflected to the user.

DMKIOFVR

On correct parameter reflection, record the error.

DMKSVD, REFSVCB

REFSVCB is called if TRACE SVC was in effect or if the virtual machine's page zero is not in real storage. Obtains the system lock before continuing. If the system lock is not immediately available, REFSVCB defers the interrupt and exits to DMKDSPRU.

DMKTRCSV

The DMKTRC module is called if TRACE SVC was invoked.

DMKPRGRF

If tracing is not active, flag user as being in instruction wait state and reflect the SVC back to the user.

DMKSVD

If the virtual machine's page zero is in real storage, generate and store an old SVC PSW. Fetch the new SVC PSW. If there is no PSW state change, store user's new PSW in RUNPSW, restore registers, and dispatch via LPSW.

DMKSVD, REFSVCA

If there is a PSW state change, obtain the system lock before continuing. If the system lock is not immediately available, defer the interrupt and exit to DMKDSPRU.

DMKDSPB

Check the altered PSW.

SVC Interrupts – Supervisor State

DMKSVCIN

Entry for SVC interrupts from supervisor state.

DMKSVC, SVCDIE

Entry is for a system failure and is a SVC 0 or SVC 4 abend condition.

DMKDMPDK

Perform partial or full real storage dump.

DMKCKPT

Checkpoint the system.

DMKCPINT

Perform an automatic IPL if indicated.

DMKSVC, SVCLINK

Entry via SVC 8 provides linkage to a called routine in R15.

DMKPTRUL

If called routine is not resident, page it in and return control to the caller by loading the SAVERTN into the old PSW and then load the old PSW. The caller's addressability, SAVEAREA address and return address are maintained in a new SAVEAREA.

DMKSVC, SVCRET

Entry via SVC 12 return control from the called routine to the calling routine and restores addressability via R12 and R13.

DMKPTRUL

If a nonresident module, unlock page to return it to DASD.

DMKSVC, SVCRLSE

Entry via SVC 16 to release the current SAVEAREA used by SVC 8 and 12. Return to caller.

DMKSVC, SVCGET

Entry via SVC 20 to obtain a new SAVEAREA. Return to caller.

DMKSVC, SVCSWIT

Entry via SVC 24 to switch control to the main processor.

External and Clock Interrupt Reflection

DMKEXTIN

Entered via the interrupt key on system console, adjust accounting to charge for supervisor overhead. If problem mode, attention interrupt, update the virtual machine PSW from the external old PSW.

DMKPSA, EXTBUTTN

Exit to dispatcher, if there is no logged-on operator, or the operator is disconnected, or there is no active terminal. If the operator was logged on and the external interrupt key was pressed, disconnect the operator's terminal.

DMKQCOCL

Clear all console requests.

DMKSCNRD

If the device is a terminal or graphic device, issue HIO to the real device.

DMKDSPCH

Exit to the dispatcher.

DMKPSA, EXTBUTTN

For 3704/3705, convert resource identifier for the NCP terminal for the indexable entry into the NICBLOK for the associated VMBLOK, then

DMKRNHND

Reset all BTUs.

DMKDSPCH

Exit to the dispatcher.

DMKPSA, EXTEXTD

Upon location X'80' timer interrupt, indicate the user end of the time slice by storing flag in the VMBLOK'S VMOSTAT.

DMKDSPCH, DMKDSPRU

If the system lock is held or is available, exit to the main entry of the dispatcher, DMKDSPCH. Otherwise, exit to DMKDSPRU.

DMKPSA, EXTTIMER

Upon processor timer interrupt, VMTLEVEL in VMBLOK as a real processor timer interrupt.

DMKTMRVT

Simulate the interrupt.

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DMKDSPCH, DMKDSPRU

If the system lock is held or is available, exit to the main entry of the dispatcher, DMKDSPCH. Otherwise, exit to DMKDSPRU.

DMKPSA, EXTCKC

Upon clock comparator interrupt reflection unchain the active TRQBLOK. Call DMKSTKIO.

DMKSTKIO

Stack the block.

DMKDSPCH, DMKDSPRU

If the system lock is held or is available, exit to the main entry of the dispatcher, DMKDSPCH. Otherwise, exit to DMKDSPRU.

Missing Interrupt Processing

DMKDID

Receives control from a timer interrupt (TRQBLOK).

DMKDIDDA

Checks for DASD devices when a missing interrupt is detected.

DMKDIDGR

Checks for graphic devices, except 1053 and 328X printers, when a missing interrupt is detected.

DMKDIDTA

Checks for tape devices when a missing interrupt is detected.

DMKDIDUR

Checks for unit record devices when a missing interrupt is detected.

DMKDIDMS

Checks for miscellaneous devices when a missing interrupt is detected.

DMKDIDLF

Cleans up certain missing interrupts at LOGOFF/FORCE times.

DMKDID, SCAN

Scans RDEVBLOKs. If RDEVBUZY flag is on, the RDEVTYPC field is examined to verify that the RDEVBUZY flag and timer interrupt are for the same class of device. If the classes match, the RDEVMID bit is turned on. If not, the scan continues. The timer is reset.

DMKDID

Stacks a CPEXBLOK to regain control.

DMKDID, DELQDV

Receives control from the CPEXBLOK and calls DMKIOSHA to perform a HALT DEVICE and CLEAR I/O.

DMKDID

Simulates I/O interrupt through DMKIOTRC. Schedules a ten second timer interrupt to check RDEVMID. A message is sent to the operator and a record is written to the system log record.

Monitor Interrupt Processing

DMKMON

The VM Monitor data collection component uses both sample and trace techniques. Selected system counters are sampled by routines entered periodically via TRQBLOK. Selected events are traced upon execution via monitor call instructions embedded at strategic points in the control program.

DMKENTTI

Entered via TRQBLOK every two seconds (unless specified otherwise with the MONITOR INTERVAL command). A new TRQBLOK is immediately stacked via a call to DMKSCHST to specify return of control to the same entry point two seconds later. This subroutine is a high frequency (relative to the PERFORM, USER, DASTAP sampler) I/O status sampler. All channels are tested for a busy condition with a TCH instruction. All control units and devices are tested for a busy condition by examining the appropriate CP control blocks. The data obtained is accumulated for later sampling by the DASTAP class of data collection in a class 6 (DASTAP) code 2 (I/O status) record. The subroutine DMKENT62 performs this collection after the standard class 6 (DASTAP) code 1 record has been collected by MONCOD61 in DMKMONTI.

DMKMONMI

Entered from DMKPRG after a monitor call in a class currently enabled (as defined in CR 8 mask) has been executed by CP in supervisor state. The monitor call instruction number and code number stored by the hardware in the PSA are used to index branch tables to reach the appropriate data collection routines. As necessary, the data is stored in the monitor I/O buffers before output. Upon completion, control returns to instruction after monitor call.

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Class	Code	Activity Being Monitored
1	0	Begin a console read
	1	Console output
	2	End a console read
	3	Begin sleep with time out
	4	User logon
	5	User logoff
2	2	User dropped from queue
	3	User added to queue
	4	User added to eligible list
3	0	Logical swap-out
	1	Start of physical swap-out
	2	End of physical swap-out
	3	Start of physical swap-in
	4	End of physical swap-in
	5	Logical swap-in
5	0	Beginning of instruction simulation
7	0	SIO for DASD seek
8	2	Device I/O counts and system clocks data collected

DMKMONPR

All data collection subroutines use a common buffer management subroutine to obtain sufficient space in the monitor buffers. When not enough space is available, a switch is made to the next buffer in the chain and the full buffer is scheduled for output via a CPEXBLOK. I/O is handled by DMKIOSQR if tape is in use, or by DMKMIAWO if a spool file is in use. If data collection gets ahead of buffer output and all the monitor buffers are filled, a temporary suspension occurs.

DMKMONIO

Handles normal and abnormal completion of buffer output to disk or tape. For normal completion, the buffer used for I/O is made available next buffer is already full, its output immediately scheduled.

DMKENTKC

Entered via CPEXBLOK at midnight if automatic monitoring to spool file is in effect and it is required to close out the current file and continue monitoring with a new file. DMKENT satisfies the nucleus residency requirements of CPEXBLOK entry point and acts as a stepping stone to DMKMIA. Goes to DMKDSP after successful call to DMKMIAKC.

DMKMIAKC

Sets up a request to invoke a MONITOR CLOSE command in DMKMCCCL.

DMKMCCCL

Executes MONITOR CLOSE command and calls DMKMIACC to complete processing.

DMKMIACC

Invoked by the MONITOR CLOSE command to close the spool file and chain the spool file block to the reader of the virtual machine where data reduction is to take place. Starts new spool file if appropriate.

DMKENTST

Entered via TRQBLOK due to previous determination by automatic monitoring facilities that a MONITOR START SPOOL command should be issued. This entry satisfies the need for CP nucleus residency and immediately calls the pageable DMKMIAIN.

DMKMIAIN

Builds a message buffer containing a MONITOR START SPOOL command and calls DMKMCCCL.

DMKMCCCL

Executes MONITOR START SPOOL command. DMKENTST gives control to DMKDSP after successful execution.

DMKENTET

Entered via TRQBLOK due to previous determination by automatic monitoring facilities that a MONITOR STOP command should be issued at this time. This entry satisfies the need for CP nucleus residency and immediately calls the pageable DMKMIAEN.

DMKMIAEN

Builds a message buffer containing a MONITOR STOP command and calls DMKMCCCL.

DMKMCCCL

Executes MONITOR STOP command. DMKENTET gives control to the dispatcher after successful execution.

DMKMIAST

Entered from DMKCPI when it is determined that automatic monitoring has been requested via the SYSMON macro in DMKSYS and that TRQBLOKs should be queued via calls to DMKSCHST to invoke a MONITOR START SPOOL command and a MONITOR STOP command at specified times in the future. If monitoring is required to start immediately because the start time has passed, a CPEXBLOK is built to give control to DMKENTSC, which invokes the DMKMIAIN mechanism described above.

All other DMKMCC, DMKMNI, DMKMNJ, and DMKMIA entry points are used as a result of the processing of MONITOR commands or special conditions.

DMKMOO

(C	lass 0), U	SER (Class 4), and DASD/Tape (DASTAP, Class 6).
Class	Code	Activity Being Monitored
0	0	Interval and swapping statistics for IPL processor
	1	APU clocks and counters
	2	System resource management data
	3	Page and swap table migration data
	4	Greater than 16 Mb page and swap table migration data
	5	DASD and Paging Storage usage by ALOCBLOK
	6	System-wide swapping data
	7	Free storage usage
4	0	Virtual machine statistics collected
	1	Shadow table maintenance
6	0	DASTAP header initialized and created
	1	Device information collected
	2	I/O utilization
	3	Channel utilization
	4	3880 Model II subsystem status
	5	3880 Model II subsystem counts

Formats records for the timer-driven monitor classes: PERFORM

Three Class 0 monitor call codes have been reserved for special purposes. They are used without actually executing monitor calls, but are a result of MONITOR command processing. They are:

Class	Code	Function
0	97 98 99	Write header record after MONITOR START command Write trailer record after MONITOR STOP command Write suspension record when data collection resumes

Program Interrupt Processing

DMKPRGIN

For a program interrupt received while in supervisor mode (indication of CP module error) and INTRDR+1 does not indicate MONITOR CALL (X'40') exit to DMKPRG, CPERROR.

DMKPRG, CPERROR

Send abend message to the system operator.

DMKDMKPK

Dump storage and initiate loading (via IPL).

DMKPRGIN

For supervisor state and MONITOR CALL save registers in DMKPRGPR.

DMKPRGMI

Do MONITOR CALL interrupt processing (DMKMON).

DMKPRG, PRNSTAT

For paging exception X'11' and EC mode with translation on call DMKVATEX.

DMKPRG, PRNSTAT

For PER interrupt (X'80'), save PER event information (PER code and PER address) in the PERBLOK (if CP PER) or the ECBLOK (if user PER), and turn on the PER pending flag (VMPERPND).

DMKVATEX

Process the exception.

DMKPRGIM

For paging exception, X'11' and EC mode with translation off, and enabled for I/O interrupts and PAGEX on call DMKVATPF.

DMKVATPF

Process the pseudo page fault.

DMKPRG, PAGEXCP

For all other page fault conditions go to DMKPTRAN.

DMKPRG, OBSLOCK

The system lock must be obtained before DMKPTRAN is called. If the system lock is not immediately available, defer the interrupt and exit to DMKDSPRU.

DMKPTRAN

Bring in the page from the auxiliary device.

DMKDSPCH

Exit to dispatcher.

DMKPRG, PRNSTAT

For segment exception X'10' with EC mode on and translation on call DMKVATSX.

DMKPRG, PRNSTAT

For a Vector Operation Exception, X'19', pass control to DMKVFRIN.

DMKVFRIN

Process the vector operation exception, (X'19').

DMKVFSOS

Obtain a vector register save area (and a VECBLOK, if necessary).

DMKVATSX

Process the exception.

DMKPRG, PRGSIMI

For the segment exception, X'10' does not follow the above parameters; process it as an addressing exception.

DMKPRG, TRANSEX

Process X'12' translation exceptions.

DMKPRG, PROG01

For a privileged operation exception of a virtual machine in supervisor mode, examine INTPR + 1. If X'02', call DMKFPS; otherwise, call DMKPRVLG.

DMKFPS

Process the exception and, if successful, dispatch the user. If unsuccessful, return to DMKPRGIN.

DMKPRVLG

Process the exception.

DMKPRV, DMKPRGSM

For virtual machines in problem mode, store the users new program PSW in VMBLOK VMPSW.

DMKPSASV

When the program interrupt occurs and the user's page 0 is not resident or the virtual machine is in EC mode, paging is performed.

DMKDSPB

Check the new PSW.

DMKPRVLG

Validate the privileged operation indicated in VMINST and perform the service.

Code Operation

X'08'	SSK - Set storage key
X'09'	ISK - Insert storage key
X ' 44 '	EX - Execute instruction
X'80'	SSM - Set system mask
X'82'	LPSW - Load PSW
X′9C′	SIO - Start I/O
X'9D'	TIO - Test I/O
X′9E′	HIO - Halt I/O
X′9F′	TCH - Test Channel
X'AC'	STNSM - Store, then AND system mask
X'AD'	STOSM - Store, then OR system mask
X'AE'	SIGP - Signal processor
X'B1'	LRA - Load real address
X'A649'	VRSVC - Save Changed Vector Registers
X'A6CA'	VACSV - Save Vector Activity Count
X'A6CB'	VACRS - Restore Vector Activity Count
X'B202'	STIDP - Store processor ID
X'B203'	STIDC - Store channel ID
X'B204'	SCK - Set TOD clock

Code Operation

X'B206'	SCKC - Set TOD clock comparator
X'B207'	STCKC - Store TOD clock comparator
X'B208'	SPT - Set CPU timer
X'B209'	STPT - Store CPU timer
X'B20A'	SPKA - Set PSW key from address
X'B20B'	IPK - Insert PSW key
X'B20D'	PTLB - Purge TLB PTLB - Purge TLB
X'B210'	SPX - Set prefix
X'B211'	STPX - Store prefix STPX - Store prefix
X'B212'	STAP - Store CPU address
X'B213'	RRB - Reset reference bit
X'B220'	SERVC - Service Call
X'B229'	ISKE - Insert storage key extended
X'B22A'	RRBE - Reset reference bit extended
X ' B22B'	SSKE - Set storage key extended
X'B221'	IPTE - Invalidate Page Table Entry
X'B22C'	TB - Test Block
X'E501'	TPROT - Test Protection
X'B6'	STCTL - Store control registers
X'B7'	LCTL - Load control registers
X'BA'	CS - Compare and swap
X'BB'	CDS - Compare double and swap

DMKPRV, LOCKET

The system lock must be obtained before other supervisor routines are called. If the system lock is not immediately available, defer the interrupt and exit to DMKDSPRU.

DMKHVCAL

On privileged operations of DIAGNOSE X'83' and the associated function code, perform the service.

DMKVSIEX

Execute privileged I/O operations of SIO, HIO, TIO and TCH.

DMKTMRTN

Perform privileged operations related to TOD clock, TOD clock comparator and the processor timer.

DMKPRGSM

Program interrupt is reflected back to the user on invalid instruction operands, unsupported instruction operand codes and DIAGNOSE '83' function codes that are not a multiple of 4.

DMKMHV

Process Service Call.

Virtual I/O Operations and Interruption Processes

CTC Device Operations between Two Virtual Machines

DMKVSIEX

Virtual I/O operation is reflected to DMKVCA, the channel device module, for processing.

DMKVCAST

For SIO, check if the CTC device (channel-to-channel adapter or 3088) is coupled. If not coupled, call DMKDIBSM.

DMKDIBSM

Simulate return status.

DMKVCA, VCASTART

For a coupled CTC device, analyze operations resulting in X-side (read) and Y-side (write) of the data transfer operation.

DMKVCA, VCASIOB

Detected interrupts are presented to users via stacked IOBLOKs and DMKSTKIO.

DMKVCBTS

CTC device TIO activity is determined by examining Y-side information to determine mode and activity.

DMKVCBSH

CTC device HIO and HDV is processed by determining the condition code to present and whether the Y-side should be notified.

DMKVCBRD

CTC device process results from RESET xxx or SYSTEM RESET commands. The CTC device status is reset but the CTC devices are not uncoupled.

DMKVCBRS

Uncoupling of the CTC device is achieved in the VDEVBLOK (VDEVNRDY flag) of the idle CTC device by an invoked DETACH xxx or user LOGOFF. Return to calling routine.

Scheduling I/O for CP and the Virtual Machine

DMKIOSQR

Entered via SVC. Entry point indicates a CP I/O event as indicated in the IOBLOK. For start request, increment the SIO count in the RDEVBLOK and start the device if it is available. If not (device busy or already scheduled) queue the IOBLOK and return the operation to the caller.

DMKIOSQV

Entered via SVC. Entry point indicates virtual machine initiated I/O event. Preserve VMBLOK address in R11, turn off IOBCP bit in the IOBLOK, add 1 to SIO count in the VDEVBLOK (or RDEVBLOK). Process the SIO if there is any available path to the device. If not, queue the IOBLOK and return the operation to the caller.

Standard DASD I/O Initiated via Diagnose

DMKDGDDK

Perform simple count-key-data disk I/O of a standard format. Entry is via DMKHVC code X'18'.

DMKSCNVU

Find device related to SIO cuu address.

DMKFREE

Allocate storage for IOBLOK and RCWTASK.

DMKDGDDK

Build and check the CCW string.

DMKIOSQV

Execute I/O. On completion, post condition code (and error return code in R15, if detected).

DMKDSPCH

Exit to dispatcher.

General I/O Operation Initiated via Diagnose

DMKGIOEX

Perform general I/O operation. Entry is via DMKHVC code 20.

DMKSCNVU

Find device related to SIO cuu address.

DMKFREE

Allocate storage for the IOBLOK.

DMKCCWTR

Build the read CCW list.

DMKDEXIN

Insert Define Extent into channel program if cache should be bypassed.

DMKIOSQV

Queue the I/O request for execution.

DMKGIO, DIAGRTN

On interrupt return, check status.

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DMKUNTFR

If no problem encountered, free storage used for CCW string and IOBLOK.

DMKGIO, DIAGRTN

Reflect the condition code and return code to the user.

DMKDSPCH

Exit to dispatcher.

DMKUNTRN

On returned error condition, convert real CSW to virtual CSW and set in user's page 0.

DMKGIO, GIOEXT

Exit via SVC 12.

Virtual Machine I/O Instruction Simulation and Interrupt Reflection

I/O Instruction Simulation

DMKVSIEX

Entry from DMKPRV to simulate I/O per VMBLOK's VMINST field.

DMKVSI, VIOSIO

On detected SIO, call -

DMKSCNVU

To locate VCHBLOK, VCUBLOK, and VDEVBLOK for the cuu called per SIO instruction.

DMKVSIEX

Determine device availability and set condition code accordingly.

DMKCCWTR

Build real channel program.

DMKDEXIN

Insert Define Extent into channel program if cache should be bypassed.

DMKIOSQV

If the operation is warranted, schedule the operation.

DMKVSI, VIOTIO

For TIO, check device status, pending interrupts, and set appropriate condition codes.

DMKVSI, VIOHIO

For HIO, check for dedicated channel, CE, CU, or device busy condition, and subchannel busy and set appropriate condition codes.

DMKVSI, VIOTCH

Check for dedicated selector or busy channel and check for pending abnormal interrupt and set appropriate condition code.

Interrupt Reflection

DMKVIOIN

Entry from DMKDSP to process the reflected virtual interrupt.

DMKSCNVU

Locate the VCHBLOK, VCUBLOK, and VDEVBLOK.

DMKVIOIN

Analyze blocks and reflect condition code to user. If condition code equals 1 (cc=1), save status from the real device (if real device) and DMKUNTFR.

DMKUNTFR

Translate and store CSW in user's page 0.

DMKVIO, VIOCC1

On TIO or HIO, free the device and set CC = 1.

DMKFRET

Fret storage for the IOBLOK.

DMKDSPCH

Exit to dispatcher.

Virtual Console Simulation

DMKVSIEX

Entry for virtual console activity comes from the SCP stored in the user's virtual machine. The program's generated CCWs and data are reflected to the attached terminal used by the virtual machine operator.

DMKVCNEX

Locate and move non-TIC CCWs from the users virtual storage to a VCONCTL block.

DMKVCN, GETCCW

Update CAW and CSW in respective control block.

DMKVCN, VCNRD

For read operation, build a read console buffer VCONBUF for the input to be read from the terminal.

DMKQCORD

Queue a console read request.

DMKVCNEX

Set return address in VCONCTL VCNRDRET field.

DMKVSTCP

Spool console activity if SPOOL CONSOLE START is specified.

DMKDSPCH

Exit to dispatcher. Wait for completion.

DMKVCN, VCNWR

Calculate and obtain free storage (VCONBUF) necessary for the write to console operation.

DMKVCN, VCNMDAT

Translate and bring in user's data page and move it into VCONBUF.

DMKQCNWT

Queue a console write request.

DMKDSPCH

Exit to dispatcher.

DMKVCN, VCNSNCN

ON a sense operation, set CE and DE in the virtual PSW. Reflect the PCI flag in the PSW if the PCI flag was set in the CCW. Set the IL flag if warranted. Move the sense data from the VDEVBLOK to user storage as designated by the CCW. Update VDEVBLOK's VDEVCSW to reflect status and count.

DMKVCN, VCNCC1

On completion of I/O operation, set appropriate status for command reject, not ready protection check, incorrect length, channel program check. Set appropriate CC and CSW in users page 0. Otherwise post pending interrupt status in VMBLOK, VCHBLOK, VCUBLOK, and VDEVBLOK.

DMKVCN, FLAGTEST

If command chaining, process the next CCW.

DMKDSPCH

Exit to dispatcher.

Local Graphic I/O and Interrupt Processing

DMKGRFEN

Entry for local graphic device enable and disable function (from DMKCPVEN and unstacked CPEXBLOK). Invoking CP ENABLE/DISABLE commands, start or terminate local 3270 display (and supported print devices) and certain system console activity.

DMKGRF, LOGUSER

Format and write out the logo at the screen.

DMKGRF, ATTNINT

Unsolicited attention for RDEVBLOK (enabled).

DMKBLDVM

Build LOGON VMBLOK for logon process.

DMKIOSQR

Schedule request to clear and display the logo.

DMKDSPCH

Exit to dispatcher to wait for interrupt. Successful logon per the next interrupt begins the operation of building the user's virtual machine.

DMKSCNRU

From the IOBLOK, locate the real device blocks related to the interrupt. Analyze IOBLOK CSW and condition code and the I/O operation to determine read/write sequential action. For unit error, retry 10 times (if applicable). If recovery fails, log off. For ATTN interrupts, attempt to log on the new user if unsolicited ATTN occurs. Otherwise, set up for READ CCW string.

DMKIOSQR

Issue the SIO.

DMKDSPCH

Wait for the response.

DMKGRFIN

Local 3270 display and certain system console interrupt entry from dispatcher. On response of CE and DE, go to auxiliary processing routine address in TRQBLOK extension TRQBCRT and execute the processing routines.

DMKGRF, RDATA

Process read response of data plus ENTER key.

DMKCNSED

Edit and modify length count. Move data to caller's buffer.

DMKQCNWT

Schedule rewrite to screen (unless inhibited).

DMKIOSQR

Perform start I/O.

DMKDSPCH

Exit to dispatcher.

DMKGRFIC

Entry point to process CONTASKS queue for local 3270 devices.

DMKGRHIN

Entry point to build channel programs for 3066 devices.

DMKFREE

Get storage for IOBLOK and TRQBLOK.

DMKGRF, BLDCCWS

Execute CONTASK, if appropriate. If not -

DMKDSPCH

Exit to dispatcher.

DMKGRF, RDMINT

For read return, determine function key action and write response (if appropriate) via KEYTBL.

DMKGRFTI

Entry point for processing timer interrupts.

DMKGRCUP

Generate the 3270 orders required to update the screen status, clear the input area, or clear the output area.

DMKGRAOT

Perform APL/TEXT translation for outbound data.

DMKGRHIN

Entry point to handle 3066 CCW strings.

Locate and Validate an ISAM Read Sequence

DMKISMTR

Entry from DMKCCW modules to locate and modify an ISAM CCW string. Using the IOBLOKS IOBCAW locate the RCWTASK. Check for the ISAM read CCW.

DMKISM, CHKRD

Check for the correct ISAM sequence as follows:

- 1. The last CCW in the RCWTASK is a TIC.
- 2. This RCWTASK points to the next RCWTASK with a minimum of 2 CCWs.
- 3. The first modified CCW is in real storage.
- 4. The last byte of the ISAM read overlays the operation code of the first CCW in the next RCWTASK.
- 5. The TIC in the RCWTASK is to the next RCWTASK's first CCW.
- 6. The date address of the first CCW in the next RCWTASK is the same address of the ISAM read +1 as it is in real storage.

DMKFREE

Storage obtained for seven double words save block.

DMKISM, CHKTSK2

Institute the ISAM read modification as follows:

- 1. Set the read to point to the save block data area.
- 2. Set the CP TIC to point to the modified CCW in the same block.
- 3. Set the modified CCW (seek head) in the save block to point to the save block data area.
- 4. Set the CP TIC in the save block to return to the RCWTASK following the modified (seek head) CCW.
- 5. Set the search CCW in the RCWTASK to point to the data area in the same block.

DOUBLEWORD SAVE BLOCK

	Read Address (2) TIC Address	
	Unused	
	Read Data Area	
	(3) Modified CCW	
(4)	TIC to RCWTASK	
	Real Read CCW	
	Real TIC CCW	

DMKISM, CHKTSK2

Return to DMKCCW module via SVC 12.

Scheduling CP and Virtual Machine I/O Operations and Interrupt Handling

DMKIOSQR

Entry to process CP generated I/O. Flag the IOBLOK as a CP generated event. Initiate I/O if path to real device is free (available). If not, queue the IOBLOK and return to caller.

DMKIOSQV

Entry to process I/O for virtual machine I/O operations. Mark IOBLOK as not CP initiated. Save VMBLOK address. If path to the RDEVBLOK or the RCUBLOK is busy queue the IOBLOK and return to caller.

DMKIOS, IOSTATDV

If available status, start the I/O and return to caller.

SIO Operations

DMKIOS, IOBSTART

If I/O request has not been reset, save the address of the active IOBLOK and set device busy. If the device is being reset, unflag scheduled device and scheduled control unit. Stack the IOBLOK and restart the device.

DMKIOS, IOSSIO

Set the subchannel path busy and chain the active IOBLOK from the RDEVBLOK.

DMKIOS, IOSSIO

Locate caller's CAW and issue the SIO. Check SIO completion. Returned condition code sets sequel action.

cc = 0 Indicates successful start

cc = 1 CCW stored, initiate sense operation

cc = 2 Busy condition, retry or requeue IOBLOK

cc = 3 Fatal error (not operational), stack the IOBLOK and return to caller

HIO Operations

DMKIOSHA

Entry point for halting a device. If device is not active, return to caller. If IOBLOK active, reset the IOBLOK to halt the device and mark the device reset in RDEVBLOK.

DMKIOS, IOS10KI

If the channel path is busy with a burst mode operation, stack the IOBLOK to halt the operation when the channel path becomes available. Return to caller.

Interrupt Processing

DMKIOTIN

Entry from I/O new PSW. Check old PSW. If problem mode, save processor status in the VMBLOK.

DMKSCNRN

Locate RCHBLOK, RCUBLOK, and RDEVBLOKs for interrupt unit.

DMKVIOIN

Process dedicated channel interrupt condition. If control unit end or channel available interrupt occurs, restart the operation, if interrupt does not occur stack it.

DMKIOTIN

If the IOBLOK is not active on RDEVBLOK interrupt, call IOTGTIOB to construct an IOBLOK and continue.

DMKIOT, IOSENSE

Schedule sense operation, then go to dispatcher.

DMKIOS, IOSRSTRT

For PCI or CE interrupts, copy and stack the IOBLOK.

DMKCNSIN

Process PCI or CE interrupts, if related to local graphic device or nondedicated TP line.

DMKIOS, DOSENSE

For split seek complete interrupt, rechain the seek and reschedule operations.

DMKSTKIO

Stack IOBLOK and restart any units freed by the interrupts.

DMKDSPCH, DMKDSPA

If the system lock is held or is available, exit to the main entry of the dispatcher, DMKDSPCH. Otherwise, exit to DMKDSPA to try to redispatch RUNUSER.

Terminal Console I/O Control, START/STOP, 3210, 3215, and Others

Enabling/Disabling

DMKCNSEN

Per unstacked CPEXBLOK, on enable or disable function, check current status of the current real device and set flag in RDEVFLAG. Build CONTASK and IOBLOK.

DMKIOSQR

Issue SIO for enabling or disabling function and check return.

DMKDSPCH

Exit to dispatcher.

Process CONTASK Data

DMKCNSIC

Entry from DMKQCN module. Build I/O CCW string as defined by the console device type. Also select the proper line code to interface with the device. Place in CONTASK. For output CONTASK determine the correct translation table applicable to terminal communications (DMKTBL). To append proper control character to the data stream for the particular device type, refer to the following labels:

- DMKCNS, INCWTTY Teletypewriters
- DMKCNS, INC2741 2741, 3767

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- DMKCNS, INC1050 1050, 1051
- DMKCNS, INC3210 3210, 3215

DMKCNS, INCFINS

Attempt to start I/O by halting the current operation, if the operation is a "prepare" CCW or the input is a read and the forthcoming output is a priority write CONTASK.

DMKFREE

Get storage to build IOBLOK, if needed.

DMKCNSIN

Set return address in IOBIRA.

DMKIOSQR

Start I/O. If busy condition encountered build CPEXBLOK and queue for later execution.

DMKDSPCH

Exit to dispatcher.

Start/Stop Terminal Interruption Process

DMKCNSIN, CMBREAK

For an active input task halted, RDEVFLAG=RDEVHIO to process priority output task.

DMKFREE

Build CONTASK for reverse break CCWs.

DMKCNS, CNSBREAK

Move the input CONTASK following the last priority write output CONTASK on the chain.

DMKCNS, CNSIOUC

For unit check with intervention required, assume an attention interruption and build a "prepare" CCW for the 2741.

DMKCNS, CNSLOGF

For unit check and timeout condition - logoff the virtual machine and re-enable the line.

DMKCNS, CNSRTRY

For data check and other conditions, retry the previous operation.

DMKQCOET

Process completed output CONTASK.

DMKCNSIN

Interpret interruption status and CCW residual count for input CONTASK completion.

DMKCNS, CNINCT

Validate input data and control characters and translate to EBCDIC from line code.

DMKTRMID

Attempt to identify, if applicable, the line code identification; PTTC/EBCD or correspondence.

DMKCNSED

Perform line editing of the input buffer.

DMKCNS, CNSRT41

Prepare and issue control CCWs to request status information from the terminal.

Processing the Control CONTASK Interrupt

DMKCNSIN, CNSCTAK

For control task interrupt return, examine the interrupt status according to control task function:

- DMKCNS, CNSTAK Reset control task.
- DMKCNS, CNSCTID Device identification.
- DMKCNS, CNSCTPR Attention signal.

DMKCNS, CNSCTPR

Write "Online" interpretation of response determines retry, or build new CONTASK and execute or stack or process next CONTASK.

DMKQCOET

Process completed CONTASK requests. If no tasks remain for the terminal, set IOBLOK'S IOBIRA to DMKCNSIN and link the IOBLOK to the user.

DMKDSPCH

Exit to dispatcher.

Console Scheduling

DMKQCORD

SVC entry to build CONTASK for input data. Set the input buffer to zeros.

DMKFREE

Get storage to build CONTASK.

DMKQCONQ

Stack CONTASK on RDEVBLOK, if RDEVCON was zero. If not, exit to the appropriate interrupt handler per RDEVTYPC and RDEVTYPE or -

DMKDSPCH

Exit to dispatcher.

DMKQCNWT

SVC entry to build CONTASK for output data. Strip trailing blanks from output message, modify byte count and determine real device destination.

DMKFREE

Get storage to build output CONTASK.

DMKQCN, WRDSCK

Update CONTASK CCW message byte count for the message text, terminal and line control information and (if appropriate) time stamp.

DMKCVTDT

If time stamp required, get the value for CONDATA area.

DMKVSPVP

Spool console message, if VDEVFLAG = VDEVCSPL.

DMKQCN, CRSCAN1

If message data contains carriage returns, X'15', create a separate CONTASK for each line.

DMKQCN, CHKCHAIN

For local and remote 3270s that are not accessed via VCNA, compute whether the 3270 screen will be filled. If not, operate as though the NORET parameter were specified and return to the caller so that the caller has an opportunity to generate more output lines.

DMKQCONQ, WAKEUPR

On first CONTASK or priority CONTASK, enqueue on chain from RDEVBLOK in appropriate location, then call related interrupt handler.

DMKQCONQ, WAKEMUP

If NORET or DFRET specified, build and stack CPEXBLOK to alert the interrupt handler and return via EXIT SVC otherwise go to specified interrupt handler.

DMKQCPTO

Entry via SVC to disconnect and logoff a virtual machine as a result of transmission line failures. Place the virtual machine in a wait state, VMRSTAT = VMCFWAIT.

DMKSCHDL

Alter virtual machine to unrunnable state.

DMKFREE

Get storage for message for the system operator.

DMKSCNRN, DMKSCNRD, DMKCVTBH, DMKSYSNM

Fill in message variables.

DMKQCNWT

Send the user disconnect message to the operator.

DMKQCP, DSCGTRQ

Build TRQBLOK, if needed, for 15 minute delay, schedule it, and exit via SVC.

DMKQCP, DSCTLOG

After time elapse, TRQBLOK is unstacked and VMOSTAT is set to VMKILL for inevitable DMKUSOFF logoff operation.

DMKDSPCH

Exit to dispatcher.

3704/3705 Interrupt Handler

DMKRNHIC

Entry via DMKQCN or via CPEXBLOK for 3704/3705 resource initialization. Locate the NICBLOK and check resource availability.

DMKRNH, LINEBRK

For resource unavailable, set RC = 12 in CONTASK save area and return task via DMKQCNET.

DMKRNH, TAGTASK

For resource available, set CONTASK values per input and output task requirements.

DMKRNH, TASKENQ

Move CONTASK from RDEVBLOK chain to NICBLOK chain.

DMKRNH, RNSTART

On 3704/3705 available condition, search NICLIST and build an IOBLOK if required.

DMKRNHIC, RNEXLST

Search the NICBLOKs for CONTASKs to be sent to 3704/3705, build and chain for output.

DMKRNH, RNCHAIN

Perform necessary function for each resource.

DMKIOSQR

Start output I/O operations.

DMKRNH, RNICHN1

Return via R7.

DMKRNHND

Entry via SVC to schedule resource control tasks.

DMKRNH, RNHNDTK

Build control CONTASK and enqueue it for execution.

DMKRNH, STKCPEX

For NORET specified, build and stack a CPEXBLOK to perform SVC exit.

DMKRNH, RNDEXIT

Attempt to start output via GOTO DMKRNHIC.

DMKRNH, RNFDISC

Entry for 3704/3705 recovery.

DMKNLDR

Load the 3704/3705, if it was not previously loaded.

DMKFRE

Get storage to build CKPBLOK (telecommunications control block), if necessary.

DMKRNH, RNSBITS

Record active line and enabled terminal flag bits.

DMKQCOET

Clear CONTASK chains.

DMKQCPTO

Force disconnect to all active users.

DMKNLEMP

DUMP the 3704/3705.

DMKNLDR

Reload the named program.

DMKRNHND

On "IPL complete" signal, reenable resources.

DMKFRET

Release the CPEXBLOK.

DMKDSPCH

Exit to dispatcher.

DMKRNHIN

Entry via IOBLOK to perform input and output interrupt processing.

DMKRNK, RNIOERR

For input process failure. Analyze the failure and if related to the 3704/3705 and not to a particular resource, either retry or dump and reload.

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DMKRNH, READBUF

Interpret response codes for each BTU received and schedule necessary control operations.

DMKRNH, CMPREAD

Generate response to a read error.

DMKRNH, CMPWRITE

Generate response to a write error.

DMKRNH, CMPCONT

Generate response to a contact task error.

DMKRNH, COMDISC

Generate response to a disconnect task error.

DMKRNH, COMCNTL

Generate response to a control task error.

DMKRNH, UNSOLIT

Generate response to an unsolicited read.

DMKQCOET

Return completed CONTASKs.

DMKRNH, RNSTART

Attempt to restart the 3704/3705.

DMKDSPCH

Exit to the dispatcher.

DMKRNHIN

Entry via IOBLOK to perform input and output interrupt processing.

DMKRNH, SCHREAD

On output, examine interrupt status per IOBLOK values and if ATTN, build and start a read CCW sequence.

DMKRNH, RNIOEUC

If unit check and fatal, dump and reload the 3704/3705.

DMKRNH, RNOREAD

If pending ATTN cleared via SIO -

DMKIOSQR

Reschedule write operations.

DMKRNH, RNSLOWDN

If unit exception, set RDEVSLOW and reschedule rejected CONTASKs.

DMKQCOET

Return only CONTASKs without CONRESP or CONSPLT set. Retain others until final response is received.

DMKRNH, RNSTART

Attempt to restart the 3704/3705.

DMKDSPCH

Exit to dispatcher.

Handling Remote 3270 with Binary Synchronous Lines

Remote Display Station and Binary Synchronous Line Enabling/Disabling

DMKRGBEN

Entered when the NETWORK ENABLE/DISABLE command is issued.

DMKFREE

Get storage for the necessary CONTASK, IOBLOK, and if applicable, BSCBLOK.

DMKRGB, LINESUP

Set up required CCWs and control data in the CONTASK for tasks. These tasks include: enabling the binary synchronous line, READ PARTITION (QUERY) processing if appropriate, enabling a device, LOGO messages, screen formatting, and disable line or device (logoff).

DMKFREE

For logon function build logon VMBLOK.

DMKIOSQR

Start line I/O or device I/O, for not busy condition.

DMKRGB, RGFTASK

For busy condition, build CPEXBLOK and exit to caller.

Request Handler for 3270 I/O Events

DMKRGBIC

Entry from DMKDSP. On a not available line condition, exit to dispatch. For available line, process the associated CONTASKs by queueing the related resource from the NICBLOK.

DMKIOS, RGSTART

Process POLL SIO on a no CONTASK queued condition.

DMKIOSQR

Process selection SIO on available resources and not in control mode per NICBLOK conditions and the CONTASK CONSTAT field.

DMKDSPCH

Exit to dispatcher.

DMKGRAOT

Perform APL/TEXT translation for outbound data.

DMKGRCUP

Generate the 3270 orders required to update the screen status, clear the input area, or clear the output area.

Secondary Interruption Processor for 3270

DMKRGAIN

Entry from DMKIOS, examine line interruption condition. Discard any of the following and go to the dispatcher: nonbinary synchronous line, copied IOBLOK, unsolicited interruption, bisync line flagged not-in-use, nonterminal class device.

DMKRGA, FATALER

For IOBFATAL condition or any nonzero condition code, free all related CONTASK, IOBLOK, IOERBLOK, and BSCBLOK.

DMKRGA, DISASTA

Log off all affected users on that line.

DMKMSWR

Send message to the system operator.

DMKDSPCH

Exit to dispatcher.

DMKRGAIN

If line or terminal response did not fall in the previous category, process via TP code branch. The code is in the fifth byte of the ending CCW or IOBCSW-8.

DMKRGC

Process the input line.

DMKDSPCH

Exit to the dispatcher.

3270 Binary Synchronous Line Error Recovery

DMKBSCER

Entry via DMKIOS to process errors related to the binary synchronous line unit check and channel error conditions. On first error pass, move the IOERBLOK pointer from the IOBLOK to the RDEVBLOK, reset retry and fatal flags, set the ERP flag and call DMKFREE.

DMKFREE

Get free storage for a work area for retry CCWs.

DMKBSC, NOTFIRST

On a not first error condition, test for unrecoverable error condition. Unrecoverable errors include: program check, protection check, chaining check, equipment check, interface control check and channel control checks. If one of these, notify the system operator. Reset flags, initiate error recording and

DMKFREE

Free IOERBLOK.

DMKIOSQR

Go back to scheduler.

DMKRGA

Analyze TP code, sense data CSW residual count and retry count to determine retry or IOBFATAL flag setting.

Real Storage Allocation and Page Management

Page Requests

DMKPTRAN

Entry to translate the virtual address provided by the caller into a real storage address. DMKPTRAN is usually called via the TRANS macro.

DMKPTR, RESTART

Returns to the caller if the virtual address in R1 is beyond the range of the user's storage size.

DMKPTR, ADDROK

Uses the Load Real Address (LRA) instruction to see if the page is resident.

DMKPTR, CHKMOVE

Moves a page in the greater than 16 Mb area to the less than 16 Mb area if the caller specified the BRING option, and if the page is the user's virtual page 0 or if the caller did not specify the VFAULT option.

DMKPTR, TESTLOCK

Locks a resident page in storage if the caller so specified.

DMKPTR, GETRADD

Sets real address in R2, makes PAGTABLE entry valid, sets condition code equal to 0, and exits to the caller.

DMKPTR, INTRAN

For a page that is not resident but is in transit, if the caller did not specify the DEFER option, it stacks a CPEXBLOK for return to DMKPTRAN and exits. If the caller did specify the DEFER option, it locates the CPEXBLOK for the real page or swap set requested, and chains another CPEXBLOK with a return address of TRANRETN to the same chain.

DMKPTR, TRANRETN

When page is no longer in transit, it restores registers and returns to RESTART for processing.

DMKPTR, READPAGE

For a page that is non-resident and not in transit, if the BRING option was not supplied by the caller, it exits to the caller with a condition code of 1. Otherwise, it checks for a DEFER option as in INTRAN. Reads in a page if necessary.

DMKPTR, SELECT

Determines if free lists need to be replenished. Calls DMKSELCT if they do.

DMKSELCT

Replenishes the free lists.

DMKPTR, SWAPFLT

Entry for swap faults. If swap set is in transit or enqueued for allocation, it branches to INTRAN. Otherwise it reclaims all pages from the free list if possible, or if not, sets up to do I/O.

DMKPTR, DOIO

Reads in a page from backing storage.

DMKPTR, SWAPIO

Stacks a CPEXBLOK to set up for I/O for a swap in request.

DMKPTR, CKDEFER

When the caller specified the DEFER option, it builds a CPEXBLOK to return to the user after the page is in storage.

DMKPTR, PAGEIN

When the page is read into storage, it removes the user from the wait state and updates the lock count if required.

Obtain, Return, Lock, and Unlock a Real Page Frame

DMKPTRFR

If the caller is DMKFREE trying to extend system free storage, it returns to the caller the storage in the EXTEND buffer. Otherwise, it branches to the PAGFREE subroutine to get a page of storage from the less than 16 Mb free list.

DMKPTREP

Obtain a page frame from the >16Mb free list or <16Mb free list, whichever is larger.

DMKPTR, PAGFREE

This entry is for requests for a page frame from below the 16 MB line. If no page frame is available, it chains a CPEXBLOK to the proper queue of tasks waiting for pages and exits to the dispatcher. If a page frame is available, it branches to GETFREE.

DMKPTR, PAGFREE2

This entry is for requests for a page frame anywhere in storage. It is called only for virtual storage page faults, not for CP pages. It will try to get a page frame from the greater than 16 MB free list if that list has more page frames than the less than 16 Mb free list.

DMKPTR, GETFREE

Obtains a page frame from the less than 16 Mb free list by unchaining the proper CORTABLE entry, and exits to the caller.

DMKPTR, GETFREE2

Obtains a page frame from the greater than 16 Mb free list by unchaining the proper CORTABLE entry, and exits to the caller.

DMKSELFE

Entered via an unstack of a CPEXBLOK built by DMKPTRFR. Checks for available pages on the flush list. If none are available, it scans the core table for a page.

DMKPTR, SELECT

Determines whether the free lists need to be replenished. If they do, it calls DMKSELCT.

DMKSELCT

Replenishes the free lists.

DMKPTTFT

Processes pages to be returned by chaining them to the appropriate free list. It processes waiting page requests first.

DMKPTRLK

Entered to lock a page in real storage. If the page is already locked, it adds one to the lock count and exits. If the page is not locked, it sets a flag to indicate that the page is locked, sets the lock count to one, and exits to the caller.

DMKPTRUL

Entered to unlock a page from real storage. It reduces the lock count by one and exits to the caller. If the lock count is now equal to zero, it resets the bit that indicated that the page was locked.

Physical Swap-Out

DMKSWAPO

Entry to process physical swap-out. Called by DMKSEL. R2 points to the VMBLOK of the owner who has a swapset to write.

DMKSWA, NEXTSET

Builds a swap set block and initializes it.

DMKPGTSW

Obtains a swap area DASD or Paging Storage slot. On return to DMKSWA, if R1 equals zero, the swap set is paged out to a paging area. Otherwise, the swap-out is done to a swap area.

DMKSWA, SWAPOINC

When swap request is met, it exits to SWAPRET to return to the caller.

DMKSWA, SWAPRET

Returns to the caller.

DMKSWAPD

Entered after completion of a swap-out, and entered via an unstack of a CPEXBLOK. For each valid entry in the SSBLOK, it calls DMKPTRPS to get the page table and swap table addresses, resets the appropriate bits in the swap table for pages swapped out, and calls DMKPTTFT to put frames on the free list.

Logical Swap-In/Prepaging

DMKSWAPI

Called from DMKSCH when a user is added to queue, to prepage a user's pages. R1 contains the address of the SCBLOK.

DMKSWA, NEXTSSBX

Loops through the chain of SSBLOKs anchored at SCBFSSB looking for SSBLOKs that are not marked as old. If it can't find any, it branches to SWAPIWSX to perform a logical swap-in.

DMKSWA, GETVIRT

Physically prepages a swap set. Calls DMKPTRAN, specifying BRING and VFAULT options.

DMKPTRAN

Reads the swap set into storage.

DMKSWA, SWAPIWSX

Logically swaps in a user's pages.

DMKSWA, SWAPIRET

Returns to caller.

Reading/Writing a DASD or Paging Storage Page to/from Virtual Storage

Virtual Storage Management - Non-EC Mode

DMKRPAGT

Entered via SVC call to read DASD page or page from Paging Storage into storage.

DMKRPASV

Entered via SVC call to read SYSSPOOL's virtual pages into storage.

DMKPGUPR

Release DASD space that was previously occupied by this virtual storage page.

DMKRPA, RESIDENT

Remove resident page frames from the user list.

DMKPTTFT

Place these page frames on the free list.

DMKRPA, STORDASD

Update the SWPTABLE with disk address in R0.

DMKPTRAN

Bring the page into storage.

DMKRPA, EXIT

Put real storage address of the virtual page is passed back to the caller in R2.

DMKRPAPT

Entered via SVC call to write out a page to DASD storage or Paging Storage.

DMKPTRAN

Locate the page to be moved and lock it.

DMKRPAPT

Store all registers in CPEXBLOK and flag CPEXR0 as a write request.

DMKPAGIO

Write the page.

DMKRPA, IORETN

Decrease page wait count. If zero results, take user out of page wait.

DMKPTRUL

Unlock the page frame. Return to caller.

Virtual Storage Management - EC Mode

DMKVATAB

Entry via BALR when an EC mode virtual machine needs a shadow table generation and update or purge operation.

DMKVATMD

Get storage to create shadow table. Flag VMBLOK to show shadow table existence.

DMKVATBC

Free shadow page, segment and copy segment when user leaves EC mode or alters CR 0.

DMKVATSI

Entry via BALR to selectively invalidate a shadow page table entry.

DMKVAURN

Entry to perform third level to first level translations and third level translations to second level address translations. Use TRANS macro to access virtual segment and page tables to get the virtual page into real storage.

DMKVATLA

Using the TRANS macro to access the virtual segment and page tables, pass the resulting page and displacement to DMKPRVLG.

DMKVATPX

Invoked by DMKPRGIN when a paging exception is received for an EC mode virtual machine.

DMKVAT, SETUPEX

Perform set up operation and develop page table address.

DMKPTRAN

Get the page.

DMKVATPX

Update the shadow table.

DMKVATSX

Invoked by DMKPRGIN when a segment exception is received for an EC mode virtual machine.

DMKVAT, SETUPEX

Perform setup operation, then invalidate the shadow page table or if none exists, allocate a new shadow table and set it invalid.

DMKVATPF

Entered via DMKVATPG from DMKPRG to simulate pseudo page fault interrupts when a paging exception occurs with pseudo page fault interrupts enabled.

DMKPTRAN

Bring in the DASD or Paging Storage page.

DMKPRGSM

Reflect program check X'14' to the user.

DMKVAT, PAGRES

Entered when the page becomes resident in storage. Build the PGBLOK, set high order bit in the translation exception address field.

DMKDSPCH

Exit to dispatcher.

Allocation and Deallocation of DASD Slots and Paging Storage Pages

DMKPGTPG

Entry to search and allocate a page (slot) of DASD space or a page from Paging Storage for virtual storage paging. Points to DMKSYSPG for ALOCBLOKs allocated as type PG.

DMKPGTPM

Entry to search and allocate a page of DASD space for page migration. DMKPGTPM is passed a SYSPLIST which it uses to begin its search for DASD space. Within a given SYSPAG level, uses the first ALOCBLOK that has space available. Finds the RECBLOK in use, or finds the RECBLOK for the cylinder that is closest to the current head position and that has an available page. If it finds an allocated cylinder with an available page, it calculates the address of the allocated page, marks the page as allocated, increases the number of pages in use, and places the address in R1. Returns to caller.

If no allocated cylinder has an available page, finds the next available cylinder. Then constructs a new RECBLOK for the allocated cylinder, and chains it to the existing RECBLOKS before calculating address and returning to caller.

DMKPGTSG

Entry to search and allocate a page of DASD space for spool file records. (Same operation as DMKPGTPM).

DMKPGUPR/DMKPGUSD/DMKPGUSP

Entry to deallocate a DASD or Paging Storage page used for paging or spooling. If the page has not yet been allocated, it resets the SWPTABLE entry to zero and returns to the caller. Otherwise it locates the RDEVBLOK for the device on which the page has been allocated by indexing into the OWNED list with the device code supplied in the DASD address. It locates the ALOCBLOK and RECBLOK for the cylinder on which the page is allocated. It then finds the bit which represents the page and sets it to zero, and decreases the number of pages in use. If the number of pages in use is zero, it deallocates the cylinder. Otherwise, it resets the SWPTABLE entry to zero, and returns to the caller.

DMKPGUSR

Entry to release a set of DASD pages that belongs to a spool file or user that is no longer needed. Operation is essentially the same as for DMKPGUPR, but when the appropriate RDEVBLOK and RECBLOK are located, it exclusively ORs the bit mask in the dummy RECBLOK against the map in the real RECBLOK to zero out the bits for the pages being deallocated.

DMKPGTCG/DMKPGTDG/DMKPGTDT

Entry to allocate contiguous pages for 370X dump, system dump spool file, and system dump, respectively. It scans the ALOCBLOK for the device to determine if the request can be satisfied. If not, it starts over. If the request can be satisfied, it marks the chosen cylinders as allocated and returns the CCPD address to the caller in GR1. It builds and chains the required RECBLOKs.

DMKPGUDU

Entry to deallocate contiguous DASD pages that were previously allocated for a CP system dump.

DMKPGUSW

Entry to deallocate N contiguous DASD or Paging Storage pages that were used for swapped pages, where N is defined in the swapset block. Deallocation processing is as normal, except the RECBLOK allocation map uses one bit per swap set, rather than one bit per DASD slot or Paging Storage page.

DMKPGUPP

Entry to deallocate DASD or Paging Storage pages that were formerly used for pages that are now in real storage and about to be swapped out.

DMKPGUVG

DMKPGT contains an internal table, PAGETABL, in which the allocation of page frames for the CP paging VMBLOK is kept. The PAGETABL is scanned for a zero bit denoting the page frame is available. The page is marked allocated by setting the bit to one and the address of the page frame is returned to the caller in R1. If no page frames are available, a CPEXBLOK is built and queued to the deferred request chain.

DMKPGUVR

Entry to release a page of virtual storage. Check the chain of deferred requests. If there are none, reset the page bit in the PAGETBL to 0 and exit to the caller. Otherwise, give the page to the first requestor in the deferred chain and stack his CPEXBLOK for the dispatcher.

Shared Segment Storage Management

DMKATSCF

Entry via SVC from the command processor if an ADSTOP, TRACE, or STORE command is to alter a shared page. The virtual machine issuing the CP command will be unshared from the named system, that is, given a private copy.

DMKERMSG

The running virtual machine is informed of the share page violation.

DMKVMASH

Entered from DMPDSP or DMKPTR via BALR. The protected shared page tables are examined for hardware change bit being on. The resulting condition code is reflected to the caller,

DMKVMASW

Entered to switch the virtual machine from one set of page tables to the other.

Temporary Disk Storage Management

DMKTDKGT

Entry to allocate temporary disk (TDISK) space. Upon entry R1 contains the device type and R0 contains the amount of requested space. For FBA devices, DMKTDKGT first converts the block count in R0 to pseudo cylinder values, before it allocates space. DMKTDKGT tries to locate an ALOCBLOK on the allocation chain, and then tries to locate enough contiguous unallocated cylinders on the ALOCMAP. If it can't find enough available space, it places a zero in R8 and returns to the caller. If it finds enough space, it places the address of the first allocated cylinder in R1 and the RDEVBLOK pointer in R8, and returns to the caller.

DMKTDKRL

Entry to deallocate TDISK space. DMKTDKRL uses the address of the RDEVBLOK in R8 to locate the ALOCBLOK for TDISK space on the allocation chain. For FBA devices, the block counts in R0 (number of blocks) and R1 (first block number) are converted to pseudo cylinder values. If the TDISK CLEAR option is in effect, DMKCPXCK is called to build CPEXBLOKs, and DMKZTD is called to clear TDISK space. DMKTDKRL then returns to the caller. If the TDISK CLEAR option is not in effect, DMKTDKRL sets the appropriate bytes in the ALOCMAP to X'02', and returns to the caller.

Paging I/O Scheduler

DMKPAGIO

Entry to initiate page I/O activity. Uses preformatted IOBLOKs from the IOBSTACK for requests on CKD or FBA DASD, or DMKPAGEX for requests on extended count-key-data devices; fills in the CCWs with DASD operation code and values from CPEXBLOK swap table and core table. Chains the CPEXBLOK on the in-transit queue. For swap requests, it gets the IOBLOK extension from DMKPAGXS and fills it in with CCWs and the SSBLOK.

DMKPAG, GETRDEV

Finds the Paging RDEVBLOK.

DMKPAG, SLOTSORT

For a swapping request, it slot-sorts and queues IOBLOKs. For a paging request, it slot-sorts IOBLOKs and temporarily queues them to the appropriate dummy RDEVBLOK.

DMKPAG, FINDIOB

Searches paging IOBLOKs to find those seeking the same cylinder address. If it finds any, it chains the channel programs together with TICs. It does not chain 3880-11/21 requests or swap set requests together.

DMKPAG, QUEUEIO

For 3880-11/21 requests and swap set requests, schedules an immediate I/O operation.

DMKIOSQR

Starts the I/O.

DMKPAG, EXITX

Starts I/O for paging requests by unstacking CPEXBLOKs from the dummy in transit queue and placing them on the in transit queue (DMKPAGQ). It dequeues from the dummy RDEVBLOK the corresponding IOBLOK for each paging CPEXBLOK.

DMKIOSQR

Starts the I/O.

DMKDSPCH

Exits to the dispatcher.

DMKPAH, UNTRANS

Upon interrupt return, unchains the CPEXBLOK from the intransit queue.

DMKSTKCP

Stacks all deferred requests for execution.

DMKPAH, UNSTACK1

Returns IOBLOK to IOBSTACK or DMKPAGEX. Returns IOBLOK extension to DMKPAGXS.

DMKDSPCH

Exits to dispatcher.

Release Virtual Storage Pages

DMKPGSSS

Entry to release partial virtual storage. Per R1 (address of first page to be released) and R2 (address of last page to be released) set partial entry flag.

DMKPGSPO

Entry to check for shared segments and decrement usage count. Store registers and flag full entry condition. Examine VMSHRSYS for shared segments. If so, decrease use count. On zero use count unchain the SHRTABLE from the active list.

DMKPGS, CKCLEAR

On NOCLEAR exit to caller. If not, store number of release pages in R8.

DMKPGS, PGOUT2

Locate page and swap tables for the segment to be released and index to the entry for the first page.

DMKPTRAN

Initiate paging.

DMKPGS, NEXTPAGE

When pages are to be released, it checks for the end of the segment. Otherwise, it exits to the caller.

DMKDSPCH

Exit to caller.

DMKPGSPS

Entry to release storage containing a named system passed by the caller. If register one is nonzero, search the page tables looking for a header equal to the named system. If found, release the swap and page tables and build new ones, if the address range still lies within the user's virtual storage size. If register one is zero, release and rebuild swap and segment tables for all segments above the normal virtual storage size that do not have SHRTABLE entries.

Free Storage Management

DMKFRTTR

Entry to assign storage to free storage management during CP initialization and whenever DMKPTRFR obtains a page from the dynamic paging area for use as free storage.

DMKFREE

Entry to obtain a block of storage, validate input doubleword request (R0).

DMKFRERC

Entry to obtain a block of free storage and return a condition code one if the request cannot be satisfied.

DMKFRE, CHEKSIZE

Checks the size of the request. For subpool size, see "DMKFRE, FREESUB" entry. For larger than 1024 doublewords, see "DMKFRE, FREE01" entry.

DMKFRE, FREESUB

On subpool size request, index into SUBTABLE. For correct size block found, remove block from chain and put the address of the block in R1. Return to caller.

DMKFRE, FREE02

For subpool size not found, get next large subpool size. Test for optimum X'28'. For correct size block found, remove block from chain and put the address of the block in R1. Return to caller.

DMKPTRCO

Obtains a page from the dynamic paging area for DMKFREEA.

DMKFREEA

Attempts to fill request for a cache-aligned block. If unavailable, calls DMKPTRCO for a page of storage from the less than 16 Mb free list. If again unavailable, branches to DMKFREEB to perform the normal free logic.

DMKFREEP

Attempts to fill request for a PRIME storage block. If unavailable, branches to DMKFREEB to perform the normal free logic.

DMKFRE, FREE16

If no block can be found to honor user request, call DMKPTRFR.

DMKPTRFR

Fetch a page from the dynamic paging area. Chain it to the free storage chain. Processing then continues. See entry DMKFRE, FREESUB.

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DMKFRE, FREE01

For a large block request, search the large storage chain. For an equal or larger block found, remove block of request size from the chain, put the address in R1, and return to the caller. Otherwise, see "DMKFRE, FREE16" entry.

DMKFRTRS

Entry to return all subpool blocks to the free storage chain per the SUBTABLE reference, as each subpool block is released, its address and length are placed in R1 and R2 respectively. Branch and link to FRET05 to return the block to the free storage chain (DMKFRELS). Repeat action through all subpools. Return to caller.

DMKFRTRA

Scans cache-aligned subpools looking for pages to return to the dynamic paging area. It is invoked whenever DMKFRERS, the regular subpool collector, is invoked.

When an entire page of contiguous blocks of cache-aligned storage is found, it loads R7 with the address of the first block (the address of the page to be returned).

DMKFRTSN

Entry to search the large storage chain for blocks needing to be returned to the dynamic paging area.

DMKFRTT

Entry to restore block to subpool or free storage. Per R0 and R1 (number of doublewords to be released and and address of the first double word, respectively), the subpool sized block is returned to the appropriate subpool. Update the pointer in the SUBTABLE.

DMKFRTT

Entry to handle requests to return storage that cannot be handled by the CP Assist Fret instruction at DMKFRET.

DMKFRT, FRET01

Return a block of storage to free storage chain by merging into the chain storage addresses in an ascending order of sequence. Return to caller if the block is not the size of a page or more.

DMKFRT, FRET22

If the block includes a whole page frame or more and is in the dynamic paging area, branch and link to FRET22J, which calls DMKPTTFT to give the block back for paging. Call DMKFRTSN to search the large storage chain for other blocks needing to be returned to the dynamic paging area. Return to caller.

DMKFRTTE

Entry to return storage (known to have been obtained from the dynamic paging area) of subpool size or greater to the free storage chain.

Virtual Machine Initialization and Termination

Attaching a Virtual Machine to the System

DMKCNSIN

Entered via interrupt from a console or terminal (not displays) device. If appropriate, determine and store device type in the RDEVBLOK. Write the system online message. Sets up to receive attention interrupt.

DMKBLDVM

On attention interrupt, build skeleton VMBLOK for LOGONxxx.

DMKCFMBK

Send read CCWs to the terminal for LOGON or DIAL response.

DMKTRMID

On response determine translate tables to be used.

DMKCFMBK

Validate command and transfer to DMKLOGON.

DMKLOGON

LOGON command execution.

DMKRPWEP

Verify LOGON password.

DMKDIAL

Dial access linkage to multiaccess system.

DMKUDR

Via user directory access, validate user logon eligibility. On acceptance of eligibility, that is the successful completion of logon, build and allocate control blocks and linkages for the user's virtual machine.

CP Initialization and Termination Procedures

Loading the CP Nucleus on a Cold Machine

The value of CPID controls the actions of DMKCKP. For a cold machine, CPID is neither "CPCP" nor "WARM." In this case, DMKCKP simply loads DMKSAV and transfers control to DMKSAVRS.

IPL

The 24 byte IPL sequence containing the IPL PSW and a read CCW is loaded from the IPL device. The read CCW reads DMKCKP from the IPL device into location X'1000'. (The load point for a V=R system is the V=R size plus X'2000', or DMKSLC plus X'1000'.) The IPL PSW with an instruction address pointing to DMKCKPT is loaded.

DMKCKPT

Initial entry point to load the system.

DMKCKP, READREST

Load the rest of the checkpoint program (DMKCKD, DMKCKF, DMKCKH, DMKCKM, DMKERP, DMKCKW, and DMKCKN).

DMKCKP, COLD

If CPID is not equal to "CPCP" or "WARM," then this is a COLD machine. Move "COLD" into CPID. Load both pages of DMKSAV into the storage location assigned by the loader during system generation. Go to DMKSAVRS.

DMKSAVRS

Load the nucleus up to DMKSAV. For processors with the Test Block facility, issue a Test Block to each 4K block of main storage before loading that page from the nucleus area. Go to DMKCPINT to initialize the system. See System Initialization for more details.

System Initialization

1

The value of CPID controls the actions of the system initialization routines. If CPID = "COLD," then this is a normal system IPL. If CPID = "WARM," then this is an automatic WARM start re-IPL following a system abend or SHUTDOWN REIPL.

DMKCPINT

Entry point to perform system initialization.

DMKCPI

Initialize the new PSWs, the control registers, the TOD clock comparator, the CPU timer, and the CPU ID field of the PSA.

DMKCPI, INITREAL

Check if multiprocessing hardware is present.

DMKCPI, CHKCSS

Check if the channel set switching feature is installed.

DMKCPI, APCHKADR

For systems defined as AP during system generation, check if the other processor is available.

DMKCPI, CALLSTA

Call DMKSTANT.

DMKSTANT

Determine the real storage size, initialize the CORTABLE, allocate free storage and initialize system paging tables.

DMKCPI, CLEARCPA

If the ECPS microcode assist is not present or the wrong level, then replace each microcode instruction in the system with a NO-OP.

DMKCPI, OBTNSAVE

Obtain storage for use by DMKPTR in case the system needs to extend.

DMKCPI, GTXBLOOP

For systems defined as AP during system generation, if the other processor is available, then get storage for use by the switch macro and call DMKAPIPR.

DMKAPIPR

Called only if the other processor is available. Initialize the PSAs for both processors and start prefixing on both processors.

DMKCPI

Call DMKMNTIO.

DMKMNTIO

Entry point to mount I/O devices defined during system generation which are available.

DMKMNT, DOHIO

Issue a HIO to each I/O device defined in DMKRIO. If the device is not available, then go to MOUNTDVI to get the next device. Otherwise continue.

DMKMNT, RELEASE

Issue a RELEASE CCW to each DASD and TAPE which is available.

DMKMNT, READLABL

Read the label from each DASD and TAPE which is available.

DMKMNT, BUILDRDC

Build RDCBLOKs for FBA devices which are available.

DMKMNT, ALLOCSIO

Read the allocation record from DASDs which are available.

DMKMNT, CHKMSS

Issue a SUSPEND CCW to MSS devices which are available.

DMKMNT, CKCPOWND

If this is a CP owned DASD, then call DMKALOCP.

DMKALOCP

Entry point to process CP owned DASD.

DMKALO, ALOCBILD

Create the Maxi-ALOCBLOK for this device.

DMKALO, FLAGRDEV

Mark this device's RDEVBLOK as CP OWNED.

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DMKALO, SAVEDRCT

Compute the displacement into the system file table for the SYSOWN volume. Save the directory and override pointers. If this is the IPL volume, then save the address of this table entry as the location of the primary directory pointer.

DMKPSTIN

Build ALOCBLOKs and RECBLOKs for each Paging Storage area specification.

DMKXSTOR

Called by DMKPST if SYSXSTOR macro was used (instead of SYSPAG macro) to build ALOCBLOKs and RECBLOKs for each Paging Storage area specification.

DMKVFIIN

Initialize Vector Facility.

DMKMNT, DEVONLIN

For each available device, mark the device and control unit as online.

DMKMNT, MOUNTDVI

Repeat DOHIO through DEVONLIN for each I/O device defined in DMKRIO.

DMKMNT, MOUNTCHI

After mounting all the devices in the system, call DMKCPZFD to initialize the cached control units.

DMKMNT

If the second processor is available in a multiprocessor configuration, then repeat DOHIO through MOUNTDVI for the second processor.

DMKMNT, SHIF3

Recompute the RDEVBLOK indexes if necessary.

DMKCPI

Call DMKSEGCP.

DMKSEGCP

Entry point to set up CP's segment, page, core and swap tables.

DMKSEG, VBUFFOK

Call DMKBLDRT to build CP's segment and page tables.

DMKSEG, GETSEG

Fill in the entries in CP's swap and core tables.

DMKSEG, CALCUL

Calculate the number of fixed head and moveable head paging pages.

DMKCPI, SSMTEST

Check to see if the VM assist is available.

DMKCPI, S370EPGM

Check to see if the 370E facility is available.

DMKCPI

Call DMKOPERC.

DMKOPERC

Entry point to locate the operator's console.

DMKOPE

Locate the operator's console.

DMKOPE, ONLINE

Move the system ID into the status area.

DMKOPE, WRITEINT

If CPID is "WARM," then this is an automatic WARM start following a system abend or SHUTDOWN REIPL. If CPID is "WARM" and the system is restarting after an ABEND, then display "VM/SP System restart due to system failure." If CPID is "WARM" and the system is restarting following a SHUTDOWN REIPL, then display "VM/SP System restart due to shutdown REIPL."

DMKOPE, INITWRIT

Display "VM/SP Release x, Service Level xxxx, created on mm/dd/yy at hh:mm:ss."

DMKCPI

Call DMKTODIN.

DMKTODIN

Entry point to initialize the time of day clock.

DMKTOD

Display "It is now hh:mm:ss time-zone day mm/dd/yy."

DMKTOD, GETDATE

If CPID is not "WARM," then allow the operator to change the date and time. If CPID is "WARM," then this is an automatic WARM start following a system abend or SHUTDOWN REIPL.

DMKTOD, NOTCHNG

Schedule a TRQBLOK for midnight tonight. This TRQBLOK will cause the midnight message to be displayed by DMKSCHMD.

DMKTOD

Schedule a TRQBLOK for 60 minutes from now. This TRQBLOK will cause storage subpools to be cleaned up by DMKTMRFR.

DMKCPI, APCHECK

For systems defined as AP during system generation, if the other processor is available, then call DMKAPICK and DMKCLKCK.

DMKAPICK

Entry point to test the other processor's clock, test for the virtual machine assist on the other processor, and complete initialization of the other processor.

DMKCLKCK

Entry point to synchronize the time-of-day clocks.

DMKCPI, DRCTSET

If the IPL volume contains a directory pointer, try to load it. Otherwise, notify the operator and try to load first the first valid directory found in SYSOWN order. Each time DMKUDRBV fails, try another backup directory from the SYSOWN list. If no more SYSOWN volumes are available, issue abend CPI002. If DMKUDRBV is successful, continue. If a valid override pointer is found on the save volume as the directory, call DMKUDROV to load it. Otherwise, notify the operator that the system defaults are used.

DMKCPI

Call DMKCPJNT.

DMKCPJNT

Entry point to continue CP initialization.

DMKCPJ, TLOOP

Verify that the interval timer is running.

DMKCPJ, OWNDECK

Verify that the warm start, checkpoint, and error recording volumes are mounted. If one of the volumes is missing, issue message DMKCPJ912W to the operator. Then load a disabled wait state PSW (code 009). If an AUTOIPL is requested, bypass getting the operator's input and initiate the WARM START.

DMKCPJ, WARMTEST

If CPID is not "WARM," then display "Start ((WARM|CKPT|FORCE|COLD) (DRAIN)) | (SHUTDOWN) :" and get the response. If CPID is "WARM," then this is an automatic WARM start following a system abend or SHUTDOWN REIPL.

DMKCPJ, PROEND

If response was "SHUTDOWN," then load a disabled wait state PSW (code 006).

DMKCPJ, AUTOWARM

If response was "WARM," "CKPT," "FORCE," or "COLD," then call DMKWRMST.

DMKWRMST

Entry point to handle WARM, CKPT, FORCE, or COLD starts. See the separate discussion of each type of start for more detail.

DMKCPJ

Call DMKIDUMP.

DMKIDUMP

Entry point to locate DASD dump space, update allocation counts, mark the 3705s as unloaded, and clear T-disk space.

DMKIDU, DMPALOC

Scan the ALOCBLOKs searching for enough contiguous CKD DASD space to hold a system dump.

DMKIDU, RECALOC

Create RECBLOKs for the dump cylinders on CKD DASD. Go to QUEUEIO.

DMKIDU, SPCFNDC

Fill in the dump SPOOL file SFBLOK for FBA DASD.

DMKIDU, DUMPOK

Update the allocation counts for FBA devices.

DMKIDU, SPCFOUND

Update the allocation counts for CKD devices.

DMKIDU

Call DMKPTRAN to write DMKSYM to the first record of the dump cylinder.

DMKIDU, ALOCLUP

Call DMKVDGAL to create ALOCBLOKs for each CP-owned volume.

DMKVDG

Create and chain ALOCBLOKs from the SYSPLIST and RDEVBLOK chain anchors.

DMKVDH

Build and initialize ALOCBLOKs and RECBLOKs, and check for spool files for DMKVDG.

DMKVDHOR

Called to reorder ALOCBLOKs following their creation.

DMKIDU, NR3705

Mark the 3705 RDEVBLOKs as not ready.

DMKIDU, RELTDK

Stack CPEXBLOKs to clear T-disk space.

DMKCPJ

Call DMKOPELO to logon the system operator.

DMKCPJ, MAPMSG

Display:

DMKCPJ957I STORAGE SIZE = xxxxx K, NUCLEUS SIZE = xxx K, DYNAMIC PAGING SIZE = xxxx K, TRACE TABLE SIZE = xxx K, FREE STORAGE SIZE = xxxx K, VIRTUAL = REAL SIZE = xxxxx K

DMKCPJ, COLDSPEC

If "DRAIN" was not specified, then call DMKCSORD to DRAIN (or STOP) 1) all system punches and printers for a COLD start, or 2) all punches and printers which are not drained for WARM, CKPT, or FORCE starts.

DMKCPJ, PPMAP

Call DMKHVDPP to initialize the program product bit map.

DMKCPJ

Call DMKSEGWR.

DMKSEGWR

Entry point to read in all pageable modules between DMKSAV and DMKCKP in order to force them to the backing device (that is, to create page image copies).

DMKCPJ

Call DMKIDUSF to get a valid SPOOL file ID for the dump SPOOL file.

DMKCPJ, STARTSYS

Call DMKIOEFL to format the error recording area if necessary and then DMKCPJ initializes the machine check new PSW.

DMKCPJ, LOAD37X

Call DMKNLDR to load 370X programs into the enabled 370X devices.

DMKCPJ, CPJENAB

Call DMKCPVAE to re-enable terminal and graphics devices and call DMKNETAE to re-enable lines and stations.

DMKCPJ

Call DMKOPEAC to auto start MONITOR and auto log the AUTOLOG1 virtual machine.

DMKCPJ

Initialize IUCV control blocks.

DMKCPJ, CPJMIH

Create and schedule missing interrupt handler TRQBLOKS.

DMKCPI, CPIEND

Display "DMKCPI966I Initialization complete."

DMKCPI

Call DMKOPEDC to disconnect the operator if the operator was not logged on when the system ABENDED.

DMKCPI, INITDONE

Go to DMKDSPCH to begin dispatching virtual machines.

Warm Start Processing

DMKWRMST

Entry point to handle WARM, CKPT, FORCE, or COLD starts. DMKWRMST is called by DMKCPJ during system initialization. For a WARM start, R2=02. Call DMKCKTMP.

DMKCKTMP

Get storage to create and initialize spool file bit map.

DMKWRM, EN3705

For 37xx devices create CKPBLOKs from the information saved in the terminal buffer.

DMKWRM, ENR3270

Enable binary synchronous lines by clearing NICBLOK offline flag (if appropriate).

DMKWRM, ACNTRT

Reconstruct the saved accounting ACNTBLOKs from the warm start data and chain them from the accounting card anchor (DMKRSPAC).

DMKWRM, WARMLOG

Retrieve the log message from the warm start area and save it in a buffer pointed to by DMKSYSLG.

DMKWRM, WARMSPL

Reconstruct the printer, punch, open reader, and delete SFBLOK chains from the data on the warm start area.

DMKWRM, WARMRECA

Update ALOCBLOKs and create corresponding RECBLOKs from the data on the warm start area.

DMKWRM, WARMHOLD

Reconstruct the SPOOL hold queue blocks (SHQBLOKs) from the data on the warm start area.

DMKWRM, RESTHASH

Reconstruct the Reader Hash Table.

DMKWRM, RESTEXT

Reconstruct the Reader Hash Table extension pages.

DMKWRMSY

Reconstruct SYSSPOOL's virtual storage.

DMKWRM

Call DMKWRNWM.

DMKWRNWM

Put open reader files in SYSSPOOL's virtual storage. Turn on the system-unique bit in the spool file bit map for each printer, punch, open reader, SHQBLOK, and spool RDEVBLOK.

DMKWRM, WARMCLR

Zero the first 8 bytes of the first record on the warm start area. Save the starting time for this system (STARTIME) in the first record and in DMKRSPCV. DMKCKP will compare these two values to ensure that the correct volume is mounted before checkpointing the system.

Checkpoint (CKPT) and FORCE Start Processing

DMKWRMST

Entry point to handle WARM, CKPT, FORCE, or COLD starts. DMKWRMST is called by DMKCPJ during system initialization. For a CKPT start, R2=20. For a FORCE start, R2=40. Call DMKCKTMP.

DMKWRM, ENDER1

Call DMKCKVWM.

DMKCKTMP

Get storage to create and initialize spool file bit map.

DMKCKVWM

Entry point to handle CKPT or FORCE start. Also, verify that a CKPT start is valid—if not, issue error message 917E and enter wait state.

DMKCKVWM, PROCCKPT

Read the first/next page from the checkpoint area.

DMKCKVWM, SCANPAGE

Get the first/next checkpoint slot on this page.

DMKCKVWM, CKVWM2B

For real device slots, restore the checkpointed RDEVBLOK data.

DMKCKVWM, CKVWM2G

For SPOOL hold queue slots, reconstruct checkpointed SPOOL hold queue blocks (SHQBLOKs) from the checkpointed data. Chain the SHQBLOKs off the SHQBLOK chain anchor (DMKRSPHQ).

DMKCKV, NEWTASK

For SPOOL file slots, reconstruct checkpointed SPOOL file blocks (SFBLOKs) from the checkpointed data. This is done by building task blocks (TSKBLOKs)—one for each SPOOL file to be reconstructed. The SPOOL file links (SPLINKs) are followed until each SPOOL file block has been retrieved and all the pages have been allocated.

DMKCKV, CKVWM5

For non-dump SPOOL file slots, allocate the DASD pages used by each reconstructed SPOOL file by beginning with the first page and following the SPOOL page linkage pointers (SPLINK blocks), allocating each page, until the last page is reached.

DMKCKV, CKVWM6E

For *dump* SPOOL file slots, allocate the DASD pages used by each reconstructed SPOOL file by beginning with the first page and sequentially allocating each page until the last page is reached. (Dump SPOOL file pages are allocated as contiguous space and are not SPLINKed together as are standard SPOOL file pages.)

DMKCKV, CKVM9

Mark the slot following the last checkpoint slot as the physical end of the checkpoint slots. Set the next SPOOL file ID to one more than the largest ID that was restored. Return to DMKWRM.

DMKWRM, WARMCLR

Zero the first 8 bytes of the first record on the warm start area. Save the starting time for this system (STARTIME) in the first record and in DMKRSPCV. DMKCKP will compare these two values to ensure that the correct volume is mounted before checkpointing the system.

DMKCKRSY

Rebuild SYSSPOOL's virtual storage. Chain the SFBLOKs off the printer or punch SPOOL file chains as appropriate. Chain the reader SFBLOKs in SYSSPOOL's virtual storage.

The following explains the difference between a CKPT start and a FORCE start:

DMKCKV, MSG915E

Display "DMKCKV915E PERMANENT I/O ERROR ON CHECKPOINT AREA" when an error occurs reading or writing a page from the checkpoint area. For a CKPT start, load a disabled wait state PSW (code 00E). For a FORCE start, if the error occurred while reading or writing the first page of the checkpoint area, then load a disabled wait state PSW (code 00E). For a FORCE start, if the error occurred while reading or writing any record other than the first page, then go to CKVWM1 to get the next checkpoint record. Display "DMKCKV944E ERROR ALLOCATING < CON|RDR|PRT|PUN > FILE user userid spoolid nnnn savedate savetime" when an error occurs allocating one of the DASD pages making up a SPOOL file. For a CKPT start, load a disabled wait state PSW (code 00E). For a FORCE start, delete the corresponding SPOOL file, and continue with the next checkpoint slot.

COLD Start Processing

DMKWRMST

Entry point to handle WARM, CKPT, FORCE, or COLD starts. DMKWRMST is called by DMKCPJ during system initialization. For a COLD start, R2=01. Call DMKCKTMP.

DMKCKTMP

Get storage to create and initialize spool file bit map.

DMKWRM

Call DMKWRNWM.

DMKWRNWM

Entry point to initialize the checkpoint area.

DMKWRM, WARMCLR

Zero the first 8 bytes of the first record on the warm start area. Save the starting time for this system (STARTIME) in the first record and in DMKRSPCV. DMKCKP will compare these two values to ensure that the correct volume is mounted before checkpointing the system.

System Shutdown

The system can be shutdown by using the SHUTDOWN command.

DMKCPSSH

Entry point to handle SHUTDOWN command.

DMKCPS, GOTMP

For AP/MP systems, halt work on the other processor.

DMKCPS, SIGPSTOP

If in single processor mode, then issue a SIGP STOP to the other processor.

DMKCPS, NOSPM

If monitor is active, then call DMKMNISH to stop it. Go to DMKDSPCH to wait for the necessary I/O to complete.

DMKCPS, CLOSECON

Call DMKVSPCO to close the caller's open console SPOOL file and the operator's open console SPOOL file.

DMKCPS, DASDCH

Locate and record statistical data for DASD, tapes and 3800 printers.

DMKCPS, NOSPM2

If SHUTDOWN without REIPL, then set CPID to "CPCP" to indicate a system shutdown. If SHUTDOWN with REIPL, then set CPID to "WARM" to indicate a system shutdown and automatic warm start. Go to DMKDMPRS to load DMKCKP.

DMKDMPRS

Entry point to re-IPL the system.

DMKDMP, RESTART

Get IPL device address.

DMKDMP, SIOIPL

Issue IPL CCW to load the IPL sequence. The 24 byte IPL sequence containing the IPL PSW and a read CCW is loaded from the IPL device. The read CCW reads DMKCKP from the IPL device into location X'1000'.

DMKDMP, B1

Load the IPL PSW read in by the IPL CCW. The IPL PSW has an instruction address pointing to DMKCKPT. DMKCKPT is the entry point to save the system warm start data. See Saving System Warm Start Data for more details.

Saving System Warm Start Data

1

DMKCKPT saves the system warm start data whenever the CPID is "CPCP" or "WARM." If the CPID is "CPCP," then DMKCKPT was invoked by the SHUTDOWN command (without the REIPL option) or following a dump to a tape or printer. (Or the system was STOPPED and then IPLed without clearing main storage.) In this case, a disabled wait state PSW (code 008) is loaded after the system warm start data is saved. If CPID is "WARM," then DMKCKPT was invoked following a dump to DASD or following the SHUTDOWN command (with the REIPL option). In this case the system will be reloaded and reinitialized after the system warm start data is saved.

DMKCKPT

Entry point to save the system warm start data and reload the system if indicated.

DMKCKP

Load the rest of the checkpoint program (DMKCKD, DMKCKF, DMKCKH, DMKCKM, DMKERP, DMKCKW, and DMKCKN).

DMKCKP, GETLIST

Copy the list of pointers from DMKRSP into DMKCKF. This list contains addresses needed by the checkpoint program. The beginning of the list is pointed to by the field ARSPPR in the PSA. This list must be copied because if the nucleus is rebuilt and then the system is shutdown, DMKCKP would be using addresses resolved by the loader for the *new* nucleus to checkpoint the *old* system. These addresses would not necessarily match.

DMKCKP

Call DMKCKDEV.

DMKCKDEV

Entry point to Halt I/O during system shutdown.

DMKCKD

Drain pending I/O interrupts.

DMKCKD, TRYSGICR

For MP systems, halt prefixing on the other processor, and drain pending I/O interrupts on the other processor.

DMKCKD, GETCON

Find a usable path to a console.

DMKCKD, CHINDEX

Get the RCHBLOK, RCUBLOK and RDEVBLOK for the first/next device defined in DMKRIO.

DMKCKD, NETWORK

For 37xx, if the system will be automatically restarting, then save the NCP name, real device address, number of NICBLOKs, and a bit map defining which terminals are enabled in the warm start terminal buffer.

DMKCKD, TEST3851

For 3851s, issue a suspend CCW.

DMKCKD, TESTTERM

For enabled terminals and graphics devices, save the device address in the warm start terminal buffer, and issue a HIO to the device.

DMKCKD, NONTERM

For non-terminal devices, issue a HIO to the device.

DMKCKD, NEXTDEV

Go to CHINDEX to handle the next system generated device.

DMKCKP, CALLERP

Call DMKERP to save MCH, CCH, OBR, and MDR errors on error recording cylinders.

DMKCKP, CALLCKF

Call DMKCKFIL.

DMKCKFIL

Entry point to checkpoint a warm system.

DMKCKF

Call DMKCKHST to validate the first record of the warm start area. Compare the clock value saved on the warm start area with the clock value saved in DMKRSPCV. DMKWRM saved the same value in both places when this system was IPLed. This ensures that the system is shutting down with the same volume it IPLed.

DMKCKF, OPLOGOFF

If CPID = "WARM," then the system will be auto restarting. Save the status of the operator (logged on/logged off) in the first record of the warm start area.

DMKCKF, TERMPUT

If CPID = "WARM," then the system will be auto restarting. Write the warm start terminal buffer containing the addresses of the enabled terminals out to the warm start area.

DMKCKF, DLM1

Write the first delimiter record.

DMKCKF, NXTLOOP

Create and save device and user accounting cards for active virtual machines.

DMKCKF, ACTCHAIN

Save the accounting records.

DMKCKF, DLM2

Write the second delimiter record.

DMKCKF, LOGNXT

Save the log message.

DMKCKF, DLM3

Write the third delimiter record. The third delimiter record contains the date, time, and day of the system log message.

DMKCKF, SAVESF

Save closed printer SPOOL file SFBLOKs.

DMKCKF, DLM4

Write the fourth delimiter record.

DMKCKF, RSPPU

Save closed punch SPOOL file SFBLOKs.

DMKCKF, DLM5

Write the fifth delimiter record.

DMKCKF, RSPRD

Save open reader SPOOL file SFBLOKs.

DMKCKF, CKPCPTRP

Save the last record of open CPTRAP SPOOL files.

DMKCKF, CKPMONIT

Save the last record of open monitor SPOOL files.

DMKCKF, DLM6

Write the sixth delimiter record.

DMKCKHPR

Save spool-to-tape SFBLOKs; monitor SFBLOK copy.

DMKCKF, SAVEDEL

Save SPOOL file SFBLOKs which are on the delete queue. These SPOOL files will be deleted after the system warm starts. They are saved in order to keep the saved allocation records valid.

DMKCKF, DLM7

Write the seventh delimiter record. The seventh delimiter record contains the SPOOL file ID count.

DMKCKF, OWNLP

Save the allocation records.

DMKCKF, DLM8

Write the eighth delimiter record.

DMKCKF, NEXTSHQ

Save the SHQBLOKs.

DMKCKF, DLM9

Write the ninth delimiter record.

DMKCKWHT

Save Reader Hash Table plus extension pages.

DMKCKWSP

Save SYSSPOOL's virtual storage.

DMKCKF

Call DMKCKHWM to write a valid first record out to the warm start area. The first record contains the STARTIME value from DMKRSPCV and the version "VM/SP."

DMKCKP

Call DMKCKMSV.

DMKCKMSV

Entry point to save the virtual machines indicated in the system name table.

DMKCKM, VMSAVEON

If there was an active V = R user and the dump was directed to DASD (CPID = "WARM"), then restore pages 1 through 4.

DMKCKM, ASGO

Call DMKCKNRD to fix up the RDEVBLOKs of the CP owned DASD so that DMKCKNIO can use them. Read in the system name table (DMKSNT).

DMKCKM, ASLOOP1

Call DMKCKNIO to save the pages specified for each virtual machine. DMKSNT specifies the name of the saved system, the volume serial number of the CP owned DASD that this system is to be saved on, the DASD cylinder and page to begin saving this system on, the userid of the virtual machine that is to be saved, and the pages of the virtual machine that are to be saved.

DMKCKM, ASTOD

Save the date, the time, the name under which this system is being saved, the userid of this virtual machine, the VMPSW, the general, floating point, and extended control registers, and the storage keys for the saved pages.

DMKCKP, AUTOWARM

Move "WARM" into CPID to indicate that the system is performing an automatic re-IPL.

DMKCKP, WARM

Load both pages of DMKSAV into the storage location assigned by the loader during system generation. Go to DMKSAVRS.

DMKSAVRS

Load the nucleus up to DMKSAV. For 3081 processors, issue a Test Block to each 4K block of main storage before loading that page from the nucleus area. Go to DMKCPINT to initialize the system. See CP Initialization for details.

Dumping the System and Automatic Re-IPL

DMKDMPDK

Entry point to write a system storage dump to the dump device. Entry occurs via ABENDxxx condition or by system restart. DMKDMP saves PSA values and determines if the dump is full of just CP portion (or SYSSPOOL's virtual storage).

DMKDMP, DMPMSG

Format and issue abend message to operator.

DMKVFRSV

Save all virtual machine's vector register save areas.

DMKDMP, DMPDASD

For dump to DASD, write out CP storage or all storage to selected DASD device.

DMKDMP, DSKEND

For dump to DASD, place the sending record number and the system file number in the dump file SFBLOK.

DMKDMP, RECSRCH

For dump to DASD, chain dump file RECBLOKs to RDEVBLOK, and link dump file SFBLOK onto the system reader chain.

DMKDMP, CKSEND

For dump to DASD, set CPID to "WARM" so that DMKCKP will automatically re-IPL the system. Go to RESTART.

DMKDMQ, DMPTAPE

For dump to tape, dump CP storage or all storage to the selected tape drive per specified tape parameters. Leave CPID equal to "CPCP." Go to RESTART.

DMKDMQ, DMPPRT

For dump to a printer, dump CP storage or all storage to the selected printer. Leave CPID equal to "CPCP."

DMKDMP, RESTART

Get IPL device address.

DMKDMP, SIOIPL

Issue IPL CCW to load the IPL sequence. The 24 byte IPL sequence containing the IPL PSW and a read CCW is loaded from the IPL device. The read CCW reads DMKCKP from the IPL device into location X'1000'.

DMKDMP, B1

Load the IPL PSW read in by the IPL CCW. The IPL PSW has an instruction address pointing to DMKCKPT. DMKCKPT is the entry point to save the system warm start data. See Saving System Warm Start Data for more details.

Dynamic Checkpoint of Spool Files and Spool Devices

DMKCKSPL

Entry from any routine that adds, deletes, or changes the status of closed spool files. Lock the routine, or wait until it becomes unlocked. Bring the map page and spool fileid bit map page into storage and set up the device code of the system residence volume.

DMKCKS, LOOPSHQ

If the change is applicable to a SHQBLOK (hold queue block), make appropriate change on the checkpoint cylinder.

DMKCKS, CKSPL1

If the change is applicable to a SFBLOK, either add, change, or delete it on the checkpoint cylinder.

DMKCKS, CKSPL5

If the change affects a spooling device RDEVBLOK (for example, a START or DRAIN command issued), mark the change on the checkpoint cylinder.

Dump a Virtual Machine with VMDUMP Command

DMKVMDEP

Entry occurs via VMDUMP command. The command options are verified.

DMKPGUVG

Used to get virtual pages for use as spool files.

DMKRPAPT

Used to write out spool records.

DMKPGTSG

Used to obtain temporary space CCPD.

DMKPTRAN

Used to fix and lock storage pages.

DMKPTRUL

Used to unlock main storage pages.

DMKPGUVR

Used to release main storage pages.

IPL the Virtual Machine

DMKCFGIP

For the IPL of a named saved system, the name is verified and resources are checked for availability. Virtual storage is set up with the saved system via SWAPTABLE, SEGTABLE, SHRTABLE updates. For the IPL of device address, the IPL simulator is loaded in the user's storage. Before the IPL simulator is loaded into the user's storage, the contents of that storage is preserved and the area is then used for the IPL simulator.

DMKVMIPL

Read in 24 bytes from the CTCA, reader, DASD or tape unit into the user's virtual location zero. The CCW pointer is now set to the IPLCCW at virtual location X'8' and the IPL CCW string is executed.

DMKVMI, IPLDONE

Control comes here upon completion of the IPL CCW string. For IPL STOP, the virtual machine is placed in console function mode to allow change to nucleus name and apparent storage size before continuation.

DMKVMI, LOADNOW

IPL address is inserted in X'02' if BC mode, or X'BA', if EC mode. The user's CAW and registers are restored and control is given to the user by invoking diagnose X'10' to restore the page which the IPL simulator overlaid and load the current PSW at virtual location zero.

Virtual Machine Termination

DMKUSOLG

Entry is the result of user invoking LOGOFF. Set flags in VMBLOK indicating logout operation.

DMKUSOFL

Entry is the result of class A user issuing a FORCE command. Set flags in the forced user's VMBLOK indicating logout operation.

DMKUSO, USO06

Retain line communication, if HOLD operand specified.

DMKUSO, USO08

Adjust return address to not run the user.

DMKUSOFF

Set VMBLOK flags. In the event of abnormal termination, save a virtual machine if it is enabled for VMSAVE.

DMKUSQFF

Continues logout processing. Called from DMKUSOFF.

DMKTRCND

Called to reset tracing.

DMKPENDA

Called to reset tracing.

DMKACOTM

Accounting called to compute the connect time for the LOGOFF message.

DMKQCNWT

Write the message to the user.

DMKSCHDL

Called to alter user dispatch status.

DMKCFPRR, DMKCSPO

Reset the virtual machine.

DMKMHCRE

Release HCBLOK for pending MSSF request.

DMKVMCAN

Release or return VMCBLOKs if VMCF is active.

DMKVATBC

Release shadow tables (if any).

DMKSCHRT

Dequeue clock comparator request (if any).

DMKBLDRL

Release segment tables, page and swap tables related to the user.

DMKUSO, USO94

Via DMKFRET return user VMBLOKs to free storage.

DMKUSO, USO93

For the system operator, clear and reinitialize the VMBLOK.

DMKFRET

Return all other virtual machine control blocks to free storage.

DMKACOFF

Punch an accounting card for the user.

DMKUSO, USO98

Free LOGOFF message area. Exit to do free storage maintenance. Exit to DMKCFM or DMKDSPCH.

DMKUSOFL

Entry is the result of the invoked FORCE command.

DMKSCNAU

Locate userid VMBLOK.

DMKUSOFL

Set VMKILL in VMBLOK, build CPEXBLOK and stack it for dispatcher.

DMKDSPCH

Upon CPEXBLOK execution, process as at LOGOFF entry DMKUSOFF.

DMKUSODS

Entry from an invoked CP DISCONN command. Set disconnected VMDISC in VMOSTAT.

DMKQCNWT

Send disconnect message to user.

DMKUSODS

Increment return address to DMKCFM by 4 to prevent a return read to the user's terminal. Clear VMTERM field to indicate the user terminal is disconnected.

DMKQCNWT

Send message to system operator informing him of user disconnect status. Exit to DMKCFM.

Console Function (CP Command) Processing

DMKCFMBK

Entry used when the ATTENTION key (or equivalent) is pressed once or twice (according to the VM or CP status) to allow the user to direct a line of input data for CP command processing. Set VMFCWAIT and VMCF bits in VMBLOK indicating wait state and console function mode.

DMKFREE

Builds an 18-doubleword CONBUF buffer for the read operation.

DMKQCORD

Read in the terminal input command line.

DMKSCNFD

Find the START and length of the command.

DMKCFCMD

Scans the CP command line, determines whether a requested CP console function is allowed for a user, and obtains the entry point of the command processor to handle the console function invoked on the command line.

DMKCFC, CMDCLC

Processes command with no subcommands.

DMKCFC, NEXTCMD

Processes command with no subcommands.

See "CP Diagnostic Aids" for a list of all CP commands and the associated processing modules.

DMKQCORD

Read in the terminal input command line.

DMKCFMAT

On NULL data and ATTN key indication, post attention interrupt pending in VDEVBLOK, VCUBLOK and VCHBLOK. Return to run the virtual machine.

DMKCFMRQ

On receipt of CP commands ATTN or REQUEST, process the same as previous entry, DMKCFMAT.

DMKCFM

On receipt of * (asterisk) return to DMKCFMBK to set up another read. If console spooling is enabled, all console input and output including comments are spooled for printer output.

DMKCFMBE

On receipt of BEGIN, simulate the start button on the virtual machine (If optional address is supplied with BEGIN command the supplied address is substituted for the location counter address).

DMKCVTHB

Convert this address to binary notation.

DMKCFMSL

On receipt of the SLEEP command or SLEEP with time value (simulation of virtual machine stop button depression) the VMBLOK's VMSLEEP bit is set. The terminal console keyboard is now inactive until the user hits an ATTENTION key or the SLEEP command times out.

Dispatching and Scheduling

Fast Reflection for the Dispatched Virtual Machine

DMKDSPA

Entry for fast reflection activity. If the user is no longer runnable, or if the system is extending, the fast reflect path is not continued and processing continues at the main dispatcher entry point.

DMKDSP, UPVIRT

If the user is running virtual timers, update and test the user's virtual timers.

DMKDSPA1

If the user is still dispatchable, build the new RUNPSW from either IOOPSW or PROPSW and redispatch the virtual machine.

PSW Validation

DMKDSPB

Entry to dispatcher when the user's PSW has been external to DMKDSP.

DMKDSP, CKPSW

Verify the PSW change.

DMKDSP, CKPEND

Unstack any pending interrupts for the user (if enabled).

MAIN Dispatch Entry

DMKDSPCH

Normal dispatch entry after each interrupt handler has finished processing, and after each CPEXBLOK, I/O request and external interrupt has been serviced.

DMKDSP, RUNTIME

If CPSTATUS indicates return from running a user (CPRUN on), first ensure that supervisor time is being charged to RUNUSER. Check the user for time-slice end or queue-slice end, store the time remaining in the time-slice, and update processor problem state time. Also update virtual timers if running.

DMKDSP, WAITIME

If CPSTATUS indicates return from wait (CPWAIT on), first ensure that supervisor time is being charged to the system. Determine the type of wait (I/O wait, page wait, or idle wait) and save the appropriate new wait time value.

DMKDSP, UNSTACK

For nonrunnable virtual machine, go to label CHKILL in DMKDSP.

DMKDSP, UNSTACK

For runnable user, check pending interruptions for the following:

• DMKDSP, CKPEND

Per interruption (VMPERPND) If user PER is active, then reflect the PER event to the virtual machine. If CP PER is active, call DMKPERIL to handle the PER event. Pseudo page faults (VMPGPND) External interruptions (VMPXINT)

• DMKDSP, UNSTIO I/O interruptions (VMIOINT)

• DMKDSP, STORECSW

I/O interruptions are reflected by swapping user PSWs and storing the unit address and status in low storage.

• DMKDSP, CLEARVMX Clear the pending bits in the VMBLOK.

DMKDSP, CKPSW

Validate the PSW.

• DMKVATBC

For virtual machine leaving EC mode, clean up the shadow tables.

• DMKVATMD

For virtual machine in BC mode and entering translate mode, initialize shadow tables.

DMKDSP, DSPERMSG

For PSW invalid, send error message to virtual machine, and place user in CP mode. If disconnected and invalid PSW, log off user.

DMKDSP, DISPATCH

Complete processing for current user. Call DMKSCHDL if necessary to alter user's dispatching priority.

Selecting the Next Unit of Work

DMKDSP, CKCPSTAK

Process a stacked request. First check the stack of IOBLOKs and TRQBLOKs. If system is not extending, unstack normally. Otherwise, only unstack paging or PCI IOBLOKs.

DMKDSP, WINDOW

Before examining the stack of CPEXBLOKs, open a window for interrupts if the system is not extending.

DMKDSP, CKCPREQ

Check the stack of CPEXBLOKs. If the system is extending, only unstack those blocks that will allow the extend to complete. If the system is not extending, unstack normally. If a CPEXBLOK for the other processor is encountered, give up the system lock and signal the other processor.

DMKDSP, CKUSERS

If no stacked requests can be unstacked, select a user for dispatching. If the system is locked for running users (such as during extend), load a wait state. Scan the run list for a dispatchable candidate. If none is found, load a wait state. If there is also a runnable user for the other processor, signal the other processor. If a runnable user is found, set up to dispatch this user.

DMKDSPWA

Loads base registers, branches to do wait time accounting, then branches to dispatch the VMBLOK.

Scheduling the Next Unit of Work

DMKSCHDL

Main entry to maintain queues of runnable and eligible users and to alter the user's dispatching status and, at queue-drop time, calculate his projected working set size and deadline priority. Also updates VMQBLOK statistical data.

DMKSCH, ADDQ

Decides to which processor the VMBLOK will have affinity.

DMKSCH, CKRSTAT

If the user is now not runnable but was runnable before, mark the user as not runnable. If the user is in the eligible list, drop him from the list. If the user is in a long wait state, set a 300-millisecond delay.

DMKSCH, CKRUN

If the user is now runnable after being in a long idle wait, add him to Q1 (recalculating his deadline priority as necessary).

DMKSCH, CKWAITING

Add users to the run list by searching the eligible list. Users are added on the basis of class (interactive or noninteractive), projected working set (compared with available main storage), availability of processor time, and priority of users presently on the run list.

Queue Drop Processing

DMKSCH, DROPTIM

Entry when a virtual machine is dropped from queue. Tells the scheduler from what queue a virtual machine is dropped by setting on the bit VMSWQ1DR in the field VMSWSTAT when a virtual machine is dropped from Q1.

DMKSCH, DROPQ

Does virtual machine resource evaluation, and drops a virtual machine from queue.

DMKTRQIL

This is the interrupt return address for the 300 millisecond queue drop delay in the scheduler. Sets the appropriate bits and calls DMKSCHDL to drop the virtual machine from queue.

Establishing Addressability

DMKTRQ, LOCKDSQ

Establishes addressability to the proper PXA before obtaining the TRL lock.

Page and Swap Set Migration

DMKPGM, DMKPGMEP

Invoked by the dispatcher if it has a CPEXBLOK stacked for page/swap set migration. It migrates pages to TYPE = PP,PG, and PM storage levels, and swap sets to non-Paging Storage TYPE = SW and PP storage levels.

DMKPGM, DMKPGMUS

Invoked from DMKSWMUS (after command parsing) if page/swap set migration is invoked via the MIGRATE command. If command is valid, a CPEXBLOK is stacked with an entry point of DMKPGMX to migrate all users, or an entry point of RESETUS to migrate one user.

DMKSWMIG

Migrate swap sets from Paging Storage TYPE = SW to next available TYPE = SW level.

DMKSWMUS

Parse MIGRATE command. If the command is valid, stack a CPEXBLOK for migration.

Swap Table Migration

DMKSTR, DMKSTRSM

Invoked if the dispatcher has a CPEXBLOK for swap table migration.

DMKSTR, DMKSTRAN

Invoked from DMKPTR if there is a segment exception.

System Performance Indicators

DMKSTP, DMKSTPX

At initialization time, control the generation of four system control constants. These control constants are then used in the calculation of the 28 system performance indicators, which in turn are used to produce six scheduling control fields. DMKSCH uses these control fields to calculate the deadline priority of the virtual machine at queue-drop time.

CP Timing Facilities

DMKSCHST

Set a clock comparator interrupt request.

DMKSCHRT

Reset a clock comparator interrupt request.

DMKTRQMD

Set up a request block for midnight date change.

DMKTRQ80

Process a real interrupt timer request.

DMKTRQCP

Process a real CPU timer interrupt.

DMKTRQTI

Update system performance indicators.

Spooling Virtual Device to Real Device

Processing Virtual Output Files

DMKVSPEX

Entry from DMKVSI to initiate SIO on a spooling device that is available (not busy and no interruptions pending).

DMKVSTOP

If output device needs to be opened, call DMKSPLOV to build control blocks SFBLOK and VSPLCTL.

DMKFREE

Get a work buffer for the CCWs and the data.

DMKVSRGC

Get first CCW.

DMKPGUVG

Obtain a virtual buffer; the address is stored in VSPVAGE.

DMKPGTSG

Obtain a DASD page; the address is stored in VSPDPAGE.

DMKVSP, PRINTER

Verify CCW opcode and set up initial CSW status.

DMKVSRMD

Get user's data in the work buffer.

DMKVSP, COMPRESS

Truncate all right-justified blanks.

DMKVSQ

Attempt to compress printer opcodes. If spooling space is available, move the CCW and data from the work buffer to the spool buffer; else call DMKPGTSG to get a new spool buffer and write out the full spool buffer.

DMKVSP, FLAGTEST

If the channel program ends (either all CCWs are processed or there is an error), go to LASTDDW; else, call DMKVSRGC to get the next CCW and go to DMKVSP, PRINTER.

DMKVSTCP

On console spooling, the following occurs:

- 1. Skip to channel 1 every 60 lines.
- 2. Write out the system console, spool file buffer every 16 lines.
- 3. Place the system console in a pseudo closed state for checkpoint recovery in the event of system failure.

DMKVSP, LASTCCW

When all CCWs are processed, post interruption pending to the VDEVBLOK, VDEVCSW and return control to the user.

DMKVSPPE

Close spooled printer/punch file via call to subroutine PRTEOF.

DMKVSP, PRTEOF

Page in the last page buffer if not resident. If the spool file is empty, purge it; otherwise, update pointers and write last page buffer to DASD. If PURGE was specified, call DMKSPKDL to purge the file; else, close the file via DMKSPLCV and call DMKFRET to fret the VSPLCTL.

Closing Virtual Output Files

DMKVSUCO

Entry via CP CLOSE command.

DMKVSPST

If the device is busy, defer close operation by building a CPEXBLOK. Stack the CPEXBLOK and exit to the dispatcher.

DMKVSPPE

On device not busy, write final buffer page to DASD storage.

DMKSPLCV

Queue closed virtual printer or punch spool file to the real spool output device, or transfer the file to another user's virtual reader. Also update the SFBLOK with number of copies printed/punched, distribution code, hold status, and file owner ID. If VSPXBLOK with TAG data exists for the spool device, copy the TAG data to the TAG record in the first spool file data buffer.

DMKSPL, TXTXFR

If a "spooled to" file, queue to the end of the reader file chain. Otherwise, chain the SFBLOK to the designated real spool printer or punch.

DMKCKSPL

Checkpoint the new spool file block.

DMKSPL, SETPEND

For a "spooled to" file find a virtual reader with the proper class and in the ready state with no active file, and no pending interrupts. Then build an IOBLOK with IOBIRA of DMKVIOIN.

DMKSTKIO

Stack the IOBLOK.

DMKSPL, SETPEND

Exit to DMKVSP.

DMKSPL, TSTHOLD

For not "spooled to" files and not in user or system hold, find printer or punch with the proper class. Then build an IOBLOK with IOBIRA of DMKRSPEX.

DMKSTKIO

Stack the IOBLOK.

DMKSPL, TSTHOLD

Exit to DMKVSP.

Processing Virtual Input Files

DMKVSWOR

Entry to open a spool input file. If VDEVSPL=0 the file needs to be opened. Build VSPLCTL block and a work buffer.

DMKVSP, SETFLAG

On file-found condition, place first DASD page address in VSPLCTL, VSPDPAGE. Obtain a virtual buffer and retain its address in the VSPLCTL block.

DMKVSP, READER

Check the CCWs for validity, move and expand the data back to its original size and the data is moved from the work buffer to user's virtual storage.

DMKVSP, RDRCOUNT

On EOF, set SFBEOF bit in SFBLOK and return to caller.

Closing Virtual Input Files

DMKVSUCR

For CLOSE operation requested via console command and the device is busy, initiate a delayed close by constructing and stacking the CPEXBLOK for the CLOSE.

DMKVSP, RDREOF

For normal end of file and VDEVSFLG indicates continuous read.

DMKVSP, OPENCONT

Locate the next file and continue reading.

DMKVSP, LASTFILE

For last file, post end status in RDEVBLOK.

DMKVSP, FILECLR

For HOLD status file (VDEVSFLG = VDEVHOLD), call DMKCKSPL.

DMKVSP, FILECLR

Call DMKVSDDL and DMKSPKDL.

DMKVSDDL

Unchain the file (except hold files) from the reader queue. Call DMKCKSPL.

DMKCKSPL

Checkpoint the file.

DMKSPKDL

Delete the file.

DMKVSP, DVICECLR

To clear the device, call DMKRPAGT.

DMKRPAGT

Releases the storage page.

DMKPGUVR

Releases the virtual buffer.

DMKFRET

Releases storage for the work buffer and VSPLCTL block.

Spooling to the Real Printer/Punch Output Device

DMKRSPEX

Entry from the dispatcher when an IOBLOK is unstacked with and interrupted for spooling unit record device. IOBRADD points to the RDEVBLOK RDEVTYPC input or output class.

DMKRSP, RSPLOUT

If RDEVSPOL indicates an available spool device (not active),

DMKFREE

Get storage for a work buffer and build a RSPLCTL block and link it to RDEVBLOK.

DMKRSP, PRNXTFIL

Search printer and punch SFBLOK chains for corresponding device, form, and class. On a found condition, unchain the block, put its address in RSPSFBLK. The FLASH name specified in the SPOOL command, if FLASH is specified, must match the flash overlay name for a 3800 printer.

DMKSEPSP

If called, provides separators for output pages or cards.

DMKTCSET

If the device is a 3800 printer, call this module to set it up.

DMKTCTET

If required, load character arrangement tables and graphic character modifications.

DMKRSP, PROCESS1

Bring first spool data DASD page to the work buffer and convert CCW addresses to real device addresses.

DMKIOSQR

Start the spool device.

DMKRSP, PRNXTPAG

Repeat the process until done.

DMKRSP, REPEAT

Reprocess and reaccess the buffer, if multiple copies are specified.

DMKCKSPL

Checkpoint records the change to COPY count.

DMKSPKDL

Delete the file on completion (unless HOLD specified). If the device is a 3800 printer, check for delayed purge.

DMKRSP, PRNXTFIL

Locate the next spool file to process.

DMKRSP, PRTIDLE

Processing for the device is complete as there are no more SFBLOK, for this device or the device was drained.

DMKFRET

Release work area and completed IOBLOK storage.

DMKDSPCH

Exit to the dispatcher.

Spooling to the Real Input Device

DMKRSPEX

Entry from the dispatcher when an IOBLOK is unstacked with an interrupt for a spooling unit record device. IOBRADD points to the RDEVBLOK RDEVTYPC input or output class.

DMKRSTIN

Handles the card reader.

DMKSPLOR

Assume there is no active file being processed on the real input file reader. The spooling operator has issued the START command to the device to "open" the reader.

DMKSPL, BUILDCTL

Build RSPLCTL and SFBLOK.

DMKPGUVG

Get virtual buffer and place its address in RSPVPAGE.

DMKPGUSG

Get DASD buffer and place its address in SFBSTART and RSPDPAGE, linked together by pointers.

DMKIOSQR

Start the reader.

DMKDSPCH

Await the interruption.

DMKRST, RDERGETID

Check that the first card in the buffer is the userid header. If so, proceed.

DMKRST, RDRCARDS

Preload the buffer with CCWs.

DMKIOSQR

Issue the SIO (SIO's of 42 cards per buffer load).

DMKRST, RDRSIO

Write the buffer to the DASD slot. Repeat until EOF detected.

DMKSPLCR

Close the file on EOF. Queue the file on reader spool chains.

DMKCKSPL

Add the spool reader file block to the checkpoint cylinder data.

DMKSPL, RDRPEND

If the file owner is logged on, and his virtual reader is available, an IOBLOK is constructed with device end pending.

DMKSTKIO

Stacks it.

DMKRST, RDREXIT4

Release storage for virtual buffer, RSPLCTL and the SFBLOK.

DMKDSPCH

Exit to the dispatcher.

Spool File Deletion

1

DMKSPKDL

With R7 not equal to zero, place the specified SFBLOK on the delete chain anchored to DMKRSPDL.

DMKSPKDL

If the delete routine is not running, build a CPEXBLOK to call DMKSPLDR.

DMKSTKCP

Stack it and exit to caller.

DMKSPKDR

Set the DELSW = X'80' (delete routine active).

DMKSPKDR

On unstacking the CPEXBLOK, if the SFBLOK is a system dump file, call DMKDRDDD.

DMKCKSPL

Delete the SFBLOK from checkpoint cylinder data.

DMKDRDDD

Deallocate DASD buffers.

DMKSPK, NEXTSFB

For complete allocation chains of RECBLOKS, call DMKPGTSR.

DMKPGUSR

Deallocate DASD buffer and return to storage held by the dummy RECBLOKs.

DMKSPK, DELSTART

For incomplete allocation RECBLOK chains, deallocate by calling DMKPGTSD.

DMKPGUSD

Deallocate a page at a time via SFBSTART and the IOBLOK until the last page is reached. Call DMKCKTSD and DMKSVGRI. After each SPLINK is deallocated, see if any more files have been added to the delete queue. If so, rechain the current file and deallocate a new file.

DMKFRET

Delete the SFBLOK, then go to DMKSPK, NEXTSFB.

DMKSPK, NEXTSFB

If the delete queue is not empty, process the next SFBLOK in identical manner. Continue until all SFBLOK deletions are completed; then call DMKFRET.

DMKFRET

Delete the IOBLOK.

DMKDSPCH

Exit to the dispatcher.

Recovery Management Support Operation

Establishing the Error Recording Base

DMKIOEFL

Entry from CP initialization module to set up pointers to error recording cylinders.

DMKIOGF1

The STIDP instruction stores processor version and model in CPUID of PSA.

DMKIOG, ISSUEINS

Check attached channels. If stand-alone channel on the 165 or 168, the address of the logout routines is stored in the DMKCCH module.

DMKIOG, CHANGEID

Set up pointers for machine check and channel check record area and extended logout areas.

DMKIOG, IOGMCHIN

Obtain storage for machine check record, extended logout area, and CPEXBLOK. The MCHAREA is also initialized.

DMKIOG, PASTDAVE

Determine the 90% full and 100% full capacity of designated error recording cylinders and store the amount in DMKIOEMX and DMKIOENI respectively.

DMKIOG, FINDREC

Check first record of the error recording cylinders for proper format. If invalid, reformat. If valid but clear, store pointer value in PSA as the first available slot for error record. If valid but used, search for first unused slot and store its value in PSA.

DMKIOGFR

If on a 3031, 3032, or 3033 processor, read frames from the SRF (service record file) device, and write them to the beginning of the error recording cylinders with unique record types.

DMKIOG, CYLFULL

When error recording area is full, inform the operator, and continue.

DMKIOEFL

Turn off the recording in progress switch and exit to caller.

Machine Check Interrupt Processing

DMKMCHIN

Entry via the machine check PSW upon detection of an unrecoverable and nonfatal processor or storage error. Disable soft machine recording; store logout area on the error recording cylinders. The system is enabled for hard machine checks by pointing the PSW to the termination routine.

DMKMCH, ENHARD

For the virtual machine, store status in the VMBLOK.

DMKMCH, MCHSYSIL

For system damage, timing facility, uncorrectable retry, multibit storage error, post system operator message, and flag the system as terminated. If the fault occurred in problem state, terminate the active virtual machine.

DMKMCH, SOFTSTG

For corrected ECC or processor retry, update soft error count and record the error and dispatch the virtual machine.

DMKMCH, MCHSKIP

For multibit storage error in problem mode, exercise storage location to clear up or flag as unavailable (permanent error).

DMKMCH, MCHCHANG

On an altered page condition, the virtual machine is reset, otherwise, the error is recorded and the virtual machine is redispatched.

DMKMCH SPFTEST

Storage key failure. Exercise the 2K page key. If CP area and solid error condition process as DMKMCH, MCHSYSIL, intermittent, restore the key and go to the dispatcher. If key failure and in virtual machine area if permanent error, mark page as unavailable, terminate the user. If intermittent condition refresh the key and dispatch the virtual machine.

DMKMCH, VIRTERM

On conditions that cause the termination or reset. The error is recorded, and both the user and the operator receive status messages. Per the termination flag, VMBLOK, the user is logged off and control returns to the dispatcher or is reset via DMKCFPRR.

DMKMCH, CHANTERM

For channel inoperative errors. DMKACR is called to attempt to recover the failing channel or channels.

DMKCFPRR

Virtual storage is released, the virtual machine is flagged dispatchable and placed in console function mode.

DMKMCH, TERM

On a hard machine check while handling a machine check, the machine check new PSW is loaded with a wait state PSW and the current PSW is enabled for hard machine checks.

DMKMCH, MCHTERM2

Locate the system or the user's VMBLOK.

DMKMCH, OPCOM

Call DMKMCTPT if system is running in attached processor or multiprocessor mode.

DMKMCH, MCHWAIT

Load disabled wait state for uniprocessor system.

DMKMCTPT

Complete processor termination for attached processor or multiprocessor system. If the error is on the attached processor and it is in problem state, signal for automatic processor recovery and stop the attached processor. If the processor is executing in multiprocessor mode and the error occurred while the processor was in problem state, signal for automatic processor recovery and stop the failing processor.

DMKMCT, SWITCH

Make sure processing is on an I/O processor and set up the appropriate wait state code.

DMKMCT, OPCOM

Issue a message to the operator and load a disabled wait state for the attached processor or multiprocessor system.

DMKMCTPR

Perform automatic processor recovery function. Allow system to convert to uniprocessor mode by calling DMKCPPUP.

DMKMCT, PREXIT

Terminate the virtual machine if it is in control. Reset the main processor timer. Clear all lock words and return to the dispatcher.

Channel Check Interrupt Processing

DMKCCHIS

Entry via DMKIOS via CSW channel error.

DMKFREE

Obtain storage and build a CCHREC block and if IOBLOK and RDEVBLOK exist, build an IOERBLOK.

DMKCCH, CCHIOERL

Store the CCHREC address, its length, and the CSW in the IOERBLOK.

DMKCCH, CCHDEPND

Call appropriate channel error analysis module. Analyze channel logout data for validity.

DMKCCH, SCNEND

Record the error on the error recording cylinder, if appropriate.

DMKCCH, CPTERM

Terminate CP if the PSA's terminate flag is set.

DMKCCH, CHANTERM

Attempt recovery from an I/O interface inoperative or a reset channel condition.

DMKCCH, CCHWAIT

Set up X'0F' wait state code and call DMKMCHST to terminate the system.

DMKMCHST

If the system is running in attached processor or multiprocessor mode, call DMKMCTST.

DMKMCH, CALLOPR

Issue an error message to the operator.

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DMKMCH, MCHWAIT

Load a disabled wait state for a uniprocessor system.

DMKMCTST

Make sure system is running on an I/O processor; load disabled wait state.

DMKMCT, CALLOPR

Issue an error message to the operator.

DMKMCT, MFAWAIT

Load a disabled wait state for attached processor or multiprocessor system.

DMKCCH, SCNEND

Unless termination is established, return to DMKIOS for recovery.

Recording the Errors of the Virtual User via SVC 76

DMKVERD

Entry via DMSPSA as a result of SVC 76 detection. Check parameters passed in R0 and R1.

DMKFREE

Obtain storage for a record buffer for the user error record.

DMKVER, BUFFUL

Using valid record type (from the buffer) branch to an appropriate routine to format that particular record type.

DMKVER, VER30

Using RDEVBLOK, VDEVBLOK and VMBLOK, convert virtual data to real values and place in record.

DMKIOERV

Record the error.

DMKDSPCH

Exit to dispatcher.

User Directory Routines

DMKUDRFU

Entry after CP detected LOGON command. DMKSYSPL points to the directory. Determine length of userid, if valid call DMKLOCKQ.

DMKLOCKQ

Lock the directory in storage.

DMKUDR, NXTPAGE

Bring in each directory page and return each page (and clear the buffer) until a UDIRBLOK match occurs or directory's last page is detected.

DMKUDR, FINDUSER

On userid found, move UDIRBLOK to caller's area.

DMKLOCKQ

Unlock the directory in storage.

DMKUDR, EXITCC0

Return to caller.

DMKUDRFD

Entry from calling routine to find the addressed (cuu) device UDEVBLOK in users directory and move it to the caller. Via UMACBLOK locate the UDEVBLOKs.

DMKUDR, FINDDEV

Check to see if the user device address is the same as in the UDEVBLOK. Search the chain until match or end of chain occurs.

DMKUDR, DEVFOUND

For found condition, post condition code zero in user's VMPSW.

DMKUDRRD

Entry from calling routine to read the UDEVBLOK addressed into the caller's buffer. Using the DASD and the user displacement from the UMACBLOK, bring in the buffer page to storage. Determine if the virtual directory page address (UDBFVADD) exists in the user directory buffer blocks. If not call-

DMKPGUVG

and get a virtual page.

DMKRPAGT

For DASD address does not match the UMACBLOK, point to the DASD page and bring in the virtual buffer page. Move UDEVBLOK into callers area and set cc=0 in VMPSW. Return to caller.

DMKUDRRV

Entry to return a virtual page used as a buffer. Determine if UDBFBLOK contains a virtual buffer page pointer (UDBFVADD). If not, exit with cc=1 set in the VMPSW. If a buffer exists, check to see if it is resident; if it is, clear it to zeros.

DMKRPAGT

Return the real page to the system.

DMKRGUVR

Return the virtual page to the system.

DMKUDRRV

Set cc = 0 and return to caller.

DMKUDRBV

Entry from DMKDIRCT or DMKCPINT to build page buffers for each UDIRBLOK.

DMKFREE

Get storage for the virtual buffer page list.

DMKUDR, GETVPAGE

Call DMKPGTVG and DMKRPAGT to get the virtual and real buffer. Save the virtual buffer address in the page list.

DMKUDR, FRETLIST

Encountered I/O error, free the virtual buffer page list, post fatal message, set cc = 3 and return to caller.

DMKUDR, ENDLIST

Swap the new virtual buffer page list with the old list. Anchor the new list to DMKSYSPL.

DMKUDR, FRETLIST

If there was a previous buffer page list, free it. Save the start of the user directory pointer in DMKSYSUD, and return to caller with a cc=0 in the VMPSW.

Save the 3704/3705 Control Program Image Process

DMKSNCP

Entry from DMKHVC and DIAGNOSE code 50. Per the system VMBLOK, locate the DMKRNTBL. The CCPARM virtual address is contained in R1 of the DIAGNOSE instruction.

DMKSNC, NAMECHK

Match via search CCPARM; CCPNAME with DMKRNTBL entries.

DMKSNC, SIZECHK

Verify DASD space requirements for 3704/3705 control program and resource data. The volume required to save (NCPVOL) as indicated in the NCPTBL entry must be available and mounted on the system, on a CP owned and supported paging device.

DMKSNC, SVRESDAT

Save resource data on the NCPVOL device. CCPARM supplies the starting address and size parameters for this write operation.

DMKSNC, SVNCPIM

Save 3704/3705 control program image on NCPVOL device. CCPARM also provides the parameters for this similar operation.

DMKSNC, SAVEFINI

Store cc=0 on no errors and return to caller.

Spool File Checkpoint and Recovery

Dynamic Checkpoint of Spool Files and Spool Devices

DMKCKSPL

Entry from any routine that adds, deletes, changes, the status of closed spool files. Lock the routine, or wait until it becomes unlocked.

DMKCKS, LOOPSHQ

If the change is applicable to a SHQBLOK (hold queue block), make appropriate change on the checkpoint cylinder.

DMKCKS, CKSPL1

If the change is applicable to a SFBLOK, either add, change, or delete it on the checkpoint cylinder.

DMKCKS, CKSPL5

If the change affects a spooling device RDEVBLOK (for example, a START or DRAIN command issued), mark the change on the checkpoint cylinder.

Reconstruction of Checkpointed Closed Spool Files

DMKCKVWM

Entry via DMKWRM during initialization when CKPT or FORCE is specified.

DMKCKV, CKVWM2B

For slots having real device entries, set or reset the RDEVDISA and RDEVDRAN and move in the checkpointed device classes into RVDEVCLAS.

DMKCKV, CKVWM2G

For slots containing spool hold queue block, chain this to the SHQ chain.

DMKCKR, BUILDSFB

Get storage for SFBLOK space and set flags depending upon its last checkpoint activity.

DMKCKR, CKVWM3C

Chain the reader SFBLOK to the appropriate individual reader chain.

DMKCKR, PRTORPUN

Chain the print or punch SFBLOK to the appropriate printer or punch chain.

DMKCKV, TASKCODE

Allocate the DASD buffers of the spool file by reading each buffer to determine the next one and then allocate this page.

DMKCKV, CKVWM6E

For the dump spool file, the buffers are allocated sequentially from the beginning to the end.

DMKCKV, FINISHED

Return to DMKWRM.

Inter-Virtual Machine Communication

DMKVMCFC

Entry from DMKHVC and the DIAGNOSE instruction code X'68'. Builds a VMCBLOK and initializes it with data from the user's parameter list, VMCPARM. The virtual address of VMCPARM is contained in bits 8-11 (rx) of the DIAGNOSE instruction.

DMKVMC, VMCFTBL

Branch table to pass control to the appropriate subroutine based on the subfunction code in VMCPARM.

Subfunction		
Code	Subroutines	
Maaaa		
X'0000'	VMCAUTH	
X'0001'	VMCUAUTH	
X'0002'	VMCSEND	
X'0003'	VMCSENDR	
X'0004'	VMCSENDX	
X'0005'	VMCRECV	
X'0006'	VMCCNCL	
X'0007'	VMCREPLY	
X'0008'	VMCQIES	
X'0009'	VMCRESUM	
X'000A'	VMCIDENT	

DMKVMC, VMCWAKUP

Notifies a virtual machine of a pending VMCF communication by posting a special external interrupt X'4001' unless:

- There is already a special external interrupt posted.
- The virtual machine is running disabled for VMCF interrupts (PSW bit 7 and CR0 bit 31).

DMKVMC, VMCXFER

Transfers data from one virtual storage to another virtual storage. Errors occurring during data transfer are reflected to originating virtual machine via the data transfer return code in the final response interrupt message header.

DMKVMCEX

Called from DMKDSP to reflect an external interrupt message header to a virtual machine. If the VMCF subfunction is a SENDX, the SOURCE data is moved into the external interrupt buffer immediately following the message header.

DMKVMCUA

Called by DMKCFP when a virtual machine is logged off or reset. Uses the VMCUAUTH subroutine (subfunction code X'0001') to dispose of existing VMCBLOKS before turning off virtual machine communication.

Inter-User Communications Vehicle

DMKIUAEP

Entry is a GOTO from DMKPRV when a B2F0 instruction is encountered in a virtual machine. An IUSAVE block is built to contain the information needed to process the request.

DMKIUACP

Entry is via a CALL from a CP module requesting an IUCV function on behalf of the system. An IUSAVE block is built to process the request.

DMKIUACU

Entry is via a CALL from a CP module requesting an IUCV function on behalf of a virtual machine. An IUSAVE block is built to process the request.

DMKIUAPD

The path description entry is located in the caller's path description segment.

DMKIUAPL

Validates a communication path for a system path. CP paths are enqueued or dequeued as requested.

DMKIUAQU

Entry to queue a MSGBLOK on an IUCV message queue. The block is queued in priority, then non-priority FIFO order. If the purge bit is on in the MSGBLOK, the MSGBLOK is not queued and DMKFRET is called to release the MSGBLOK space.

DMKIUA, CKIDLE

Resets the IUCV wait conditions to allow an IUCV external interrupt to be reflected.

DMKIUA, BTABLE

Branch table used to determine the module and subroutine to handle the IUCV request.

DMKIUBRK

Entry is via DMKDSP to determine if there are any IUCV interrupts to be reflected to the virtual machine. The interrupts are reflected in the following sequence: IUCV control interrupts, priority replies, non-priority replies, priority messages, and then non-priority messages.

DMKIUBTB

Table containing the CP system service entry points for connects, messages, severs, quiesces, and resumes.

DMKIUCEP

General entry for this IUCV module.

DMKIUC, ACCEPT

Handle IUCV request to complete a communications path.

DMKIUC, CONNECT

Handle IUCV request to establish a communications path.

DMKIUEEP

General entry point for this IUCV module.

DMKIUERC

Perform the copying of data from the source to the target virtual machine for APPC/VM RECEIVEs.

DMKIUE, RECEIVE

Handle IUCV request to receive the message and cause the actual data transfer. If the message did not require a response, the MSGBLOK is moved to the source's REPLY queue; otherwise, the MSGBLOK is moved to the target's RECEIVE queue.

DMKIUGEP

General entry point for this IUCV module.

DMKIUGGP

This routine is used to locate an available path description entry to be used in establishing a connection.

DMKIUG, PURGE

Handle the IUCV request of the source to cancel a message. The MSGBLOK is located and marked as purged.

DMKIUG, REJECT

Handle the IUCV request of the target to refuse a message. The MSGBLOK is moved to the source's REPLY queue and marked as rejected.

DMKIUJEP

General entry point for this IUCV module.

DMKIUJ, SEVER

Handle the IUCV request to sever an IUCV or APPC/VM path.

DMKIULEP

General entry point for the IUCV module.

DMKIULRP

Perform the copying of data from the source virtual machine to the target virtual machine for APPC/VM SENDS when there is a predefined receive or answer area defined by the target.

DMKIUL, REPLY

Handle the IUCV request to answer a message and cause the actual data transfer of the reply. The MSGBLOK is moved to the source's REPLY queue.

DMKIUL, SEVER

Handle IUCV request to halt communications over a particular path.

DMKIUL, TESTCMPL

Handle IUCV request to test completion of a previously sent message. If the message has completed, the MSGBLOK is moved to the source's REPLY queue.

DMKIUNEP

General entry point for this IUCV module.

DMKIUNIN

Stack an external interrupt of type X'4000' to indicate an IUCV event.

DMKIUN, DESCRIBE

Handle IUCV request to describe a message in the invoker's SEND queue.

DMKIUN, SEND

Handle IUCV request to send a message across a communication path. A MSGBLOK is built and queued on the target's SEND queue.

DMKIUN, SETCMASK

Handle the five types of IUCV control interrupts. These control interrupts are Connection Pending, Connection Complete, Path Severed, Path Quiesced, and Path Resumed. Only those bits set are enabled.

DMKIUN, SETMASK

Handle the IUCV request to change the external interrupt submask. Only those bits set to 1 are enabled, and only those type IUCV interrupts are reflected.

DMKIUN, TESTMSG

Handle IUCV request to test a message. If messages are queued, the condition code is set to 1. If replies are queued, the condition code is set to 2. If both are queued, the condition code is set to 3.

DMKIUPEP

General entry point for this IUCV module.

DMKIUP, DCLBFR

Handle IUCV request to declare a buffer to contain the external interrupt data.

DMKIUP, QUERY

Handle IUCV request to determine the length of an IUCV external interrupt buffer and the maximum number of connections allowed for this virtual machine.

DMKIUP, QUIESCE

Handle IUCV request to prevent incoming messages on the established communication path.

DMKIUP, RESUME

Handle IUCV request to allow incoming messages on a previously quiesced communication path.

DMKIUP, RTRVBFR

Handle IUCV request to retrieve a buffer and terminate IUCV communications.

DMKIUSEP

General entry for this IUCV module.

DMKIUSET

To set the state of an APPC/VM path when a function completes.

DMKIUS, RECEIVE

Handle APPC/VM request to receive a message or signal and to cause the actual data transfer.

DMKIUS, SEND

Handle APPC/VM request to send a message or signal across a communication path.

Console Communication Services

The Console Communications Services (CCS) function of CP links with the VTAM Communications Network Application (VCNA) to provide support for SNA terminals. The following processing paths are the major paths used by CCS.

Unsolicited Interrupt

DMKVCPIL

Entered via call from IUCV (DMKIUE). Register 1 contains IUCV external interrupt buffer. For this path DMKVCPIL is the controlling module that calls all other modules.

DMKIUACP

Does IUCV receive.

DMKVCVKS

Locates SNA control blocks.

DMKVCPIL

Decodes function code (WEBFUN) to determine operation (Attention interrupt with no data).

DMKCFMAT

Posts an attention to the virtual machine.

DMKVCPIL

Stacks a CPEXBLOK to dispatch the virtual machine.

DMKVCRNR

If a batched write is pending, send the write.

DMKVCPIL

Exits to caller.

If data was received with Attention interrupt, DMKVCPIL determines the environment.

DMKFREE

If VM environment, obtains a buffer.

DMKVCPIL

Moves the WEBLOK's WEBDATA field to the buffer.

DMKFREE

Obtains space for a dummy CONTASK for editing.

DMKSCNED

Edits the data.

DMKFRET

Releases storage for CONTASK.

DMKVCPIL

Determines if #CP is in the data.

If #CP not in data, DMKVCPIL chains the buffer address off the virtual machine's VCONRBUF.

DMKCFMAT

Posts attention to virtual machine.

DMKVCPIL

Stacks a CPEXBLOK to dispatch the virtual machine.

DMKVCVEB

Builds a WEBLOK.

DMKVCVLY

Issues an IUCV reply to indicate input data accepted.

DMKFRET

Releases the buffer storage.

DMKVCVNR

If a pending batched write exists, sends the write.

DMKVCPIL

Exits to caller.

If #CP was in data, DMKVCPIL calls:

DMKCFMEN

Passes input to CP.

DMKVCVEB

Builds a WEBLOK.

DMKVCVLY

Issues an IUCV reply to indicate input data accepted.

DMKFRET

Releases the buffer storage.

DMKVCVNR

If a pending batched write exists, sends the write.

DMKVCPIL

Exits to caller.

If not VM environment, DMKVCPIL calls:

DMKFREE

Obtains a buffer.

DMKVCPIL

Moves the information from WEBLOK's WEBDATA field to the buffer.

DMKFREE

Obtains space for a dummy CONTASK for editing.

DMKSCNED

Edits the data.

DMKFRET

Releases the CONTASK storage.

DMKCFMEN

Passes input to CP.

DMKVCVEB

Builds a WEBLOK.

DMKVCVLY

Issues an IUCV reply to indicate input data accepted.

DMKCFRET

Release the buffer storage.

DMKVCVNR

If a pending batched write exists, sends the write.

DMKVCPIL

Exits to caller.

Initiate a READ

DMKVCRRD

Called via SVC from DMKQCORD. For this path DMKVCRRD is the controlling module and calls all other modules.

DMKSCHRT

If a timer has been set for the user, resets it.

DMKVCVIX

If a batched write exists, sends the write.

DMKVCVCE

Writes a Trace Element.

DMKFREE

Gets storage for a WEIBLOK.

DMKVCVEB

Builds a WEBLOK.

DMVCVIX

Does a two-way send of the Read to the virtual service machine.

DMKVCVCE

Writes a Trace Element.

DMKVCRRD

Exits to Dispatcher.

Return from a READ

DMKVCRMT

Invoked from IUCV (DMKIUE). At entry all registers are set up by IUCV. For this path DMKVCRMT is the controlling module and calls all other modules.

DMKVCVCE

Writes a Trace Element.

DMKVCRMT

Decodes the WEBLOK for the function, mode, and logical aid (Enter key). Moves the data from WEBLOK's WEBDATA field to a buffer obtained via a call to DMKFREE. Edits the data for line end characters and tab characters. Moves the data to the user buffer pointed to by the CONTASK.

DMKSCNED

Edits as directed by the CONPARM.

DMKVCRMT

Stores a return address in the save area pointed to by CONRETN.

DMKVCVER

Sets a redisplay timer for the virtual service machine.

DMKVCVIN

Frees the WEIBLOK, IXBLOK, and CONTASK built when the Read was initiated.

DMKVCVND

If any Writes are chained to the SNARBLOK, sends then to the VSM.

DMKVCVND

If any Reads are chained to the SNARBLOK, sends them.

DMKVCRMT

Exits to Dispatcher via GOTO.

Initiate a WRITE

DMKVCRWT

Entered via branch from DMKQCN. For this path DMKVCRWT is the controlling module and calls all other modules. Determines type of Write by examining the CONTASK: If Full Screen Write, or Diagnose Write, and if a batched Write is pending, calls:

DMKVCVIX

Sends the batch write to the virtual service machine.

DMKVCVLD

Obtains a WEIBLOK.

DMKVCVEB

Obtains a WEBLOK.

DMKVCRWT

Moves the output line from the CONTASK to the WEBLOK. If Console Mode, sets on the batching bits in the SNARBLOK.

DMKVCVER

Sets a timer.

DMKVCRWT

If WEBLOK is to be sent immediately, that is if CONPARM = priority, or highlight, or alarm, or the pace counter has reached zero, calls:

DMKVCVIX

Sends the WEBLOK.

DMKVCVCE

Writes a Trace Element.

DMKVCRWT

If a response is expected to the Write, exits to Dispatcher via GOTO. If a response is not expected, sets the return code in savearea pointed to by CONRETN.

DMKQCOET

Release the CONTASK.

DMKVCRWT

Exits to Dispatcher via GOTO.

CP Directories

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This part contains the following directories:

- CP Module Entry Point Directory
- CP Module-to-Label Cross-Reference
- CP Label-to-Module Cross-Reference.

CP Module Entry Point Directory

Module Name	Entry Points	Attributes, Function
DMKACO		Pageable.
	DMKACOCL	Closes the current accounting spool file.
	DMKACODS	Creates an account card buffer for a VMBLOK (for a
		disconnect user).
	DMKACODV	Builds an account card buffer for a VDEVBLOK.
	DMKACOFF	Creates an account card buffer for a VMBLOK (for a
		logged-off user).
	DMKACON	Provides additional accounting function at logon time (for installation use).
	DMKACOQU	Collects the accounting records on the system accounting chain (DMKRSPAC) and puts them into spool file format.
	DMKACOTM	Creates a connect and usage time message for a user.
	DMKACOSA	Creates an accounting buffer for a VCNA logical unit user.
DMKACR	DMKACRCO	Resident. Marks all paths through specified channels offline, and checks that there are still online paths to all system-owned
	•	devices.
	DMKACRCT	Terminates one or more failing channels.
	DMKACRCV	Recovers from loss of one or more channels or from the
		loss of a processor and its channels.
	DMKACRC3	Sends an offline message to the operator.
DMKACS		Resident.
	DMKACSCV	Recovers from the loss of one or more channels.
DMKALG		Pageable.
	DMKALGON	Handles the AUTOLOG command.
DMKALO		Pageable.
	DMKALOCP	Special processing required for mounting CP-owned volumes during system initialization.
DMKAPI		Pageable.
		This module is entered from DMKCPI only if in attached processor or multiprocessor mode. It is also entered from DMKCPU as part of the vary online processor function.
	DMKAPICK	Test for virtual machine assist and 370E on non-IPL'ed
	DMWADDD	processor. Also test if its interval timer is running.
	DMKAPIPR DMKAPIAP	Initializes the PSAs for each processor.
	DMKAPIAP	Initializes the control registers for the second processor.
DMKAPS	DMKAPSIL	Pageable. Handles pending messages representing spool requests for
	DMKAPSCN	the *SPL System Service. Handles pending CONNECTs from a virtual machine to
		the *SPL System Service.
	DMKAPSSV	Handles pending SEVERs from a virtual machine to the *SPL System Service.
DMKAPT		Pageable.
	DMKAPTSP	Handles pending messages from a virtual machine requesting the *SPL System Service to read SPLINK(s)
	DMKAPTSF	into the virtual machine's data area. Handles pending messages from a virtual machine requesting the *SPL System Service to read the SFBLOK for the specified spool ID into the virtual machine's data
	DMKAPTRX	area. Handles pending messages from a virtual machine requesting the *SPL System Service to read the XABs (External Attribute Buffers) for the specified spool file ID into the virtual machine's data area.

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Module Name	Entry Points	Attributes, Function
DMKAPU	DMKAPUSE	Pageable. Handles pending messages from a virtual machine requesting the *SPL System Service to select a spool file from the system queue for the virtual machine to process.
DMKAPV	DMKAPVCL	Pageable. Handles pending messages from a virtual machine requesting the *SPL System Service to either close and purge, close and requeue, or change the copy count of the specified spool ID.
DMKAPW	DMKAPWPG	Pageable. Removes an *SPL System Service logical spool control block from the logical print chain for a virtual machine.
DMKAPX	DMKAPXMG	Pageable. Sends messages from a virtual machine using the *SPL System Service.
DMKAPY	DMKAPYSD	Pageable. Routes commands and messages from the *SPL System Service to the virtual machine.
DMKAPZ	DMKAPZNO	Pageable. Notifies all idle virtual machines that are using the *SPL System Service when a spool file becomes available.
DMKATS	DMKATSCF	Pageable. Notifies the virtual machine that the command has replaced the shared system with a private copy of that shared system. The user continues to run without the shared copy of the named system. Called by the command processors via an SVC if the command execution is to change a shared page.
DMKBIO		Pageable.
	DMKBIOCN	Handles pending CONNECTs from a virtual machine to the DASD Block I/O System Service.
	DMKBIOIL	Handles pending messages representing I/O requests for
	DMKBIOSV	DASD Block I/O. Handles pending SEVERs from a virtual machine to the
	DMKBIORS	DASD Block I/O System Service. Handles the resetting of a connection to the DASD Block I/O System Service.
DMKBLD	DMKBLDEC	Pageable. Allocates storage for a virtual ECBLOK and the two TRQBLOKs required for a virtual machine with the ECMODE option, and initializes these blocks.
	DMKBLDRL	Releases real segment, page, and swap tables to free
	DMKBLDRT	storage. Creates and initializes segment, page, and swap tables as a function of virtual storage size, which is part of the
	DMKBLDVM	process of building a user's virtual machine. Creates and partially initializes a VMBLOK for a virtual machine, identified by its terminal real device block.
DMKBOX		Pageable. Provides the system or user logo (header) for printed output.
	DMKBOXMS	Minimum screen VM logo data.
	DMKBOXNS DMKBOXPR	Normal screen VM logo data. Printer separator page logo data.

Module Name	Entry Points	Attributes, Function
DMKBSC	DMKBSCER	Pageable. Line error processing for remote 3270s on binary synchronous lines only. Examines the error condition resulting from a unit check or channel error that occurred while executing a CP-generated BSC line channel program. If the error is uncorrectable, DMKMSW is called to notify the operator. After return from DMKMSW, the original channel program is terminated and the fatal flag is set in the IOBLOK. If the error is correctable, the channel program is re-executed up to a maximum of seven retries.
DMKBTS	DMKBTS	Pageable. (Note: This module is not supplied with source code.) Bootstrap loader routines for the 37x5 processor.
DMKCAC	DMKCACOF DMKCACON DMKCACQY	Pageable. Processes the CACHE command for 3880 control units. Processes the CACHE OFF command for 3880 Model 13 or 23. Processes the CACHE ON command for 3880 Model 13 or 23. Processes the CACHE QUERY command for 3880 Model 11, 21, 13, or 23.
DMKCAO	DMKCAOWN	Pageable. Processes the CACHE OWN command for the 3880 Model 13 and Model 23 control units.
DMKCCD	DMKCCDAS DMKCCDSK	Resident. Validates CCW commands to nondedicated DASD. Insert SEEK command.
DMKCCF	DMKCCFBA DMKCCFBD	Resident. Validates command codes for nondedicated FBA. Validates command codes for dedicated FBA.
DMKCCH	DMKCCHER DMKCCHIS DMKCCHNT DMKCCHRF DMKCCHRT	Resident. Operates with the I/O interrupt handler to schedule a device-dependent error recovery procedure when a channel data check, control check, or interface control check is detected. Continue channel check processing after obtaining the global system lock. Entry from DMKIOS when a channel check occurs when storing a CSW after a SIO. Entry from DMKIOTIN when a channel check occurs on an I/O interrupt. Reflects channel check information to the virtual machine. Entry from DMKIOE to allow error messages to be printed.
DMKCCO	DMKCCODD DMKCCOMP DMKCCOMS DMKCCOTH	Resident. Validates dedicated DASD command codes. Validates tape command codes. Validates 3851 MSC command codes. Validates other command codes.
DMKCCS	DMKCCSEN DMKCCSLK DMKCCSRM	Resident. Handles sense type ('X4') commands. Obtains the system lock. Gets larger rcwtask for CCW translation.

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Module Name	Entry Points	Attributes, Function
DMKCCT	DMKCCTCN DMKCCTDL DMKCCTLC DMKCCTRM	Resident. Validates console command codes. Validates dial command codes. Validates SDLC command codes. Validates terminal command codes.
DMKCCW	DMKCCWCN DMKCCWCW DMKCCWRE DMKCCWRT DMKCCWSB DMKCCWTR	Resident. Obtains control data for other CCW translation modules. Writes control data for other CCW translation modules. Reenters DMKCCWTR. Entry for return from other CCW translation modules. Obtains control data for a user. Takes the list of virtual CCWs associated with a user's SIC and translates it into a real CCW list.
DMKCDB	DMKCDBDC DMKCDBDI	and translates it into a real CCW list. Pageable. Processes DISPLAY, DCP commands. Executes the DISPLAY command to display real storage locations. Displays virtual storage locations, storage keys, general registers, floating-point registers, vector registers, PSW, CAW, and CSW at the terminal.
DMKCDM	DMKCDMDM DMKCDMDU	Pageable Processes DUMP and DMCP commands. Dumps the contents of the specified real storage locations on the virtual printer spool file. Dumps the contents of the specified virtual storage locations, registers, PSW, and storage keys on the virtual printer spool file.
DMKCDS	DMKCDSCP DMKCDSTO	Pageable. Processes STORE and STCP commands. Stores data into real storage (STCP command). Stores data into virtual storage (STORE command).
DMKCFC	DMKCFCCO DMKCFCMD	Pageable. Gets the address of the routine that processes the CP console function that was requested. Override command/function classes. Processes a CP console function.
DMKCFD	DMKCFDAD DMKCFDLO	Pageable. Processes LOCATE and ADSTOP commands. Stops virtual machine at specified address (ADSTOP command). Displays address of real device blocks, or VMBLOK and/o virtual device blocks (LOCATE command).
DMKCFF	DMKCFFSB	Pageable. Processes subroutines of DMKCFG.
DMKCFG	DMKCFGCL DMKCFGII DMKCFGIP DMKCFGIR	Pageable. Handles Diagnose code X'64'. Entry to IPL from LOGON (DMKLOG). Entry to IPL from a command line (DMKCFM). Distinguishes a re-IPL for users of the Protected Application Facility.
DMKCFH	DMKCFHAS DMKCFHSV	Pageable. Saves a virtual machine's storage space including register and PSW (for VMSAVE). Saves a virtual machine's storage space including register and PSW (SAVED System).

Module Name	Entry Points	Attributes, Function
DMKCFJ		Pageable.
	DMKCFJSL	Processes the SLEEP command.
	DMKCFJBE	Processes the BEGIN command.
	DMKCFJRQ	Presents an attention interruption to the virtual machine
		to simulate a real request key interruption.
DMKCFM		Resident.
		Processes DIAGNOSE code 8. It scans the command line
		and goes to the required module.
	DMKCFMAT	Posts an attention interrupt pending for the virtual machine.
	DMKCFMBK	Puts the terminal in console function (CP) mode (ATTN
		key pressed twice). Scans the command line and goes to
		the command- handling routine.
	DMKCFMEN	Entered when DIAGNOSE code 8 is executed. Scans the
		command line and goes to the command-handling routine.
	DMKCFMRU	Runs the virtual machine.
	DMKCFMSD	Execute a CP command that is in the text of the SEND
		command.
	DMKCFMWU	Entered when SLEEP time expires.
DMKCFO		Pageable.
	DMKCFOSA	Processes the SET SASSIST command.
	DMKCFOSC	Processes the SET CPASSIST command.
	DMKCFOSF	Processes the SET FAVORED command.
	DMKCFOSP	Processes the SET PRIORITY command.
	DMKCFOSQ	Processes the SET QDROP command.
	DMKCFOSR	Processes the SET RESERVE command.
	DMKCFOS3	Processes the SET S370E command.
DMKCFP		Pageable.
		Simulates the operator's console for the virtual machine.
	DMKCFPRR	Handles system resets for other CP routines. Resets the virtual machine.
		virtual machine.
DMKCFQ	,	Pageable.
	DMKCFQRD	Handles virtual device reset for other CP routines.
DMKCFR		Pageable.
	DMKCFREP	Handles virtual device reset for other CP routines.
DMKCFS		Pageable.
Difficito		Processes the CP SET command for general users.
	DMKCFSAC	Processes the SET ACNT command.
	DMKCFSAP	Processes the SET AUTOPOLL command.
	DMKCFSAS	Entry point for VMSAVE enable/disable.
	DMKCFSCC	Processes the SET CPCONIO command.
	DMKCFSCP	Processes the SET CUPID command.
	DMKCFSEC	Processes the SET ECMODE command.
	DMKCFSEM	Processes the SET EMSG command.
	DMKCFSIM	Processes the SET IMSG command.
	DMKCFSIS	Processes the SET ISAM command.
	DMKCFSLE	Processes the SET LINEDIT command.
	DMKCFSMG	Processes the SET MSG command.
	DMKCFSNT	Processes the SET NOTRANS command.
	DMKCFSPX	Processes the SET PAGEX command.
	DMKCFSRN	Processes the SET RUN command.
	DMKCFSSA	Processes the SET SVCACCL command.
	DMKCFSSM	Processes the SET SMSG command.
	DMKCFSVC	Processes the SET VMCONIO command.
	DMKCFSVS	Processes the SET VMSAVE command.
	DMKCFSWG	Processes the SET WNG command.
	DMKCFS37	Processes the SET 370E command.

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Module Name	Entry Points	Attributes, Function
DMKCFT		Pageable.
		Processes user's terminal options.
	DMKCFTRM	Entry point for the TERMINAL command processor.
DMKCFU		Pageable.
	DMKCFUDU	Processes the SET DUMP command.
	DMKCFULO	Processes the SET LOGMSG command.
	DMKCFUMI	Processes the SET MITIME command.
	DMKCFUMO	Processes the SET MODE command.
	DMKCFUMW DMKCFUPA	Processes the SET MINWS command. Processes the SET PAGING command.
	DMKCFURE	Processes the SET RECORD command.
DMKCFV		Pageable.
21111111	DMKCFVSB	Processes the SET STBYPASS command.
	DMKCFVSM	Processes the SET STMULTI command.
	DMKCFVMI	Processes the SET MIH command.
DMKCFW		Pageable.
	DMKCFWEP	Processes SCREEN command parameters.
DMKCFY		Pageable.
	DMKCFYAG	Processes the SET AFFINITY command.
	DMKCFYAS	Processes the SET AFFINITY command.
	DMKCFYPF	Allocates or releases the retrieve buffer. Releases the PFKTABLE if not needed.
	DMKCFYSA	Processes the SET ASSIST command.
	DMKCFYSC	Processes the SET CONCEAL ON/OFF command, placing
		the user in the Protected Application Facility.
	DMKCFYSP	Processes the SET PFNN command.
	DMKCFYSM	Processes the SET TIMER command.
DMKCKD		Pageable.
	DMKCKDEV	Checkpoint program device halting routine.
	DMKCKDGP	Finds a usable path to a device.
	DMKCKDSW	Handles requests to switch from one processor to another.
DMKCKF		Pageable.
	DMKCKFIL	Checkpoints the onto the warm start cylinders during
		system shutdown. (Saves open CPTRAP file when the system crashes.)
DMKCKH		Pageable.
~~~~~	DMKCKHST	Checks validity of warm start cylinders.
	DMKCKHPR	Handles read or write requests to and from the warm start
		area.
	DMKCKHAC	Saves printer or punch spool files which are active on the
		system printer or punch, or adds open spool file from the
		system reader to the delete queue.
	DMKCKHIO	Handles I/O requests.
DMKCKM		Pageable.
	DMKCKMSG	Displays messages for the checkpoint program.
	DMKCKMSV	Saves multiple virtual machines during system shutdown or system abend.
DMROWN		
DMKCKN	DMKCKNRD	Pageable. Fixes up the RDEVBLOKs of all CP-owned volumes for use
	DWIGUNAD	by DMKCKM and DMKCKNIO.
	DMKCKNIO	Routine to do I/O for the checkpoint program.

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Module Name	Entry Points	Attributes, Function
DMKCKP	DMKCKPT	Pageable. IPL entry point for system initialization, system shutdown
	DMKCKPER	and system abend. Displays fatal error message and loads a disabled wait state PSW (code 007).
	DMKCKPRG	Loads a disabled wait state PSW (code 007).
DMKCKR	DMKCKRSY	Pageable. Rebuilds SYSSPOOL's virtual storage from the CKPT cylinders.
DMKCKS	DMKCKSCV DMKCKSPL	Pageable. Performs checkpoint processing. Converts a user-unique ID to CCPD or PPPD. Performs a checkpoint on any alterations in the spool file set up to allow the recovery routine to get them if warm start fails.
	DMKCKSIN	Initializes the check point cylinder after a successful warn start from the standard recovery procedure or after a cold start.
DMKCKT	DMKCKTIN DMKCKTMP	Pageable. Recovers spool file data if new RECBLOK needed. Obtains pages for and initializes the Spool File Bit Map. Sets up constants in DMKRSP.
	DMKCKTSD DMKCKTSU DMKCKTUU	Gives system-unique spool ID back. Finds a system-unique user ID. Finds a user-unique spool ID and a system-unique spool II
DMKCKV	DMKCKVWM	Pageable. Allocates pages of spool files saved on CKPT cylinders, recovers RDEVBLOK information and SHQBLOKs, updates the system spool file bit map, and rebuilds the use reader chains.
DMKCKW	DMKCKWHT DMKCKWSP	Pageable. Saves the Reader Hash Table plus extension pages onto the warm start cylinders. Saves SYSSPOOL's virtual storage onto the warm start
DMKCLK	DMKCLKCK DMKCLKCC DMKCLKMP DMKCLKAP DMKCLKSC	cylinders. Pageable. Determines if the clock should be synchronized. (Called from DMKCPI) Handles CLKCHK signal request. Synchronizes the clocks. Handles SYNC signal request. Handles the TOD-sync-check external interrupt.
DMKCMD		Pageable. Contains tables which describe the syntax, entry points, and functional groups for subcommands. These tables enable DMKCFCMD to do centralized class validation for CP commands.
DMKCNS	DMKCNSEN DMKCNSIC	Resident. Real console terminal manager (along with DMKCNT). Enables or disables a low-speed terminal line. Entered from DMKQCN to initialize read and write CCWs for the CONTASK built by DMKQCN and DMKQCO.
	DMKCNSIN	Interruption return point and handler for terminal I/O.
DMKCNT	DMKCNTED	Resident. Real console terminal manager (along with DMKCNS). Edits the input line for the following characters: escape, line end, line delete, and character delete.

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Module Name	Entry Point	Attributes, Function
DMKCPB		Pageable.
	DMKCPBEX	Simulates the operator's console for the virtual machine. Processes the EXTERNAL command to present an external
	DMKCPBNR	interruption to the virtual machine. Processes the NOTREADY command to cause the virtual
	DMKCPBRS	device to appear not ready. Processes the RESET command to reset all pending interrupts from the specified device.
	DMKCPBRW	Processes the REWIND command to issue a rewind to the real tape device.
	DMKCPBRY	Processes the READY command to simulate a device end interrupt to the specified device.
	DMKCPBSR	Processes the SYSTEM command to simulate system reset and PSW restart to allow clearing of storage.
DMKCPE		Resident. Contains data constants that define the end of the CP nucleus.
DMKCPI	DMKCPINT	Pageable. Prepares system for operation. Initializes CP during IPL. Mainline system initialization routine (first half).
DMKCPJ	DMKCPJNT	Pageable. Mainline system initialization routine (second half).
DMKCPM	DMKCPMIO	Pageable. Initializes all resources necessary for an AP/MP environment and reverts to uniprocessor mode.
DMKCPN	DMKCPNAS DMKCPNVY	Pageable. Assigns a 3480 tape device. Processes the VARY ONLINE command.
DMKCPO	DMKCPOFF	Pageable. Process the VARY OFFLINE PROCESSOR command.
DMKCPP	DMKCPPUP	Pageable. Releases all resources necessary for an AP/MP environment and reverts to uniprocessor mode.
DMKCPS	DMKCPSSH DMKCPSH	Pageable. Processes the SHUTDOWN and HALT commands. Processes the SHUTDOWN command. Processes the HALT command.
DMKCPT	DMKCPTNF	Pageable. Processes the VARY command. Processes the VARY command.
DMKCPU	DMKCPUVY	Pageable. Processes the VARY ONLINE PROCESSOR command.
DMKCPV	DMKCPVAA DMKCPVAC	Pageable. Punches user accounting records. Processes the ACNT command to create accounting records for logged-on users. Also, resets accumulated accounting information.
	DMKCPVAE	Enables system low-speed lines for system restart.
	DMKCPVDS	Processes the DISABLE command to disable an active line after the current user is finished with it.
	DMKCPVEN	Processes the ENABLE command to enable the system's low-speed lines for system logon.

Module Name	Entry Points	Attributes, Function
DMKCPW	DMKCPWFB DMKCPWUN	Pageable. Initiates IOBLOKs and builds RDCBLOKs. Unassigns a device and resigns it from the path group.
	DMKCPWVF	Varies a device offline.
DMKCPX		Pageable.
	DMKCPXCK DMKCPXZT	Checks the number of t-disks to be released or cleared. Builds and stacks a CPEXBLOK.
DMKCPY		Pageable.
	DMKCPYLK	Processes the LOCK command to lock specified pages of a user's virtual storage space into real main storage.
	DMKCPYUL	Processes the UNLOCK command to unlock pages that were locked by operator command (LOCK).
DMKCPZ		Pageable.
	DMKCPZAS DMKCPZFD	Assigns a 3480 tape device to CP Checks cached control units for status and cache validity.
	DMKCPZMG	Stacks message DMKCPJ328 on the DMKCPIMS queue
	DMKCPZPG	Varies on 3880 storage control devices.
	DMKCPZVR	Checks cached control units for $V = R$ user owning the cache.
DMKCQC		Pageable.
	DMKCQCPT	Processes the QUERY CPTRAP command.
DMKCQG		Pageable.
	DMKCQGID	Processes the class G and class D QUERY commands. Processes the QUERY <vaddr>/<userid> command.</userid></vaddr>
	DMKCQGQA	Processes the QUERY VIRTUAL ALL command.
	DMKCQGQC	Processes the QUERY VIRTUAL CONSOLE command.
	DMKCQGQD	Processes the QUERY VIRTUAL DASD command.
	DMKCQGQG	Processes the QUERY VIRTUAL GRAF command.
	DMKCQGQH DMKCQGQL	Processes the QUERY VIRTUAL CHANNEL command. Processes the QUERY VIRTUAL LINES command.
	DMKCQGQS	Processes the QUERY VIRTUAL STORAGE command.
	DMKCQGQT	Processes the QUERY VIRTUAL TAPES command.
	DMKCQGQU	Processes the QUERY VIRTUAL UR command.
DMKCQH		Pageable.
	DMKCQHFI	Entry point from DMKCPI/DMKLOH for QUERY FILES processing.
	DMKCQHFS	Processing. Processes the QUERY FILES command.
	DMKCQHIP	Processes the QUERY FILES command for the DMKCPI call.
	DMKCQHNG	Processes the QUERY PUN (general) command.
	DMKCQHNS	Processes the QUERY PUN (authorized) command.
	DMKCQHRG	Processes the QUERY RDR (general) command.
	DMKCQHRS	Processes the QUERY RDR (authorized) command.
	DMKCQHTG DMKCQHTS	Processes the QUERY PRT (general) command. Processes the QUERY PRT (authorized) command.
DMKCQI		Pageable.
	DMKCQIFR	Format response to the QUERY RDR/PRT/PUN command.

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Module Name	Entry Points	Attributes, Function
DMKCQP		Pageable. Processes the class B and class G QUERY command.
	DMKCQPQA	Processes the QUERY ALL command.
	DMKCQPQD	Processes the QUERY DASD command.
	DMKCQPQG	Processes the QUERY GRAF command.
	DMKCQPQL	Processes the QUERY LINES command.
	DMKCQPQP	Processes the QUERY PROCESSR command.
	DMKCQPQS	Processes the QUERY STOR command.
	DMKCQPQT	Processes the QUERY TAPES command.
	DMKCQPQU	Processes the QUERY UR command.
	DMKCQPSC	Entry called by DMKCQQ to handle the QUERY raddr
		command.
DMKCQQ		Pageable.
	DMKCQQID	Processes the QUERY <raddr>/<userid> command.</userid></raddr>
	DMKCQQQL	Processes the QUERY LINKS command.
	DMKCQQQS	Processes the QUERY SYSTEM command.
	DMKCQQQT	Processes the QUERY TDSK command.
	DMKCQQPT	Entry called by DMKCQP to get Path information.
DMKCQR		Pageable.
		Processes the QUERY command.
	DMKCQRAF	Processes the QUERY AFFINITY command.
	DMKCQRDP	Processes the QUERY DUMP command.
	DMKCQRHD	Processes the QUERY HOLD command.
	DMKCQRPG	Processes the QUERY PAGING command.
	DMKCQRPR	Processes the QUERY PRIORITY command.
	DMKCQRQD	Processes the QUERY QDROP command.
DMKCQS		Pageable.
	DMKCQSSC	Processes QUERY SCREEN command.
	DMKCQSMI	Processes QUERY MITIME command.
	DMKCQSNA	Processes QUERY NAME command.
	DMKCQSPS	Processes QUERY PSTOR command.
	DMKCQSQC	Processes COMMANDS command.
DMKCQT		Pageable.
	DMKCQTCL	Processes the QUERY CPLANG command.
	DMKCQTRE	Searches remote devices for QUERY GRAF.
	DMKCQTSN	Searches SNA devices for QUERY GRAF.
	DMKCQTST	Processes the QUERY STATUS command.
DMKCQU	DIFFECTION	Pageable.
	DMKCQUSE	Processes the QUERY SECUSER command.
	DMKCQUST	Processes the QUERY SET command.
	DMKCQUTE	Processes the QUERY TERMINAL command.
DMKCQY	DMRCONCA	Pageable.
	DMKCQYCA	Processes the QUERY CPASSIST command.
	DMKCQYCL DMKCQYCP	Processes the QUERY CPLEVEL command. Processes the QUERY CPUID command.
	DMKCQYLM	Processes the QUERY LOGMSG command.
	DMKCQYPF	Processes the QUERY PF < NN > command.
	DMKCQYSA	Processes the QUERY SASSIST command.
	DMKCQYSP	Processes the QUERY SPMODE command.
	DMKCQYS3	Processes the QUERY S370E command.
	DMKCQ155	Processes the QUERY TIME command.
	DMKCQYUI	Processes the QUERY USERID command.
	DMKCQYUS	Processes the QUERY USERS command.
	DMKCQYVS	Processes the QUERY VMSAVE command.
	DMKCQYRG	Displays the page range of a saved VMSAVE system.
	DMINOWING	Displays the page range of a saved VNISAVE system.

Module Name	Entry Points	Attributes, Function
DMKCRM	DMKCRMCN DMKCRMIL DMKCRMQS DMKCRMSV	Pageable. The Collection Resource Management (CRM) system service. It provides a communication path between the TSAF virtual machine and CP. Handles pending connections. Handles pending messages. Handles quiesces. Handles severs.
DMKCSB	DMKCSBLD	Pageable. Processes the LOADBUF command (real UCS or FCB
DIWEGGG	DMKCSBLF DMKCSBSP	buffer). Reloads the last FCB buffer. Spaces to the first line of the next form.
DMKCSC	DMKCSCLV	Pageable. Searches through module DMKFCB for the requested FCB image, which will be loaded into the virtual FCB buffer. This entry point will be called for virtual printer buffer
	DMKCSCLD	loading. Obtains the appropriate CCWs and points to the correct data module for loading the UCS/FCB buffer of the real printer. Searches for the requested UCS/FCB image in the data module. This entry point will be called for real printer buffer loading.
DMKCSF		Pageable. Processes real spooling commands for real unit record devices.
	DMKCSFBS DMKCSFFL DMKCSFRP DMKCSFSP	Processes the BACKSPACE command. Processes the FLUSH command. Processes the REPEAT command. Processes the SPACE command.
DMKCSO		Pageable. Processes real spooling commands for real unit record devices.
	DMKCSODR DMKCSOSD DMKCSOST	Processes the DRAIN command. Restarts a device after it has been drained. Processes the START command by device type.
DMKCSP	DMKCSPSP	Pageable. Processes the SPOOL command.
DMKCSQ	DMKCSQCL DMKCSQFR DMKCSQHL	Pageable. Processes the CLOSE command. Processes the FREE command. Processes the HOLD command.
DMKCSR	DMKCSRGT DMKCSRPO	Pageable. Searches the device type from the SPOOL command. Processes the SPOOL command from general users and spooling operators.
DMKCST	DMKCSTAG	Pageable. Processes class G commands. Entry point to process the TAG command.
DMKCSU	DMKCSUCG DMKCSUCS	Pageable. Processes the class D and G spooling commands. Processes the CHANGE (general) command. Processes the CHANGE (authorized) command.

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Module Name	Entry Points	Attributes, Function
DMKCSV	DMKCSVOG DMKCSVOS DMKCSVPG DMKCSVPS	Pageable. Processes the ORDER (general) command. Processes the ORDER (authorized) command. Processes the PURGE (general) command. Processes the PURGE (authorized) command.
DMKCSW	DMKCSWCH	Pageable. Processes the CHANGE command for general users and spooling operators.
DMKCSX	DMKCSXTG DMKCSXTS	Pageable. Processes the TRANSFER command for general users. Processes the TRANSFER command for general users and spooling operators.
DMKCSY	DMKCSYTR	Pageable. Along with DMKCSX, processes the TRANSFER command.
DMKCVT	DMKCVTAB DMKCVTBD DMKCVTBH DMKCVTDB	Resident. Processes the conversion routines. Forces a CVT001 abend. Converts a word of binary data into a doubleword of decimal digits. Converts a word of binary data into a doubleword of hexadecimal data. Converts a decimal field into a fullword of binary data.
	DMKCVTDT DMKCVTHB	Converts a decimal field into a fullword of binary data. Converts data and time to EBCDIC and inserts it into a specified location. Converts the designated hexadecimal field into a binary fullword.
DMKCVU	DMKCVUFP	Pageable. Converts a floating-point doubleword into 17 bytes of decimal data.
DMKDAD	DMKDADER	Resident. Processes 3375/3380 error recovery for CP and DIAGNOSE I/O.
DMKDAS	DMKDASER	Resident. DASD error retry program. Retries the failing DASD channel program.
DMKDAU	DMKDAUER	Resident. FBA error retry program. Retries the failing FBA channel program.
DMKDDC	DMKDDC	Residency not applicable. Performs data decompaction.
DMKDDR	DMKDDREP	Residency not applicable. This is the DASD dump restore program. It saves data from a direct access volume onto a tape or tapes. It returns data to DASD from tape that has been placed on the tape by this program. It copies data from one device to another of the same type. It prints a translation of each record specified on the SYSPRINT device. Prints a translation of each record specified on the console. Initial program loaded or run under CMS if on a CMS disk. DASD dump restore program entry point
	DMKDDRED	End-of-load module for CMS.

Module Name	Entry Points	Attributes, Function
DMKDEF	DMKDEFDG DMKDEFDS	Pageable. Processes the DEFINE command to define a virtual device. Processes the DEFINE (general) command. Processes the DEFINE (authorized) command.
DMKDEG	DMKDEGIN	Pageable. Processes the DEFINE STORAGE and DEFINE CHANNEL commands.
DMKDEI	DMKDEIMS	Pageable. Define or redefine MSS disks.
DMKDEX	DMKDEXIN	Resident. Cache bypass routine for virtual I/O.
DMKDGD	DMKDGDDK DMKDGDUL	Resident. Processes simple disk I/O. Performs simple disk I/O of a standardized format with a minimum of CCW chain manipulation. Unlocks user pages upon completion of I/O.
DMKDGF	DMKDGFIN	Resident. Handles simple disk interruptions.
DMKDIA	DMKDIADR	Pageable. Releases a terminal line that has been in use by the virtual machine via the DIAL command. The line is detached from the virtual machine and made available for normal log on
	DMKDIAL	to the system. Processes the DIAL command. Attaches a user's terminal as a dedicated device to an existing virtual 270X terminal line in the virtual machine addressed by the command line. This process is finished in DMKDIFDI.
DMKDIB	DMKDIBSM	Pageable. Simulates sense data and status for virtual I/O to a simulated I/O device (2702 line or CTCA) that has not yet been activated through either the console function DIAL for 2702 lines, or the console function COUPLE for virtual CTCAs.
	DMKDIBCP DMKDIBDR	COUPLE command processor. Establishes a virtual connection between two channel-to-channel adapters on a single virtual machine. Releases a terminal line that has been in use by the virtual machine via the DIAL command. The line is detached from the virtual machine and made available for normal logon
DMKDID	DMKDIDDA DMKDIDGR DMKDIDLF DMKDIDMS DMKDIDTA DMKDIDUR	the virtual mathine and made available for normal logon to the system. Resident. Handles missing interruptions. Examines RDEVBLOKs for DASD. Examines RDEVBLOKs for graphic devices (except 1053 and 328X printers). Examines RDEVBLOKs for missing interrupts during LOGOFF/FORCE. Examines RDEVBLOKs for Mass Storage System devices. Examines RDEVBLOKs for tape devices. Examines RDEVBLOKs for unit record devices (except 3800 and 3289E printers).
DMKDIF	DMKDIFDI	Pageable. Finishes DIAL command processing begun in DMKDIAL.

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Module Name	Entry Points	Attributes, Function
DMKDIR		Pageable or standalone.
		Initial program loaded or run under CMS if on a CMS
	DMKDIRCT	disk. Builds a user directory on a system owned volume using
	DMRDIRCI	pre-allocated cylinders.
	DMKDIRED	End of load module for CMS.
DMKDMP		Resident.
		Writes a dump of main storage, control registers,
		floating-point registers, general registers, and clocks to a
		specified device. Calls DMKDMQ for additional functions.
	DMKDMPDK	Writes the dump on the specified device.
	DMKDMPRS	Initial program loads the system over again.
DMKDMQ		Resident.
	DMKDMQEN	Writes dump to tape or printer.
	DMKDMQLW	Loads a disabled wait state PSW.
	DMKDMQGP DMKDMQMC	Locates an enabled path to a device.
	DMKDMQMC	Handles machine checks during dump. Handles program checks during dump.
	DMKDMQIC	Switches to other processor.
	DMKDMQTL	Types a line on the system console.
DMKDNC		
DMKDNC	DMKDNC	Residency not applicable. Performs data compaction.
	DMINDINC	renorms data compaction.
DMKDRD		Pageable.
	DIGUDDDDD	Process spool files.
	DMKDRDDD	Delete system dump spool file.
	DMKDRDER	Manipulates input spool files via a DIAGNOSE code X'0014' issued by the virtual machine.
	DMKDRDMP	Reads a system dump spool file via a DIAGNOSE code
		X'0034' issued by the virtual machine.
	DMKDRDSY	Reads the system symbol table CSECT via a DIAGNOSE
		code X'0038' issued by the virtual machine.
DMKDSB		Resident.
	DMUDGDDD	DASD error retry program.
	DMKDSBRD DMKDSBSD	Processes unsolicited device end interruptions. Collects DASD sense data.
	DMRDSDSD	Conects DASD sense data.
DMKDSP		Resident.
		Entered after each interruption handler is finished
		processing and after each stacked CPEXBLOK, I/O
		request, and external interruption has been serviced. It
		updates the CPU times charged to the user that has
		received service, updates all virtual timers, and reflects
		any pending interruptions for which the user is enabled. After the user's status has been updated, the
		highest-priority runnable user is dispatched.
	DMKDSPA	Immediate redispatch path for virtual machines. The only
		status update that occurs is for virtual timers.
	DMKDSPB	Process new virtual PSW and dispatch. Entered if the
	DMZDSDOU	virtual PSW has been entered outside of DMKDSP.
	DMKDSPCH	Main entry point. Updates timers and dispatches user.
	DMKDSPIS	Saves pending interrupts for the $V = R$ virtual machine.
	DMKDSPE	Processes interrupt from virtual interval timer.
	DMKDSPQS	Nonexecutable dispatched user's maximum time slice.
	DMKDSPRU	Entered in attached processor mode when the system lock
		is not held.
	DMKDSPWA	Entry point for leaving active wait with a runnable user $(AB/MB \text{ cr})$
	1	(AP/MP only).

Module Name	Entry Points	Attributes, Function
DMKEIG		Pageable. Analyzes the 2880 channel logout and sets appropriate bits in the ECSW field according to the results of this analysis. It moves the channel logout to the channel check record.
DMKEMR	DMKEMR	Pageable Contains the framework for the common response messages which are generated at various places within CP. Module DMKERM references DMKEMR in order to write messages which require variable data to be inserted. This module contains no executable code and contains message text for responses 30,001 and up.
DMKENT		Resident. Meets the CP nucleus residency requirements for TRQBLOK and CPEXBLOK entries to pageable VM Monitor module DMKMIA.
	DMKENTEC	Used to invoke a MONITOR STOP command via a CPEXBLOK.
	DMKENTET	Used to invoke a MONITOR STOP command via a TRQBLOK request.
	DMKENTFI	Used to complete monitor shutdown processing, via CPEXBLOK.
	DMKENTGP	Used for CACHED controller monitoring via a CPEXBLOK.
	DMKENTGQ	Used to gather subsystem counts for CACHED controller monitoring via a CPEXBLOK.
	DMKENTKC DMKENTSC	Used to invoke a MONITOR CLOSE command via a CPEXBLOK. Used to invoke a MONITOR START SPOOL command via
	DMKENTSC	a CPEXBLOK. Used to invoke a MONITOR START SPOOL command via
	DMKENTTI	a TRQBLOK request. High frequency I/O status sampling routine, entered every
	DMKENT62 DMKENT63	two seconds via TRQBLOK request. Collect control unit and device busy and queue counts. Collect channel busy and queue counts.
DMKEPS	DMKEPSWD	Pageable. Prompts the user to enter a password, types masking characters if appropriate, reads the password from the terminal, and checks the password for a match.
DMKERM	DMKERMSG	Pageable. Accesses the requested message from the CP message repository and inserts the module ID, message number, and data. It also prints the message.
DMKERP	DMKERPCP	Pageable. Record CP OBR/MDR records queued by the ERP (error recording facility).
DMKEXT	DMKEXTIN DMKEXTSP DMKEXTSL	Resident. CP first level interrupt handler. Handles all SIGNAL actions after CP initialization. Second level interrupt handler for MP external interrupts.
DMKFCB	DMKFCB	Pageable. Contains the forms control load buffer images that the LOADBUF command uses to load the forms control buffer in the 3811 control unit for the 3203 or 3211 printer. The LOADVFCB command also uses DMKFCB to load the forms control buffer in the virtual 3203 or 3211 printer.

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Module Name	Entry Points	Attributes, Function
DMKFMT		Standalone program. Accepts parameters from the console or IPL device (card reader) and per forms partial or complete formatting, allocating, and labeling of 3330, 3340, 3350, 3380 and 2305 DASD devices. The FORMAT program also write-checks the surfaces. Bad surfaces are flagged to prevent their use No alternative tracks are assigned. OS labels are written to be compatible with OS, but labels indicate to OS that n space is left on the DASD device. All input parameters are verified for correctness.
DMKFPS	DMKFPS	Resident. Fast privileged simulation for selected privileged operations.
DMKFRE	DMKFREDE	Resident. Free storage manager. Entry to resume deferred free call when running AP/MP.
	DMKFREE	Gets space from free storage and processes the CP assist FREE instruction (E622).
	DMKFREEA	Supplies a cache-aligned block of storage, if the block requested will be referenced often and returned infrequently.
	DMKFREEP	Provides a fast path to obtain prime subpool blocks.
	DMKFREMX DMKFRERC	Maxsize for CP assist use. Special entry point to acquire free storage. If the storage request cannot be satisfied, a condition code of one is returned to the caller. (This entry point may extend free
	DMKFREXX	<ul> <li>storage to satisfy the request. See DMKFREXX below.)</li> <li>Special entry point to acquire free storage. If free storage would have to be extended to satisfy the request, a condition code of one is returned to the caller. Free storage will not be extended (and hence the caller will not lose control).</li> <li>Split counters for DMKMOO.</li> </ul>
	DMKFRESU DMKFRET	Entry for Extend processing. Processes the CP assist FRET instruction (E621).
DMKFRT	DMKFRTRA	Resident. Free storage management module. Returns any complete cache-aligned pages to the dynamic
	DMKFRTRS DMKFRTSN	paging area. Returns subpools to free storage chain. Scans the free storage chain to return page frame sized
	DMKFRTT	blocks back to the dynamic paging area. Returns storage to a subpool or the large storage chain. Handles requests to return storage that cannot be handled
	DMKFRTTE	by the CP Assist Fret instruction at DMKFRET. Returns storage to the large storage chain. This entry is called by DMKUSP to return storage blocks known to have been obtained from the dynamic paging area.
DMKGIO	DMKFRTTR	Assigns storage to free storage management; does not release pages. Pageable. Initializes supervisor operations for tape, unit record, and
	DMKGIOEX	nonstandard disk I/O operations. Checks device validity and initializes I/O operations on tape, unit record, and nonstandard disk I/O programs per supervisor call. This module presents resultant condition code and CSW (if warranted) to the user.
DMKGRA	DMKGRAOT	Pageable. Performs APL/TEXT translations for outbound 3270 data.

Module Name	Entry Points	Attributes, Function
DMKGRC		Resident.
	DMKGRCUP	Generates the data stream necessary to update the 3270
		screen, according to parameters input in register 2.
	DMKGRCQY	Determine if terminal or its controller has extended data
		stream capability.
	DMKGRCWC	Process a write CONTASK.
DMRODD		
DMKGRD	DMRCDDDD	Resident
	DMKGRDBR	Entry for branch table to DMKGRD routines from
	DMKGRDCC	DMKGRE, DMKGRF, DMKGRG, and DMKGRI. Builds CCWs for 3270 devices.
	DMKGRDIC	Starts a CONTASK.
	DMKGRDSR	Handles a read or control CONTASK.
	DMRGRDSR	Handles a read or control CONTASK.
DMKGRE		Resident.
	DMKGRECL	Clears the screen.
	DMKGREFD	Finishes processing after full-screen I/O is done.
	DMKGRESM	Handles screen management.
DMKGRF		Resident.
DMIXGIU		Supports local 3270, 3278 Model 2A, and 3279 devices.
		DMKGRF processes interruptions and CCWs for the
		devices. The processing includes message handling and
		screen management.
	DMKGRFIN	Handles the interruption via an IOBLOK.
	DMKGRFEN	Enables or disables the device.
	DMKGRFIC	Starts a CONTASK from DMKQCN. If a buffer larger
		than 4K bytes is required, a CCW is built to indicate that
		indirect addressing will be used.
	DMKGRFTI	Processes clock comparator timer interrupts.
DMKGRG		Devident
DMKGKG	DMKGRGBR	Resident. Entry for branch table to DMKGRG routines from
	DMINGRODIC	DMKGRD, DMKGRE, DMKGRF, and DMKGRI.
	DMKGRGBK	Handles the BREAK (PA1) key (3270 only).
	DMKGRGCL	Handles the DMERRY (1717) key (5210 only). Handles the CANCEL (PA2) key (3270 and 3066).
	DMKGRGCR	Handles the CLEAR key (3270 only).
	DMKGRGER	Handles the ENTER key (3270 and 3066).
	DMKGRGPF	Handles the PF keys key (3270 only).
	DMKGRGTI	Handles the clock comparator interrupts.
	DMKGRGTR	Handles the TEST REQUEST key (3270 only).
DMKGRH	DIFFERENCE	Resident.
	DMKGRHIN	Processes channel programs for 3066 display.
DMKGRI		Resident.
	DMKGRISB	Contains various subroutines used by DMKGRD,
		DMKGRE, DMKGRF, and DMKGRG.

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Module Name	Entry Points	Attributes, Function
DMKGRT		Resident. Contains common data areas and subroutines for 3270
	DMKGRTAB	display support. Computes the next tab position and creates the data stream to position the cursor and insert a logical tab
	DMKGRTAC	character if necessary. Count of entries in AID table.
	DMKGRTAI DMKGRTB DMKGRTBL DMKGRTB5 DMKGRTFD	Accesses total AID table. Orders for 327x Model 2 display terminal. Table of buffer addresses for 3270s with 80 character lines. Table of buffer addresses for 3270s with 132 character lines 'VM/370 online' message.
	DMKGRTFM	Brings in the VM/370 logo and initializes buffer and the CCWs to write the logo in DMKGRF and DMKRGB.
	DMKGRTFO DMKGRTPF DMKGRTP6	Header for 'VM/370 online' message. Accesses PF Key portion of the AID table. Accesses table entry for PF6. This is used for the PA 3 key.
DMKGRU	DMKGRUTB	Resident, non-executable. Orders for 3278 Model 3 display terminal.
DMKGRV	DMKGRVTB	Resident, non-executable. Orders for 3278 Model 4 display terminal.
DMKGRW	DMKGRWTB	Resident, non-executable. Orders for 3278 Model 2A display terminal.
DMKGRX	DMKGRXTB	Resident, non-executable. Screen management data streams for 3278 Model 5 display station.
DMKHPS	DMKHPSDG DMKHPSQR	Pageable. Handles the graphic communication DIAGNOSE. Handles an I/O operation directed to a logical device by
	DMKHPSQV	CP. Handles an I/O operation directed to a logical device by a
	DMKHPSHT DMKHPSEX DMKHPSDI DMKHPSRE	virtual machine. Executes virtual HALT I/O. Reflects special external interrupt. Terminates a logical device. Resets all logical devices owned by a virtual machine.
DMKHPT	DMKHPTEX DMKHPTDI DMKHPTRE	Pageable. Reflects a special external interrupt. Terminates a logical device. Resets all logical devices owned by a virtual machine.
DMKHVC	DMKHVCAL	Resident. Performs services for the virtual machine as requested via the DIAGNOSE instruction. The specific service performed depends on the code in the DIAGNOSE instruction.
DMKHVD	DMKHVDAL	Pageable. Performs services for virtual machines as requested by the DIAGNOSE instruction.
DMKHVE	DMKHVEAL	Pageable. Performs DIAGNOSE X'2C' and X'30' for the virtual machine.
	DMKHVEYL DMKHVEPC	Data table, DASD cylinder/device. Data table, DASD pages/cylinder.

Module Name	Entry Points	Attributes, Function
DMKHVF	DMKHVFAL	Pageable. Performs DIAGNOSE X'C8', X'CC', and X'D4' for the virtual machine.
	DMKHVFCR	Locates or creates a specified LANGBLOK.
DMKIDR	DMKIDRCN DMKIDRFN	Pageable. The Identify System Service. It enables authorized virtual machines to identify themselves as owner of a specific resource or to revoke the ownership of a resource by other virtual machines. Handles pending connections. Handles requests to find the resource owner, received from
	DMKIDRFX	APPCVM while processing connections to a resource name. Cleans up pending messages sent to the TSAF virtual
	DMKIDRIN	machine when the *CRM path has been severed. Handles revoke messages received by the TSAF system service from the TSAF virtual machine.
	DMKIDRSV	Service from the ISAF virtual machine. Handles severs of the path to *IDENT (the Identify System Service).
DMKIDU	DMKIDUMP DMKIDUSF	Pageable. Initializes DASD dump cylinders. Assigns a spool file ID to the dump SFBLOK.
DMKIMG	DMKIMGBG	Pageable. Provides a CMS interface for a VS-based IEBIMAGE program.
DMKIOC	DMKIOCVT	Resident. Converts VM/SP device type to OS/VS device type.
DMKIOE	DMKIOECC	Resident. This is the error recording module. It receives all requests for error recording and passes control to the proper pageable routine after checking if a recording is in progress. If a previous request for error recording is in progress, the current request is queued on the appropriate queue for recording at a later time. It makes a check to determine if the recording cylinder is full. DMKIOE also interfaces with the pageable module that initializes and erases the error recording cylinders. Handle channel check records passed from Channel Check
	DMKIOEFL	Handler. Format pages on the recording cylinders.
	DMKIOEFM DMKIOEMC	Clear and format the recording cylinders. Handle machine check records passed from Machine Check Handler.
	DMKIOEMI DMKIOERN	Format the Missing Interrupt Handler records. Process a 3704/3705 or remote 3270 request.
	DMKIOERR	Schedule recording for unit check, channel data check, and hardware environmental counts.
	DMKIOESD	Format hardware environmental counters. Schedule statistical data recording.
	DMKIOESR DMKIOEST	Schedule statistical data recording. Schedule the update of a statistical data request.
	DMKIOEVR	Process an SVC 76 or Missing Interrupt Handler request.

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Module Name	Entry Points	Attributes, Function
DMKIOF		Resident. Records system and I/O errors on the system disk in predefined error recording cylinders.
	DMKIOFC1	Records channel check error from SIO in DMKIOS when $cc = 1$ .
	DMKIOFCN	Handles CONNECTs from a virtual machine to the Error Logging System Service.
	DMKIOFIN	Initializes pointers to available recording pages at IPL and after an erase has been completed.
	DMKIOFM1 DMKIOFOB	Records machine checks. Entry for a stacked outboard error recording request.
	DMKIOFST DMKIOFSV	Updates statistical data counters. Handles SEVERS from a virtual machine to the Error Logging System Service.
	DMKIOFVR	Records errors when requested by SVC 76.
DMKIOG	DMKIOGF1 DMKIOGF2	Pageable. Called at initialization to locate the error recording device, locate the last outboard error record and system recordings made on the cylinders, and set the in-storage pointers to the correct values. Initialization for RMS functions is performed after first making a test to determine if CP is running under CP. RMS functions are not activated for a virtual CP environment. This module also erases the recording areas. Contains all function of DMKIOG except erase. Erases (1) error records or (2) error records and frame records from the error recording cylinders, depending on input parameters.
<b>ДМКІОН</b>		Pageable Called at initialization if the system error recording area requires formatting, or called while processing a CPEREP CLEARF request. Reads and formats machine check and channel check frames obtained from an SRF device (7443). Recognizes the presence of multiple SRF devices in an attached processor environment. Attempts to read frames from each available SRF device that was generated and format the respective frames at the beginning at the error
	DMKIOHFR	recording cylinders. For 3031/3032/3033 processors, reads frames from SRF devices, formats them in 4096-byte blocks, and writes the records to the error recording cylinders. The appropriate CPU id data is stored in the header portion of each frame record formatted by DMKIOHFR.
DMKIOJ	DMKIOJBL	Resident. Builds and formats outboard and miscellaneous data records.
DMKIOQ		Resident. IOBLOK queue handler and device path finder.
	DMKIOQFC DMKIOQFP DMKIOQFX	Finds a channel path to the device. Finds a path to the device. Finds a fixed path to the device.
	DMKIOQQD DMKIOQUS	Queues an IOBLOK on a real device queue. Queues an IOBLOK on a real control unit queue.
	DMKIOQSK DMKIOQDE	Queues an IOBLOK on a real channel queue. Dequeues the next IOBLOK from a real device queue.
	DMKIOQDE	Dequeues the next IOBLOK from a real device queue. Dequeues the next IOBLOK from a real control unit queue.
	DMKIOQDH	Dequeues the next IOBLOK from a real channel queue.
	DMKIOQHU DMKIOQDQ	Queues an IOBLOK first an a real channel queue. Subroutine to dequeue mini-IOBLOKs from RBLOKs and unchain mini-IOBLOKs from other IOBLOKs.
	DMKIOQQU	Subroutine to queue control unit busy IOBLOK.

Module Name	Entry Points	Attributes, Function
DMKIOS		Resident. Schedules requests for virtual machine and program I/O
	DMKIOSEN DMKIOSER DMKIOSHA DMKIOSQR DMKIOSQV DMKIOSQE	operations. Handles sense operations after a unit check. Error recovery processing. Halts an active device and drains all interruptions. Schedules CP-generated I/O operation. Schedules a virtual machine I/O operation. Reschedules an I/O operation after an I/O error on the channel.
	DMKIOSRC DMKIOSRH DMKIOSRQ DMKIOSRS DMKIOSRU DMKIOSRW DMKIOSC1 DMKIOSC3 DMKIOSST	Error recording. Restarts channel. Requeues request. Restarts device. Restart control unit - channel. Processes the IOBLOK used for REWIND. Handles a deferred condition code 1 interrupt. Handles a deferred condition code 3 interrupt. Issues SIO.
DMKIOT	DMKIOTIN DMKIOTRC	Resident. Processes all I/O interrupts. Processes all pseudo I/O interrupts generated by DMKACRCV.
DMKISM	DMKISMTR	Pageable. Finds and modifies an ISAM CCW string.
DMKIUA	DMKIUAEP DMKIUACP DMKIUACU DMKIUARF DMKIUAPD	Pageable. Prepare to handle user IUCV request. Prepare to handle CP IUCV request on behalf of the system. Prepare to handle CP IUCV request on behalf of a virtual machine. Reflect IUCV message to a virtual machine. Validate an IUCV communications path.
DMKIUB	DMKIUAQU DMKIUBRK	IUCV message queue routine. Pageable. Reflects messages or replies to a virtual machine.
DMKIUC	DMKIUBTB DMKIUCEP	Entry to return CP service entry points. Pageable. Process request for IUCV functions (Accept, Connect, Declare Buffer, Quiesce, Query, Resume, and Retrieve Buffer).
DMKIUE	DMKIUEEP DMKIUEIN	Pageable. Process request for IUCV functions (Send, Describe, Receive, Reply, Test Completion, Set Control Mask, Set Mask, and Test Message). IUCV external interrupt queuing routine.
DMKIUG	DMKIUGER DMKIUGGP	Pageable. Processes request for IUCV functions (Reject and Purge). IUCV routine to obtain a communications path.
DMKIUJ	DMKIUJEP	Pageable. Processes request for IUCV function (Sever).
DMKIUL	DMKIULEP	Pageable. Processes request for IUCV function (Reply and Test Completion).

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Module Name	Entry Points	Attributes, Function
DMKIUN	DMKIUNEP DMKIUNIN	Pageable. Handles IUCV functions. IUCV external interrupt queueing routine.
DMKIUP	DMKIUPEP	Pageable. Services request for IUCV functions.
DMKIUS	DMKIUSEP DMKIUSET	Pageable. Handles IUCV functions. Sets states for APPC/VM functions.
DMKJRL	DMKJRLQU DMKJRLSE DMKJRLLO DMKJRLSL DMKJRLIL	Pageable. Processes the QUERY command. Processes the SET JOURNAL command. Processes LOGONs with invalid passwords. Processes LINKs which are successful. Processes LINKs with invalid passwords.
DMKLD00E	LDRGEN	Loader - utility program. Loads assembled program modules into storage at locations other than those assigned by the assembler. It completes linkage among the modules and transfers control to one of the loaded modules for execution.
DMKLNK	DMKLNKIN DMKLNKSB	Pageable. Links to a virtual DASD because of an issued LINK command. LINK subroutines.
DMKLNM	DMKLNMSG	Pageable. Processes error and response messages for the LINK command.
DMKLOC	DMKLOCK DMKLOCKD DMKLOCKQ DMKLOCKT	Resident. Allows a system resource to be marked in use or not available by a unique 8-character name. Dequeues a locked name. Queues or locks a name. Tests to determine if a name is locked.
DMKLOG	DMKLOGA DMKLOGON DMKLOGOP	Pageable. Logs on a user or operator. Processes the AUTOLOG command. Logs on a user. Logs on the operator.
DMKLOH	DMKLOHRC	Pageable. Updates VMBLOK to LOGON a user or to RECONNECT a user.
DMKLOJ	DMKLOJEP	Pageable. Logs on a user. Creates virtual devices for the virtual machine being logged on.
DMKLOK	DMKLOK DMKLOKDF DMKLOKPS DMKLOKSO	Resident. handles all locking requests when CP is in attached processor mode. Processes an obtain, defer lock request. Processes all spin lock requests. Processes an obtain, defer request for VMBLOK lock.
	DMKLOKSP DMKLOKVM DMKLOKVR	Processes an obtain request for a spin lock that previously failed. Processes an obtain, defer request for VMBLOK lock. Processes a release request for VMBLOK lock.

Module Name	Entry Points	Attributes, Function
DMKLOM	DMKLOMSG	Pageable. Constructs and sends logon-related messages to a user or
	DMKLOMSS	to the operator. Handles the allocation of a MSS disk after the volume has been mounted.
DMKMCC	DMKMCCCL	Pageable. Handles first level MONITOR command processing.
DMKMCD	DMKMCDIN DMKMCDLI DMKMCDTI DMKMCDST DMKMCDSE	Pageable. Processes MONITOR INTERVAL commands. Processes MONITOR LIMIT commands. Processes MONITOR TIME commands. Processes MONITOR STOP commands. Processes MONITOR SEEKS commands.
DMKMCH	DMKMCHIN	Resident. Processes a machine check interruption.
DMKMCI	DMKMCIMS	Pageable. Enables or disables soft machine check recording.
DMKMCT	DMKMCTFS DMKMCTMA DMKMCTPR DMKMCTPT DMKMCTST	Resident. This module is called by the machine check handler in attached processor mode. Handles unsuccessful SIGP recovery. Handles malfunction alert. Handles processor recovery. Handles processor termination. Handles system termination.
DMKMES		Pageable, nonexecutable. This is the CP message repository. It contains most CP messages. Module DMKERM references this message repository to write out messages.
<b>DMKMHC</b>	DMKMHCIN DMKMHCRE DMKMHCCP DMKMHCVM	Resident. Processes MSSFCALL and Service Call external interrupts. Releases a virtual machine HCBLOK at logoff. Handles MSSF service requests and Service Call requests for CP. Handles MSSF service requests for a virtual machine.
DMKMHV	DMKMHVSM	Pageable. Simulates DIAGNOSE X'0080' (MSSFCALL) or Service Call for a virtual machine.
DMKMIA	DMKMIACC DMKMIADL DMKMIAEN DMKMIAIN DMKMIAKC DMKMIAMU DMKMIARO DMKMIAWO	Pageable. Provides various facilities associated with automatic monitoring using spool files. Used for MONITOR CLOSE processing. Used for DMKMCC display function. Used to invoke a MONITOR STOP command. Used to invoke a MONITOR START command. Used to invoke a MONITOR CLOSE command. Generates informational messages for monitor user. Opens monitor spool file, gets SFB, etc. Writes a monitor data buffer to a spool file buffer.

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Module Name	Entry Points	Attributes, Function
DMKMID	DMKMIDNT	Pageable. Changes the date in the system low storage at midnight and resets the clock comparator for the next midnight occurrence. DMKMID also sends messages to all users about the date change.
DMKMNI	DMKMNIDK DMKMNIFI DMKMNISH DMKMNIST DMKMNITH DMKMNITR	Pageable. Constructs spool file header record. Completes monitor shutdown. Initializes MONITOR shutdown. Processes MONITOR AUTO STOP/START command. Handles monitor tape header processing. Writes the MONITOR trailer record.
DMKMNJ	DMKMNJDS DMKMNJGT DMKMNJSP	Pageable. Displays automatic monitoring information defined by SYSMON macro in DMKSYS. Initialize the monitor TRQBLOK for timer driven event sampling. Handles monitor processing for SPOOL to USERID
DMKMNL	DMKMNLIN DMKMNLCP DMKMNLTQ	parameters of START command. Pageable. Performs initialization functions required for 3880 storage control monitoring. Reads and stores the subsystem status by issuing a Sense Subsystem Status SIO, and stacks a TRQBLOK to call DMKMNLTQ. Performs a Sense Subsystem Counts SIO.
DMKMNT	DMKMNLMR DMKMNLFI DMKMNTIO	Writes the latest subsystem counts to the monitor. Terminates 3880 storage control monitoring. Pageable. Marks the I/O devices online during system initialization.
DMKMON	DMKMONIO DMKMONMI DMKMONPR	Pageable. Processes commands and requests associated with the MONITOR, including MONITOR CALL interruptions within CP. Processes tape interruptions returned by DMKIOS. Processes a MONITOR CALL program interruption. Gets space for monitor record and manages buffers.
DMKMOO	DMKMOO00 DMKMOO40 DMKMOOTI	Pageable. Paged in and locked when MONITOR START command is issued. Handles PERFORM (class 0) data collection routine. Handles USER (class four) data collection routine. Handle timer request interruptions.
<b>DMKMPO</b>	DMKMPOEX DMKMPOPX DMKMPORS DMKMPOSP	Pageable. Processes SPMODE multiprocessor external interrupts. Simulates SPX instructions. Processes SPMODE restart interrupts. Simulates SIGP instructions.

Module Name	Entry Points	Attributes, Function
DMKMSG	DMKMSGEC DMKMSGMS DMKMSGNH DMKMSGSM DMKMSGWN	Pageable. Transmits messages to logged-on users for the MESSAGE, SMSG, or WARNING commands. Receives and retransmits lines for the ECHO command for the number of times specified. ECHO command processor. MESSAGE command processor. MSGNOH command processor. SMSG command processor. WARNING command processor.
DMKMSW	DMKMSWR	Resident. Allows system communication with the operator for the enhancement of error recovery procedures.
DMKNEA	DMKNEAAH DMKNEADF DMKNEADH DMKNEAVT	Pageable. Process the network ATTACH function. Access the RDEVBLOK and NICBLOK. Process the network DETACH function. Access the RDEVBLOK and NICBLOK to determine if the virtual address is already attached.
DMKNEM	DMKNEMOP	Pageable. Gets a 5-byte mnemonic opcode for a System/370 binary opcode.
DMKNES	DMKNESEP DMKNESHD DMKNESPL DMKNESWN	Pageable. Processes NETWORK operands as follows: POLLDLAY SHUTDOWN VARY Processes the NETWORK VARY EP command to switch an NCP communication line to EP mode. Processes the NETWORK SHUTDOWN command. Processes the NETWORK POLLDLAY command. Processes the NETWORK VARY NCP command to switch an EP communication line to NCP mode.
DMKNET	DMKNETAE DMKNETDF DMKNETWK	Pageable. Decodes NETWORK command and enables bisync lines. Enable binary synchronous lines and remote stations. Obtains addresses of the RDEVBLOK and NICBLOK for remote devices being defined in DMKVDSDF. NETWORK command decoder.
DMKNLD	DMKNLDR	Pageable. Loads the 3705 network control program. These routines may be called by a console command from DMKNET or internally by DMKCPI (for LOAD) or DMKRNH (for DUMP).
DMKNLE	DMKNLEMP	Pageable. Dump the 3705 Network Control Program.
DMKNMT	DMKNMTBL	Pageable. Construct an Image Library for TEXT files on user disks.
DMKOPE	DMKOPERC DMKOPEEM DMKOPELO DMKOPEDC	Pageable. Locates the operator's console during system initialization. Returns from DMKCNS for entry DMKOPERC. Logs on the system operator during system initialization. Disconnects the system operator during system initialization.
	DMKOPEAC	Sets up auto monitor and logs on the AUTOLOG1 user's virtual machine during system initialization.

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Module Name	Entry Points	Attributes, Function
DMKOPR	DMKOPRWT	Resident. Provides the necessary support for the CP system console. Certain routines within the control program cannot call DMKQCN to issue writes to the system console. This module determines the system's primary console and builds a channel program to handle the requested call.
DMKOVR		Stand-alone. Uses preallocated cylinders to build a Command Class Override file on a specified DASD volume. DMKOVRDE This is the override entry point.
DMKPAG	DMKPAGIO	Resident. Constructs IOBLOKs and schedules the tasks that move virtual storage pages between auxiliary storage and main storage. It also calculates the total system paging load at user-specified intervals.
DMKPAH	DMKPAHIO	Resident. Handles paging I/O and swapping I/O interrupts.
DMKPEI	DMKPEINT	Pageable. Creates a PEXBLOK chain for the PER command.
DMKPEL	DMKPELSD DMKPELCR DMKPELCH	Pageable. Completes handling of the CP PER command. Computes PER control registers 9, 10, and 11. Changes/Merges two PEXBLOK chains.
DMKPEN	DMKPENDA DMKPEND DMKPENSV DMKPENGT	Pageable. Ends all CP PER tracing activity. Handles the PER END subcommand. Handles the PER SAVE subcommand. Handles the PER GET subcommand.
DMKPEQ	DMKPEQRY	Pageable. Handles the QUERY PER command.
DMKPER	DMKPERIL	Pageable. Handles PER program interrupts.
DMKPET	DMKPETAL DMKPETAB	Pageable. Produces an instruction display for the PER event. Produces a display of the TRACEBACK table.
DMKPGM	DMKPGMEP	Pageable. Invoked by the dispatcher if the dispatcher has a CPEXBLOK stacked for page/swap set migration. It migrates pages from storage levels corresponding to high-speed devices (PP, PG if necessary, SW), to lower levels. For swap set migration, DMKSWMIG is called.
DMKPGS		Pageable. Releases the pages of a user's virtual storage space, both from real storage and auxiliary DASD. Also locates and releases a named system which resides in the user's virtual storage.
	DMKPGSPO DMKPGSPP DMKPGSPR	Releases a user's entire virtual storage. Releases only a specified amount of virtual storage. Calls DMKPTSPW to ensure that the user is not in page wait. Then it releases and unlocks a specified amount virtual storage.
	DMKPGSPS DMKPGSSS	Releases a named system from a user's virtual storage. Releases virtual storage but bypasses any virtual storage that contains a named system.

Module Name	Entry Points	Attributes, Function
DMKPGT		Resident.
		Allocates DASD pages (slots) that are used either for
		virtual storage paging or for spool file page buffers.
	DMKPGTCG	Allocates contiguous DASD pages to contain 370X dump
		spool file.
	DMKPGTDG	Allocates contiguous DASD pages to contain a system
·		dump file that was previously spooled to tape.
	DMKPGTDT	Allocates contiguous DASD pages for system dump.
	DMKPGTGC	Creates prototype RECBLOK (for DMKVDG).
	DMKPGTPG	Allocates one DASD or Paging Storage page for system
	DARDOWDM	paging.
	DMKPGTPM	Allocates one DASD or one swap set page for page
	DMKPGTSG	migration.
	DMKPGISG	Allocates one DASD or Paging Storage page for system spooling.
	DMKPGTSW	Allocates contiguous DASD pages for swapping.
	DMIRIGISW	Anocates contiguous DASD pages for swapping.
DMKPGU		Resident.
		Performs DASD storage management.
	DMKPGUAL	Locates the ALOCBLOK for a given DASD or Paging
		Storage slot address
	DMKPGUDU	De-allocates contiguous DASD space for system dump.
	DMKPGUPR	Releases DASD storage used for virtual storage paging.
	DMKPGUSD	Releases one page of DASD storage used for virtual
		spooling.
	DMKPGUSP	Releases one page of DASD storage used for virtual
		paging.
	DMKPGUVG	Allocates a page of virtual storage belonging to the CP
	DIGUDGUUD	paging VMBLOK.
	DMKPGUVR	Releases a virtual storage page.
	DMKPGUPP	Deallocates DASD slots that were formerly used for pages
	DMKPGUSR	that are now in real storage and about to be swapped out. Releases DASD pages belonging to a spool file that is no
	DMRFGUSK	longer needed.
	DMKPGUSW	Deallocates DASD slots that were used for swapped pages.
	Dimini GOOM	Dealectates Dried slots that were abla for swapped pages.
DMKPIA		Pageable.
	DMKPIALD	Contains the FOB buffer images that the LOADBUF
		command uses to load the Font Offset Buffer of the 3289
		Model 4 printer. This module does not contain executable
		code.
DMUTDID		
DMKPIB	DMEDIDID	Pageable.
	DMKPIBLD	Contains the buffer images that the LOADBUF command
		uses to load the UCSB of the IBM 3262 Printer, Models 1 and 11. This module does not contain executable code.
		and II. This module does not contain executable code.
DMKPMA		Resident.
	DMKPMACD	Tests virtual device restrictions.
	DMKPMAEX	Terminates preferred machine assist for the virtual
		machine.
	DMKPMAI1	Initializes PSA and builds control register 2 channel
		masks.
	DMKPMAI2	Initializes fields for privileged operation simulation.
	DMKPMAIN	Processes PMA program interruptions.
	DMKPMAMC	Routes external damage machine checks to PMA virtual
		machine.
	DMKPMASS	Saves PMA environment and establishes virtual machine
	DIFFERENCE	environment.
	DMKPMASW	Dispatches preferred virtual machine. Saves data for
	DATADA	interruption handlers.
	DMKPMAWT	Processes when PMA guest loads a wait state PSW.
	DMKPMAXF	Processes extended storage fetch for PMA virtual machine.
	DMKPMAXS	Processes extended storage stores for PMA virtual
		machine.

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Module Name	Entry Points	Attributes, Function
DMKPRG	DMKPRGIN DMKPRGRF DMKPRGSM	Resident. Processes a hardware program interruption. Reflects an SVC interruption to the virtual machine. Simulates a virtual program interruption.
DMKPRV	DMKPRVLG	Resident. Simulates a privileged operation.
DMKPRW	DMKPRWIP DMKPRWTP DMKPRWTB	Resident. Simulates IPTE instruction. Simulates TPROT instruction. Simulates TEST BLOCK instruction.
	DMKPRWSK DMKPRWXK	A flag that indicates that the extended key instructions should be simulated. Simulates ISKE, RRBE, and SSKE instructions. For guest operating systems with single key virtual storage, simulates ISK, SSK, and RRB instructions. (XKEYMODE must be set to B'1').
DMKPSA	DMKPSACG DMKPSADU DMKPSAFC DMKPSAFP DMKPSAID DMKPSAPO	Resident. Charges accumulated time to a virtual machine. PSW restart processing. Forces an SVC 0 type of dump. Checks fetch protection per the CAW key. Checks for fetch protection violation per PSW key. Gets virtual address for any instruction. Returns the real storage location of virtual page 0 for a virtual machine if that page is in storage, or returns a nonzero return code.
	DMKPSARR DMKPSARS DMKPSARX DMKPSASC DMKPSASP	Gets the virtual address for an RR instruction. Gets the virtual address for RS, SI, or SS instruction. Gets the virtual address for an RX instruction. Checks storage protection per the CAW key. Checks for a storage protection violation per the PSW key.
DMKPST	DMKPSTIN	Pageable. Initialize Paging Storage ALOCBLOKs and RECBLOKs.
DMKPTR		Resident. Manages the inventory of real system pages, provides real storage space for CP functions and for pages of user and CP virtual storage.
	DMKPTRAN DMKPTRAQ	Translates user virtual storage address to a real storage address. Performs working set calculations each time a page is
	DMKPTRCO DMKPTREP	acquired in real storage. Obtains a page of real storage for cache-aligned pools. Obtains a page from $> 16Mb$ or $< 16Mb$ free list,
	DMKPTRFP	(whichever is larger). Resumes extend processing once a page frame has been obtained.
	DMKPTRFR DMKPTRLK	Gets a page of real storage. Locks a page of real storage and processes the CP assist
	DMKPTRPS	instruction, PTRLK (E602). Determines swap table and page table addresses for a given
	DMKPTRUL	virtual address. Unlocks a page of real storage and processes the CP assist instruction, PTRUL (E603).

Module Name	Entry Points	Attributes, Function
DMKPTS	DMKPTSAD DMKPTSAE DMKPTSPW DMKPTSRS	Resident. Decodes a page frame entry into a 26-bit real address. Encodes a 26-bit real address into a page frame table entry. Called to defer execution of system reset functions when user's virtual machine is in page wait. Resets pages belonging to a user.
DMKPTT	DMKPTTAL	Resident. Allocates virtual page slots that DMKPTTPM used for page moves between the less than 16 Mb dynamic paging area and the greater than 16 Mb dynamic paging area. This function is invoked when extended storage is initially brought online, or when the $V = R$ area is unlocked and the greater than 16 Mb page frames are initialized for the first time.
	DMKPTTCL DMKPTTFT DMKPTTPM	Clears a 4K page of extended storage. Releases a page of real storage. Moves a 4K page of data from one page frame to another, regardless of whether the page is in the extended storage area.
DMKPXA		Resident, nonexecutable. Used as an extension to the PSA.
DMKPXB		Resident, nonexecutable. Used as an extension to the PSA.
DMKQCN	DMKQCNWT	Resident. Starts and queues a console write request. If a buffer larger than 4K bytes is required, an indirect address list is used to address the noncontiguous buffer.
DMKQCO	DMKQCOCL DMKQCOET DMKQCONQ DMKQCORD DMKQCOSY	Resident. Clears CONTASK stack and returns all blocks to free storage. Processes completed CONTASKs for virtual console spooling, return or no return options, and returns the CONTASK blocks to free storage. Queues up CONTASK. Start and queues a console Read request. Synchronizes virtual machine console activity with
DMKQCP	DMKQCPTO	internal supervisor activity (used during virtual system reset and logoff). Pageable. Disconnects the virtual machine. Sets the TOD clock comparator request to logoff the virtual machine after a 15-minute delay.
DMKQCQ	DMKQCQED	Resident. Edits data streams for console writes.
DMKQVM	DMKQVMEP DMKQVMRT DMKQVMRS DMKQVMRX	Resident. Process the CP QVM command. Performs the switch to native mode for the $V = R$ user. Performs the switch back to VM/370 for non-370E operating system. Performs the switch back to VM/370 for 370E operating system.
DMKREI	DMKREIPL	Pageable. Re-IPLs a user of the Protected Application Facility.

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Module Name	Entry Points	Attributes, Function
DMKRET	DMKRETGT DMKRETPT	Resident. Gets an input line from the retrieve buffer. Puts an input line into the retrieve buffer.
DMKRGA	DMKRGAIN	Resident. This is the second-level interruption handler for remote 3270 stations. This module supports the 3270 remote display and printer stations. It processes interruptions and
	DMKRGATM	CCWs for the remote stations, including message handling and screen management. Processes time interrupts for the following 3270 display conditions: - completion of poll delay period
	DMKRGASP DMKRGA2	<ul> <li>completion of 60 second delay on priority messages</li> <li>more timeout</li> <li>3 second 'Not Accept' message</li> <li>Complete processing full screen WSF and QUERY</li> <li>If a buffer larger than 4K bytes is required, an indirect address list is used to address the noncontiguous buffer.</li> </ul>
DMKRGB	DMKRGBCL	Resident. Supports the 3270 remote display and printer stations. It processes interruptions and CCWs for the remote stations including message handling and screen management. Clears the display screen after full screen I/O.
	DMKRGBCO DMKRGBIC	Continues output operations on a currently selected remote station. Initializes and schedules CONTASKs.
	DMKRGBEN DMKRGBSN	Enables and disables bisync lines and remote stations. Screens NICBLOK list for output messages; does general poll if none found.
	DMKRGBMT DMKRGBPL DMKRGBRE DMKRGBSL DMKRGBUP	Formats the display screen. Issues a specific or general poll. Starts I/O on a teleprocessing line. Selects a remote station. Updates a remote 3270 screen.
DMKRGC		Resident. Processes 3270 input data and status messages after validation by DMKRGA.
DMKRGD	DMKRGDOB	Resident. Performs the blocking of output data to display terminals and updates the CONTASK. If a buffer larger than 4K bytes is required, an indirect address list is used to addres
	DMKRGDOI	the noncontiguous buffer. Performs the blocking of output data to display terminals and updates the CONTASK. If a buffer larger than 4K bytes is required, an indirect address list is used to address the noncontiguous buffer.
DMKRGE	DMKRGESK	Resident. Handles skip processing for BSC and VM/SNA devices.
DMKRIO	DMKRIO	Resident. Exists as a CSECT and defines the machine's configuration. A basic DMKRIO is shipped with the system. DMKRIO can be changed at system generation or whenever new machines are added by using the appropriate macros.
DMKRND	DMKRND	Residency not applicable. Invoked via the NCPDUMP command in CMS. This is the interface between the dump spool file and the OS-SSP dump format program for printing and formatting dumps of the 3704 and 3705 communications controllers.

Module Name	Entry Points	Attributes, Function
DMKRNH	DMKRNHIC	Resident. Initializes and schedules the CONTASK fields that comprise the 3704 and 3705 Network Control Program
	DMKRNHIN	transmission header. This is the secondary interruption handler for the 3704 and 3705 communication controllers it is read when operating in NCP or PEP mode.
	DMKRNHND	Schedules control functions for the 3705 or 3704 Network Control Program.
DMKRPA		Resident. Virtual storage mapping.
	DMKRPAGT DMKRPAPT	Page-in from DASD to user's virtual storage. Page-out to DASD from user's virtual storage.
DMKRPD	DMKRPDEP	Pageable. Process security DIAGNOSE X'A0' instruction.
DMKRPI	DMKRPICN DMKRPIRA	Pageable. Process IUCV connect request. Process access verification.
DMKRPW	DMKRPWEP	Pageable. Process password verification.
DMKRSE	DMKRSERR	Pageable. Real UR device I/O error handler. Retries and attempts to recover from real unit record device I/O errors.
DMKRSF	DMKRSFPR DMKRSFSD DMKRSFPB	Pageable. Real UR device I/O error handler. Gets 3211 type printers error information. Formats 3800 printer hardware environmental counters. Purges 3800 page buffer on a 3800 Printing Subsystem when called due to an error.
DMKRSP	DMKRSPER DMKRSPEX	Resident. Manages all spooling operations on the real system unit record devices including printing and punching user-created spool files and reading and queueing reader files from the real card reader. Processes spooling errors (ERP). Processes spooling operations. Entered via a GOTO when DMKDSPCH unstacks an IOBLOK with an interruption for the spooling unit record device.
DMKRSQ		Pageable. Handles the spool file buffers when data chaining between DASD buffer is required for example, for 3800 Load Graphic Modification or Load Copy Modification CCW.
	DMKRSQDC	Obtains buffers needed for data chaining and after the associated CCWs.
	DMKRSQFR	Free the buffers obtained by DMKRSQDC.
DMKRST		Pageable. Handles operations on the real system unit record card readers.
	DMKRSTIN	Processes an interrupt from a real card reader.
DMKSAD	DMKSADM	Stand-alone. Produces a stand-alone dump of real storage on a tape or
,	DMKSADWT	printer. Utility that writes the stand-alone dump program on the IPL volume.

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Module Name	Entry Points	Attributes, Function
DMKSAV	DMKSAVNC DMKSAVRS	Pageable. DMKSAVNC is entered via an LDT card from DMKLDR. DMKSAVRS is entered via a BALR from DMKCKP. DMKSAV saves and restores a page image count of the CP nucleus on the system residence disk. Writes a page image copy of the CP nucleus. Restores a page image copy of the CP nucleus.
DMKSBL	DMKSBLTR	Pageable. Creates a line of small block letters for DMKSEP.
DMKSCH	DMKSCHDL DMKSCHST DMKSCHRT SWAPIN	Resident. Maintains the run list, the dispatch list (true run list), and the eligible list. Also calculates projected working set sizes and deadline priorities, keeps statistics on processor use, and monitors favored execution users. Alters a user's dispatching status. Establishes a clock comparator interrupt request. Resets a clock comparator interrupt request. Interrupt return address for the prepaging function.
DMKSCN	DMKSCNAU	Resident. Scans module. Searches the chain of VMBLOKs for one whose userid
	DMKSCNFD DMKSCNEP DMKSCNLI	matches the one pointed to by register one. Finds the next field in an input message buffer. Determines if there is an online path from either processor. Searches the logged-on virtual machines for any links to a specified minidisk. A link is any virtual device whose RDEVBLOK pointer and relocation factor match those
	DMKSCNP DMKSCNPH	specified. Finds the RCHBLOK and RCUBLOK that represents the next logical path to the device.
	DMKSCNRA	Calculates a bit mask defining the indicated device path. Computes a full real device address (in CCU form) from the RDEVADD, RCUADD, and RCHADD entries in the real device, control unit, and channel blocks.
	DMKSCNRD	Computes a real device address (in CCU form), from the RDEVADD, RCUADD, and RCHADD entries in the real device, control unit, and channel blocks.
	DMKSCNRN	Returns the name of the real device to the caller in register 1.
	DMKSCNRU DMKSCNVD	Returns the addresses of the real channel, control unit, and device blocks for a given real device to the caller. Computes a full virtual device address (in CCU form), plus the addresses of the virtual channel and control unit
	DMKSCNVN DMKSCNVS	blocks from a specific virtual device block. Returns the name of the virtual device to the caller in R1. Searches all the real device blocks for a device whose volume serial number matches the one pointed to by R1.
	DMKSCNVU	Returns the addresses of the virtual channel, control unit,
	DMKSCNDC	and device blocks for a given real device to the caller. Returns the addresses of the RDEVBLOK that is given the device code (the $D$ of $CCPD$ or $PPPD$ ).
DMKSCO	DMKSCOLI	Pageable. Searches the logged-on virtual machines for any links to a specified minidisk. A link is virtual device whose RDEVBLOK pointer and relocation factor match those
	DMKSCONP	specified. Finds the RCHBLOK and RCUBLOK that represent the next logical path to the device.

Module Name	Entry Points	Attributes, Function
DMKSEG	DMKSEGPG DMKSEGWR	Pageable. Initializes CP page, segment, core, and swap tables. Creates page image copies of all the pageable modules between DMKSAV and DMKCKP.
DMKSEL		Resident Called when pages are needed to replenish the free list. Controls the order by which the system searches for free pages.
	DMKSELCT DMKSELFE	Replenishes the free lists. Entered via a CPEXBLOK built by DMKPTRFR, on extend condition.
	DMKSELFD	Entered via a CPEXBLOK built by DMKPTRFR, after a page has been written.
	DMKSELSL	Entered via a CPEXBLOK built by DMKPTRFR, on deferred request.
DMKSEP	DMKSEPSP	Pageable. Prints and punches the respective output separators on real spooling devices.
	DMKSEPTL	Prints the trailer page.
DMKSEQ	DMKSEQDA DMKSEQLA DMKSEQSA	Pageable. Contains a data area. Contains printer separator logo. The area for the sequence number.
DMKSEV	DMKSEV70	Pageable but locked. Analyzes 2870 channel logout and sets appropriate bits in the ECSW field according to the results of analysis. It moves the channel logout to the check record.
DMKSFB	DMKSFBNS	Pageable. Process Diagnose Code X'D8'.
DMKSIX		Pageable but locked. Analyzes 2860 channel logout and sets appropriate bits in the ECSW field according to the results of analysis. It moves the channel logout to the check record.
DMKSNC	DMKSNCP	Pageable. Save a page-form version of a 3704/3705 network control program. The name of the network control program and the DASD location at which it is to be saved is defined in the CP module DMKSYS.
DMKSND	DMKSNDNH	Pageable. Processes the SEND command (which is used to send commands) and replies to disconnected virtual machines.
DMKSNT	DMKSNTBL	Pageable. This module is assembled by the installation system programmer. It describes the system to be saved via the SAVESYS command and to be initial program loaded by name. Shared segments may be specified. These segments consist of all reenterable code and no altering of this storage is allowed. There is no executable code in this module.
DMKSPC	DMKSPCEX DMKSPCHF DMKSPCQC DMKSPCSY DMKSPCUS	Pageable. Used by PROFS to reduce the number of spool files used for electronic mail. Determines if spool file concatenation is installed. Concatenates reader files. Processes the QUERY CHAIN command. Processes the SET CHAIN SYSTEM command. Processes the SET CHAIN USER command.

Module Name	Entry Points	Attributes, Function
DMKSPK	DMKSPKDL DMKSPKDR	Pageable. Deletes used files from the system and deallocates the DASD page space. Deletes all SFBLOKs in the DMKRSPDL chain and exits to the dispatcher.
DMKSPL	DMKSPLCR DMKSPLCV DMKSPLOR DMKSPLOV	Pageable. Spool file manager. Closes and queues a real reader spool file for virtual input. Closes and queues a virtual printer or punch spool file for processing. Initializes control blocks and buffers for real input reader files. Initializes control blocks and buffers for virtual printer and punch output spool files.
DMKSPM	DMKSPMEP	Pageable. Processes the CP SPMODE command. Turns the single processor mode environment on and off.
DMKSPR	DMKSPROT	Resident. Processes requests for new tape for SPTAPE DUMP.
DMKSPS	DMKSPSIO	Pageable. Performs the processing requested by the SPTAPE command and handles all returns from the I/O interrupt handler due to the command.
DMKSPT	DMKSPTEP	Pageable Validates the format of the SPTAPE command and initiates the processing to write, read, or scan a tape for specified printer and punch spool files.
DMKSRM	DMKSRMEP	Pageable. Sets and displays system performance indicators.
DMKSSP	DMKSSP01	This module is found in the starter system only. It builds RCHBLOKs, RCUBLOKs, and RDEVBLOKs necessary to configure a minimum CP system. From the starter system, a real CP system figured based on the REALIO deck of the installation. Entered as a result of an IPL operation. Constructs the I/O blocks and system modules for a minimum system configuration.

Module Name	Entry Points	Attributes, Function
DMKSSS		Resident.
		Services routines for all other modules that require access
	DIGRAGA	to the MSS.
	DMKSSSAS DMKSSSCA	Attaches a 3330V to the system with a VOLID. The communicator device address.
	DMKSSSCA	The VMBLOK address of the communicator virtual
	DMIREBUCY	machine.
	DMKSSSDE	Demounts an MSS volume from a 3330V.
	DMKSSSEN	Returns to the appropriate requesting routine after an
	DMKSSSHR	MSS volume mount is complete. The CCPD of the SDG table containing shared VUAs.
	DMKSSSIIK DMKSSSL1	Processes a DEDICATE statement with the 3330V parameter.
	DMKSSSL2	Processes a DEDICATE statement with raddr and volid specified, when the raddr is a 3330V.
	DMKSSSL3	Processes a DEDICATE statement with a volid but no raddr.
	DMKSSSLN	Allocates a 3330V device and mounts the required 3330V system volume.
	DMKSSSMQ	Serves as the anchor for the MSSCOM control blocks that are queued for MSS mounts, demounts, and pack change
	DATECCON	interrupts. Does not contain executable code.
	DMKSSSNS DMKSSSNV	The CCPD of the SDG table containing nonshared VUAs. The anchor of the SDG in which a VUA was last selected
	DIMESSONV	for a mount request.
	DMKSSSRL	Issues a relinquish request to destage any changed
		cylinders of the volume mounted on the specified VUAs.
	DMKSSSVA DMKSSSVM	Attaches a 3330V to the system or a virtual machine. The userid of the communicator virtual machine.
DMKSST		Pageable. This is a service routine for modules that require access to
		MSS.
	DMKSSTBL	Builds SDG tables of VUAs in CP configuration.
	DMKSSTFV	Finds an available VUA on which to mount a virtual volume.
DMKSSU		Resident.
		Handles service requests for processing whenever an attention interrupt or a cylinder fault is detected on a 3330V and whenever a reset is required for a virtual device
	DMKSSUCF	defined on a 3330V. Resets a virtual device defined on a 3330V, including
	DMKSSUI1	purging any I/O waiting for an MSS volume mount. Reschedules an I/O operation that had previously caused a cylinder fault.
	DMKSSUI2	Queues an I/O request that has just caused a cylinder fault. Sets the missing attention handler timer interrupt
	DMKSSULO	value. Checks for unfinished MSS processing before completing logoff.
DMKSSV		Pageable. This is a service routine for the MSS communicator virtual
	DMKSSVHV	machine. Process DIAGNOSE Code X'78'.
	DMKSSVUS	Quiesces all MSS mount and demount activity.
DMKSTA	DMKSTANT	Pageable. Clears main storage, initializes the CORTABLE, and
		allocates main storage.

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DMKSTD       DMKSTDAT       Resident, nonexecutable.         DMKSTK       Starting address of STDATA table.         DMKSTK       Resident.         Stacks a CPEXBLOK.       Stacks a CPEXBLOK.         DMKSTKICP       Stacks a CPEXBLOK LIFO (used by EXTEND and machine check).         DMKSTKDP       Stacks a CPEXBLOK for current processor only.         DMKSTRP       Pageable.         DMKSTPX       Pageable.         DMKSTRM       Stacks CPEXBLOK for the other processor is varied online control fields.         DMKSTP       Pageable.         DMKSTRM       Stacks CPEXBLOK for machine indicators and scheduling control fields.         DMKSTR       Pageable.         DMKSTRM       Invoked from DMKPTR if there is a segment exception.         Invoked from DMKSTRAN or DMKSTRSM to process a pseudo page.       Invoked from DMKSTRAN or DMKSTRSM to process a pseudo page.         DMKSVC       Resident.       Handles any SVC interrupt.         DMKSVDIN       Resident.       Controls logical swap-in and physical swap activities.         DMKSWAPD       DMKSWAPD       Called after completion of swap out. For successful swap outs, it resets in transit and change bits for SWPTABLE entries and puts page frames on the free list. For unsuccessful swap outs, it resets in transit and change bits for SWPTABLE entries and puts page frames on the free list. For unsuccessful swap outs, it calls DMKSCH.	Module	Entry	
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DMKSTRPMInvoked from DMKSTRAN or DMKSTRSM to process a pseudo page. Invoked if the dispatcher has a CPEXBLOK for swap table migration.DMKSVCDMKSVCINResident. Handles any SVC interrupt.DMKSVDDMKSVDINResident. Handles problem state SVC interrupts.DMKSWADMKSWAPOControls logical swap-in and physical swap out. Called by DMKSEL to process a physical swap out. Called after completion of swap out. For successful swap outs, it resets in transit and change bits for SWPTABLE entries and puts page frames on the free list. For unsuccessful swap outs, it calls DMKPGTSW to allocate a new swap slot.DMKSWMDMKSWAPRProcesses pre-paging requests from DMKSCH. Invalidates a swap set block (SSBLOK) entry.DMKSWMIGPageable. Migrates swap sets from Paging Storage (TYPE = SW) to next available TYPE = SW level or, as a second choice, to the first available TYPE = SW level or, as a second choice, to the first available TYPE = SW level or, as a second choice, to the first available TYPE = SW level or, as a second choice, to the first available TYPE = SW level or, as a second choice, to the first available TYPE = SW level or, as a second choice, to the first available TYPE = SW level or, as a second choice, to the first available TYPE = SW level or, as a second choice, to the first available TYPE = SW level or, as a second choice, to the first available TYPE = SW level or, as a second choice, to the first available ron-Paging Storage TYPE = PP level. Invoked if page/swap set migration is invoked by the MIGRATE command. If the command is valid, a CPEXEBLOK is stacked with an entry point of RESETUS to migrate one user.DMKSYMDMKSYMPageable. Provides a symbol table of selected CSECTS and entry points	DMKSTR		Pageable.
DMKSTRSMInvoked if the dispatcher has a CPEXBLOK for swap table migration.DMKSVCDMKSVCINResident. Handles any SVC interrupt.DMKSVDDMKSVDINResident. Handles problem state SVC interrupts.DMKSWADMKSWAPOResident. Controls logical swap-in and physical swap activities. Called by DMKSEL to process a physical swap out. Called by DMKSEL to process a physical swap out. Called after completion of swap out. For successful swap outs, it resets in transit and change bits for SWPTABLE entries and puts page frames on the free list. For unsuccessful swap outs, it calls DMKPGTSW to allocate a new swap slot.DMKSWMDMKSWAPPProcesses pre-paging requests from DMKSCH. Invalidates a swap set block (SSBLOK) entry.DMKSWMIGPageable. Migrates swap sets from Paging Storage (TYPE = SW) to next available TYPE = SW level or, as a second choice, to the first available non-Paging Storage TYPE = PP level. Invoked if page/swap set migration is invoked by the MIGRATE command. If the command is valid, a CPEXBLOK is stacked with an entry point of DMKFGMX to migrate all users, or an entry point of RESETUS to migrate one user.DMKSYMDMKSYMPageable. Provides a symbol table of selected CSECTS and entry points.			Invoked from DMKSTRAN or DMKSTRSM to process a
DMKSVCINHandles any SVC interrupt.DMKSVDDMKSVDINResident. Handles problem state SVC interrupts.DMKSWADMKSWAPOResident. Controls logical swap-in and physical swap activities. Called by DMKSEL to process a physical swap out. Called by DMKSEL to process a physical swap out. Called after completion of swap out. For successful swap outs, it resets in transit and change bits for SWPTABLE entries and puts page frames on the free list. For unsuccessful swap outs, it calls DMKPGTSW to allocate a new swap slot.DMKSWAPIProcesses pre-paging requests from DMKSCH. Invalidates a swap set block (SSBLOK) entry.DMKSWMIGPageable. Migrates swap sets from Paging Storage (TYPE = SW) to next available TYPE = SW level or, as a second choice, to the first available non-Paging Storage TYPE = PP level. Invoked if page/swap set migration is invoked by the MIGRATE command. If the command is valid, a CPEXBLOK is stacked with an entry point of DMKPGMX to migrate all users, or an entry point of RESETUS to migrate one user.DMKSYMDMKSYMPageable. Provides a symbol table of selected CSECTS and entry points.		DMKSTRSM	Invoked if the dispatcher has a CPEXBLOK for swap table
DMKSVDINHandles problem state SVC interrupts.DMKSWAMKSWAPOResident. Controls logical swap-in and physical swap activities. Called by DMKSEL to process a physical swap out. Called after completion of swap out. For successful swap outs, it resets in transit and change bits for SWPTABLE entries and puts page frames on the free list. For unsuccessful swap outs, it calls DMKPGTSW to allocate a new swap slot. Processes pre-paging requests from DMKSCH. Invalidates a swap set block (SSBLOK) entry.DMKSWMPageable. Migrates swap sets from Paging Storage (TYPE = SW) to next available TYPE = SW level or, as a second choice, to the first available non-Paging Storage TYPE = PP level. Invoked if page/swap set migration is invoked by the MIGRATE command. If the command is valid, a CPEXBLOK is stacked with an entry point of DMKPGMX to migrate all users, or an entry point of RESETUS to migrate one user.DMKSYMPageable. Provides a symbol table of selected CSECTS and entry points.	DMKSVC	DMKSVCIN	
DMKSWAPO DMKSWAPDControls logical swap-in and physical swap activities. Called by DMKSEL to process a physical swap out. Called after completion of swap out. For successful swap outs, it resets in transit and change bits for SWPTABLE entries and puts page frames on the free list. For unsuccessful swap outs, it calls DMKPGTSW to allocate a new swap slot.DMKSWAPI DMKSWAPRProcesses pre-paging requests from DMKSCH. Invalidates a swap set block (SSBLOK) entry.DMKSWMIGPageable. Migrates swap sets from Paging Storage (TYPE = SW) to next available TYPE = SW level or, as a second choice, to the first available mon-Paging Storage TYPE = PP level. Invoked if page/swap set migration is invoked by the MIGRATE command. If the command is valid, a CPEXBLOK is stacked with an entry point of DMKSPGMX to migrate one user.DMKSYMDMKSYMPageable. Provides a symbol table of selected CSECTS and entry points.	DMKSVD	DMKSVDIN	
DMKSWAPI DMKSWAPRProcesses pre-paging requests from DMKSCH. Invalidates a swap set block (SSBLOK) entry.DMKSWMPageable.DMKSWMIGPageable.DMKSWMIGMigrates swap sets from Paging Storage (TYPE = SW) to next available TYPE = SW level or, as a second choice, to the first available non-Paging Storage TYPE = PP level. Invoked if page/swap set migration is invoked by the MIGRATE command. If the command is valid, a CPEXBLOK is stacked with an entry point of DMKPGMX to migrate all users, or an entry point of RESETUS to migrate one user.DMKSYMPageable. Provides a symbol table of selected CSECTS and entry points.	DMKSWA		Controls logical swap-in and physical swap activities. Called by DMKSEL to process a physical swap out. Called after completion of swap out. For successful swap outs, it resets in transit and change bits for SWPTABLE entries and puts page frames on the free list. For unsuccessful swap outs, it calls DMKPGTSW to allocate a
DMKSWAPRInvalidates a swap set block (SSBLOK) entry.DMKSWMPageable.DMKSWMIGPageable.DMKSWMIGMigrates swap sets from Paging Storage (TYPE = SW) to next available TYPE = SW level or, as a second choice, to the first available non-Paging Storage TYPE = PP level.DMKSWMUSDMKSWMUSDMKSWMUSInvoked if page/swap set migration is invoked by the MIGRATE command. If the command is valid, a CPEXBLOK is stacked with an entry point of DMKPGMX to migrate all users, or an entry point of RESETUS to migrate one user.DMKSYMPageable. Provides a symbol table of selected CSECTS and entry points.		DMKSWAPI	
DMKSWMIGMigrates swap sets from Paging Storage (TYPE = SW) to next available TYPE = SW level or, as a second choice, to the first available non-Paging Storage TYPE = PP level.DMKSWMUSInvoked if page/swap set migration is invoked by the MIGRATE command. If the command is valid, a CPEXBLOK is stacked with an entry point of DMKPGMX to migrate all users, or an entry point of RESETUS to migrate one user.DMKSYMPageable. Provides a symbol table of selected CSECTS and entry points.		DMKSWAPR	
DMKSWMUSInvoked if page/swap set migration is invoked by the MIGRATE command. If the command is valid, a CPEXBLOK is stacked with an entry point of DMKPGMX to migrate all users, or an entry point of RESETUS to migrate one user.DMKSYMPageable. Provides a symbol table of selected CSECTS and entry points.	DMKSWM	DMKSWMIG	Migrates swap sets from Paging Storage (TYPE = SW) to next available TYPE = SW level or, as a second choice, to
DMKSYM Provides a symbol table of selected CSECTS and entry points.		DMKSWMUS	Invoked if page/swap set migration is invoked by the MIGRATE command. If the command is valid, a CPEXBLOK is stacked with an entry point of DMKPGMX to migrate all users, or an entry point of RESETUS to
DMKSYS Resident.	DMKSYM	DMKSYM	Provides a symbol table of selected CSECTS and entry
DMKSYS Exists as a CSECT that defines the system residence volume, paging space, operator ID, dump ID, storage size, and time zone.	DMKSYS	DMKSYS	Exists as a CSECT that defines the system residence volume, paging space, operator ID, dump ID, storage size,
DMKSYSRM Real storage size of the processor.		DMKSYSRM	1

Module Name	Entry Points	Attributes, Function
DMKTAP	DMKTAP DMKTAPER	Pageable. Examines the error condition resulting from a unit check while executing a CP generated tape channel program. Positioning of the tape is required on read/write commands and the channel program is reexecuted. If the error condition is uncorrectable, a call is issued to the message writer (DMKMSW) to notify the operator. Upon regaining control from DMKMSW, the original channel program may be reexecuted or terminated. Retries the failing tape channel program, after a tape
	DMKTAPRL	positioning command has been executed. Performs tape release to determine two- or four-channel switch capability.
DMKTAQ	DMKTAQRP DMKTAQSE DMKTAQRE	Pageable. Continues tape error recovery started by DMKTAP. Repositions tape following a read-type error. Continues checking for the cause of the original device error. Entered following a tape reposition operation.
DMKTBL	DMKTBL	Resident. Contains the terminal translate tables.
DMKTBM		Pageable, nonexecutable. Contains terminal translate tables for APL/ASCII for non-TTY terminals.
DMKTBN	DMKTBNAO DMKTBNAI DMKTBNAE DMKTBNEA	Pageable, nonexecutable. Contains terminal translate tables for APL/ASCII for TTY terminals. EBCDIC APL to ASCII APL translation. ASCII APL to EBCDIC APL translation. ASCII to EBCDIC translation. EBCDIC to ASCII translation.
DMKTCS	DMKTCSET DMKTCSSP DMKTCSCO DMKTCSTR DMKTCSML	Pageable. Sets up the 3800 prior to printing the file. Sets up the 3800 prior to printing the separator. Sets up the forms overlay sequence control. Prints trailer page on a 3800 printer. Loads members from image library.
DMKTCT	DMKTCTET	Pageable. Loads character arrangement table, WCGM's, LCS's, and graphic character modifications into a 3800 printer.
DMKTDK	DMKTDKGT DMKTDKRL	Pageable. Allocates and deallocates cylinders (CKD) or blocks (FBA) of TDISK space from CP-owned volumes. Allocates TDISK space. Deallocates TDISK space.
DMKTEE	DMKTEESF	Residency not applicable. Formats the subtype specific information of a CP trace entry. (This module contains the first half of the process. The second half is contained in DMKTEF.)
DMKTEF	DMKTEFSF	Residency not applicable. Formats the subtype specific information of a CP trace entry. (This module contains the second half of the process. The first half is contained in DMKTEE.)

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Module Name	Entry Points	Attributes, Function
DMKTEM	DMKTEMEP	Residency not applicable. Formats a CP trace entry. (This is the main module for trace entry formatting. The support modules are DMKTEE, DMKTEF, and DMKTES.)
DMKTES	DMKTESUB	Residency not applicable. This module performs miscellaneous subroutines for the main trace entry formatter, (DMKTEM).
DMKTHI	DMKTHIFA DMKTHIIO DMKTHILO DMKTHIPA DMKTHIQQ DMKTHIUG DMKTHIUS	Pageable. Displays data about use of and contention for major system resources. Processes the INDICATE FAVORED command. Processes the INDICATE I/O command. Processes the INDICATE LOAD command. Processes the INDICATE PAGING command. Processes the INDICATE QUEUES command. Processes the INDICATE USER command for general users. Processes the INDICATE USER command for primary system operators, system resource operators, and system analysts.
DMKTMR	DMKTMRCC DMKTMRCK DMKTMRPT DMKTMRSP DMKTMRTN DMKTMRVT	Resident. Simulates the CPU timer and time-of-day clock comparato instructions for virtual machines operating in EC mode. Entered after expanded virtual machine assist processing of a virtual SCKC instruction. Simulates virtual clock comparator interruptions. Calculates user's total virtual problem time. Entered after expanded virtual machine assist processing of a virtual SPT instruction. Simulates timer instruction. Simulates virtual CPU timer interruptions.
DMKTOD	DMKTODIN	Pageable. Initializes the time of day clock.
DMKTPE	DMKTPERP	Pageable. Performs 3480 error recovery processing.
DMKTRA	DMKTRACE	Pageable. Processes the TRACE command line. Provides a virtual machine with facility to track SVC instructions, program interrupts, external interrupts, successful searches, or all instructions with output on the printer or terminal. TRACE command processor.
DMKTRC	DMKTRCEX DMKTRCIO DMKTRCIT DMKTRCND DMKTRCPB DMKTRCPG DMKTRCPV DMKTRCSV DMKTRCSW	<ul> <li>Pageable.</li> <li>Processes the TRACE command functions.</li> <li>Traces external interruptions.</li> <li>Traces I/O interruptions.</li> <li>Sets the needed SVC B2 for instruction tracing.</li> <li>Ends tracing.</li> <li>Puts back user instructions altered by tracing.</li> <li>Traces program interruptions.</li> <li>Traces privileged instruction interruptions.</li> <li>Processes an SVC, Branch, or full instruction TRACE.</li> <li>Traces virtual and real CSWs.</li> </ul>
DMKTRD	DMKTRDSI DMKTRDWT	Pageable. Split from DMKTRC. Traces I/O operations (SIO, TIO, HIO, TCH). Serialization entry for I/O and CCW tracing.

Module Name	Entry Points	Attributes, Function
DMKTRK	DMKTRKIN DMKTRKFP DMKTRKVA	Resident. Handle interrupts caused by alternate tracks. Examine Command Rejects for virtual SIO. Verify alternate track address for DMKCCW.
DMKTRM	DMKTRMID	Pageable. Identifies a 2741 terminal as either a 2741P (PTTC/EBCD) or 2741C (correspondence) from the user command. It sets ADEVTYPE the RDEVBLOK to TYP2741P or TYP2741C and sets flag RDEVIDNT on if the terminal was successfully identified.
DMKTRP	DMKTRPRE DMKTRPST	Pageable. Stops CPTRAP processing due to LOGOFF. Processes the CPTRAP command line.
DMKTRQ	DMKTRQCP DMKTRQIL DMKTRQMD DMKTRQRT DMKTRQST DMKTRQTI DMKTRQ80	Resident. Processes TRQBLOKs and maintains TRQBLOK queues. Processes TRQBLOKs for the CPU timer. Processes TRQBLOKs for queue drop delay. Processes TRQBLOKs for midnight time and date changes. Resets a TOD clock timer request. Establishes a TOD clock timer request. Processes the end of the performance interval. Processes TRQBLOKs for real timers.
DMKTRR	DMKTRRST	Residency not applicable. Reduces the reader file created by the CPTRAP facility.
DMKTRT	DMKTRTCM	Pageable. Common processing routine to enter data into a CPTRAP file.
DMKTRU	DMKTRUAC DMKTRUAC	Pageable. Activates the CPTRAP facility. Stops an active CPTRAP facility.
DMKTRX	DMKTRXCP DMKTRXEX DMKTRXTT DMKTRXVT	Pageable. Interface to CPTRAP facility, records data generated by CP code. Exit routine for the interface entry points. Interface to CPTRAP facility, records internal CP trace table entries. Interface to CPTRAP facility, records data generated by virtual machine code.
DMKTTX	DMKTTXIN DMKTTXSK	Pageable. Determines I/O operations to be performed on a certain device, constructs CCWs and data streams. See also DMKTTY. Handles input contasks (reads). Entry for translation routines.
DMKTTY	DMKTTYOP	Pageable. Determines I/O operations to be performed on a certain device, constructs CCWs and data streams. See also DMKTTX. Handles output contasks (writes) and outbound reads.
DMKTTZ [,]	DMKTTZLF	Resident, nonexecutable. Contains CCWs and data pointed to by certain CCWs for TTY terminals.

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Module Name	Entry Points	Attributes, Function
DMKUCB	DMKUCB	Pageable. Contains the UCB buffer load images used by the LOAD command to load the universal character set buffer in the 3811 control unit. This module contains no executable code.
DMKUCC	DMKUCCLD	Pageable. Contains the UCB buffer load images used by the LOAD command to load the universal character set buffer in the 3203 printer control unit. This module does not contain executable code.
DMKUCS		Pageable. Contains the UCS buffer load images that the LOAD command uses to load the universal character set buffer in the 2821 control unit. This module does not contain executable code.
DMKUDR	DMKUDRBV	Pageable. Allows the DMKDIRCT or DMKCPINT programs to build a list of virtual page buffers, one for each UDIRBLOK
	DMKUDRDS	page on disk. Allows the DMKDIRCT program to swap the active user directory to the newly created user directory. Puts specified UDEVBLOK into the caller's buffer.
	DMKUDRFU	Finds a given user ID in the user directory and moves the user's directory entry into the caller's buffer.
	DMKUDRMD DMKUDRRD DMKUDRRV	Reads the account block. Reads the next user directory into the caller's buffer. Releases a virtual page used by the directory program as a buffer.
	DMKUDRXI	Reads the UIPLBLOK into the caller's buffer.
DMKUDU	DMKUDUMN	Pageable. Updates in-place the CP directory on the object DASD page and updates in-place the virtual system page (if used) on the paging device. Entered from DMKHVD when a class B virtual machine issues a DIAGNOSE code '84' instruction.
DMKUNT		Resident. Untranslates CCWs and CSWs.
	DMKUNTFR	Releases pages and free storage used for the CCW chain. Also processes the CP assist instruction, UNTFR (E605).
	DMKUNTIS	Finds the RCWTASKS that have been patched to handle OS ISAM self-modifying sequences and put them back the way DMKCCW had them to allow DMKUNTRN and DMKUNTFR to operate correctly.
	DMKUNTRN	Translates a real CSW into a virtual CSW. Also processes the CP assist instruction, UNTRN (E610).
	DMKUNTRS	Relocates sense byte information. For a 3330, 3340, 3350, 3375, or 2305, computes virtual cylinder member in byte 5 and 6 of the sense byte from data by unrelocating the real cylinder number given by the hardware.
	DMKUNTFB	Relocates sense-byte information for FBA devices. The virtual physical address from sense bytes 3-6 is computed from the real physical address given by the hardware. For formats 0 4, sense bytes 18-21 and for format 1 message A, bytes 8-10 are also computed from the real physical address given by the hardware.

Module Name	Entry Points	Attributes, Function
DMKURS	DMKURSTA	Pageable. Displays status messages for real unit record devices.
DMKUSO	DMKUSODS	Pageable. Processes user termination. Processes the DISCONN (disconnect) command.
	DMKUSOFF DMKUSOFL	Logs off a user. (Calls DMKUSQ.) Processes the FORCE command.
DMKUSP	DMKUSPFM	Pageable. Maintains free storage subpools and free storage chain.
DMKUSQ	DMKUSQFF	Pageable. Continues LOGOFF processing. (Called by DMKUSO.)
DMKVAT	DMKVATAB	Resident. Storage management for EC mode virtual machine. Allocates, initializes and maintains shadow, segment, and
	DMKVATBC DMKVATEX	page tables for virtual machines that can relocate. Returns active shadow tables to free storage. Services page or segment exceptions for virtual EC machines.
	DMKVATMD DMKVATPF	Allocates and initializes shadow tables. Handles pseudo page fault interruption from a VS1 virtual machine.
	DMKVATPX	Processes paging exceptions for a virtual machine that performs paging.
	DMKVATSI	Selectively invalidate shadow page table entries.
	DMKVATSX DMKVATZP DMKVATZS	Processes segment exception for a virtual machine that performs paging. Processes the CP assist instruction, ZAPPAGE (E60B). Processes the CP assist instruction, ZAPSEGS (E60A).
DMKVAU	DMKVAULA DMKVAURN	Resident. Virtual - virtual to virtual address translation. Virtual (shadow) — virtual-to-real address translation.
DMKVCA	DMKVCAST	Pageable. Simulates I/O for a virtual channel-to-channel device (channel-to-channel adapter or 3088). Simulates the channel and device operations of the channel-to-channel device connected between two virtual machines under VM/SP HPO.
DMKVBM	DMKVBMIN	Pageable. Initializes the virtual buffer space. This involves creating an address apace, putting the new pseudo VMBLOK on the buffer manager chain, and building the segment, page, and
	DMKVBMSC	allocation tables needed to manage the address space. Scans the buffer manager chain for the requested pseudo VMBLOK.
	DMKVBMVG	Gets a page of virtual memory from the specified address space; marks the page and cylinder where this buffer was allocated.
	DMKVBMVM DMKVBMVR	Builds a pseudo VMBLOK for system paging. Returns a page of virtual memory to the specified address space; deallocates the page and cylinder where the buffer resided.

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Module Name	Entry Points	Attributes, Function
DMKVCB	DMKVCBRD	Pageable. Simulates I/O for a virtual channel-to-channel device. Selectively resets a virtual channel-to-channel device without decoupling the CTC device from the Y-side
	DMKVCBRS	adapter. Does a final reset for a virtual channel-to-channel device and and disconnects the device from its coupled twin on
	DMKVCBSH	the Y-side virtual machine. Simulates the execution of a HALT I/O or HALT DEVICE instruction for a virtual machine channel-to-channel device.
	DMKVCBTS	Simulates the TEST I/O instruction for a virtual channel-to-channel device that has no interruptions pending.
DMKVCH	DMKVCHDC	Pageable. Processes the ATTACH and DETACH real devices and channels) command.
DMKVCN	DMKVCNEX	Resident. Simulates all SIOs to a virtual console. If a buffer larger than 4K bytes is required, this module:
		• Gets the number of pages necessary for the noncontiguous buffer from the page manager, and locks the pages in storage.
		• Gets and builds an indirect address list from free storage, and uses the indirect address list to address the noncontiguous buffer.
		• Returns the pages of the buffer via the page manager, and returns the indirect address list space to free storage after the operation is complete.
DMKVCP	DMKVCPIL DMKVCPRE	Resident. Process the function specified in the control area of an IUCV SEND request. Process the reply to the write request.
	DMKVCPSR	Process the asynchronous passback from the send.
DMKVCQ	DMKVCQRE DMKVCQSR DMKVCQAT	Resident. Processes the reply to write request. Processes the asynchronous passback from a send. TTY ATTENTION (BREAK key) processing.
DMKVCR	DMKVCRNR DMKVCRMT	Resident. Sends any pending batch writes to the VSM. Processes the reply to the read. At completion of full-screen read, when an indirect address list is being used, data is moved from WEBLOK to storage using the
	DMKVCRRD DMKVCRWT	indirect address list. Request the VTAM Communications Network Application (VCNA) to perform a read operation. Request the VCNA to perform a write operation.
DMKVCS	DMKVCSWT	Resident. Requests VCNA/VSCS to perform a write operation.

Module Name	Entry Points	Attributes, Function
DMKVCT	DMKVCTCH DMKVCTCN	Process color attribute changes for SNA logical units. Initiates the communication path for the VTAM Service Machine and VTAM Logical Units and sets up the initial
		environment.
	DMKVCTCU	Process the accounting data.
	DMKVCTDA	Disable VSM access to CP.
	DMKVCTEN	Enables VSM access to CP.
	DMKVCTER DMKVCTLO	Process 'TERM' command change for LUs. Release SNA CCS control blocks and process accounting data.
	DMKVCTQS	Suspend activity for the user's virtual machine.
	DMKVCTRM	Terminal characteristics.
	DMKVCTSV	Break the IUCV communication path.
	DMKVCTTM	Send a request to VCNA to redisplay the input line.
DMKVCU		Pageable.
	DMKVCUIL	Processes asynchronous requests for service from VM/VTAM terminals.
DMKVCV	DMKVCVCE DMKVCVEB	Write trace entry for the CCS transaction.
	DWKVCVEB	Build WEBLOK. When obtaining WEBLOK for a full-screen write, an indirect address list is used to move
		data to WEBLOK.
	DMKVCVER	Set redisplay timer.
	DMKVCVIN	Release 'read' control blocks.
	DMKVCVIX DMKVCVKS	Build IXBLOK and do IUCV SEND to VCNA. Locates control blocks.
	DMKVCVKS	Build and chain a WEBLOK.
	DMKVCVLD	Issue an IUCV reply for a two-way send.
	DMKVCVND	Issue IUCV send for unsent writes/reads.
	DMKVCVUT	Release 'write' control blocks.
DMKVCW		Resident.
	DMKVCWCN	Initiates the IUCV communication path for the VSA and VTAM logical unit and sets up the initial environment.
	DMKVCWQS	Suspends activity for the virtual machine.
	DMKVCWRM DMKVCWSV	Resumes the IUCV communication path with the VSA. Breaks the IUCV communication path with the VSA.
DMKVCX	DMKVCXD2	Process abend due to invalid control block chain.
	DMKVCXFU	Error handler for IUCV transactions.
	DMKVCXGF	Begin logoff of user. Build WEBLOK and move in the system logo
	DMKVCXGO DMKVCXIO	Build WEBLOK and move in the system logo. Process unrecoverable I/O error.
	DMKVCXIO	Send the color attribute for the LU to the VCNA.
	DMKVCXOX	Do one-way IUCV SEND of logo to VCNA.
	DMKVCXSA	Process VCNA logic error.
DMKVDA		Pageable.
	DMKVDAAA	Processes the ATTACH command.
	DMKVDAAC	Processes the ATTACH CHANNEL command.
DMKVDB	DMKVDBMD	Pageable. Handles multiple-address messages for DMKVDA.
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DMKVDC		Pageable.
	DMKVDCPS	Acquires virtual blocks for devices that are likely to be
		attached by the ATTACH command.
	DMKVDCSC	Scans the ATTACH and DETACH command lines and checks syntax.
DMKVDD		Pageable.
	DMKVDDDE	Handles the DETACH command.

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Module Name	Entry Points	Attributes, Function				
DMKVDE	DMKVDEDC	Pageable. Verifies the existence of a device specified on an ATTACH command.				
DMKVDF	DMKVDFRE	Pageable. Processes the error messages detected in the DETACH command.				
DMKVDG	DMKVDGAL	Pageable. Creates and chains ALOCBLOKs from the SYSPLIST and RDEVBLOK chain anchors.				
DMKVDH	DMKVDHBB	Pageable. Builds and initializes an ALOCBLOK for a specified type				
	DMKVDHFR	of system DASD area. Builds and initializes a RECBLOK for SYSPP, SYSPG, and SYSSW type ALOCBLOKs.				
	DMKVDHPG	Checks for spool files in the passed ALOCBLOK and, if				
	DMKVDHOR	they exist, adjusts the page RECBLOKs. Orders ALOCBLOK chains according to SYSPAG macros or selects SYSTEM ordering, as appropriate.				
DMKVDR	DMKVDREL	Pageable. Releases a virtual or real device from a virtual user.				
DMKVDS	DMKVDSAT DMKVDSDF DMKVDSLK	Pageable. Attaches a virtual device to a user. Defines a new virtual device for user. Links a virtual DASD device to a user.				
DMKVDT	DMKVDTPG	Pageable. Attaches a 3880 Storage Control Unit Model 11 or Model 21.				
DMKVER	DMKVERD DMKVERO	Pageable. Processes error records from virtual machine via SVC 76. Processes SVC 76 from DOS or CMS/DOS. Processes SVC 76 from OS, VS/1, VS/2, VM/370, or VM/SP.				
DMKVFC	DMKVFCQV DMKVFCVV	Pageable. Process the QUERY VECTOR command. Process the VARY ON/OFF VECTOR command.				
DMKVFD	DMKVFDDP	Pageable. Display vector registers.				
DMKVFE	DMKVFEST	Pageable. Store vector registers.				
DMKVFI	DMKVFIIN	Pageable. (Note: This module is not supplied with source code.) Perform CP initialization of the Vector Facility.				
DMKVFR	DMKVFRIN DMKVFRRE DMKVFRSV	Resident. Process Program Interrupt '19'. Restore a virtual machine's Vector Facility status. Save a virtual machine's Vector Facility status.				
DMKVFS	DMKVFSOS	Pageable. Obtain a vector register save area and (if necessary) a VECBLOK.				
	DMKVFSRS	Release a vector register save area.				

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Module Name	Entry Points	Attributes, Function					
DMKVIO	DMKVIOC1 DMKVIOCL DMKVIOIN DMKVIOMK	Resident. Records and translates the interrupts and status associated with virtual I/O operations. Reflects condition code 1 CSW status. Clears the VDEVIO queue. Translate a virtual I/O interruption. Address of a table of interruption masks, indexable by device address.					
DMKVMA	DMKVMAER	Resident. Issues message DMKVMA456W when a shared segment user attempts to store into the segment. The virtual machine is placed in console function mode.					
	DMKVMASH DMKVMASW	Checks all protected shared pages associated with shared named systems and determines if they have been changed. If they were changed, the page is returned to CP free storage and the condition code is made nonzero. Switches the user's segment table entries from one					
		protected shared page table to the other.					
DMKVMC	DMKVMCFC	Pageable. Main entry for all VMCF subfunctions. Called by DMKHVC when a DIAGNOSE X'0068' instruction is executed. Builds a VMCBLOK with information from					
	DMKVMCEX	user-supplied parameter list, validates the subfunction code, and passes control to appropriate VMCF subroutin Called by DMKDSP to reflect the VMCF external interrupt message header and optional SENDX data to a virtual machine. Copies the message header from the VMCBLOK to the user's external interrupt buffer. If					
	DMKVMCVA	interrupt is for a SENDX request, move SENDX data to the optional area in the external interrupt buffer. Branched to from the DMKVMCFC entry point or called by DMKCFP during a system reset. Releases the master VMCBLOK and any final response VMCBLOKs (VMCRESP bit). Returns other VMCBLOKs to the origina SOURCE users with the notification that this user is not available.					
DMKVMD	DMKVMDEP	Pageable. Dumps guest virtual machine to spool blocks in binary form. The output is read by DIAGNOSE X'14'. VM/IPCS Extension or a user-written routine may be used to process the guest virtual machine dump.					
DMKVME	DMKVMEDP	Pageable. Provides the dumping service for the VMDUMP command and DIAGNOSE Code X'94'.					
DMKVMG	DMKVMGCN	Pageable. Handles pending CONNECTs from a virtual machine to the Signal system service.					
	DMKVMGIL	Handles pending messages representing signals for the					
	DMKVMGQS	Signal system service. Handles QUIESCEs from a virtual machine on a path to the Signal system service.					
	DMKVMGRM	Handles RESUMES from a virtual machine on a path to					
	DMKVMGSV	the Signal system service. Handles pending SEVERs from a virtual machine to the Signal system service.					
DMKVMI		Pageable. Loaded into the user's virtual storage when invoked. Performs an IPL of a virtual machine.					
	DMKVMIPL	Simulates a user's IPL sequence.					

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Module Name	Entry Points	Attributes, Function
DMKVRR		Resident.
Dinitivitit	DMKVRRDD	For the $V = R$ virtual machine, restores information to the
		RDEVBLOK associated with the dedicated device.
	DMKVRRIS	Saves data from interruptions that occur while a path to
	DMKVRRC	the dump device is being obtained. Restores I/O interruption data to the appropriate IOBLOK
	Dimit vitilitie	for the $V = R$ virtual machine. The IOBLOK is then placed
		on the IOB chain.
	DMKVRRRS	Restores the VMBLOK for the $V=R$ virtual machine and ensures that all dedicated devices are attached. SPMODE
		is restored if it was active.
DMKVRS	DMKVRSSV	Saves the $V = R$ virtual machine's VMBLOK, ECBLOK,
	DIMINUMOOV	CPU timer, and clock comparator. A list is built to
		include all dedicated devices.
DMKVSC		Resident.
21111 00	DMKVSCAN	Verifies CCW translation bypass for SIO or SIOF
	DATABACCO	instruction.
	DMKVSCSC	Scans a $V=R$ channel program for exceptional conditions, such as sense commands, no-ops, I/O to and from page 0,
		etc., without actually translating the program.
	DMKVSCVR	Scan for conditions indicating NOTRANS is a valid
		operation.
DMKVSD		Resident.
	DMKVSDAD	Adds a reader file to the user's reader chain.
	DMKVSDDL DMKVSDFH	Deletes a reader file from a user's reader chain. Finds the Reader Hash Table entry for the given user.
	DMIRVODITI	
DMKVSE	DMEAGEED	Resident.
	DMKVSEER DMKVSETR	Recovers from SYSSPOOL paging error. Gets a control block from SYSSPOOL's virtual storage.
DMKVSF	DMKVSFID	Resident.
	DMKVSFID	Finds the specified spool file's SFBLOK. Finds the next spool file in the system.
	DMKVSFNU	Finds the user's next SFBLOK.
DMKVSG		Resident.
Difficience	DMKVSGAI	Assigns a user-unique spoolid.
	DMKVSGRI	Returns a user-unique spoolid.
	DMKVSGUM	Updates a user's SPUMAP.
DMKVSI		Resident.
		Simulates the operation of privileged I/O instructions
	DMKVSIEX	issued by virtual machines. Simulates an SIO, TIO, HIO, TCH, or CLCH. (HIO and
		CLCH are processed by DMKVSJ.)
	DMKVSIFT	Reentry from DMKVSJ to release the IOBLOK.
DMKVSJ		Resident.
	DMKVSJEX	Simulates the operation of the privileged I/O instructions
		HIO and CLCH issued by virtual machines.

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Module Name	Entry Points	Attributes, Function
DMKVSP	DMKVSPDC DMKVSPFC DMKVSPEX DMKVSPPE	Resident. Simulates all user SIOs to a virtual unit record device (real reader, punch, printer, or pseudo timer) that is spooled rather than dedicated. Clears a device. Clears a file. Simulates an SIO to a spooled unit record device. Printer end-of-file processing.
	DMKVSPST DMKVSPTO DMKVSPUS DMKVSPWA	Stacks a CPEXBLOK. Checks whether the virtual reader is empty. Unstacks a CPEXBLOK. Nonexecutable index work area for the 3211.
DMKVSQ	DMKVSQPD	Resident. Locate next available slot in a printer/punch DASD buffer and move CCW and data into that slot.
DMKVSR	DMKVSRGC DMKVSRMD	Resident. Finds and validates the next non-TIC CCW in the user's channel program. Locates user's data area and moves data between the
DMKVST	DMKVSTCP DMKVSTOP DMKVSTPT	user's area and the in-storage work buffer. Resident. Handles CP requests to print on the user's virtual printer. Writes a print line to the console. Opens output spool file. Puts a CP-generated line on the user's spooled printer.
DMKVSU	DMKVSUCO DMKVSUCR	Pageable. Stops processing the file currently in the spooled printer or punch and clears all pending status from the spooled printer or punch. Stops processing the file currently in the spooled card reader and clears all pending status from the spooled card reader.
DMKVSV	DMKVSVLD	Resident. Verifies the validity of 3800 load CCWs and sets up the in-storage work buffer.
DMKVSW	DMKVSWTO DMKVSWOT DMKVSWOR DMKVSWOC DMKVSWFC DMKVSWDC	Resident. TIO to spooled reader. Reader file half open-finish open. Opens reader file. Searches the reader file chain. Starts clearing old reader files. Clears device blocks.
DMKVSX	DMKVSXCL DMKVSXRD DMKVSXSE DMKVSXSI DMKVSXSR DMKVSXTR	Resident. Simulates all user SIOs to a virtual card reader or pseudo timer that is spooled rather than dedicated. Simulates sense CCWs for spooled unit record devices and sense ID CCWs for spooled printers. Simulates a close of a spooled unit record reader. Locates the next data record in a reader file. Simulates a sense to a spooled unit record. Simulates a sense ID to a spooled printer. Simulates an SIO to a spooled unit record reader. Simulates an SIO to timer.

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Module	Entry	
Name	Points	Attributes, Function
DMKWAI		Resident. Scans dispatch queues for dispatchable work that may have been readied by another processor.
	DMKWAIST	Scans the dispatch list of the other processor. When it finds a dispatchable unit of work, it moves it to the dispatch list of this processor and exits to the dispatcher.
	DMKWAITA	Active wait processing.
DMKWRM	DMKWRMST	Pageable. Warm start processing. Retrieves system log messages and accounting cards from the warm start cylinder of the IPL
	DMKWRMSY	pack. Rebuilds SYSSPOOL's virtual storage from the WARM cylinders.
DMKWRN	DMKWRNWM	Pageable. Initializes the checkpoint area cylinders and checkpoints any SHQBLOKs or open SFBLOKs.
	DMKWRNSB	Sets bit in system spool file bit map.
DMKXAB	DMKXABDG	Pageable. Processes the DIAGNOSE X'B4' and X'B8' instructions.
DMKXAD	DMKXADVS	Pageable. Copies the XAB (External Attribute Buffer) associated with a virtual printer to a spool file for that device.
DMKXST	DMKXSTOR	Pageable. Initialize Paging Storage ALOCBLOKs and RECBLOKs, if the SYSXSTOR macro was used in DMKSYS.
DMKZTD	DMKZTDDF	Pageable. Clears the first CKD cylinder or first FB-512 block of a TDSK minidisk if SYSCLR=NO was specified on the SYSRES macro.
	DMKZTDST	Cleans CKD and FBA-512 T-Disk Space.

DMKACO	ACNTIOCT ACNTSTOP ACORETN ACTIDCUR AEXTSP AQCNWT CHECKVF CPEXREGS DEVCARD DMKERMSG DMKPTRUL	ACNTNCYLL ACNTTIME ACTIDISP AFREE ARSPAC CHGSFB CPEXRO DFRET DMKFREE DMKFREE DMKFREE DMKFREE IOBLOK NICMDL OPNSFB RDEVPS R1 R5 SAVEVAC SFBOFORM SFBUSER SNAUSER TEMPSAVE	ACNTUSER ACTIBF1R ACTIFLAG AFREEP ARSPPU CLASDASD CPEXR11 DISCREC DMKFREEP DMKQCORD DMKSYSCK FTROPRDR DMKSYSCK FTROPRDR IOBSIZE NICOPRDR R10 R6 SAVEWRK1 SFBACNT SFBACNT SFBACNT SFBACNT SFBACNT SFBACNT	ACNTNUM ACCNTVTIM ACTIBF1V ACTIBF1V ACTIID AFRET ASYSLC CLASFBA CPEXR12 DISPMSG DMKFRET DMKFRET DMKFRET DMKFRET DMKFRET NICSIZE PREFIXB RDEVSTA3 R11 R7 SAVEWRK2 SFBCLAS SFBRECNO SIGQUI SPLINK	ACNTPGRD ACNTVTT ACTIBF2R ACTILIMT ALARM ASYSVM CLASGRAF CPEXSIZE DMKCKSPL DMKLOCRD DMKSCHDL DMKVFRSV F1 IOBVADD NICTMAT PRIORITY RDEVTMAT R12 R8 SAVEWRK3 SFBCOPY SFBRECSZ SIGRES SPNXTPAG TIMEDISP	ACNTRFLG ACNTVVT ACTIBF2V ACTIPCH AP BLANKLEN CLASTERM C1 DMKCKTUU DMKLOCRQ DMKSCNAU DMKVI0IN F255 IPUADDRX NICTYPE PSA RDEVTYPC R13 R9 SAVEWRK4 SFBDATE SFBSIZE SIGXC	ACNTRMID ACOACCL ACTIBLOK ACTIBLOK ACTISFB APSTAT1 BLANKS CLASURI DATE DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB DMKCVTAB T SCOCGRAF SCOCG SF SCOCGRAF SF SCOCGRAF SF SCOCGRAF SF SCOCGRAF SF SF SF SF SF SF SF SF SF SF SF SF SF	ACONTRMTE ACOACCM ACTICL ACTISFCK APTRAN BRING CONTROL DE DMKCVTBH DMKPGUVG DMKSCNVD DOLOCAL INHIBIT LOCK NOCARRY1 RDEVBLOK RDRCHN R15 SAVEREGS SAVEWRK7 SFBFLAG3 SFBSYSID SKIPADD	ACNTSIZE ACOCHK ACTICLAS ADDSFB APTRLK CALLCVT CPEXADD DEDTEST DMKCVTDT DMKCYTDT DMKCYTDT DMKSTKCP DORDEV IOBCSW MP NORET RDEVFTR REMOTE R2 SAVER10 SETTDK SFBFNAME SFBFIME SNACARD STCODE	ACNTSNA ACOEXIT ACTICNT ADSPCH APUOPER CC CPEXBLOK DEFER DMKDSPCH DMKDSPCH DMKDSPCH DMKSTKIO DOTERMID IOBIRA NICBLOK NOTMID RDEVMDL RETRY R3 SAVER2 SETUP
DMKACR	DMKACSRF DMKSCNMU IOBCP MCHAREA OWNDRDEV RCUCHA RDEVAOF RDEVLNCP	ADSPCH APUOPER CCS4HARD DMKCVTBH DMKSCNRU IOBFLAG MCHFLAG1 POFFLINE RCUCHD RDEVBLOK RDEVBLOK RDEVBLOK RDEVPTH7 R0 R4 TYP3705	DMKDSPCH DMKSYSOC IOBFPNT MCH1GERR PREFIXB RCUDISA RDEVBOF RDEVNICL		DMKFRET ERRMSG IOBLOK MFAMASK RCHADD RCUPRIME RDEVDISA	EXTMASK I OBRADD MP RCHBLOK RCUSTAT RDEVD I SB	ARIODV CPCREGO DMKLOKIO FFS IOBUSER NICSIZE RCHCUTBL RCUSUB	RCUTYPE RDEVFLAG	BCTWAIT CO DMKMCHST F1 LOCKSAV OPERATOR RCHSTAT RDEVADD RDEVFOFF RDEVPTH4 RDEVTYPC R15	APSTAT1 BLANKS DMKACSCV DMKQCNWT F2 LPUADDR OWNDLIST RCUBLOK RDEVALT RDEVLCEP RDEVPTH5 RDEVTYPE R2 TIMEDISP
DMKACS	ACRLOCK ARIOCT CCS4S0FT CPEXSIZE DMKFREE DMKSTKCP IOBCAW IOBLOK	AFREE ARIOCU CDC	CPSTAT4 DMKFRET	AFRET ASYSVM CLASTERM CPSTAT5 DMKIOSMQ FFS IOBCSW IOBOERR IOBTIO	ALLCHANS BCTWAIT CODE CSW DMKIOTRC F1 IOBFATAL IOBFATAL IOBPATHF IOBUNSL	ALOKSP BUSY CONTINUE CUE DMKLOKIO F2 IOBFLAG IOBGDTO IOBUSER	AP CAW CPEXADD DMKACRIO DMKSCNMU F4 IOBFPNT IOBRADD IOERBLOK	DMKCCHCF DMKSCNPH F8 IOBHVC IOBRETRY	APUOPER CCS4DGRD CPEXREGS DMKCNSIN DMKSCNRA F9 IOBIOER IOBSIZE IOERCCUA	ARIOCH CCS4HARD CPEXR11 DMKCVTAB DMKSCNRD IOBBPNT IOBLINK IOBSPEC IOERCHID

**CP** Module-to-Label Cross Reference

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HODOLL					10000220)					
	LPUADDR RCHBUSY RCUDVTBL RDEVASGN RDEVMID	RCUFIOB RDFVBLOK	MP RCHDISA RCUPRIME RDEVBZCH RDEVPTHS	PREFIXA RCHFIOB RCUSCED RDEVCSW RDEVQCNT	PREFIXB RCHSTAT RCUSTAT RDFVCTI	PROCIPL RCUADD RCUSUB RDEVCUA RDEVSTAT R1 R6	PSA RCUBLOK RCUTYPE RDEVCUB RDEVSTA2 R10 R7	RDEVADD	RCHADD RCUCHA RDEVAIOB RDEVFIOB RDEVSTA5 R13 R9	RDEVLIOB RDEVTFLG R14 SIGPR
DMKALG	R1 R7	AFREE CPEXBLOK DMKSCHDL R10 R8 VCONCTL	DMKSCNVD R11 R9	DMKSTKCP R12 SAVEAREA	DMKSYSJR R13 SAVEREGS	R14 SAVER11	LOCK	BUFFER DMKFREE MP R2 SAVER9	BUFNXT DMKFREEP PSA R3 SAVEWRK1	RO R4
DMKALO	RDEVPRDV R12	MP RDEVBLOK	CLASFBA DMKMNTFB NORET RDEVCODE RDEVSER R14	DMKSCNRD OWNDLIST RDEVCUP RDEVSTA5 R15	DMKSYSOW OWNDRDEV RDEVFLAG	DMKCPIMS DMKSYSVL PSA RDEVFTR	DMKCPINU DMKTODTB RDCBLOK RDEVMDL RDEVTYPE R4	RDCPAGAP RDEVMD02	DMKCPISH FTR35MB	F1 RDCPAGMA
DMKAP I	DMKDGDA6 DMKDMPSA DMKFRELP DMKPXB DMKUNTRN DPOKTLB FF LOCK MICSKYMD PREFIXB PSAEXT R0 R4 SHRLKCNT	ARIOCT CPDASAAV CPSTAT3 CSSFEAT DMKCCWB5 DMKCCW0 DMKDGDA8 DMKDSP0 DMKFRET DMKQCNWT DMKVATZP DPSEGPRT FFS LPUADDR MP PRIMEHDR PSAEXTX R1 R5	CO DMKCCWB6 DMKCCW1 DMKDGDA9 DMKDSP1 DMKIOGA9 DMKVATZS DPSTAT F1 LPUADDRX MPUOPER PRIMEHI PSENDCLR R10 R6 SVCNPSW	CPEXSIZE CPSUPER C1 DMKCCWB7 DMKCPEML DMKDGDDK DMKDGDDK DMKPRGIN DMKSCNMI DMKSCNMI DMKVAUZP DSPRQ F4096 L8 MSSFMASK PRIMELO PXA R11 R8 TEMPRO TRACPROC	CPTMASK C14 DMKCCWB8 DMKDGDA0 DMKDGFA0 DMKDSP3 DMKPRVCA DMKSCNVU DMKVFRIP EMSMASK IDLEWAIT MCHEK PAGEWAIT PRNPSW RQLOCK R12 R9 TEMPR2	CPMICAVL CPUID C2 DMKCCWGN DMKDGDA1 DMKDBPAA DMKDSP4 DMKPRWSK DMKSTPX DMKYFRNP EXNPSW IOMASK MFAMASK PGREAD PROBTIME RSRTNPSW R13 SAVEAREA TEMPR3 TRLLOCK	CKCMASK CPMICON CPUMODEL C6 DMKCCWL1 DMKDGDA2 DMKDGDA2 DMKDSP5 DMKPSADU DMKSVCIN DMKVMASH EXOPSW IONTWAIT MICBLOK PMAAVAIL PROCIO	DMKCCWB1 DMKCCWL2 DMKDGDA3 DMKDMPMA DMKDSP6 DMKVDSP6 DMKVAITA EXTMASK IPUADDR MICDASA PMASTAT PROCIPL RUNCR1 R15 SAVER11 TEMPR5	CPSHRLK CP370EAV DMKCCWB2 DMKCCWL3 DMKDGDA4 DMKDMPPA DMKFREE DMKPTRUX DMKSYSNP	DMKCCWB3 DMKCCWL4 DMKDGDA5 DMKDMPPX DMKFREEP DMKPXA DMKUNTFR DPMICON FASTCPU LASTUSER MICISKE PREFIXA PSADSPRQ RUNUSER R3
DMKAPS	ADSPCH CPEXBLOK DMKFRET	AFREE CPEXSIZE DMKIUACP	AFREEP DMKAPTEP DMKPTRLK	DMKAPUSE	ALPRTBLK DMKAPVCL FFS	AP DMKAPXMG F3	APTRLK DMKCVTHB F6	ARSPPR DMKDSPCH LOCK	BLANK DMKFREE LPRCPXAD	CPEXADD DMKFREEP LPRFLG3

#### EXTERNAL REFERENCES (LABELS AND MODULES)

# EXTERNAL REFERENCES (LABELS AND MODULES)

						NODULLO)					
		LPRLSPLC LSPLFLG1 R10 R6 SFBINUSE	LSPLNEXT R11 R7	LPRNEXT LSPLSIZE R12 R8 SFBPNT	LPRPATH LSPPRINT R13 R9	LPRSEVER LSPSFBLK R14 SAVEAREA	LPRTBLOK L8 R15 SAVEREGS	LPRTSIZE MP R2 SEVER	LPRUSRID PSA R3 SFBBCONV	LSPIOPND RO R4 SFBFLAG	LSPLCTL R1 R5 SFBFLAG4
DMKA	νPT	ADSPCH CPEXREGS DMKIUACP F4096 LPRTBLOK PSA R3 SEVER XPAGNUM	IOERETN	DMKPTRAN IOKEEP LSPIOPND	DMKPTRLK LOCK LSPLCTL	DMKPTRUL LPRCPXAD LSPLFLG1	DMKRPAGT LPRCTIOP LSPLNEXT	APTRLK DMKDSPCH DMKSTKCP LPRFLG3 LSPPURGE R13 R9 SFBUSER	DMKXABDG LPRLSPLC LSPSFBLK	FFS LPRREADB LSPSPLNK	F0 LPRSEVER MP R2 SAVER5
DMKA	νPU	LPRSESID LSPPRINT R13 R9 SFBFLAG3	LPRCLASS LPRFORM LPRTBLOK LSPSFBLK R14 SAVEAREA	FORMNTRY LPRCNVD LPRIDLE LPR5AD LSPSPLNK R15 SAVEREGS SFBFLASH	LPRCNVRT LPRLSELD LSPCONVT MP R2 SFBBCONV SFBINUSE	LPRDEFLT LPRLSPLC LSPID PSA R3 SFBCLAS	LPRDEST1 LPRNCNVD LSPLCTL R0 R4 SFBCONV SFBLDMID	LPRFLASH LPRNEXT LSPLFLG1 R1 R5 SFBCOPY	LOCK LPRFLG1 LPRN038D LSPLNEXT R10 R6	LSPLPRTB R11 R7 SFBFILID	LPRBE38D LPRFLG2 LPRSELDA LSPLSIZE R12 R8
DMKA	PV	LPRRECBF MP	LPRTBLOK PSA R3 SEVER	LSPCONVT R0 R4 SFBBCONV	LSPIOPND R1 R5 SFBCLAS	LSPLCTL R10 R6	LSPLFLG1 R11 R7 SFBCOPY		LSPLSIZE R13 R9 SFBFILID	LOCK LSPPRINT R14 SAVEAREA SFBFLAG	R15 SAVEREGS
DMKA	∿P₩	AFREE LPRPATH PSA R5	AFRET LPRTBLOK RO R7	R1	LSPIOPND R11	DMKFREE LSPLCTL R12 SFBFILID	R13	DMKIUACP LSPLNEXT R14	LOCK LSPLSIZE R15	LPRLSPLC LSPPURGE R3	LPRNEXT MP R4
DMKA	νPX	AFREE BUFSIZE MP R3 ZEROES	AFRET DMKAPSSV PSA R4	AP DMKFREE RO R5	BLANK DMKFRET R1 R7	R11	R12	BUFFER DMKMSGMS R13 SAVEREGS	R14	R15	LOCK R2
DMKA	РҮ	ADSPCH DMKDSPCH IXSIZE R10 R6	AFREE DMKFREE LOCK R11 R7	DMKFRET	LPRNEXT R13	DMKPTRLK LPRPATH R14		F2	BUFCNT IXBLOK PSA R3 VMBLOK	BUFFER IXIRA RO R4	BUFNXT IXREGS R1 R5
DMKA	PZ	AFREE LPRNEXT R14	AFRET LPRPATH R15	ALPRTBLK LPRTBLOK R8	AP MP SAVEAREA	DMKFREE PSA SAVEREGS	DMKFRET RO	DMKIUACP R1	LOCK R11	LPRFLG3 R12	LPRIDLE R13

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DMKATS	DMKLOCKD DMKPTSPW F15 PAGACT PREFIXA R11 R7 SAVEWRK6 SHRFLAG SHRUSECT	APTRLK CORFREE CPEXSIZE	CPPTLBR DMKPGUAL DMKSCNVS F256 PAGCORE PSA R13 R9 SAVEWRK8 SHRNAME SWPAPP	F4 PAGINVAL RDEVBLOK R14	DEFER DMKPTRAN DMKSTKCP F4096 PAGRBITS RDEVCODE R15 SAVEREGS SEGFLAG SHRPAGE SWPCODE	DMKDSPCH DMKPTRAQ DMKSYSAP F8 PAGSHR	CORBPNT CORTABLE DMKERMSG DMKPTRLK DMKVMASH LASTUSER PAGSWP RDEVTYPE R3 SAVEWRK2 SEGPAGE SHRSEGNM SWPFLAG	DMKFREE DMKPTRSC FFS LOCK PAGTABLE R0 R4	CPEXADD DMKFREEP DMKPTRUL F0 LPUADDR PAGTOT R1 R5 SAVEWRK44 SEGTABLE SHRTABLE	
DMKBIO	BIRLCBYT BIRPARML BIRSKDA BIRTICAD C1	BIOPATH BIOVDEV BIREXIND BIRECIND BIRRWCNT BIRRSKDAT BIRRSKDAT BIRTRGCL DEFER DMKPTRLK IOBCSW IOERSIZE RDEVFTR R14	BIREXOFF BIRLCOFF BIRRWIDA BIRSKOP BIRWCNT DMKDSPCH DMKDTRUL IOBCYL	BIRLCRD BIRRWOP BIRSKTRK BIRWIDA	BIREXTLI BIRLCWR BIRRWRD BIRSRCYL BIRWOP DMKFRET FTRRPS IOBIOER PSA RDEVSTA6 R3	BIORPS BIRBIOBL BIREXWR BIRLOCLI BIRSECAR BIRSRDA BIRWRD	BIOSECDS BIRBLOK BIRIDAL BIRMSGID BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSECOP BIRSE	BIRCCWS BIRIDAW1 BIRPAGES BIRSECT BIRSROP BRING DMKPSAFP F2 IOBMISC2	BIOPARMA BIOSTAT1 BIRCMSRD BIRIDAW2 BIRPAGE1 BIRSIZE	BIRIDAW3 BIRPAGE2 BIRSKCYL BIRSRTRK CLASFBA
DMKBLD	DMKRIORN FFS KEEPSEGS MICSIZE NICTYPE PAGTONLY RDEVPCHG RDEVTYPE R12 R8	ASYSVM CORPGPNT DMKCVTBH DMKSCNRD F0 LASTUSER MP NICUSER PREFIXA RDEVPS RDEVUSC8 R13 R9 SAVEWRK9 SSBHEADL	DMKERMSG DMKSYSDS F1 LLTTY NEWPAGES NICWTH PREFIXB RDEVPSUP RDEVUSER R14 SAVEAREA	CORTABLE DMKFREE DMKSYSLE F15 LL2741 NEWSEGS NORET PSA RDEVSCRL RDEVSCRL RDEVVM2 R15 SAVEREGS SEGINV SWPFLAG	DMKFREEA DMKSYSLL F16 LL3066 NICBLOK OLDVMSEG PSALANG RDEVSNA RDEVSNA RDEVWTH R2	CPPTLBR DMKFREEP DMKSYSSZ F255 LOCK NICCIBM PAGBMP RDEVASTB RDEVSNRB RDEVSNRB RDEVSNRB RDEV3101 R3 SAVER10 SEGPLEN SWPRECMP	C1 DMKFRET DMKFREK F4 LOCKSAV NICGRAF PAGCORE RDEVAVM1 RDEVSTA3 R0 R4 SAVER11 SEGTABLE SWPSEGNO	DEFER DMKLOKDF DMKTRQCP F4095 LPUADDR NICLLEN PAGPGSWP RDEVBLOK RDEVTFLG R1 R5 SAVER2 SNARBLOK		AQCNWT CORIOLCK DELSEGS DMKQCNWT DMKVRR F8 MICRSEG NICTERM PAGTABLE RDEVLLEN RDEVLLEN RDEVTYPC R11 R7 SAVEWRK1 SPECIALV SYSLOCS TYP3705

ZEROES VMBLOK VMSIZE VRALOC WAIT DMKBOX ATTRPRHI ATTRSKIP ATTR457 ATTR7 10 SF BSCPCCW1 BSCPCCW2 BSCRCVD BSCREAD BSCBLOK BSCFLAG BSCFLAG1 BSCHALT DMKBSC AFREE AFRET DMKFRET BSCRSTRT BSCRVI BSCSHUT CC CCC CDC CHC DMKFREE BSCRESP DMKRGACC ENQ FTRDIAL F1 F15 F7 F8 1 FCC DMKIOEST DMKMSWR IOBMISC2 IOBRCAW IOBRCNT IOBERP IOBFATAL IOBFLAG IOBIOER IOBLOK 1 OBCAW IOBCSW IOERCCRA IOERCCRL **IOERCSW** IOBRSTRT IOBSPEC IOBUNSL IOERACT IOERBLOK IOERCAN IOBSTAT IOERINFO IOERLOC **I OERMSW** IOERNUM IOERFLG2 NOTUSED IOERIND3 IOERDATA IOERDW IOEREXT 10ERFLG3 RDEVBLOK RDEVBSC RDEVFTR PSA IOERREAD IOERSIZE LOCK PRGC PRTC R12 R13 R15 R14 R2 R11 RDEVIOER RO R1 R10 R8 R9 SAVEAREA SAVEREGS SILI R5 R7 R3 R4 R6 SKIP TPOPFSLB TPOPRDXP UĊ UE WRITE ZEROES BLANKS DMKCVTBD ARIOUC ASYSOP BLANK DMKCAC ADSPCH AFREE AFRET AQCNWT ARIOCU DMKCVTBH DMKCVTHB DMKDSPCH DMKERMSG DMKFREE DMKIOSQR DMKQCNWT DMKSCNFD DMKSCNMU DMKFRET IOBCAW 10BCC3 IOBFATAL DMKSCNRA DMKSCNRD DMKSCNRU F255 F3 F8 F9 IOBLOK IOBRADD IOBSIZE IOBSTAT IOBUSER LOCK L2 NORET IOBLINK IOBIRA RCUBLOK RCUDVTBL RCUOWNER OPERATOR PSA RANGE RCUADD RCUCACH ON OFF RDEVBLOK RDEVBPAG RDEVCUB RDEVCUP RDEVDISA RDEVPPAG RDEVSTAT RDEVSTA5 RCUSIZE RCUTYPE R2 R12 R13 R14 R15 R3 R1 R10 R11 RO SAVEAREA SAVEREGS SAVEWRK1 SAVEWRK2 SAVEWRK3 **R7 R8** R9 R5 R4 SAVEWRK5 SAVEWRK6 SAVEWRK7 SAVEWRK8 SAVEWRK9 SILI VMBLOK ZEROES SAVEWRK4 ARIOUC AQCNWT BLANK BLANKS DMKCAO AP APSTAT1 APUOPER ARIOCU AFREE AFRET DMKCVTBH DMKCVTHB DMKERMSG DMKFREE DMKFRET DMKLOKSW DMKQCNWT DMKSCNAU DMKSCNFD DMKSCNMU LOCK MP NORET OWNID DMKSCNRA DMKSCNRU DMKUDRFU F3 L7 L8 RCUCACH RCUCHA RCUDVTBL RCUOWNER RCUPRIME RCUSIZE RCUSUB RANGE RCUBLOK PSA RDEVCUB RDEVCUP **RDEVSTA5 RO** R1 R11 R12 RCUTYPE RDEVBLOK RDEVCUA R15 R2 R3 R4 R5 R6 **R7** R8 R14 R13 SAVEWRK1 SAVEWRK2 SAVEWRK3 SAVEWRK4 SAVEWRK5 SAVEWRK6 SAVEAREA SAVEREGS SAVER11 RQ SAVEWRK8 SAVEWRK9 TIMEDISP VMBLOK CCWBD4 CCWNX13 CCSSENSE CCWBD2 CCWCLEAR CCWCTLCM CCWFORC BADCCW DMKCCD BADHEDNO CC AP CCWSRCH3 CCWTC CCWUSIDA CCWINV CCWMAN2 CCWNOOP CCWNXT CCWNX11 CCWGEN DESTRTCC DEXTENT DMKCCOCH DMKCCSEN DENDCC CLASDASD CLASFBA C1 CD CHEKISAM DMKSYSPC DMKTRKVA DRHA DWHA FIRSTRCW FTR35MB DMKCCSLK DMKCCSRM DMKCCWCN DMKCCWRT F3 F4095 F8 F9 HADISAM F2 F4 F10 F15 F16 IOBSTAT LOCK LOCR INVCCW INVCCW1 IOBALTSK IOBCYL IOBLOK HADRCGEN 1 DA NEEDSEEK PRECCW LOCRCCHH LOCSCCHH LOCSRR L4 MEMO1 MEMO2 MEMO3 MP RCWCNT RCWCOMND RCWCTL RCWCCNT RCWCCW PRECTL PREFLAG PRVFLAG PSA RCWADDR RCW2311 RCWRCNT RCWREL RCWTASK RDEVBLOK RCWFLAG RCWGEN RCWHMR RCWISAM RCWPNT RDEVMDL RDEVMD02 RDEVSTA3 RO RDEVCKDX RDEVECKD RDEVFTR RDEVLOW READ R1 R14 R15 R2 R3 R4 R6 R11 R12 R13 R10 SAVEAREA SAVER10 SEEKOFF SENSE SHPSLIM SILI SKALTCYL SKCECYL **R8 R7** TYP3330 SSM SYSSERV THISRCW **TYP2305** TYP2311 TYP2314 **TYP3340** SKIP SMCOM WCKDNXTK WRTUKD WRTUPD XRIGHT16 **TYP3375** TYP3380 VIRCOMND VIRFLAG VMBLOK TYP3350 ZEROES CCSBUFCK CCSCKDON CCSHNSEN CCWCLEAR CCWCTL CCWFORC CCWGEN CCWINV DMKCCF AP CCWNOOP DMKCCSEN DMKCCSRM DMKCCWCN DMKCCWRT CCWTC CCWUSIDA CD CCWMAN2 CCWNX11 HADRCGEN IDA INVCCW1 DMKSYSPC FFS F8 F15 F16 F7 F1 MP NEEDSEEK PRVFLAG PSA RCWADDR MEMO1 MEM02 IOBCYL IOBLOK LOCK

RCWFLAG

RCWGEN

RCWHMR

RCWREL

RCW2311

RDCBLKAP RDCBLKFA

1982,

1987

RCWCCW

RCWCOMND RCWCTL

EXTER	RNAL REFE	RENCES (L	ABELS AND	MODULES)
RDCBLKMA R14		RDEVBLOK R3	RDEVRDC R4	R6

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	RDCBLKMA R14 SYSSERV	RDCBLOK R15 VIRCOMND	RDEVBLOK R3 VMBLOK	RDEVRDC R4 XR1GHT16	RO R6 ZEROES	R1 R7	R10 R8	R11 SAVEAREA	R12 SAVER10	R13 SKIP
DMKCCH	CPEXBLOK C1 DMKIOECC DMKSYSRM F0 IGTERMSQ IOBHIO IOBSTAT IOERCCUA IOERS80 MCNPSW NORET RCHBLOK RDEVBUZY R12 R8	CCHSIOB CCS4HARD CPEXREGS DEFER DMKLOKDF DUMPSAVE F1 IGVALIDB IOBHVC IOBTIO IOERCHID IOERCHID IOER2860 MODEL135 OPERATOR RCHCUTBL RDEVSTAT R13 R9 SAVEWRK5	CPEXR10 DEVCCH DMKLOKI0 ECSWBYT3 F16 INTERCCH IOBI0ER IOBUNSL IOERCLOG IOER2870 MODEL145 PMAMODE RCHDED RDEVSTA4 R14 SAVEAREA	CPEXR11 DMKACRCT DMKMCHST ECSWLOG F2 INTTIO IOBLOK IOBLOK IOBUSER IOERCPID IOOPSW MODEL155 PMASTAT RCHSTAT RCHSTAT RCHSTAT RDEVTYPC R15 SAVEREGS	CLASGRAF CPEXR13 DMKCVTBH DMKOPRWT FAILADD F255 IOBCAW IOBOERR IOELPNTR IOERCSW IPUADDR MODEL165 PREFIXA RCHSTIDC RDIDX R2	CPEXSIZE DMKDSPCH DMKPSAPO F41LCCW F4096 IOBCCH IOBCCH IOERBLOK IOERBLOK IOERBLOK IOERBLOK IOERECSW IPUADDRX MOD3031 PREFIXB RCUADD RSRTNPSW R3 SAVER4	CCHLOG45 CCPROGID COMPSYS DMKDSPRU DMKDSPRU DMKPTRAN FAILCSW F7 IOBCP IOBRCAW IOERB80 IOEREXT LOCK MOD3090 PROCI0 RCUBLOK R0 R4 SAVER6	CCRECTYP CONTROL CPSTAT5 DMKFREE DMKQCNWT FAILECSW HIOCCH IOBCSW IOBRSTRT IOERCCH	CPCCHLK CPUID DMKFREEP DMKSCNVU FFS IFCC IOBFATAL IOBSPEC IOERCCRA IOERLOGL MCHAREA MOD4381 PSA	CCSW4 CPEXADD CSW DMKFRET DMKSTKCP FXDLOG IGPRGFLG IOBFLAG IOBFLAG IOBFEC3 IOERCCRL
DMKCCO	IDA MEMO1	CHEKISAM DMKCCWRT INVCCW MEMO2 RCUPRIME RCWISAM R0 R4	CCWNOOP CLASDASD DMKSYSPC INVCCW1 MODNOP	DWHA IOBCYL MP RCUTYPE RCWTASK R10 R7 SETID TEMPR6 TYP3420	CCWNX11	CCWNX13 CLASTAPE F15 IOBLOK PRECCW RCWCCNT RDEVBLOK R12 SAVEAREA SKIP	F4 IOBRELCU PRECTL RCWCCW RDEVCMDK R13	CCWNX9 CMDRDC F4095. LOCK PREPRD RCWCNT RDEVCRDC R14 SENSE SNSSUBCT TYP3330 TYP3705	R15 SENSEA4	TYP3350
DMKCCS	DMKDSPRU DMKUNTRS I OBRESRV LOCK RCWADDR RCWREL	CPEXSIZE DMKFREE FFS IOBSENSE LOCKSAV RCWCCNT RCWTASK	CLASDASD C1 DMKFREEP FIRSTRCW IOBSIZE LPUADDR RCWCCW RCWVCAW	DEFRSENS DMKFRET FTRRSRL IOBSPEC2 MEMO2 RCWCOMND RCWVCNT	DMKLOKDF F4096 IOBSPEC3 MEM03	DMKCCOCH DMKLOKIO IDA IOBSPEC5 MP RCWFLAG RDEVADD	DMKCCWCN DMKRSFSD INVCCW IOBUNREL PRECCW RCWHEAD RDEVALT	DMKSTKCP INVCCW1 IOERBLOK PREVRCW RCWHMR RDEVBLOK	DMKSTKDE IOBCSW IOERDATA PRVFLAG RCWPNT RDEVCRDC	PSA RCWRCNT

MODULE

### EXTERNAL REFERENCES (LABELS AND MODULES)

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	R10 R6 SENSEE4 TYP2305 VIRFLAG	R11 R7 SENSEPID TYP3330 VMBLOK	TYP3340	R13 R9 SKIP TYP3350 XRIGHT16	SKIPSENS TYP3375	R15 SAVEREGS SNSSUBCT TYP3380	R2 SAVER10 SNSSUBST TYP3800	R3 SAVER12 SYSSERV TYP38003	R4 SAVER8 THISRCW UNLOAD	R5 SAVEWRK7 TYPUNSUP VIRCOMND
DMKCCT	AP CD DMKSYSPC MP RDEVSADN R15 SYSPRIV VMBLOK	AVMREAL CIRCLEC F15 PSA RDEVTYPC R6 TEMPSAVE ZEROES	R7		CCWCLEAR CPSPMODE F8 RCWCOMND R0 SAVEAREA TYP2955	CPSTAT2 HADAPOLL RCWCTL R1	CCWINV DMKCCSEN IDA RCWFLAG R11 SENSEIO VIRCOMND	LOCK RCWREL R12 SILI	CCWNX11 DMKCCWRT MEMO2 RDEVBASE R13 SKIP VIRTDISA	MEMO3 RDEVBLOK R14 SMCOM
DMKCCW	DMKCCSLK DMKFREEP DMKSCNVU FTREXTSN F4095 IOBCAW IOBSPEC2 LOCWORK PREPRD RCWCOMND RCWREL RDEVBLOK RESINCHA R3 SAVER10 SENSE STRTNEW TEMPR3	BRING CLASFBA CORFLUSH CPEXSIZE DMKCCDAS DMKCCSRM DMKFRET DMKSTKCP FWDTIC F4096 IOBCLN IOBSPEC3 LPUADDR PREVRCW RCWCTL RCWSHR RDEVCKDX R0 R4 SAVER12 SHRFLAG SVSEN THISRCW VIRCOMND	CORSHARE CPSHRLK DMKCCDSK DMKCCTCN DMKISMTR DMKSYSCS F0 F7 IOBCYL IOBSPEC4 MEM01 PROTCAW RCWFLAG RCWTASK RDEVCMDK R1 R5 SAVER2 SHRLKCNT SWPFLAG TICBLK	CORSWPNT CPSTAT2 DMKCCFBA DMKCCFDL DMKPMAWD DMKSYSRM F1 F8 IOBDIAG IOBUNREL MEMO2 PRVCOMND RCWGEN RCWVCAW RDEVECKD R10 R6 SAVER8 SHRNOPRT SWPPAG TYPSDLC VIRTDISA	CLASTAPE CORTABLE CPSTAT4 DMKCCFBD DMKCCFBD DMKCCFLC DMKPSAPO DMKSYSRV F15 GT16MEG IOBFLAG IOBFLAG IOBFLAG IOBFLAG RCWHEAD RCWVCNT RDEVFTR R11 R7 SAVER9 SHRTABLE SWPVPAGE TYPUNSUP VIRTUAL	C1 DMKCCODD DMKCCTRM DMKPTRAN DMKUNTFR F16 HADAPOLL IOBHVC IOERBLOK NEEDSEEK PSA RCWHMR RCW2311 RDEVMDL R12 R8 SAVEWRK1 SILI SYSVIRT	CLASURI CPEXBLOK DEFER DMKCCOMS DMKDEXIN DMKPTRFR DMKVMASH F2 HADISAM IOBLOK IOEREXT PAGCORE RCWADDR RCWINVL RDCBLKAP RDEVRDC R13 R9	DMKCCOTH DMKDIBSM DMKPTRLK ENDWORK F240 HADRCGEN IOERSIZE PAGSHR RCWCCNT RCWIO RDCBLOK RDEVSTA3 R14	DEVTABLE DMKCCOTP DMKDSPCH DMKPTRUL FFS F3 HADUTIC IOBMISC2 LOCK PCIF RCWCCW RCWPNT RDCLENG RDEVSTA6 R15 SAVEREGS SAVEWRK9	DEXTENT DMKCCSEN DMKSCRVD FIRSTRCW F4 IDA IOBSENSE LOCR PRECCW RCWCNT RCWCNT RCWRCNT RCWRCNT RDCSTART RDEVTYPC R2 SAVER1 SAVREG14 SPMPFX TEMPR2 TYP3851
DMKCDB	ACORETBL CORTABLE DMKERMSG DMKVATAB F3 PREFIXB R13 SAVEAREA SPMPFX	CPSTAT4 DMKFREE DMKVFDDP F4 PROCIO R14	AFRET C1 DMKFRET FFS F4095 PROCIPL R15 SAVER2 VMSIZE	APSTAT1 DEFER DMKPSAPO F0 F4096 PSA R2 SAVEWRK1 XKEYMODE	F1 F6 RANGE R3 SAVEWRK2	APUOPER DMKCVTBH DMKPTSAD F10 INVLD R0 R4 SAVEWRK3 X2048BND	AQCNWT DMKCVTDB DMKQCNWT F15 LOCK R1 R5 SAVEWRK4 X40FFS	BRING DMKCVTHB DMKSCNFD F16 MPUOPER R10 R6 SAVEWRK5 ZEROES	CORDISA DMKCVUFP DMKSYSAP F2 NORET R11 R7 SAVEWRK6	CORFLAG DMKDMPTR DMKSYSRM F24 PREFIXA R12 R8 SAVEWRK8
DMKCDM	ACORETBL CORDISA DMKCVUFP	CORFLAG	AFRET CORTABLE DMKERMSG		APTRAN C1 DMKFRET	APUOPER DEFER DMKPSAPO	AQCNWT DMKCVTBD DMKPTRAN	BRING DMKCVTBH DMKPTSAD	BUFFER DMKCVTDB DMKQCNWT	BUFNXT DMKCVTHB DMKSCNFD

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	F24 NORET R11 R7	DMKSYSRM F3 PREFIXA R12 R8 SAVEWRK8	DMKVATAB F4 PREFIXB R13 R9 SPMPFX	F4095 PROCIO R14	F4096 PROCIPL R15	F6 PSA R2	FO INVLD RANGE R3 SAVEWRK2 XPAGNUM		F10 LOCK R1 R5 SAVEWRK4 X2048BND	F2 MPUOPER R10 R6 SAVEWRK5 X40FFS
DMKCDS	CPSTAT2 DMKPAGIO DMKSYSRM F1 F8 PAGINVAL R11 R7	BLANKS CPEXFPNT C1 DMKPGTPG DMKTRCIT F15 LOCK PREFIXA R12 R8 SAVEWRK5	DMKTRCPB F16 LOCKSAV PREFIXB R13 R9	DMKATSCF DMKPSAP0	DMKPSASC DMKVATBC F4 MICBLOK PROCIPL R15	CPEXR15 DMKCVTDB	DMKVFEST F4096 MP RUNUSER R3 SAVER2	CPEXR7	CPEXSIZE DMKFREEP DMKSCNFD EXTMODE F6 NORET R1 R5 SAVEWRK2	CPSPMODE DMKFRET DMKSYSAP F0 F7 PAGCORE R10 R6
DMKCFC	DMKCFHSV DMKCMDDG DMKCMDQS DMKCPBRW DMKCPYUL DMKCSPSP DMKCSVPS DMKERMSG DMKERMSG	DMKCDBDC DMKCFJBE DMKCMDIG DMKCMDQV DMKCPBRY DMKCQSQC DMKCSQCL DMKCSXTG	CTFLAST DMKCDBDI DMKCMDIN DMKCMDSA DMKCMDSA DMKCSBLD DMKCSQFR DMKCSXTS DMKFRET DMKKSQFR DMKKSQFR DMKCSXTS	DMKCFJSL DMKCMDNO DMKCMDSC DMKCPSH DMKCSBVL DMKCSQHL DMKCSQHL DMKKVTDB DMKHVCDE DMKMSGSM DMKSPMEP F4 RDEVBLOK R11 R7	AQCNWT CPINITD CTNAME DMKCDMDU DMKCFTRM DMKCFTRM DMKCPSSH DMKCSFBS DMKCSFBS DMKCSFBS DMKCSTAG DMKKVCDG DMKKVCDG DMKKSCTHB RDEVFLAG R12 R8	DMKCFWEP DMKCMDQA DMKCMDSO DMKCPTNF DMKCSUCG DMKDEFDG DMKHVCDU DMKPEINT DMKSWMUS F9 RDEVIDNT R13 R9	R14	DMKCMDAT DMKCMDQP DMKCPBEX DMKCPVDS DMKCSFSP DMKCSVOG DMKDIAL DMKLOGON DMKPTRUL DMKTRPST MESSAGE RDEVTYPC R15 SAVEREGS	DMKCMDCA DMKCMDQQ DMKCPBNR DMKCPVEN DMKCSODR DMKCSVOS DMKCSVOS DMKMCCCL DMKQCNWT DMKVMDEP MP RDEVTYPE R2 SAVER1	DMKCMDDE DMKCMDQR DMKCPBRS DMKCPYLK DMKCSOST DMKCSVPG DMKCSVPG DMKERMCP DMKMSGEC DMKQVMEP DUMP NORET R3 SAVER2
DMKCFD	PSA R3	DMKSCNFD R0 R4	APTRAN DMKCVTBH DMKSCNRU R1 R5 SAVEWRK5	AQCNWT DMKCVTHB DMKSCNVU R10 R6 SAVEWRK7	R11 R7	BLANKS DMKFREE F1 R12 R8 VMSIZE	BRING DMKFRET F3 R13 SAVEAREA ZEROES	F4 R14	CPSTAT2 DMKPTRAN F6 R15 SAVEWRK1	C1 DMKQCNWT NORET R2 SAVEWRK2
DMKCFF	AFREE CPSEGPRT DMKPGSPR F0		AP C1 DMKPTRAN F15	APSTAT1 DEFER DMKQCNWT F16			AQCNWT DMKERMSG DMKVMAS1 F256	ASYSVM DMKFREE DMKVMAS2 F3	AVMREAL DMKFRET EXTMODE F4	BRING DMKPGSPP FFS F4096

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	SAVTIME	SAVEWRK7 SEGINV SHRSEGNM	PSA R2 SAVEAREA	PAGBMP PSWCC2 R3 SAVEREGS SAVEWRK9 SEGPROT	SAVFPRES SHRBPNT SHRTSIZE SWPSHR	MICBLOK PAGSHR R1 R5 SAVER6 SAVGREGS SHRFLAG SHRUSECT SWPTABLE VMBLOK	MICCREGO PAGSWP R10 R6 SAVEWRK1 SAVKEYS SHRFPNT SWPAPP SWPVM VMSIZE	PAGTABLE R11 R7	R12 R8 SAVEWRK3 SAVPSW SHRNOPRT SWPCYL	NEWPAGES PAGTSWP R13 R9 SAVEWRK5 SAVTABLE SHRPAGE SHRPAGE SWPFLAG TTSEGCNT
DMKCFG	DMKCVTDB DMKPGTPG DMKSCNFD EXTMODE F7 MICEVMA3 NORLSE PREFIXB	DMKCVTHB DMKPGUPR DMKSCNRU FFS IOERETN	DMKPMA DMKSCNVD FO IOKEEP MICPMAMP PAGSHR PROTBLOK RDEVOWN R14 SAVEAREA	DMKDSPCH DMKPMACD DMKSCNVS F1 LOCK	DMKPTRAN DMKSCNVU F16 LPUADDR MICPMMSK PAGTABLE PROTERR RDEVTYPC R2 SAVERETN SAVEWRK9	DMKPTRLK DMKSNTBL F2 L2 MICPMPSA PMAAVAIL PSA RDEVTYPE R3	DMKCFFSB DMKFRET DMKPTRUL DMKSSSMQ F3 L2048 MICVTMR PMAMODE PSAMSS R0 R4 SAVER6 SEGPAGE TRANMODE	DMKHVDIP DMKRPAGT DMKRRCND F4 L4 MP PMAON PSWCC1 R1 R5 SAVEWRK1	DMKCVTBD DMKPGSPO DMKRPAPT DMKVATMD F4095 MICBLOK MSSPRES PMASTAT PSWCC2 R10 R6 SAVEWRK2 SHRNAME TYP2314	DMKPGSPS DMKSCHRT
DMKCFH	DMKSCNVS F256 NORET RDEVTYPE R3	F3 NOTRESP R0 R4 SAVEWRK1	DMKSNTBL F4 PSA R1 R5 SAVEWRK2 SAVNAME	F4096	EDIT F6 RDEVCODE R11 R7	DMKQCNWT ERRMSG F8 RDEVCUP R12 R8 SAVEWRK6	ASYSVM DMKCVTDT DMKQCORD EXTMASK IOERETN RDEVFLAG R13 R9 SAVEWRK7 SAVUSER TYP2305 XKEYMODE	DMKRPAGT F0 IOKEEP RDEVOWN R14 SAVCREGS SAVEWRK8 SEGINV TYP2314	DMKRPAPT F1 LOCK RDEVSTA5 R15 SAVDATE	CPSTAT4 DMKFRET DMKSCNFD F2 MP RDEVTYPC R2 SAVEAREA SAVFPRES SHRFPNT TYP3340
DMKCFJ	AFREE DMKSCHST MP R2 TRQBLOK	AP DMKSCNFD PSA R3 TRQBSIZE	DMKCFMAT DMKTRCIT RUN R4 TRQBTOD	DMKCFMWU DMKTRCPB R0 R5 TRQBUSER	DMKCVTAB F1 R1 R6 TRQBVAL	F2 R11	DMKCVTHB F3 R12 SAVEREGS	DMKERMSG F6 R13 SAVERETN	F60 R14	DMKSCHRT LOCK R15 TRQBIRA
DMKCFM	ADSPCH BALRSAVE CLASTERM CPEXSIZE DMKERMCP DMKSCHRT IOMASK	CONFSOP CPINITD DMKERMSG	AFREEP BOXBLOK CONFSS CRTFSII DMKFREE DMKSTKCP MP	AFRET BRING CONPNT CRTFSSA DMKFREEP DMKVIOMK NICATRB	AP BUFCNT CONTASK C1 DMKFRET EDIT NICBLOK	ERRMSG	APTRAN BUFINLTH CPEXBLOK DMKBOXNS DMKGRTDT FFS NICD3277	CPEXREGS DMKCFCMD DMKPTRAN F0	DMKDSPB DMKQCNWT F255	ATTN CLASGRAF CPEXR8 DMKDSPCH DMKQCORD HILIGHT NOAUTO

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	NORET RDEVSNA R15 SAVEREGS SNARVMB TRQBLOK VCONANF	NOTIME RDEVSNRB R2 SAVER11 SYSTEM TRQBSIZE VCONCBRK		PSA RDEVTYPE R4 SAVEWRK2 TEMPR1 TRQBVAL VCONDIAG	RO R5 SAVEWRK6 TREXLOCK TYPBSC	TYP2741	R11 R7 SNARFG1 TREXTERM TYP3277	R12 R8 SNARFG3 TRQBCPQ TYP3278	RDEVIDNT R13 R9 SNARILER TRQBFLG2 TYP3279 WAIT	R14 SAVEAREA SNARPRMT
DMKCFO	DPMICON I PUADDRX R10 R6	DMKDSPCH DPSTAT	CPSTAT2 DMKERMSG FF MICBLOK R12 R8	F2 MICDASA R13 R9	CP370EAV DMKLOKSW F256 MICEVMA3 R14	F3 MICFSSE R15 SAVEREGS	C6 DMKSCNAU F4 MP R2	BUFFER CPEXR2 DMKCFMBK DMKSCNFD F5 PSA R3 SAVEWRK1	BUFNXT CPEXR5 DMKCFPRR DMKSTKCP F8 R0 R4 SAVEWRK2	DMKSTKOP I PUADDR R1 R5
DMKCFP	AEXTSP BIOBLOK DMKBLDRT DMKPGSPP DMKVDREL LPUADDR PGBSIZE RUNUSER R3 SIGCR XRIGHT24	DMKPMAEX DMKVMCUA MICBLOK PGPNT R0 R4 SIGSENSE	DMKCVTBH DMKPTRUL FFS MICCREGO PROBSTRT R1 R5	DMKFREE	DMKFRET DMKQCNWT F16 MICSPT PROCIO R11 R7			DMKLOCKQ DMKSCNVD I PUADDR OLDVMSEG RCHBLOK R14	ASYSVM DELPAGES DMKMHCRE DMKTRCPB KEEPSEGS OPERATOR RCHDED R15 SAVEREGS VMSIZE	DMKPGSPO DMKVATBC LOCK
DMKĊFQ	DMKIOEMI DMKSCHST F0 IDAHSTRT IOBLOK IOERSIZE MIHMSGID PSA RDEVBUZY RDEVSTA4 R2 SAVERETN SAVERETN SAVERETN SAVEWRK9 TRQBSIZE TYP3851	ASYSVM CLASFBA CONPNT CPEXR8 DMKCVTAB DMKIOSCB DMKSCNRD F1 IDAHWRK1 IOBMISC LOCK MIHMSGLN PSAMSS RDEVCLAS RDEVCLAS RDEVCLAS RDEVCLAS RDEVTYPE R3 SAVER11 SILI	DMK10SQR DMKSSUCF F255 10BBPNT 10BM1SC2 LOCKSAV MP RCUBLOK RDEVCON RDEVP10B R0 R4 SAVER12 SYSV1RT TRQBUSER VCONANF2	CONRESP CPEXSIZE DMKDIBDR DMKLOKIO DMKSTKCP F4 IOBCAW IOBSIZE LPUADDR MSSPRES RCUCUBSY RDEVCSW RDEVPS R1 R5 SAVER4 TIMEDISP TRQBVAL VCONBFSZ	CONRETN CPSPMODE DMKDSPCH DMKLOKSW DMKSYSTV F8 IOBCC3 IOBSTAT MIHDE NICATRB RCUPRIME RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA RDEVCUA R	DMKDSPRQ DMKPTRLK DMKVCBRD IDAENTRY IOBUSER MIHMISC NICBLOK RCUSUB RDEVCUB RDEVQIOB R11 R7 SAVEWRK1 TRQBCPQ TYPCTCA VCONCTL	CONTASK CRTEXT DMKERMSG DMKPTRUL DMKVIOMK IDAEWRD1 IOBFATAL IOBFATAL IOBFATAL IOBFATAL IOBFATAL IOBFATAL ROVDED RDEVSCHD RDEVSCHD R12 R8 SAVEWRK2 TRQBFLG2 TYPSS VCONFSS	CLASURO CONUSER CRTEXTSZ DMKFREE DMKPTTFT DMKVSUCO IDAHEAD IOBFPNT IOERBLOK MICSIZE RDEVAIOB RDEVFIOB RDEVSNA R13 R9 SAVEWRK6 TRQBIRA TYP3210 VCONNCB	DMKFREEP DMKQCOCL DMKVSUCR IDAHPAGE IOBIOER IOEREXT MIHMSGDV PMAMODE RDEVAIRA RDEVFTR RDEVSTAT R14 SAVEAREA	CONFSOP CPEXRO DMKBIORS DMKFRET DMKSCHRT FFS IDAHSIZD IOBIRA IOERPNT MIHMSGDW PMASTAT RDEVBLOK RDEVIOER RDEVSTA3 R15 SAVEREGS SAVEREGS SAVEWRK8 TRQBSAVE TYP3330

MODULE

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DMKCFR	ADSPCH CLASDASD CPEXRO DMKERMSG DMKLOKSW DMKVI0IN IOBIRA IOBSPEC IOERSIZE MIHMSGLN RDEVCSW R10 R6 SAVEWRK1 TRQBSIZE ZEROES	CPEXR11 DMKFREE DMKPTRLK FFS IOBLINK IOBSPEC3 LOCK MP RDEVIOER R11 R7 SAVEWRK2	CPEXSIZE DMKFREEP DMKPTRUL F1 IOBLOK IOBSPEC4 LOCKSAV PSA RDEVMID R12 R8	DMKFRET DMKSCHRT F15 IOBMID IOBSPEC5 LPUADDR RCWCCNT RDEVPS R13 R9 SAVEWRK6	CRTEXTSZ DMKHPSHT DMKSCHST IOBCAW IOBMISC IOBTIO MIHDE RCWCCW RDEVRRES R14 SAVEAREA	DMK10EMI DMKSCNRD 10BCSW 10BM1SC2 10BUNSL MIHMSG RCWHEAD RCWHEAD RDEVSTA3 R15	DMKCVTAB DMKIOSHA DMKSCNVD IOBFLAG IOBWSG IOBUSER MIHMSGCS RCWRCNT RDEVSTA4 R2 SAVER1	DMKCVTBH DMKIOSQR DMKSTKCP IOBHIO IOBRES IOBVADD	DMKSTKIO IOBHVC IOBRREL IOERBLOK MIHMSGDW RDEVAIOB	DMKDSPCH DMKLOKIO DMKUNTFR IOBIOER IOBSIZE IOEREXT MIHMSGID
DMKCFS	DMKRPAPT F8 MICEVMA MICSSKE PAGPGSWP R0 R4	AFRET ASYSVM CPSTAT3 DMKERMSG DMKSCNFD IOERETN MICEVMA2 MICSTBVR PREFIXA R1 R5 SAVEWRK3 SYSNAME VMBLOK	DMKSCNRD IOKEEP MICEVMA3	DMKFRET DMKSNTBL LOCK MICEVMA4 MICVPSW PSA R11 R7 SAVEWRK5	LPUADDR	R13 R9 SAVEWRK7 TYP2305	F0 MICBLOK MICRRBE NORET RDEVCODE R14 SAVEAREA	DMKPTRUL F2 MICCREG MICRSEG OWNDLIST	F4 MICCREG0 MICSIZE OWNDRDEV RDEVTYPE R2	DMKCVTBH DMKRPAGT F4096 MICDASA MICSPT4 OWNDVSER RDIDX R3 SAVEWRK1
DMKCFT	F4 NICBLOK PSA RDEVFLAG	RDEVADD RDEVLLEN RDEVTMCD R12 R8 SYSLOCS	DMKUDRMD F8 NICD3277 RDEVAPLO RDEVNICL RDEVTYPC R13 SAVEAREA TYPBSC	DMKUDRRV LOCK NICD3278 RDEVAPLP RDEVPCHG RDEVTYPE R14 SAVEREGS TYPTTY	DMKSCNFD DMKVCTER L2 NICFLAG RDEVASTB RDEVPSUP RDEVUSC8 R15 SAVEWRK1 TYP3277	DMKSCNVD F1 L3 NICLLEN RDEVATOF RDEVSCRL	F2 L4 NICPSUP RDEVAVM1 RDEVSNA RDEV3101 R3 SAVEWRK3 VCONANF	DMKCVTBH DMKSYSES F24 MP NICSIZE RDEVAVM2 RDEVSNRB R0 SAVEWRK4 VCONBRK X40FFS	DMKSYSLD F255 NICAPL NICTEXT RDEVBLOK RDEVTAPL R1 R5	DMKSYSLE F3 NICATOF NICTMCD RDEVCORD RDEVTEXT R10 R6 SNARBLOK
DMKCFU	DMKFRET	BRING C1 DMKDIDTR DMKIOEIR DMKPTRAN DMKSCNPH	DMKLOCK DMKPTSMW DMKSCNRA	DMKDMPAU DMKLOCKD	BUFNXT DMKCKTUU DMKDMPDV DMKLOCRD DMKQCORD DMKSCNRU	DMKDMPRC	DMKMCIMS	DMKCVTDB DMKDMPSW DMKPGTDS	ASYSLC CORSWPNT DMKCVTDT DMKCVTDT DMKSCHST DMKSCHST DMKSYSDW F16 IOKEEP IRMRLADD	DMKCVTHB DMKFREE DMKPGTVR DMKSCNAU DMKSYSLG F2 I RMAND

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#### EXTERNAL REFERENCES (LABELS AND MODULES) MODULE

	LOCK	LOGMBLOK		LOGMNXT	LOGMSEQ		LOGMSIZD		MCHAREA	MCHMODEL
	R11	MOD3081 RCUBLOK RDEVSTAT R12	R13	R14	NOAUTO RDEVBLOK RDEVTYPE R15	RDEVUSER R2	RDIDX R3	RDEVFLAG R0 R4	R1 R5	RDEVPEND R10 R6
	R7 SAVEWRK5 SWPCYL TRQBVAL	R8 SAVEWRK7 SWPFLAG TYPPRT	R9 SAVEWRK8 SWPRECMP UCASE	SAVEAREA SFBFILID SYSLOCS VCONCTL	SYSTEM	SAVER14 SFBLOK TRQB1RA VCONOMSG	SAVEWRK1 SFBORIG TRQBLOK VMBLOK	SAVEWRK2 SFBSTART TRQBSIZE ZEROES	SFBSYSID	SAVEWRK4 SFBUSER TRQBUSER
DMKCFV	AFREE BUFFER DMKERMSG EXTMODE PAGE2K R15 SAVEREGS	AP BUFNXT DMKFREE FF PREFIXB R2 SAVEWRK1	APSTAT1 CPMICAVL DMKPTRAN FO PSA R3 SAVEWRK2	F1 RO R4	APUOPER CPSTAT2 DMKSCNFD F2 R1 R5 SAVEWRK5	F255 R10 R6	ASYSLC DEFER DMKSYSMS F8 R11 R7 SYSLOCS	AVMREAL DFRET DMKVATAT LOCK R12 R8 TEMPRO	BLANKS DMKCVTBD DMKVATBC MP R13 R9 VMBLOK	BRING DMKCYTDB DMKVATMD NORET R14 SAVEAREA ZEROES
DMKCFW	AP LOCK R12 R8	AQCNWT MP R13 R9	BUFFER NORET R14 SAVEAREA	DMKERMSG PSA R15 SAVEREGS	DMKQCNWT RDEVBLOK R2 SAVEWRK8	RDEVSNA R3	DMKSCNFD R0 R4 VMBLOK	DMKVCTCH R1 R5 ZEROES	F3 R10 R6	F8 R11 R7
DMKCFY	DMKUDRRV LPUADDR MICSIZE NICD3278 PFKFLAG PROTATRQ RETDLEN R2 SAVEWRK1	DMKERMSG F0 LPUADDRX MICSKYMD NICSIZE PFKIMM	DMKFREE F1 MICBLOK MICSSKE PAGPGSWP PFKLNG PFKLNG PROTSIZE R0 R4 SAVEWRK4	PFKRET PSA R1 R5 SAVEWRK5 TYPTTY		BLANKS CPSTAT2 DMKSCNAU F3 MICEVMA MICWORK PFDCPYSP PFKTABLE RDEVBLOK R11 R7 SAVEWRK8 TYP3278	BUFCNT CPSTAT4 DMKSCNFD F7 MICEVMA3 MP PFDVAL PFKTBLSZ RDEVNICL R12 R8 TEMPSAVE VCONBRK	F8 MICISKE NICBLOK PFKADDR PFTAB RDEVTYPC R13 R9	DMKUDRFU IPUADDR MICRRBE NICDTYPE PFKCOUNT PREFIXA RDEVTYPE R14	DMKCVTDB DMKUDRMD LOCK MICRSEG NICD3277 PFKDWDS PREFIXB RETBUF R15 SAVEREGS TRQBLOK
DMKCKD	AP BCTWAIT CLASGRAF DMKCKFCN FREESAVE NICBLOK NICTYPE RCUBLOK RDEVCUA RDEVNICL R10 R6 TYP3277	DMKCKFC2 FXDLOG NICDISA PREFIXA RCUCHA RDEVCUB	DMKCKFTE FO NICDISB PREFIXB RCUDVTBL RDEVDED	ARIOCH CCO CLASTERM DMKCKFVR IOBLOK NICENAB PRNPSW RCUPRIME RDEVDISB RDEVTYPC R13 R9 TYP3705	DMKCKNTB IOBRADD NICFLAG PROCIPL	I PUADDR NICLGRP PSA RCUTYPE RDEVFLAG	R2	LOCK NICSIZE RCHBLOK	MP NICSTAT RCHCUTBL RDEVAUTO RDEVMAX	
DMKCKF	ACCNT ACNTDEVT ACNTTIME	ACNTIOCT		ACCTUSER ACNTNEXT ACNTVTT		ACNTPGRD	ACNTCONT ACNTRFLG ACTIFLAG	ACNTRMID		

#### EXTERNAL REFERENCES (LABELS AND MODULES)

ADSPCH AFREEP ALOCBLOK ALOCCYL2 ALOCDU ALOCFLG ALOCPNT ALOCRECS AP APSTAT1 APUOPER ARIODV ARIOPR ARIOPU ARIOPU ASFBACO ASYSLC ASYSOP ASYSVM BFFCCPD BFFENTRY BFFFSIZE BFFREAL BLANK BLANKLEN BLANKS BUFFCV BUFFDLM BUFFOPLG CALLCVT CHECKVF CKPLOAD CKPTLIST CL CLASDASD CLASFBA CLASGRAF CLASTERM CLASURO CPEXADD CPEXBLOK CPEXREGS CPEXSIZE CPID CPSTAT5 CPTRAPER DATE DEDTEST DEVCARD DISCREC DMKCKHAC DMKCKHDL DMKCKHIN DMKCKHPR DMKCKHST DMKCKHSZ DMKCKHWM DMKCKNIO DMKCKNRB DMKCKNRD DMKCKNTB DMKCKPE DMKCKPER DMKCKHFL DMKCKHST DMKCKHSZ DMKCKHWM DMKLOKIO DMKCKNRB DMKCKNRD DMKCKNTB DMKCKPE DMKIOCVT DMKIOECL DMKIOECQ DMKIOEES DMKIOEID DMKIOEIP DMKIOEIQ DMKIOEMQ DMKIOEMX DMKIOFEP DMKIOZT DMKORWT DMKRSPAD DMKRSPSF DMKRSPAC DMKRSPCV DMKRSPDL DMKRSPDP DMKRSPHQ DMKRSPMN DMKSYSCK DMKSYSTP DMKSYSFL DMKSYSHL DMKSYSHL DMKSYSHL DMKSYSGG DMKVRSYSOC DMKSYSOW DMKSYSRM DMKSYSSP DMKSYSTP DMKSYSFL DMKSYSWI DMKSYSWI DMKSYSHL DMKSYSGG MKKSYSOC DMKSYSOW DMKSYSRM DMKSYSSP DMKSYSTP DMKSYSFL DMKSYSWI DMKSYSWI DMKSYSHL DMKSYSGG MKVRRSYSOC DMKSYSOW DMKSYSRM DMSYSSP DMKSYSTP MKSYSFL DMKSYSWI DMKSYSWI DMKSYSHL DMKSYSGG MKVRR POLOCAL DORDEV DOTERMID DOVMTERM FFS FREESAVE F0 FI F255 F4096 LISTIZE LOCK MP NICBLOK FORMNTRY FORMOPER FORMSEND FORMUSER FREESAVE F0 FREESAVE F0 FI F255 F4096 LISTIZE LOCK MP NICBLOK NICDTYPE RDEVADD RDEVALLN RDEVBLOK RDEVCDE RDEVMDL RDEVNICL RDEVPS RDEVSNRB RDEVSTA3 RDEVTMAT RDEVTYPC RDEVTYPE RDIDX RECBLOK RECYL RECOUMP RECFLG RECFULL R11 R12 R14 R14 R15 R2 R2 R3 R4 R4 R5 R6 R6 R7 R11 R12 R14 R15 R2 R3 R4 R5 R6 R8 R9 SAVEVAC SETTDK SETUP SETUP1 SETUP2 SFBCLAS SFBCON1 SFBDEST SFBDIST SFBFIRST SFBFLAG2 SFBFLAG5 SFBLAST SFBLOK SFBOFORM SFBORIG SFBPURGE SFBRECNO SFBRECS SFBSIZE SFBSTART SFBSYSID SFBUFORM SFBUSER SHOBSIZ SNACARD SNARBLOK SNARLUN SNAUSER SPLINK SPNXTPAG SPPREPAG STARTIME STCODE TEMPSAVE TERMEXIT TERMRMID TODATE TRAPDATA TYPBSC TYPPRT TYPPUN TYP3210 VACOK VACOVFR VCONCTL VCONNICB VCONRDEV VECOPVF VECSTAT VECUSER VMBLOK VSPSFBLK ZERO ZEROES **R7** SFBCLAS SFBCONTO SFBCOPY SFBOFORM SFBORIG SFBPNT SHQBSIZE SKIPADD SYSLOCS TYP3210 USERCARD VSPLCTL ALARMALPRTBLKAPARIODVATTNAUTOIPLAVMREALBALRSAVEBUFFCVBUFFCVBUFFDLMBUFFSTATBUFFVERBUSYCAWCCCC0CC1CC22CC3CECKPLOADCLCLASFBACLASURICLASUROCPSTAT5CSWCUEDEDMKCKDSWDMKCKDVADMKCKFCVDMKCKFLGDMKCKFLGDMKCKFRUDMKCKFFSFDMKCKFWADMKCKFWADMKCKFWSGDMKCKNRBDMKCKPDMKCKPCSDMKCKFDVDMKCKPERDMKCKPERDMKCKPFLDMKCKPFNDMKCKPSNDUMPSAVED1D4ECKDFFSF4096INTKFLININTTIOIONPSWIOOPSWLOCKLPRLSPLCLL8MPPOWEROFFPSARCHADDRCHBLOKRCUADDRCUBLOKRCUCHARCUCHARCUSUBRCUTYPERDEVADDRDEVBLOKRDEVCKPRDEVCLASRDEVCUARDEVDEDRDEVDELPRDEVDISARDEVDRANRDEVEXTNRDEVFLAGRDEVFSEPRDEVIMAGRDEVRFGRDEVSEPRDEVSELRDEVSELRDEVTYPCRDEVTYPERDEVUSERRDEVFLAGRDEVFFERDEVIMAGRDEVRFGRSPSLKRSPXBLOKRSPXESTRSPXFCBR2R3R4R5R6R8R9SFBDATESFBFLAGSFBFLAG2SFBFLAG2SFBFNAMESFBFTYPESFBLASTSFBLOKSFBOFORMSFBPNTSFBPURGESFBRECERSFBSTZESFBSTZESFBSTZESFBFLAGSFBFLAG2SFBSTART DMKCKH ARIOCT ARIOCU ARIODV CKPLOAD CLASDASD CLASFBA APSTAT1 ARIOCH CC2 CC3 AVMREAL BLANK CPSPMODE CPSTAT2 DMKCKDCP DMKCKDSW ASCHN DMKCKM ALARM AP CPID CC3 C1 CCO CC1 CC0CC1CC2CC3CKPLOADCLASDASDCLASDASDCLASFBACP1DCPSPMODECPSPMODECPSTAT2CPSTAT4CPSTAT5CPXSTORC1C1C14C15C2DATEDMKCKDCPDMKCKDCPDMKCKDWDMKCKFOCDMKCKFOWDMKCKFVMDMKCKFVMDMKCKFVTDMKCKNPZDMKCKNBCDMKCKNBCDMKCKNBHDMKCKDCDMKCKDCDMKCKDCDMKCKNIODMKCKNPSDMKCKNPXDMKCKNPYDMKCKNPZDMKCKNPZDMKCKNPZDMKCKNPDDMKCKPMPOWNDLISTOWNDVSERF4096F60LOCKMESSAGEMPNOADDNOTIPLRCUDVTBLRCUPRIMERCUSUBRCUTYPERAGINVALPROCIPLPSARCHBLOKRCHCUTBLRCUBLOKRDEVMDLRDEVPRDVRDEVQUEDRDEVSTA5RDEVTYPCRDIDXRECBLOKRECCYLR0P10P10P10P10P10P10P10P10P10 
 OWNDLIST
 OWNDRDEY
 OWNDVSER
 PAGCORE

 RCUDVTBL
 RCUPRIME
 RCUSUB
 RCUTYPE

 RDEVMDL
 RDEVPRDV
 RDEVQUED
 RDEVRDC

 R1
 R12
 R14

 R2
 R14
 R14
 R14 R15 R2 R3 R4 SAVCREGS SAVDATE SAVFPRES SAVGREGS SAVKEYS R5 R6 SÁVNAME SÁVPSW R9 **R7** R8

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MODULE	EXTE	RNAL REFE	RENCES (L	ABELS AND	MODULES)					
	SAVTABLE SSBENTRY VMBLOK		SAVUSER SSBHEADL XKEYMODE		SEGMIG SWPCYL	SEGPAGE SWPFLAG	SEGTABLE SWPKEY1	SPM SWPRECMP	SSBCYL SYSNAME	SSBENTRL TODATE
DMKCKN	ALARM CLASFBA DUMPSAVE L8 RCUCHA RDEVMDL R1 R7 TYP3380	MP RCUPRIME	ARIODV CSW D8 OWNDLIST RCUSUB RDEVPRDV R12 R9	CAW DE F255 OWNDRDEV RCUTYPE RDEVQUED R14 SILI	CC DMKCKDGP IDA OWNDVSER RDEVADD RDEVSTAT R15 TYP2305	INTTIO PSA RDEVBLOK	I ONPSW RCHADD RDEVCUA	CC3 DMKCKFTR LOCK RCHBLOK RDEVCUP RDEVTYPE R4 TYP3340	L3 RCUADD RDEVDISA	CKPLOAD DMKCKP L5 RCUBLOK RDEVECKD R0 R6 TYP3375
<b>ДМКСКР</b>		INTSVCL POWEROFF R11 SEGTABLE	CKPLD C6 DMKCKFTP DMKSAV INTTIO PRNPSW R12	AP CKPLOAD DE DMKCKFWA DMKSAVRS IONPSW PROPSW R14 SIGSENSE VMBLOK	DMKCKHDC ECKD IPLPSW PSA R15	DMKCKDSW	CAW CPSTAT5 DMKCKFDV DMKCKHIO FREESAVE MCNPSW RDEVECKD R3 TEMPR12	DMKCKMDV F0 MP	DMKCKFIL	CC2 CSW DMKCKFLI DMKCKMSV INTKFLIN PMAMODE R1 R6 TEMPR6
DMKCKR		DMKRSPFC DMKVSDAD MP RDEVSTAT R14	DMKRSPND DMKVSETR OPNSFB RDRCHN R15 SAVEREGS SFBEOF	ARSPPR DMKPTRUL DMKRSPPC DMKVSGUM PRTCHN RHTBLOK R2 SAVER2 SFBFLAG SFBRSTRT VMBLOK	PSA RHTVIRT R3 SAVEWRK1	DMKRSPST FORMBLOK RDEVBLOK R0 R4 SAVEWRK3 SFBINUSE	DMKRSPSW FORMOPER RDEVCLAS R1 R5 SAVEWRK4	DMKRSPTP FORMUSER RDEVDISA R10 R6 SAVEWRK6 SFBMON	DMKRSPCR DMKSCNRU F1 RDEVDRAN R11 R7	DMKSYSCO F4096 RDEVFLAG R12 R8 SAVEWRK8 SFBOPEN
DMKCKS	DMKRSPSC PRTCHN RDEVTYPC R0 R6	DMKRSPST PSA RDEVTYPE R1 R7 SAVEWRK2 SFBLOK	DMKRSPSW RDEVBLOK RDEVXSEP R11 R8	DMKRSPTP RDEVCKP RDRCHN R12 R9 SAVEWRK4 SFBRECS	RSPXBLOK R13 SAVEAREA	FFS RDEVEXTN RSPXDEST R14 SAVEREGS	F4096 RDEVFSEP RSPXFCB R15 SAVER0 SAVEWRK7	LOCK RDEVIMAG	DMKRSPDC MP RDEVPRFG RSPXFORM R3 SAVER2 SAVEWRK9	PCHCHN RDEVSTAT RSPXINDX R4 SAVER8
<b>DMKCKT</b>		DMKRSPSM DMKSYSWI LOCK	DMKLOCRD	ALOCMAX DMKLOCRQ DMKRSPSW DUMP NOTUSED RECMAP R12		DMKRSP9P F2	DMKRSPFC DMKSCNRU F255	ASYSOP DMKRSPFL DMKSYSCA F256 RDEVCODE RECUSED R2	DMKSYSCH F3	DMKCVTBD DMKRSPPC DMKSYSSL F4 RDEVTYPC RHTVIRT R4

MODULE	EXTER	RNAL REFE	RENCES (LA	ABELS AND	MODULES)					
	R5 SAVEWRK4 SFBUSER	R6 SAVEWRK5 TYP2314	R7 SAVEWRK6 TYP3330	R8 SAVEWRK7 TYP3350	SAVEAREA SFBFILID TYP3375	SAVEREGS SFBLOK TYP3380	SAVERO SFBSIZE VMBLOK	SAVER1 SFBSIZEB VMSIZE	SAVEWRK1 SFBSYSID	SAVEWRK2 SFBTYPE
<b>ДМКСК</b> У	DMKDSPNP DMKPTRAN DMKRSPST DMKSYSOW LOCK OWNDVSER RECYLAG RECCYL RSPXFLAG R13 R9 SFBDATE SFBRECNO SPTIME	LPUADDR PCHCHN RDEVFSEP RECMAP RSPXFOLD R14 SAVEAREA SFBDUMP	AFREEP APUOPER CPSTAT5 DMKFREE DMKQCOSY DMKRSPTA DMKWRNSB MP PRTCHN RDEVIMAG RECMAX RSPXFORM R15 SAVEREGS SFBFILID SFBSTART TEMPR1 TSKSFB TYP3350	DMKRPAGT DMKRSPTC FFS NPRCNT PSA RDEVPRFG RECPNT RSPXINDX R2 SAVER2 SFBFLAG	ASYSVM C1 DMKFRET DMKRPAPT DMKRSPTP F0 NPRNAME RDEVALLN RDEVSTAT RECUSED RSPXSETU R3 SAVEWRK1 SFBFLAG2 SFBUSER TSKCCPD	BRING DEFER DMKLOKIO DMKRSPDC DMKSCNDC F1 NPRPNT RDEVBLOK RDEVTYPC RHTBLOK R0 R4 SAVEWRK3	DMKMCHST DMKRSPHQ PAKSCNRU F255 NPRTBL RDEVCLAS RDEVTYPE RHTVIRT R1 R5 SAVEWRK5 SFBLOK SPLINK TSKFLAG3 TSKSYSID	CLASURO DMKCKTSD DMKPGUBN DMKRSPND DMKSNTQN F256 OPNSFB RDEVDISA RDEVDISA RDEVXSEP RSPXAUTO R10 R6 SAVEWRK6 SFBMISC1 SPNXTPAG TSKFLAG4	DMKPGUVG DMKRSPPC DMKSTKCP F4096 OWNDLIST RDEVDRAN RDIDX RSPXBLOK R11 R7 SAVEWRK7 SFBMON SPPREPAG TSKLAST TSKTIME	DMKPGUVR DMKRSPSCO DMKSYSCO IOKEEP OWNDRDEV RDEVEXTN RECBLOK RSPXFCB R12 R8 SAVEWRK8 SFBPNT
DMKCKW	AP DMKRSPHE PSA R15 SEGINV	CKPLOAD DMKRSPHL RHTBLOK R2 SIGSAVE	CONTINUE DMKRSPHT RHTECNT R3 SWPCODE		DMKCKHPR DMKRSPWI RHXADDR R5 SWPFLAG	DMKCKNIO F4096 RHXTABLE R6 SWPPSTOR	LOCK RO R7	DMKCKNRD LOKSAVE R1 R8 TRANMODE	DMKCKP MP R12 R9 VMBLOK	DMKCKPER PAGCORE R14 SAVCNT
DMKCLK	AEXTSP CPSUPER EMSRCLKC L8 R12 SIGRES	ALARM CPTIDLE EMSREC MCHEK R13 SIGSYNC	APSTAT1 CPWAIT EMSRSYNC NOTIME R14 SIGXC	APSTAT4 CO EXTMODE OPERATOR R15 SYNCMASK	APUOPER DMKPSADU FF PROCIPL R2 TIMEDISP	IDLEWAIT PROCSCHK SAVEAREA	CPCREGO DMKQCOSY I PUADDRX PSA SAVEREGS VMBLOK	LOCK RO	LPUADDR R1 SIGEMS	CPSTAT3 EMSPSYNC LPUADDRX R11 SIGQUI
DMKCMD	DMKCFSIS DMKCFSWG DMKCFVSB DMKCQGQA DMKCQFS DMKCQFQL DMKCQRDP DMKCQSSC DMKCQYPF DMKNEAAH DMKNLEMP	DMKCFOSR DMKCFSLE DMKCFS37 DMKCFVSM DMKCQGQC DMKCQHNG DMKCQFQP DMKCQRHD DMKCQTCL DMKCQYSA DMKNEADH	DMKCFOS3 DMKCFSMG DMKCFUDU DMKCFYAG DMKCQGQD DMKCQHNS DMKCQPQS DMKCQRPG DMKCQTST DMKCQYSP DMKCQTST DMKCRSDS DMKSRMOS	DMKCFSAC DMKCFSNT DMKCFULO DMKCFYAS DMKCQGQG DMKCQHRG DMKCQPQT DMKCQVSE DMKCQVSE DMKCQVSE DMKCQVSB DMKCRSHD DMKSRMSS	DMKCFSAP DMKCFSPX DMKCFVMI DMKCFYSA DMKCQGQH DMKCQGQH DMKCQRQU DMKCQRQU DMKCQVTI DMKCQVTI DMKCQYTI DMKNESPL DMKTIDP	DMKCFSCC DMKCFSRN DMKCFUMO DMKCFYSC DMKCQGQL DMKCQGUTG DMKCQUTG DMKCQUTE DMKCQVUTE DMKCQVUT DMKCQVUTE DMKCQTTB DMKCTDB DMKTHIFA	DMKCFSCP DMKCFSSA DMKCFUMW DMKCFUSM DMKCQGQS DMKCQQQL DMKCQQQL DMKCQYUS DMKCQYUS DMKCQYUS DMKCQYUS DMKCQYUS	DMKCFSEC DMKCFUPA DMKCFUPA DMKCFUPA DMKCQSQA DMKCQQQA DMKCQQQA DMKCQQVS DMKCQYVS DMKCQYVS DMKCQYVS DMKCQYVS	DMKCQCPT DMKCQGQU DMKCQPQD DMKCQQQT DMKCQSPS DMKCQYCP	DMKCFSIM DMKCFSVS DMKCFSVS DMKCQGID DMKCQHFG DMKCQPAG DMKCQRAF DMKCQSRE DMKCQSRE DMKCQYLM DMKJRLSE DMKNLDR DMKTHIPO
DMKCNS	ADSPCH	AFREE	AFREEP	AFRET	ALARM	ALOKSP	APSTAT1	APTRAN	APUOPER	ASYSVM

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	CONTSIZE CPID DMKCVTBH DMKLOKSW DMKTBLCI DMKTBNAO F10 IOBCAW IOBLOK IOERBLOK IOERBLOK LOCKSAV PSA RDEVAIOB RDEVCON RDEVPSUP	DMKTBLCO DMKTBNBE F15 IOBCC1 IOBMISC IOERCCRA LOGDROP PSASYSID	C1 DMKERMSG DMKOPEEM DMKTBLPI DMKTBNEA F16 IOBCC3 IOBMISC2 IOERCCW LOGHOLD RCHBLOK RDEVASTB RDEVCTL RDEVLOG RDEVRCNT	DMKTBLPO DMKTBNEB F2 IOBCSW IOBRADD IOERDATA LPUADDR RCHBUSY RDEVATNC RDEVDISA RDEVNDLF RDEVREST	CONCOMND CONRETN CPEXADD DE DMKFREEP DMKFREEP DMKTBMMI DMKTRMID F4 IOBERP IOBRES IOEREXT LPUADDRX RCHSTAT RDEVATOF RDEVDISB RDEVNOHD	CONRTRY CPEXBLOK DEFER DMKFRET DMKGCOET DMKTRET DMKQCOET DMKTTXIN F8 IOBFATAL IOBSIZE IOERFLG3 NOAUTO RCUBLOK RDEVATSW RDEVDROP RDEVNRDY RDEVSCHD RDEVSCHD RDEVTYPE R12 R8 SYSTEM	DMKIOERR DMKQCPTO DMKTBMNI DMKTTYOP IFCC IOBFLAG IOBSPEC IOERNUM PREFIXA RCUBUSY RDEVENAB RDEVPCNT RDEVENAB RDEVPCNT RDEVUSC8 R13 R9	DMKCFMAT DMKIOEST DMKSCNEP DMKTBMNO EDIT IL IOBIOER IOBSTAT IOERREAD PRGC RCUSCED RDEVBLOK RDEVEPMD RDEVPREP RDEVSTA2 RDEVUSER R14 SAVEAREA TIMEDISP	DMKIOSQR DMKSCNRD DMKTBNAE F0 INHIBIT IOBIRA IOBUNSL IOERSIZE PRIORITY RCUSTAT RDEVFCNS RDEVPRFG RDEVSTA3 RDEVVM2 R15 SAVERO	CONTASK CPEXSIZE DMKCNTED DMKSCNRU DMKSCNRU DMKTBNAI F1 INTREQ IOBLINK IOBUSER LOCK PRTC RDEVACTV RDEVBZCH RDEVFLAG
DMKCNT	AP F256 R12 R8	BALRSAVE IOBLOK R13 R9	BALR3 LOCK R14 TEMPRO	BALR6 MP R15 TEMPSAVE	CONADDR PSA R2 UCASE	CONCNT RDEVBLOK R3 VMBLOK	CONPARM RO R4	CONTASK R1 R5	DMKTBLUP R10 R6	EDIT R11 R7
DMKCPB	F16 LOCK RCWTASK R13	DEFER DMKFRET DMKSTKCP F3 MICBLOK RDEVBLOK R14	DEVICE DMKIOSQR DMKTRCEX F4 MICEVMA2 RDEVBUZY R15	DMKIOSRW DMKVATBC F6	DMKCFQRD DMKPGSPO DMKVATMD IOBCAW MP REWIND R3	DMKPSAPO	DMKCVTHB DMKPTRAN	DMKDSPCH	DMKERMSG	BLANKS C1 DMKFREE DMKSCNVU F1 IOBUSER RCWHEAD R12 R8 TRANMODE
DMKCPE	AP	MP								
DMKCPI	CPUVERSN C6	CPSPMODE CPWAIT DAMAGRPT DMKCCWB7	CPSTATUS CP370EAV DEFER DMKCCWB8	CPSTAT2 CP370EON DMKAPICK DMKCCWGN	CPSTAT3 CSADDR DMKAPIPR DMKCCWL1	CKPTLIST CPEXSIZE CPSTAT4 CSSFEAT DMKCCWB1 DMKCCWL2	CPSTAT5 CSW DMKCCWB2 DMKCCWL3	CPASTAVL CPINIT CPSUPER CO DMKCCWB3 DMKCCWL4	CPMICAVL CPUID C1 DMKCCWB4 DMKCCWL5	CPCREGO CPMICON CPULOG C14 DMKCCWB5 DMKCCW0

EXTERNAL REFERENCES (LABELS AND MODULES)

	DMKDMPC2 DMKDSP1 DMKFREP DMKIOEID DMKOPEDC DMKOPEDC DMKSYSHL DMKSCNVU DMKSYSHL DMKSYSHL DMKSYSTP DMKUDROV DMKVRRC FFS IDLEWAIT IPUADDRX LANGPAGE L6 MICSIO NORET PRNPSW PSAPGID	DMKDMPMA DMKDSP2 DMKFRELG DMKIOEIP DMKOPERC DMKOPERC DMKSYSUD DMKSYSUD DMKSYSUD DMKSYSUD DMKUNTFR DPMICON FO IFCC KEYMASK LANGSIZE MCHEK MICSKYMD NOTIME PROBTIME PSASYSID	DMKDMPMP DMKDSP3 DMKFREMX DMKIOEIQ DMKOPRWT DMKPTRLX DMKRIOPU DMKSTANT DMKSYSID DMKSYSVL DMKSYSVL DMKUNTF1 DPOKTLB F1 INTMASK LANGADDR LANGADDR MFAMASK MICSTPT NOTIPL PROCIO PSBCLR2	DMKIOEMQ DMKPGRD DMKPTRPQ DMKRIORD DMKSYSVM DMKUNTRN DPSEGPRT F2 INTPR LANGBLOK LANGBLOK MICBLOK MICBLOK MICBLOK MICBLOK MICBLOK MICBLOK MICBLOK SFSIZE SYSIPLDV	DMKDMPRY DMKDSP5 DMKFRE14 DMKIOEMX DMKPTRUL DMKRIORN DMKSYCIN DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKSYSNP DMKS	DMKDMPSÅ DMKDSP6 DMKFRE15 DMKIOFEP DMKPMAI2 DMKPTRUX DMKRIOSF DMKSVCNO DMKSYSOC DMKSYSOC DMKSYSWM DMKVATZS EXNPSW F3 IOERETN LANGHIGH	DMKDMPSD DMKERMCP DMKIOCVT DMKIOJBL DMKPRGIN DMKPTSAD DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMKSYCNS DMK	DMKEXTIN DMKIOECL DMKIOTIN DMKPRWSK DMKRSYSAP DMKSYSAP DMKSYSAP DMKSYSAP DMKSYSAP DMKSYSAP DMKTODIN DMKVMASH EXTMODE F4096 IPLCCW1 LANGNEXT LPUADDRX MICISKE MPGEND PGREAD	DMKDSPRQ DMKFREAP DMKFREAP DMKFREAP DMKFSADU DMKSADU DMKSSADU DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL DMKSYSFL SC SC SC SC SC SC SC SC SC SC SC SC SC	DMKDSPO DMKFREE DMKIOEES DMKMNTIO DMKPTRAN DMKRIOCU DMKSYSHE DMKSYSHE DMKSYSHE DMKSYSSZ DMKUDRBV DMKVRRDD FF HARDSTOP IPUADDR LANGNTSZ L4 MICSCSP MSSPRES PREFIXB PSAMSS
DMKCPJ	DMKCQHFI DMKFREEP DMKLOKSW DMKRIORN DMKSTKMP DMKSYSRV EXTMODE LPUADDR NORET PROCIPL RDEVSTAT R14	CPEXR12 DMKCKRSY DMKCSOSD DMKFREHI DMKMCHIN DMKSPWA DMKSTKOP DMKSYSTE FFS LPUADDRX NOTIME PSA RDEVTYPC R15 SAVEREGS SYSTEM	DMKCVTBD DMKFRELO DMKMCHST DMKRSPWC DMKSTPX DMKSYSTS F1 MCNPSW OPERATOR	DMKCPEND DMKDIDEP DMKFRET DMKSCHST DMKSYSCA DMKSYSTV F4 MP OWNDLIST RDEVBLOK	DMKDIDTR DMKHVDPP DMKNLDR DMKSCNEP DMKSYSCV DMKSYSWA F4095 NICBLOK OWNDRDEV RDEVDED RECOVRPT R4 SSCIXADD TIMEDISP TYP3310	CPSTAT2 DMKCPIF1 DMKCDMPCA DMKIDUMP DMKOPEAC DMKSCNMU DMKSYSEA DMKSYSWV F8 NICDISA OWNDVSER RDEVDISA R0 R5	CPSTAT5 DMKCPIMS DMKCPIMS DMKIDUSF DMKOPELO DMKSCNRA DMKSYSEV DMKVRRS HARDSTOP NICNAME POWEROFF RDEVFLAG R1 R6	DMKIOEFL DMKQCNWT DMKSCNVS DMKSYSOC DMKWRMST IOELPNTR NICSIZE PREFIXA	CPUMODEL DMKCPIWC DMKERMSG DMKIOGAP DMKQCORD DMKSEGWR DMKSYSOW DMKWRMSY	DMKCPVAE DMKFREE DMKIUACP DMKQCOSY DMKSTKCP DMKSYSRM EDIT LOCK NOAUTO PROCIO
DMKCPM	CPSHRLK	ALOKSP BALR1 CLASTERM CPSTAT2 DMKPTRAN	CPUVERSN	APSTAT1 BRING CPEXADD CSW DMKSCNRA	APTRAN BUSY CPEXBLOK CUE DMKSTKCP	C1	APUOPER CC CPEXREGS DE DMKTAPRL	DEFER	ARIOCT CLASDASD CPEXR12 DMKDSBRD FTRFH	ARIOCU CLASSPEC CPEXSIZE DMKFREEP F0

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### EXTERNAL REFERENCES (LABELS AND MODULES)

	RDEVPTH3 RDEVSTA5 R14		RDEVPTH5 RDEVTYPC R2	RDEVPTH6 RDEVTYPE R3 SAVEWRK3	RDEVNRDY RDEVPTH7	RDEVPTH8 R1 R5 SAVEWRK5	RDEVPPAG RDEVRRES R10 R6 SAVEWRK6	RDEVDED RDEVPTHS RDEVSTAT R11 R7 SAVEWRK8	IOBSPEC RCHBLOK RCUPRIME RDEVDISA RDEVPTH1 RDEVSTA2 R12 R8 SHRLKCNT TRACSTRT	RDEVFLAG RDEVPTH2 RDEVSTA4 R13 R9 S1L1
DMKCPN	DMKSCNPH IOBASN IOBLINK IOBSTAT PREFIXA RCUBLOK RDEVFOFF	I OBLOK I OBUSER PSA RCUCHB RDEVNATH RDEVUSER R3 SAVER13	DMKDSPCH DMKSCNRN IOBBADCH IOBMISC IOBXTRA PSAPGID RCUDISA RDEVNRDY	DMKFREE DMKSCNVU IOBCAW IOBPATHF IOERBLOK RANGE RCUSTAT	I OEREXT RCHADD RDEVALT	DMKFRET	F1 IOBFATAL IOBRADD LOCK RCHDED RDEVATT RDEVSTA2 R12 R8	APUOPER CPEXSIZE DMKLOKIO F2 IOBFUNCT IOBSETP LPUADDR RCHPROC RDEVBLOK RDEVSTA4 R13 R9 SAVEWRK3 ZEROES	F4 IOBIOER IOBSIZE MP RCHSTAT RDEVDED RDEVSTA5 R14	BLANK DMKCPWFB DMKQCNWT F8 IOBIRA IOBSPEC5 NORET RCHSTIDC RDEVDISA RDEVTYPC R15 SAVEREGS SAVEWRK5
DMKCPO	DMKFREEP DMKSCHST DMKSYSOW LOCK NORET RCHBLOK RCUTYPE RDEVDRAN	CPEXR11 DMKCFPRR DMKCFPRT DMKSCNEP DMKVFRSV LPUADDR OFFLPROC RCHCUTBL RDEVADD RDEVENAB RDEVPTH5 RDEVSYS R14 SAVEAREA	CPEXR12 DMKCPPUP DMKLOKIO DMKSCNMU FF LPUADDRX OWNDLIST RCHPEND RDEVAOF RDEVEPLN RDEVAOF RDEVEPLN RDEVTYPC R15 SAVEREGS SIGEMS	DMKLOKSW DMKSCNRA FFS MCHAREA OWNDRDEV RCHSTAT RDEVBLOK RDEVFLAG RDEVFLAG RDEVTH7 RDEVTYPE R2	CPSHRLK DMKCVTBD DMKMCTAF DMKSCNRD F0 MCHMODEL PMAMODE RCUBLOK RDEVBOF RDEVLNKS RDEVPTH8	CPSTAT2 DMKCVTBH DMKPTRAN DMKSCNRN F1 MOD3081 PMASTAT RCUCHA RDEVBUZY RDEVPEND RDEVRCVY	DMKPTRUL DMKSPKDL F2 MOD3090 POFFLINE RCUCHD RDEVCUA RDEVPTHS RDEVRSVD R1 R5 SAVEWRK2 TIMEDISP	RCUDVTBL RDEVDED RDEVPTH1 RDEVSCHD R10 R6 SAVEWRK3 TRQBIRA	AVMREAL CPEXBLOK DEFER DMKEXTST DMKRIOPR DMKSTKMP IPUADDR MPUOPER PRIORITY RCUPRIME	DMKRSFSD DMKSYSOC I PUADDRX NOADD PSA RCUSUB RDEVDISA RDEVDISA RDEVPTH3 RDEVSTAT R12 R8 SAVEWRK7 TRQBSIZE
DMKCPP	APSTAT1 ARIODV CORPGPNT CPEXFPNT CP370EON	ACTIVTRQ APSTAT2 ASCHN CORSHARE CPEXPROC CSADDR DMKDMPRY	APSTAT4 ASFBACO CORSWPNT CPEXREGS CO	CPEXR11 C1	CPEXSIZE C2	DEFER	CPPTLBR DMKCFMBK	ARIOCH CLASFBA CPEXADD CPSTAT2 DMKCVTBH	AMCHAREA ARIOCT CORFLAG CPEXBLOK CPSTAT5 DMKDMPAA DMKFREEP	ARIOCU CORIOLCK CPEXBPNT CPUMCELL DMKDMPC2

MODULE EXTERNAL RI

### EXTERNAL REFERENCES (LABELS AND MODULES)

	DMKQCNWT DMKVMAS1 F256 IUCVCNT MICBLOK PAGERATE PREFIXB PSAEXTX RDEVCODE R13 R9 SAVEWRK8 SHRSGPRT	DMKMCTVM DMKSCHCA DPAPUOP F4 LASTUSER MICPMPSA PAGINVAL PRIMEHDR PSALANG RDEVPROC R14 SAVEAREA SAVEAREA SAVEWRK9 SHRTABLE	DMKPGUSP DMKSCHCU DPSTAT F4096 LOCK MP PAGTABLE PRIMEHI PSAPGID RDEVTYPC R15 SAVEREGS SENSE SENSE SIGIPR	DMKPTRAN DMKSCHLI DSPRQ F7 LPUADDR MPUOPER PAIMELO PXA RDEVTYPE R2 SAVER1 SHRFLAG SIGRES	DMKPTRLK DMKSCHTQ FF F8 LPUADDRX NORET PGSRATIO PROCIO RCHBLOK RDIDX R3 SAVER11 SHRFPNT SIGSENSE	DMKPTRSC DMKSCNVS F0 IOBBPNT MCHAREA OPERATOR PMAAVAIL PROCIPL RCHCUTBL R0 R4 SAVEWRK2 SHRNAME SIGSTART	PMAMODE PSA RCHPROC R1 R5 SAVEWRK3 SHRNOPRT SIGXC	DMKPTSAD DMKSTKCP F15 IOBLOK MCHFIX PAGCORE PMASTAT PSACPXBP RCUBLOK R10 R6 SAVEWRK44 SHRPAGE SNASTATS	DMKPTSPW DMKSYSNP F16 IPUADDR MCHLEN1 PAGECUR POFFLINE PSADSPRQ RCUDVTBL R11 R7 SAVEWRK66 SHRSEGCT SPMPFX	DMKPTTFT DMKVMASW F2 IPUADDRX MCHREC PAGELOAD PREFIXA PSAEXT RDEVBLOK R12 R8 SAVEWRK7 SHRSEGNM STACKVM
	SWPCHG1 TRAPDATA TYP3350 XPAGNUM	SWPCYL TRLANCHR TYP3375 ZEROES	SWPFLAG TRLCT TYP3380		SWPTABLE TRQBFPNT VECOPVF		SYSNAME TRQBVAL VGPTR	SYSTEM TTSEGCNT VMBLOK	TIMEDISP TYP2314 VSMPTR	TRACSTRT TYP3330 XCPEND
DMKCPS	DMKIOSHA DMKSCNFD DMKSYSTP IOBHVC IOBSIZE IOERSIZE MONFLAGI NPRTBL RCHCUTBL RCHCUTBL	CPSHRLK C8 DMKDMPRY DMK10SQR DMKVSUCO 10B1MSTK 10BSNSE 1PUADDR MONFLAG3 PMAAVAIL RCUBLOK RDEVDED RDEVSTA5 R15 SAVEREGS	DMKLOKIO DMKSCNRA FO IOBIOER IOBSPEC IPUADDRX MONIOBF PMASTAT RCUCHA RDEVDELP RDEVTYPC R2 SAVER11	DMKLOKSW DMKSCNRU F4 IOBIRA IOBSTAT LOCK MONUSER POWEROFF RCUDVTBL RDEVDISA RDEVTYPE R3 SAVEWRK1 SIGQUI	DFRET DMKDSPCH DMKMNISH DMKSCNVD F4096 IOBLINK IOBUSER LPUADDR MP PREFIXA RCUPRIME RDEVFIOB R0 R4	DMKERMSG DMKPRGMC DMKSCNVN INTREQ IOBLOK IOBVADD LPUADDRX MPUOPER PREFIXB RCUSUB RDEVIMAG R1 R5 SAVEWRK3 SIGSHD	CPSTAT5 DMKCFQRD DMKCFQRD DMKFREE DMKPTRAN DMKSCNVU IOBCAW IOBMISC IOBXTRA L4 NORET PRIORITY RCUTYPE RDEVIOER R10 R6	DMKCPWUN DMKFREEP DMKQCNWT DMKSNTQN IOBCP IOBMISC2 IOERBLOK MONAIOB NPRCNT PROCIPL RDEVAIOB RDEVNRDY R11 R7 SAVEWRK6 SILI	DMKFRET DMKRSFSD DMKSPKDL IOBFATAL IOBRADD IOERETN MONARDB NPRNAME PSA RDEVBLOK RDEVBLOK RDEVQIOB R12 R8 SAVEWRK7 SPMPFX	CPID C1 DMKCVTHB DMKIOESR DMKSCNEP DMKSTKCP IOBFLAG IOBRUNLD IOEREXT MONCOM NPRPNT RCHBLOK RDEVCTRS RDEVREW R13 R9
<b>ДМКС</b> РТ	DMKSCNRN F8 NOADD RCUTYPE RDEVPRDV R1 R5	AFRET CLASDASD DMKCVTBD DMKSCNRU F9 NORET RDEVADD RDEVPTHS R10 R6 SAVEWRK3	DMKCVTBH DMKVFCVV IOBBADCH ON RDEVALT RDEVSTAT R11 R7	DMKCVTHB FFS IOBLOK PSA RDEVBLOK RDEVSTA2 R12 R8	F1 IOBMISC RANGE RDEVCUA RDEVSTA3 R13 R9	DMKFREE F2 IOBSIZE RCHBLOK RDEVCUP RDEVSTA5 R14 SAVEAREA	AQCNWT DMKAPYSD DMKFRET F3 IOBXTRA RCUBLOK RDEVDISA RDEVTYPC R15 SAVEREGS SAVEWRK8	RDEVTYPE R2 SAVER2	BUFCNT DMKCPUVY DMKQCNWT F5 LPUADDR RCUPRIME RDEVPEND RDEVUSER R3 SAVER9 TYP2305	DMKSCNFD F7 MP RCUSUB RDEVPPAG

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DMKCPU	CPUID DMKCFMBK DMKFREEP DMKLOKTR DMKSYSAP F7 L2048 MPFEAT	APUOPER CPAPRINP CPUMCELL DMKCLKCK DMKFRET DMKVMAS1 HCBLOK MCHAREA MPUOPER PMAAVAIL PWTPAGES R3 SAVER11	CPUMODĖL DMKCPMIO DMKIOGAP DMKMHCCP EXTMASK HCMSFCW MCHCPEX MSFBLOK PMAMODE	CP370EON DMKCPOFF DMKLOKDS DMKPTRAN FF HCMSFDB MCHFIX MSFLNG PMASTAT R1 R5 SAVEWRK2 SAVEWRK2 SHRSEGNM	DMKCVTBH DMKLOKFR DMKPTRFR FO IOERETN MCHLEN1 MSFRSP POFFLINE R10 R6 SAVEWRK4	CO DMKDMPCP DMKLOKIO DMKPTRUL F1 IPUADDR MCHREC NORET PREFIXA R11 R7 SAVEWRK8 SHRTABLE	BRING CPSHRLK C1 DMKDSPNP DMKLOKRL DMKPTTFT F16 IPUADDRX MFAMASK ON PREFIXB R12 R8 SAVEWRK9	F255	DMKAPICK DMKFREAP DMKLOKSW DMKSTPC2 F4 LPUADDR MICPMPSA PAGTABLE PROCIPL R14 SAVEAREA SHRFPNT	DMKLOKSY DMKSTPX F4096 LPUADDRX
<b>DMKCPV</b>	DMKQCNWT DMKVCTDA NOTERM	DMKACOFF DMKRNH DMKVCTEN PSA RDEVENAB R0 R4	DMKACOTM DMKSCNAU FFS RCHBLOK	DMKSCNFD F3 RCHCUTBL RDEVLOG R10 R6	DMKCVTBH DMKSCNMU F4 RCUBLOK	DMKCVTHB DMKSCNRA F5 RCUDVTBL RDEVPEND R12 R8	ARIOCT CPEXREGS DMKERMSG DMKSCNRD F8 RDEVBASE RDEVSTAT R13 R9 TYPSDLC	DMKFREEP DMKSCNRN LOCK RDEVBLOK	DMKGRFEN DMKSCNRU MP RDEVDED RDEVTYPC R15	NORET RDEVDISA RDEVTYPE R2
DMKCPW	IOBASNCT IOBLOK IOBSPEC5 IOERSIZE PSAPGID RDCBLKFA RDCLENGC RDEVADD RDEVADL	CPSHUT DMKFREEP DMKSCNRU IOBCAW IOBMISC IOBSTAT LOCK RCHBLOK RDCBLKMA RDCUENGF RDEVALT RDEVDISA RDEVLNKS RDEVOWN RDEVSER RDEVUNF R12 R8 SAVEWRK5 TYP3370	CLASURO CPSTAT4 DMKFRET DMKSCONP IOBCC3 IOBMISC2 IOBTIO LPUADDR RCHDISA RDCBLKMXX RDCPAGAP RDEVASGN RDEVDRAN RDEVPAG RDEVSPL	DMKSPKDL IOBCSW IOBPATHF IOBUASN MP RCHPROC RDCBLKPG RDCPAGCG RDEVBLOK RDEVENAB RDEVENAB RDEVENAB RDEVENAB RDEVENAB RDEVENAB RDEVENAB RDEVSTAT RSPXFLAG R14	DMKIOSQR DMKSSSDE IOBDYNP IOBPGID IOBUNSL MSSPRES RCHSTAT RDCBLOK RDCPAGFA RDEVBPAG RDEVEPLN RDEVPTHS RDEVSTA2 RSPXFMNT R15 SAVEREGS	DE DMKLOKIO DMKSTKCP IOBFATAL IOBPROC IOBUSER NICSIZE RCUBLOK RDCCKD RDCCKD RDCVAGMA RDEVBUZY RDEVEXTN RDEVEXTN RDEVEXTN RDEVSTA4 RSPXFPND R2	DMKSTKIO IOBFUNCT IOBRADD IOBXTRA NORET RCUDISA RDCFBA RDCFBA RDCFCSZ RDEVCRDC RDEVFLAG RDEVFLAG	CPEXRO DMKCVTBH DMKQCNWT DMKTAPRL IOBIOER IOBRESGN IOERBLOK PREFIXA RCUSTAT RDCFLAG RDCSIZE RDEVCTRS RDEVFOFF RDEVMO04 RDEVRRES	F0 IOBIRA IOBSIZE IOERDATA PSA RDCBLKAP RDCFPNT RDCSTART RDEVCUP RDEVFTR RDEVRSVD RDEVSYS R1 R5 SAVEWRK2 TYPFBA	DMKSCNPH IFCC IOBLINK IOBSPEC IOEREXT PSAMSS RDCBLKCG RDCLENG

# EXTERNAL REFERENCES (LABELS AND MODULES)

<b>DMKC</b> PX	ADSPCH CPEXR2 L15 R13 SAVEREGS	AFREEP CPEXR5 L16 R14 SAVEWRK1	MP R15	AP DMKDSPCH PSA R2 SAVEWRK3	APTRLK DMKFREEP RDEVBLOK R3 VMBLOK	ASYSVM DMKPTRLK RO R4	CPEXADD DMKPTRUL R1 R6	CPEXBLOK DMKSTKCP R10 R7	CPEXRO DMKZTDST R11 R8	CPEXR11 LOCK R12 SAVEAREA
DMKCPY	DMKSELVR F4 OPERATOR R14	CORDISA CPXSTOR DMKPTRAN DMKSYSAP F4095 PREFIXA R15 SAVEREGS	DMKSYSRC F4096 PREFIXB R2 SAVER11	APSTAT1 CORFPNT DEFER DMKPTRUL DMKSYSRM F9 PSA R3 SAVEWRK1 TIMEDISP	DMKCVTBH DMKPTTAL DMKSYSSP INTPR R0 R4 SAVEWRK2		DMKDSPNP DMKQCNWT F0 LOCK R10 R6	ASYSVM CPSPMODE DMKDSPN2 DMKSCNAU F1 MP R11 R7 SAVEWRK7 XKEYMODE	DMKERMSG DMKSCNFD F2 MPFEAT R12 R8 SAVEWRK8	DMKSELCP F3 NORET R13 R9
DMKCPZ	DMKDSPCH DMKSCNRD I OBCP I OBS I ZE PROC I PL RCUSUB RDEVCU11	IOBCSW IOBSTAT PSA RCUTYPE RDEVCU2 RDEVRRES R1 R5 SAVEWRK3	DMKFREE DMKSYSOW IOBFLAG IOBUSER RCHBLOK RDEVALLN RDEVDED	DMKFRET FFS IOBIOER IOERBLOK RCHPROC RDEVALT RDEVDISA RDEVSTAT R11 R7	CAW CPSTAT5 DMKIOSQR FLAG IOBIRA IOERSIZE RCUBLOK RDEVBLOK RDEVFLAG	RCUCACH RDEVBPAG RDEVFTR3 RDEVSTA4 R13 R9	F255 IOBLOK LPUADDR RCUDISA RDEVCODE RDEVCODE RDEVOWN RDEVSTA5 R14	RDEVPEND RDEVSYS R15 SAVEREGS	RDEVCUB RDEVPPAG RDEVUSER R2	DMKSCNRA I OBCC3 I OBRADD PREF I XA RCUSTAT RDEVCUP RDEVPRDV RDI DX R3 SAVER8
DMKCQC	ADSPCH CPEXSIZE DMKSTKCP OFF R10 R6 SELDISP	AFREE DMKCVTBH F0 ON R11 R7 SELECT	AFREEP DMKCVTHB F3 02ENTRY R12 R8 SELENTRY	HALF1ENT O2MAXLEN R13 SAVEAREA	HALF1EN1	BLANK DMKFREE HALF1RLN 03SIZE R15 SAVEWRK1 TRAPDATA	PREFIXA R2 SAVEWRK2	CPEXADD DMKFRET HALF1VAL PSA R3 SAVEWRK4 TYPNUMAX		DMKSCNFD NORET R1 R5
DMKCQG	AFREE CLASTERM DMKSCNAU F7 PSA RDEVSER R12 R8 SAVEWRK4 TYPPUN TYP3370 VMBLOK		R14 SAVEAREA SAVEWRK6	BLANK DMKCVTBD DMKSCNRN NICDXSC RDEVATT RDEVSTA5 R15 SAVEREGS SAVEWRK7 TYP2311 TYP3800	DMKSCNVN NICRFLG RDEVBLOK RDEVTYPC R2 SAVER0	DMKSCNVU NICRSPL RDEVBPAG RDEVTYPE R3 SAVER11 SNARBLOK TYP3210	DMKERMSG ERRCODE NICSIZE RDEVNICL RO R4 SAVER2 SNARLUN TYP3310	DMKFREE F1 NICTYPE	R10 R6 SAVEWRK2 TYPBSC TYP3340	DMKQCNWT F3 NOTERM RDEVPS R11 R7 SAVEWRK3 TYPCTCA TYP3350

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MODULE	EXTE	RNAL REFE	RENCES (LA	ABELS AND	MODULES)					
<b>ДМКС</b> QН	AFREE DMKCVTDB DMKRSPCR F8 R15 SAVER2 SFBFLAG4 SFBUHOLD	AFRET DMKERMSG DMKRSPCU NORET R2 SAVEWRK1 SFBINUSE SFBUSER	DMKSCNFD PSA R3 SAVEWRK2	AQCNWT DMKFRET DMKVSFID R0 R4 SAVEWRK4 SFBNOHLD ZEROES	ARSPPR DMKLOCRD DMKVSFNS R1 R5 SAVEWRK6 SFBOFORM	DMKVSFNU R10 R6 SAVEWRK8		BLANKS DMKPTRUL F2 R12 R9 SFBCLAS SFBSHOLD	DMKCQIFR DMKQCNWT F3 R13 SAVEAREA SFBDEST SFBSYSID	DMKCVTBD DMKRSPCP F4 R14 SAVEREGS SFBFLAG SFBUFORM
DMKCQI	AFREE DMKFRET DMKVSFNU R10 R6 SFBACNT SFBFLAG SFBFLAG SFBSVSID SFBSVSID SPCHAR3 TYPPRT	AFRET DMKLOCRD ERRMSG R11 R8 SFBBCONV SFBFLAG2 SFBFLAG2 SFBFIMON SFBTIME SPCMCHR TYPRDR	F1 R12 R9 SFBCLAS SFBFLAG3	NORET R13 SAVEAREA SFBCONV SFBFLAG4 SFBOFORM SFBTYPE SPCOPYFG	SFBORIG	BLANKS DMKPTRLK PSA R15 SAVEWRK1 SFBDATE SFBFNAME SFBPNT SFBUHOLD SPFLAG1	RDEVBLOK R2 SAVEWRK2 SFBDEST SFBFTYPE SFBPURGD	DMKCVTBD DMKQCNWT RDEVSNA R3 SAVEWRK4 SFBDIST SFBINUSE SFBINUSE SFBINUSE SFBIRECNO SPCHAR SPLINK	DMKERMSG DMKRPAGT R0 R4 SAVEWRK6 SFBDUMP SFBLDBEG SFBSH0LG SPCHAR1 SPSPLNKC	SFBSTART SPCHAR2
DMKCQP	BLANKS DMKCQQPT DMKRIORN DMKSYSRV I PUADDR RANGE RC40	DMKSCNEP DMKURSTA I PUADDRX RCHBLOK RDEVADD RDEVDRAN RDEVOWN	DMKCQTSN DMKSCNFD ERRMSG LPUADDR RCHCUTBL RDEVATT	DMKSCNMU FFS L8 RCHPROC RDEVAUTO RDEVFLAG RDEVPRDV	APUOPER CLASSPEC DMKCVTBH DMKSCNRA FLAGS MPGEND RCUBLOK RDEVBLOK RDEVFTR RDEVPS R0 R4 SAVEWRK1 TYP3330	DMKERMSG DMKSCNRN F1 MPUOPER RCUCHA RDEVBPAG RDEVLCEP RDEVSER R1 R5	DMKFREE DMKSCNRU F2 NORET RCUDVTBL	ARIOCU CLASURI DMKFRET DMKSCNVU F3 PROCIO RCUPRIME RDEVCUP RDEVLNKS RDEVSTAT R11 R7 SAVEWRK4 URSFILE	ASYSVM CLASURO DMKQCNWT DMKSYSRC F6 PROCIPL RCUSUB RDEVDED RDEVMOUT RDEVSTA3 R12 R8 SAVEWRK7 URSPATH	AVMREAL DMKAPYSD DMKRIOCT DMKSYSRM F8 PSA RCUTYPE RDEVDISA RDEVNCP RDEVSTA5 R13 R9 SAVEWRK8 URSSTACK
DMKCQQ	AFREE DMKAPYSD DMKSCNEP F2 PSA RCUSUB RDEVFLAG RDEVTYPC R2 SAVERO SAVEWRK6	AFRET DMKCQPSC DMKSCNFD F3 RANGE RCUTYPE RDEVOWN RDEVTYPE R3 SAVER1 SAVEWRK7	DMKSCNPH F4 RCHBLOK RC40 RDEVPPAG R0 R4 SAVER11	AQCNWT DMKCVTBH DMKSCNRA F8 RCHPROC RDEVADD RDEVPRDV R1 R5 SAVER2 SNARBLOK	BLANK DMKCVTHB DMKSCNRD I PUADDR RCUBLOK RDEVAOF RDEVPS R10 R6 SAVER9 SNARLUN	BLANKS DMKERMSG DMKSCNRU I PUADDRX RCUCHA RDEVBLOK RDEVPTHS R11 R7 SAVEWRK1 TYPBSC	DMKSCNVD LPUADDR RCUCHCNT RDEVBOF RDEVSNRB R12 R8	DMKFRET DMKSCNVU MPGEND RCUDISA RDEVCUA RDEVSTA3 R13 R9	MPUOPER	DMKSCNAU F1 NORET RCUSTAT RDEVCUP RDEVSYS R15 SAVEREGS
DMKCQR	AFREE DMKDMPAL DMKRSPPU NORET R13 R9	DMKSCHPG PRIORITY R14	AQCNWT DMKDMPSW DMKSCNAU PSA R15 SAVEREGS	DMKSCNFD RDEVBLOK R2	BLANKS DMKFREE DMKSCNMU RDEVTYPC R3 SAVEWRK2	RO R4	DMKQCNWT	CLASTAPE DMKRIOPR F1 R10 R6 SFBSHOLD	DMKCVTBD DMKRSPHQ F2 R11 R7 SHQBLOK	DMKCVTBH DMKRSPPR F8 R12 R8 SHQSHOLD

MODULE	EXTE	RNAL REFE	RENCES (LA	ABELS AND	MODULES)					
	SHQUSER	TYPPRT	TYPPUN	VMBLOK						
DMKCQS	DMKPGTPB	DMKPGTPI DMKSYSTV RDEVADD R10 R6	DMKDIDEP DMKPGTSB	DMKPGTSI F1	DMKPTRAN F4	F5 RDEVSTA3 R14	DMKPTSMW F60	DEFER DMKHVCDG DMKQCNWT LOCK RDEVTYPE R2	NORET RDEVUSER R3	DMKCFCTB DMKPGTCP DMKSCNFD PREFIXA
<b>ДМКС</b> QТ	DMKSCNRD IOBCC3 IOBSPEC5 LANGBLOK NICSIZE RDEVMAX R10 R6	IOBCSW IOBSTAT LANGLANG NICSTAT RDEVNICL R11 R7 SAVEWRK7	DMKERMSG EQCHK IOBFATAL IOBUSER	FLAGS IOBIOER IOERBLOK NICDISA NORET RDEVPEND R13 R9 SNARBLOK	DMKFRET F1 IOBIRA IOERCSW NICDXSC PREFIXA RDEVSNRB R14	R15 SAVEREGS SNARFG2	F8 IOBMISC2 IOEREXT NICFLAG RANGE RDEVTYPC R2	CCC DFRET DMKRIORN IFCC IOBQSTAT IOERLEN NICNAME RDEVBLOK RDEVUSER R3 SAVEWRK1 SNARNXT VMBLOK	IOERPNT NICRDED RDEVDED	DMKSCNFD IOBCAW IOBSIZE IOERSIZE NICRFLG RDEVDISA R1 R5
DMKCQU	ACNTDATA CPMICON F255 NORET RDEVSCRL R0 R5 SNARTTY VMBLOK	CPSTAT2 LPUADDR PREFIXB	AFRET DFRET NICAPL PSA RDEVSNRB R11 R7 TYPTTY	APSTAT1 DMKCVTBD NICATOF RDEVAPLP RDEVTAPL R12 R8 VCONBRK	NICBLOK RDEVASTB	RDEVTFLG R14 SAVEAREA	NICLLEN RDEVAVM2 RDEVTMCD R15	NICSIZE RDEVBLOK RDEVTYPC R2 SAVEWRK8	CLASSPEC DMKSCNAU NICTEXT RDEVLLEN RDEVTYPE R3 SNARBLOK VCONSCRN	NICTMCD RDEVNICL RDEV3101 R4 SNARFG2
DMKCQY	DMKSCNAU DMKSYSOC IOKEEP	C1 DMKERMSG DMKSCNFD DMKSYSOW I PUADDR OWNDRDEV PFKRET RDEVBLOK R11 R7 SAVEWRK4	DMKSCNMU DMKSYSTI I PUADDRX	DFRET DMKFRET DMKSCNRD FO LOCK PFDATA PFTAB RDEVPS R13 R9	DMKPGUVG DMKSNTBL F1 LOGMBLOK PFDCMD PFTABS RDEVSNRB R14 SAVDATE	DMKCPEID DMKPGUVR DMKSYSDW F2	DMKPTRAN DMKSYSLG F4 LOGMNXT PFKADDR PREFIXB RDEVTYPC R2	DMKCVTBD DMKPTRUL DMKSYSLU F4096 LOGMTXT PFKCOUNT PROCIPL RDEVTYPE R3	ASYSVM CPSTAT3 DMKCVTBH DMKQCNWT DMKSYSND F8 NORET PFKFLAG PSA R0 R4 SAVEWRK1 SAVTABLE	DMKRPAGT DMKSYSNM HEADER ON PFKIMM PSASYSID R1 R5 SAVEWRK2
DMKCRM	ADSPCH DMKIDRIN IXSIZE R13	AFREE DMKIUACP LOCK R14	AFRET DMKPTRLK MP R15	AP DMKPTRUL PMSGLIM R2	APTRLK FFS PSA R3	ASYSVM IXBLOK RO R4	DMKDSPCH IXIRA R1 R5	DMKFREE IXRO R10 R6	DMKFRET  XR1 R11 R7	DMKIDRFX IXR11 R12 R8

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MODULE	EXTERNAL	REFERENCES	(LABELS	AND MODU	JLES)
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	R9 SSCBADDR		SAVEREGS SSC I XADD	SEVER SSCIXLST	SRTBLOK SSCRTADD	SRTFLAG SSCRTCNT	SRTGIND SSCTSFPT	SRTIPND SSCTSFVM	SRTNEXT VMBLOK	SRTRVPND
DMKCSB	DMKSCNVU IMGFCBF1 IOBFLAG IOBSTAT LOADPAG2 PSA RDEVTYPC R10 R6 SAVEWRK5 TYP3289E	R11 R7 SAVEWRK6 TYP4245	F1 IMGFCBTI IOBLINK LOADFLG1 LOADSIZE RDEVDED RDEVUSER R12 R8 SAVEWRK7 TYP4248	RSPXBLOK R13 R9 SAVEWRK8	LOADSZDW RDEVDRAN RSPXFCB R14 SAVEAREA SILI UCSBFOLD	DMKPTRUL F4 IMGHDRSD IOBMISC2 LOADIMAG LOADUCS RDEVEXTN RSPXFLAG R15 SAVEREGS SKIP UCSBNAME	DMKQCNWT F8 IMGNAME IOBRADD LOADIML1 LOAD2CCW RDEVFLAG RSPXFOLD R2	IOBCAW IOBRES LOADIML2 LOCK RDEVFTR RSPXINDX R3 SAVEWRK1 TYP3203	DMKSCNRD IMGCCWDS IOBCSW IOBRSTRT LOADNAME MP RDEVSPL	BUFFER DMKCVTHB DMKSCNRU IMGCCWLN IOBFATAL IOBSIZE LOADPAG1 NORET RDEVSTAT R1 R5 SAVEWRK4 TYP3262 UCSCCWOB UCSREGS
DMKCSC	AFREE DEFER DMKUCSLD IMGNEXT LOADNAME PSA R15 SILI UCSCCW07	AFRET DMKFCBLD FO IOBLOK LOADPAG1 RDEVBLOK R2 SKIP UCSRDCC1	AP DMKFREE F1 IOERETN LOADPAG2 RDEVTYPE R4 SYSTEM VMBLOK	APTRAN DMKFRET F4095 IOKEEP LOADPARM RO R5 TYP3203 XPAGNUM	APTRLK DMKPIALD F4096 LOADFLG1 LOADSPAC R1 R6 TYP3211 ZEROES	IMGBUFLN	I MGHDR LOADHEAD		IMGHDRSZ LOADIML1	C1 DMKUCCLD IMGNAME LOADIML2 MP R14 SAVER1 UCSCCWS
DMKCSF	AFREE DMKAPYSD F1 IOBLINK PROCIO RDEVSPL RSPSFLOK R14 SAVEWRK1 SFBLDMID	R15 SAVEWRK2	F255 IOBMISC RDEVBACK RDEVTERM RSPXFLAG R2 SAVEWRK4	AQCNWT DMKERMSG F3 IOBRADD RDEVBLOK RDEVTYPC RSPXSFIL R3 SAVEWRK6 SFBRECOK	RDEVTYPE RO R4 SAVEWRK7		I OBCP LOCK RDEVEXTN	CLASURO DMKSCNFD IOBCSW MP RDEVFLAG RSPLCTL R11 R9 SFBFLAG VMBLOK	IOBFLAG MSGADDR RDEVRSTR RSPMISC R12	DE DMKSTKIO IOBIRA NORET RDEVSPAC RSPSFBLK R13 SAVEREGS SFBLDBEG
DMKCSO	RDEVFSEP RDEVSCHD RDEVUSER	DMKSCNRU F4096 IOBMISC MSGADDR RDEVBZCH RDEVIMAG RDEVSEP RDEVXSEP RSPXBLOK	F6 IOBRADD NORET RDEVCFCB RDEVIOER RDEVSPAC RDIDX	DMKSPKDL F8 IOBSIZE NPRCNT RDEVCLAS RDEVLDBG	RDEVLDMD RDEVSTAT RSPBF210	C1 DMKFRET DMKURSTA IOBCSW IOBUSER NPRPNT RDEVDFCB RDEVLOAD	F0 IOBFATAL IOERETN NPRTBL RDEVDISA RDEVNRDY RDEVSTA4 RSPFLAG2	IOKEEP PSA RDEVDRAN RDEVOVLY RDEVTERM	F2 IOBIRA LOCK RDEVAIOB RDEVEXTN RDEVPRFG RDEVTYPC RSPSFBLK	RSPSFLOK

MODULE	EXTER	RNAL REFE	RENCES (LA	ABELS AND	MODULES)					. ·
	R3 SAVEWRK2 SYSTEM URSUSER	R4 SAVEWRK3 TYPPRT VMBLOK	R5 SAVEWRK4 TYPPUN XPAGNUM	R6 SAVEWRK5 TYP3211 ZEROES	R7 SAVEWRK6 TYP3800	R8 SAVEWRK7 TYP38003	R9 SAVEWRK8 TYP38008	SFBFLAG2	SAVEREGS SFBLOK URSOPER	SAVEWRK1 SFBREQUE URSSTART
DMKCSP	ACICLR1 ACITUSR CLASURO DMKSCNFD PSA R4 SAVEWRK9	ACICODE ACIUDIR CLEAR DMKUDRFU R0 R8 TYPRDR		F2 R11	ACIPARMS AP DMKCVTDB F255 R12 SAVEREGS	BLANK DMKERMSG F3 B13	F4 R14	ACISIZE BUFFER DMKFRET F8 R15 SAVEWRK5	ACISPOOL CLASTERM DMKRPIRA LOCK R2 SAVEWRK7	CLASURI DMKSCNAU MP R3
DMKCSQ	DMKVSUCO MP	DMKVSUCR PSA	DMKERMSG FFS R0 R4 SAVEWRK4 SFBFLAG	F1 R1	ARSPPU CLEAR DMKFRET F2 R10 R6 SAVEWRK6 SFBFNAME SHQBSIZE TYP3210	F3 R11	BLANK DMKAPZNO DMKRSPSC F4096 R12 R8 SAVEWRK8 SFBINUSE SHQCKPT VSPLCTL	DMKSCNFD F7 R13	DMKSCNVU F8 R14 SAVEAREA SFBCLAS SFBNOHLD SHQPNT	SFBDEST
DMKCSR	DMKSYSCO F2 MP R2 SAVER11	AFRET DMKFREEP DMKSYSPR F3 PSA R3 SAVER2 SFBUHOLD	IOBCSW RO R4 SAVER6	APTRLK DMKLOCRD DMKVIOIN IOBIRA R1 R5 SAVER9 TYPPUN	BLANK DMKLOCRQ DMKVSFNU IOBLINK R10 R6 SAVEWRK2 TYPRDR	DMKVSUCO IOBLOK R11 R7	DMKPTRUL	FORMBLOK IOBUSER R13 R9	DE DMKSCNVU FORMUSER IOBVADD R14 SAVEAREA SFBINUSE VSPSFBLK	F1 LOCK R15 SAVEREGS SFBLOK
DMKCST	DMKSCNVU PSA R3 SAVEWRK2	DMKVSFID RO R4	DMKPTRLK FFS R1 R5 SAVEWRK4	DMKPTRUL F1 R10 B6	BLANKS	DMKRPAPT F3 R12 R8 SAVEWRK7	DMKRPIRA F4 R13 R9	ACITAG BUFFER DMKFRET DMKSCNFD F8 R14 SAVEAREA SAVEWRK9 SKIP	DMKSCNVD LOCK R15 SAVEREGS	AFRET CLASTERM DMKLOCRQ DMKSCNVN MP R2 SAVEWRK1 SFBFLAG TYPPRT
DMKCSU	AFREE CLEAR DMKUDRFU F7 PLSIZE R3 SAVEWRK1 VMBLOK	F8 PSA R4	F1 LOCK R0 R5	F2 MP R1 R6	ARSPPU DMKERMSG F24 PLFLAG1 R11 R7 SAVEWRK6	F255 PLFRET R12 R8	BLANK DMKFRET F3 PLIMSG R13 R9 SFBLOK	F4 PLIST R14	BUFFER DMKQCNPL F5 PLNORET R15 SAVEREGS TYPPRT	BUFNXT DMKSCNFD F6 PLR2 R2 SAVER11 TYPPUN
DMKCSV	AFREE BUFNXT	AFRET CHGSFB	AP CLEAR	ARSPPR COUNT	ARSPPU DMKAPWPG	ARSPRD DMKCKSPL	BLANK DMKCVTBD	BLANKS DMKCVTDB	BR I NG DMKERMSG	BUFFER DMKFREE

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MODULE	EXTE	RNAL REFE	RENCES (L	ABELS AND	MODULES)					
	DMKFRET DMKSCNFD F3 PLFLAG1 R11 R7 SAVEWRK8 SFBINVS SFBUSER VMBLOK		DMKLOCRQ DMKUDRFU F6 PLIMSG R13 R9 SFBBCONV SFBBCONV SFBLOK SFBXABL	DMKVSDAD F8 PLIST R14 SAVEAREA	DMKVSDDL LOCK PLR2 R15 SAVEREGS SFBCONV SFBPNT	DMKVSFNS MP PLSIZE R2	DMKVSFNU NORET PSA R3 SAVEWRK2 SFBFILID SFBSIZE	FFS NOTRESP RO R4 SAVEWRK4 SFBFLAG	F1 OFF R1 R5 SAVEWRK5	F2 OWNID R10 R6 SAVEWRK6 SFBINUSE
DMKCSW	DMKSTKIO	DMKPGTSG DMKSYSFL FORMUSER IOBUSER R13 R9 SAVEWRK5 SFBFILID	DMKVIOIN FO IOBVADD R14 SAVEAREA SAVEWRK6	DMKPGUVR DMKVSFNS F1 LOCK R15 SAVEREGS SAVEWRK7 SFBFLAG4 SFBSIZE SPCHAR1	SAVEWRK8 SFBFLASH SFBSTART SPCHAR2 SPSPLNKC	DMKPTRUL FFS F8 PSA R3 SAVER3 SFBBCONV SFBFNAME SFBSYSID SPCHAR3	DMKRPAGT FLAG IOBCSW R0 R4 SAVER6 SFBCLAS SFBINUSE	FORMBLOK 10B1RA R1 R5 SAVER7 SFBCONV SFBLAST	DMKSCNAU FORMNTRY IOBLINK R10 R6	FORMOPER IOBLOK R11 R7 SAVEWRK2 SFBDEST SFBDFORM SFBUSER SPFCB
DMKCSX	DMKQCNPL F3 IOBVADD PLIST R14	ACICODE ACIUSRID BLANK DMKCVTBD DMKRPIRA F4 LOCK PLR2 R15 SAVEREGS SFBLOK	BLANKS DMKCVTDB DMKSCNAU F6 MP PLSIZE R2	F8 NORET PSA R3 SAVEWRK1	DMKSTKIO IOBCSW NOTRESP RO R4	AP CLASURI DMKFREEP DMKUDRFU IOBIRA OFF R1 R5	ACIRUSR APSTAT1 CLEAR DMKFRET DMKVI0IN IOBLINK OWNID R10 R6 SAVEWRK5 TYPPRT	IOBLOK PLFLAG1 R1∙1 R7		ARSPPU DMKCSOSD DMKLOKSW F2 IOBUSER PLIMSG R13 R9
DMKCSY		AFRET DMKCKTSD DMKPTRUL DMKVSGRI	DMKRPAGT	ARSPPR DMKFREE DMKRPAPT F1	ARSPRD DMKFRET DMKRSPHQ LOCK	BLANK DMKLOCRD DMKVSDAD PCHCHN	BRING DMKLOCRQ DMKVSDDL PRTCHN	CHGSFB DMKPGTSG DMKVSETR PSA	COUNT DMKPGUVG DMKVSFNS R0	DMKAPWPG DMKPGUVR DMKVSFNU R1

DMKCSY	AFREE DMKCKSPL DMKPTRLK DMKVSGAI R10 R6 SFBCONV SFBLAST SFBSIZE SFBXABL TYPPRT		APTRLK DMKCKTSU DMKRPAGT F0 R12 R8 SFBDUMP SFBMON SFBSTART SHQBLOK TYPRDR	ARSPPR DMKFREE DMKRPAPT F1 R13 R9 SFBFILID SFBNORET SFBSYSID SHQPNT YMBLOK	ARSPRD DMKFRET DMKRSPHQ LOCK R14 SAVEAREA SFBFLAG SFBOFORM SFBTUSE SHQSHOLD ZEROES	SFBFLAG2 SFBORIG SFBTYPE	BRING DMKLOCRQ DMKVSDDL PRTCHN R2 SAVER13 SFBFLAG3 SFBPNT SFBUFORM SFBUFORM SPLINK	CHGSFB DMKPGTSG DMKVSETR PSA R3 SAVER2 SFBFLAG4 SFBPURGD SFBUHOLD SFBUHOLD	DMKVSFNS RO R4 SFBBCONV SFBINUSE SFBSEEN	DMKVSFNU R1 R5 SFBCLAS SFBLNVS SFBSHOLD SFBXAB
DMKCVT	BALRSAVE PSA	BALR1 RO	BALR2 R1	CPID R14	DATE R15	F1 R2	F10 R3	F240 TEMPSAVE	F60 TODATE	LOCK
DMKCVU	F1	F240	F4	LOCK	PSA	RO	R1	R10	R11	R12

MODULE	EXTER	RNAL REFER	RENCES (LA	ABELS AND	MODULES)					
	R13	R2	R5	R6	R7	R8	R9	SAVEAREA	SAVEREGS	TEMPSAVE
DMKDAD	F16 IOBCAW IOBRADD IOERACT IOERDATA IOERIGNR IOERREAD L4 PSA RDEVTYPE R3 SAVER11 SEGINV	ASYSVM CPEXADD DMKCVTBH DMKPTTC3 F2 IOBCP IOBRCAW IOERADR IOERCAW IOERADR IOERSIZE L8 RCWCOMND PEFVELAC	DMKDSPCH DMKQCNWT F3 IOBCSW IOBRCNT IOERBLOK IOERIND4 IOERSNSZ NORET RCWHEAD RDEVFTR R1 R5 SAVEWRK2 SILI ZEROES	DMKERMSG DMKSTKCP F4095 IOBREL IOERCAN IOERDW IOERINFO IOERSTAT NOTUSED RCWPNT RDEVIOER R10 R6 SAVEWRK3 SKIP	CPEXRO DMKFREE EQCHK F4096 IOBFATAL IOBRSTRT IOERECF IOERLOC IOERSTRT OPERATOR RCWRCNT RDEVNRDY R11 R7 SAVEWRK4 TEMPSAVE	IOERMSG IOERVOL1 PAGCORE RCWTASK RDEVOWN R12 R8 SAVEWRK5 TRANMODE	CPEXR5 DMKFRET FLAG IDA IOBIOER IOERCEMD IOERFLG1 IOERFLG1 IOERVSER PAGINVAL RDEVAIOB RDEVRRES R13 R9 SAVEWRK6 TYP3375	CPEXSIZE DMKMSWR FTRRSRL IFCC IOBLOK IOBSPEC2 IOERCPEX IOERFLG2 IOERFLG2 IOERNUM LOCK PCI RDEVALLN RDEVSER R14 SAVEAREA SAVEWRK7 TYP3380	C1 DMKPTSAE F1 IL IOBMISC IOBSTAT IOERCSW IOERFLG3 IOERPEND L1 PRGC RDEVBLOK RDEVSTAT R15 SAVEREGS SAVEWRK8 UC	F10 INTREQ IOBPAG IOBUNSL IOERDASD IOERHA IOERPNT L2 PRTC RDEVCUB RDEVCUB
DMKDAS	DMKDSPCH DMKPTTC1 FTRRSRL F4096 IOBFATAL IOBRSTRT IOERCAL IOERDW IOERIND4 IOERREAD L24 PRIORITY RDEVDISA RDEVSTA4 R14 SAVEAREA	FTR35MB F8 IOBFLAG IOBSIZE IOERCAN IOERECF IOERINFO IOERSIZE L3 PRTC RDEVFLAG RDEVSTA5 R15 SAVEREGS	DMKFREE DMKFTTC3 FTR70MB IDA IOBIOER IOBSPEC IOERCCRA IOEREXT IOERLOC IOERSNSZ L4 PSA RDEVFTR RDFVSYS	DMKFREEP DMKQCNWT F1 IFCC IOBLOK IOBSPEC2 IOERCCRL IOERFLG1 IOERMSG IOERSTAT NORET RCWCOMND RDEVIOER RDEVTYPE	DMKFRET DMKSTKCP F10 IL IOBSTAT IOERCEMD IOERFLG2 IOERMSW IOERSTRT NOTUSED RDEVAIOB RDEVMOUT R0	ALOCBLOK CD CPEXSIZE DMKIOEX DMKTRKIN F15 IOBALTSK IOBPAG IOBUNSL IOERCPEX IOERFLG3 IOERVOL1 OPERATOR RDEVALLN RDEVNRDY R1 R5 SAVEWRK2 SILI VMBLOK	DMKIOERR FFS F16 IOBCAW IOBRADD IOERACT IOERCSW IOERFLG4 IOERVSER PAGCORE RDEVBLOK RDEVOWN R10 R6 SAVEWRK3 SKIP	DMKIDEST FLAG F2 IOBCP IOBRCAW IOERADR IOERDASD IOERHA IOERPNT LOCK PAGINVAL RDEVCUB RDEVCUB RDEVRES R11	DFRET DMKMSWR FTREXTSN F256 IOBCSW IOBRCNT IOERALTR IOERIGNR IOERQUE L1 PCI RDEVCUP RDEVSER R12 R8 SAVEWRK5 TRANMODE	IOERIND3 IOERRDRO L2 PRGC RDEVDED RDEVSTAT R13
DMKDAU	DMKFREEP FTRRSRL I OBCAW	F0 IOBCP IOBRSTRT IOERCCRL	DMKMSWR F1 IOBCSW IOBSIZE IOERCEMD IOERFLG1	F10 IOBERP IOBSPEC IOERCPEX IOERFLG2	F16 IOBFATAL IOBSTAT IOERCSW IOERFLG3	ALOCBLOK CCC C1 DMKPTTC2 F4095 IOBFLAG IOBUNSL IOERDASD IOERIGNR IOERREAD	F4096 IOBIOER IOERACT IOERDATA IOERIND3	CDC DMKCVTBH DMKQCNWT F8 IOBLOK IOERADR IOERDEC IOERIND4	CL DMKDSPCH DMKSTKCP IDA IOBRADD IOERBLOK IOERDW IOERINFO	FFS IFCC IOBRCAW IOERCAN IOERECF IOERLOC

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		I OERVSER PSA RDEVFLAG RDEVSTA4 R14 SAVEAREA SEGPAGE	RDCBLKFA	RDEVTYPE R2	RDEVMOUT	OPERATOR RDEVAIOB RDEVNRDY R0 R4 SAVEWRK1 TYP3370	RDEVALLN	PAGINVAL RDEVBLOK RDEVRDC R10 R6 SAVEWRK3 VMBLOK	RDEVCUB	PROCIO RDEVDED RDEVSER R12 R8 SAVEWRK9 XRIGHT16
DMKDDC	RO R4	R1 R5	R10 R6	R11 R7	R12 R8	R13 R9	R14	R15	R2	R3
DMKDDR	ATTN CE C2 D3 L10 R0 R4 SKIP TYP2420 TYP3430	BALRSAVE CLASDASD DATACHK D5 L16 R1 R5 SM TYP3330 TYP3480	BLANKS CLASFBA DE EQCHK L2 R10 R6 TIMER TYP3340 TYP8809	BUSOUT CLASTAPE DMKDDC ERRMSG L4 R11 R7 TYPFBA TYPFBA TYP3350 UC	BUSY CLASTERM DMKDDT HEADER L5 R12 R8 TYP2305 TYP3375 UE	CARDIN CMDREJ DMKDNC IFCC L7 R13 R9 TYP2311 TYP3380 WAIT	CAW CONTINUE DMKDNT INPUT L8 R14 SAVERET TYP2314 TYP3410	CC CPCLOSE DUMP INTREQ L9 R15 SCAN TYP2319 TYP3411	CCC CSW D1 LINECT MODESET R2 SENSE TYP2401 TYP3420	CD CUE D2 L1 READ R3 SILI TYP2415 TYP3422
DMKDEF	DMKSCNVD F3 NICVDEVB RDEVATT R13 R9	DMKSCNVN F4 PLFLAG1 RDEVBLOK R14 SAVEAREA SAVEWRK9 TYP1403 TYP3211 TYP3380	DMKSCNVU F5 PLFRET	CDDEF CONTINUE DMKFRET DMKVCBRS F8 PLIMSG RDEVNICL R2 SAVER2 TIMER TYP2305 TYP3277 TYP3525 X40FFS	LINE PLIST	DMKLÓCKQ FFS LOCK PLNORET R0 R4 SAVEWRK2 TYPFBA TYP2319	DMKCVTBH DMKNEARV FTR3088 L3 PLR2 R1 R5 SAVEWRK3 TYPIBM1 TYP2501 TYP3330	DMKCVTDB DMKPMACD FTR4WCGM L7 PLSIZE R10 R6 SAVEWRK4 TYPPRT	NICBLOK PSA R11 R7 SAVEWRK6 TYPTELE2	DMKDEGIN
DMKDEG	DMKUDRRV NEWPAGES R1 R5	NEWSEGS R10 R6	ASOFF DMKERMSG FTR4WCGM PLFLAG1 R11 R7 SAVEWRK5	DELPAGES DMKFREE F15 PLIMSG R12 SAVEAREA SAVEWRK6	DMKFRET F16 PLIST R13	DMKBLDRL DMKPGSPO F2 PLNORET R14 SAVERO SAVEWRK8		DMKCFPRR DMKSCNFD F5 PLSIZE R2 SAVER2 TYP38003	DMKCFSAS DMKUDRFU F8 PSA R3 SAVEWRK1 VMBLOK	DMKCVTBD DMKUDRMD LOCK R0 R4 SAVEWRK2 VMS1ZE
DMKDE I	LOCK PSA RDEVTYPC R3	R4	LPUADDR RDEVBLOK R1 R5	APSTAT1 DMKFRET L3 RDEVDED R10 R6 SAVEWRK6	L4 RDEVDISA R11 R8	AQCNWT DMKQCNWT L7 RDEVFLAG R12 SAVEAREA SAVEAREA SAVEWRK8	R13 SAVEREGS	BLANKS DMKSCNRU L9 RDEVLNKS R14 SAVER2 VIRTUAL	DMKSSSDE MSSPRES	DMKCVTBH F8 NORET RDEVSTAT R2 SAVEWRK2

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### EXTERNAL REFERENCES (LABELS AND MODULES)

PIC	DOLL					noboleo,					
D₩	IKDEX	AFREE LOCK RCWPNT R14 SAVEAREA	AFRET MP RCWRCNT R15 SAVEREGS	AP PSA RCWREL R2 SAVEWRK1	CC RCWADDR RCWTASK R3 SAVEWRK2	DMKFREE RCWCCNT R0 R4 SAVEWRK3	DMKFRET RCWCCW R1 R5 SAVEWRK6	IOBCAW RCWCOMND R10 R6 SAVEWRK8	IOBLOK RCWCTL R11 R7 SAVEWRK9	IOBSENSE RCWFLAG R12 R8 SILI	IOBSPEC3 RCWGEN R13 R9 VMBLOK
DM	IKDGD	DMKDSPCH DMKPTRAN DMKSTKDE F4096 IOBLOK L1 PSA RDEVBLOK R0 R4	APTRLK CORFLAG CPEXREGS DMKDSPRU DMKPTRFR DMKSYSCS F5 IOBMISC L16 PSAMSS RDEVCMDK R1 R5 SAVEWRK4 TYP3330	CPEXRO DMKFREE DMKFTRLE DMKVMASH F6 IOBMISC2 L2 RCWADDR RDEVCUP R10 R6	DMKPTRLK FFS F8	CORSHARE CPSHRLK DMKFREH1 DMKPTRUL F1 IDA IOERBLOK L4 RCWCNT RDEVFTR R12 R8	CPSTAT2 DMKFRET DMKPTTFT F15 IOBCAW	CPSTAT4 DMKIOSQV DMKPTTPM F16 IOBCYL IOERSIZE L6 RCWCTL RDEVMDL R14	C1 DMKLOKDF DMKSCNVU F3 IOBFLAG LOCK MP RCWFLAG	F4 IOBHVC LOCKSAV MSSPRES RCWIO RDEVSTA6 R2	DMKDGFIN
DM	IKDGF	ACORETBL CPEXADD DMKFRET IOBCC1 IOBSIZE L16 RDEVMDL R3	CPEXBLOK	AFREE CPEXREGS DMKLOKDF IOBCSW IOERBLOK L6 R1 R5	AFRET CSW DMKPSACC IOBFATAL IOERDATA PSA R10 R6	IOBFLAG	APSTAT1 DEFER DMKVMASH IOBHVC IOERPNT RCWCCW R12 R9	FFS IOBIOER IOERSIZE	F16 IOBLINK LOCK	BRING DMKDSPRU F8 IOBLOK LOCKSAV RDEVECKD R15 ZEROES	CC DMKFREE IOBCAW IOBMISC LPUADDR RDEVLOW R2
DM	IKD I A	DMKIOSHA DMKSCNRU IOBIRA LOCK NICLINE RDEVEPDV RDEVTYPE R2 SAVER11 SAVEWRK9 TYPIRM1	AFREE CLASTERM DMKLOKIO DMKSCNVD IOBLINK LOCKSAV NICSIZE RDEVHT RDEVUSER R3 SAVER2 SNARBLOK TYPTELE2 VCONREMF	DMKQCNWT DMKSCNVU IOBLOK LPUADDR NICSTAT RDEVNICL R0 R4 SAVEWRK1 SNARDIPG TYP3210	DMKQCOCL FFS IOBMISC NICBLOK NICSWEP RDEVPEND R1 R5 SAVEWRK2 SNARFG3 TYP3277	DMKQCOSY F240 IOBRADD NICCIBM NICCIBM NICTYPE RDEVPS R10 R6 SAVEWRK3 SNARLUN TYP3278	DMKSCHRT F255 IOBSIZE NICDTYPE PSA RDEVSNA R11 R7	DMKSCNAU F3 IOBUSER NICD3275 RDEVAIOB RDEVSNRB R12 R8 SAVEWRK5 SNARVMB	DMKSCNFD IOBCP IOERBLOK NICD3277 RDEVBLOK RDEVSTAT R13 SAVEAREA	NICD3278 RDEVCORD RDEVSTA3 R14 SAVEREGS SAVEWRK7 TRQBSIZE	DMKFRET DMKSCNRN IOBIOER IOERSIZE NICEPAD RDEVDIIP RDEVTYPC R15 SAVERO SAVEWRK8
DM	IKD I B	DMKCVTHB DMKPTRLK	CPEXSIZE DMKDSPCH DMKPTRUL	DMKERMSG	DMKQCOSY	ALOKSP CE DFRET DMKFREEP DMKRIORN DMKSTKCP F3	DMKACODV DMKFRET DMKRNHND	DMKBLDVM DMKHPTDI DMKSCHRT	DMKLOKIO	DMKCVTBD DMKLOKSW	DMKPGTMV DMKSCNRD

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MODULE	EXTERNAL REF	ERENCES (LABELS A	D MODULES)						
	RDEVDED RDEVDIS RDEVNRDY RDEVPRE RDEVTMAT RDEVTYF R14 R15 SAVEAREA SAVEREC SAVEWRK5 SAVEWRF	NICDMSG NICDXSG NICSWEP NICTMA RCUBLOK RCUDVTI V RDEVADD RDEVAT A RDEVDISB RDEVEPI P RDEVPS RDEVRCY C RDEVTYPE RUNUSEI R2 R3 S SAVERO SAVERI 6 SAVEWRK7 SAVEWRI P TRQBFPNT TRQBLOI	A IOERSIZE NICENAB NICUSER L RCWADDR RDEVBASE V RDEVEPMD Y RDEVRUN R0 R4 SAVER2 8 SAVEWRK9	NICEPMD NICVDEVB RCWCCW RDEVBLOK RDEVFLAG RDEVSNA R1 R5 SAVER8 SILI TYPBSC	NICFLAG NORET RCWCNT RDEVCORD RDEVHIO RDEVSNRB R10 R6 SAVEWRK1 SKIP TYPCTCA	RDEVLCEP RDEVSTAT R11 R7	LOGHOLD NICRFLG PRGC RCWCTL RDEVCUA RDEVLNCP RDEVSTA3 R12 R8 SAVEWRK3	IOBUSER LPUADDR NICSESN PRIORITY RCWFLAG RDEVCYL RDEVNICL RDEVTFLG R13 R9 SAVEWRK4 SNARFG1 UC	
DMKDID	ADSPCH AFREE ASYSVM CE CPEXBLOK CPEXREC DMKCVTBH DMKDSPC DMKLOKIO DMKSCHS FTR3270E F1 IOBCAW IOBCLRI IOBIOER IOBIRA IOBRES IOBSIZE IOERCCUA IOERCHI IOERSIZE IOER286 MIHGRAF MIHMISC MODEL135 PMAMODE RCUCHA RCUDISA RDEVBLOK RDEVBUZ RDEVIOER RDEVMID RDEVRSTA RDEVSCH RO R1 R4 R5 TRQBIRA TRQBLOH TYP3330 TYP3800	H DMKERMSG DMKFREI T DMKSCNEP DMKSCNI F10 F15 0 IOBCP IOBCSW IOBLINK IOBLOK IOBSPEC IOBSPEC D IOERCPID IOERCSV 0 IOER2870 LOCK MIHMSG MIHMSGC PMASTAT PROCIPI RCUFIOB RCUPRIN RCUFIOB RCUPRIN RDEVCSW RDEVCU/ RDEVCSW RDEVCU/ RDEVSIZE RDEVST/ R10 R11 R6 R7 TRQBSAVE TRQBSIZ	E CRTEXT DMKFREEP A DMKSCNRD F2 IOBFATAL IOBFATAL IOBFATAL IOBFATAL IOBFATAL IOBFATAL IOBFATAL IOBFATAL IOBFATAL IOBFATAL SMIHMSGDV PSA E RCUQCNT RDEVCUB ID RDEVPIOB T RDEVSTA2 R12 R8 E TROBTOD	DMKSCNRU F3 IOBFCNS IOBMINI IOBSTAT IOERECSW L2 MIHMSGDW RCHADD RCUSTAT	CLASTAPE CUE DMKGRFIN DMKSTKIO F4 IOBFLAG IOBMISC2 IOBUNSL IOEREXT MCHAREA MIHMSGID RCHBLOK RCUSUB RDEVDROP RDEVPS RDEVSTA4 R14 SAVEAREA	DE DMKIOEMI DMKSTKMP F8 IOBFPNT IOBPROC IOBUSER IOERLEN MCHMODEL MCHMODEL MCHMODEL MCHMODEL MCHSTIDC RCUTYPE	IFCC IOBHIO IOBRADD IOERBLOK IOERLOGL MIHCE MIHTAPE RCUADD RDEVADD RDEVFORC RDEVQBSY RDEVTYPC R2	DMKIOTRC DMKSYSTV INTREQ IOBHVC IOBRCNT IOERCCH IOERCCH IOERPNT MIHDASD MIHUR RCUBLOK RDEVAIOB RDEVFTR	
DMKD I F	DMKSCNVU DMKSTKI IOBRCAW IOBSIZE NICDISA NICDXSC NICSESN NICSIZE OPERATOR PSA RDEVBASE RDEVBLC RDEVFLAG RDEVPTH RDEVTYPE RDEVUSE R14 R15	CSWLMEP CTRMLTI O DMKLOKSW DMKPGTI O DMKSYSCK DMKSYSI IOBUSER IOBVADI NICEPAD NICEPMI NICSTAT NICTELI RCHBLOK RCHCUTI K RDEVCON RDEVCUJ S RDEVSNRB RDEVST/ R RDIDX RUNUSEI R2 R3 S SAVERETN SAVERTI 9 SNARBLOK SNARDI/	CONDCNT DE DDMKQCNWT DDMKVIOIN LASTUSER NICFLAG NICFLAG NICFLAG CONCTANT CUBLOK RDEVCYL T RDEVSTA3 R0 R4 SAVER13 L SNARDIPG	F1 LOCK NICLTRC RCUDVTBL RDEVDED RDEVSTA4 R1 R5 SAVEWRK1 SNARFG2	GRAFDEV LOCKSAV NICNAME NICTYPE RDEVADD RDEVDIIP RDEVTFLG R10 R6	DMKCVTBD DMKSCHRT IOBCSW LPUADDR NICQPNT NICUSER RDEVAIOB RDEVEPDV RDEVTMAT R11 R7 SAVEWRK5	RDEVEPLN RDEVTRQ R12 R8 SAVEWRK6 TRQBFPNT		

DMKDIR

ATTN

BLANK

BUSY

CAW

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CLASDASD CLASFBA

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	FLAG	TYP3148 TYP3340 UC	FSCBBUFF FTR2311T NOTUSED R15 SCAN TYP1403 TYP3158 TYP3350 UE	FSCBD	CLASURO FSCBFN FTR3270 ROUTE R3 SILI TYP2305 TYP3210 TYP3280 UIUCCHN UIUCSTAT		H7 R1 R5 TIMER TYP2314 TYP3215 TYP3525 UIUCDISP	DE FSTD L1 R10 R6 TYPCTCA TYP2501 TYP3262 TYP3800 UIUCGLBL WRITE	L16 R11 R7 TYPIBM1 TYP2540P TYP3277 TYP38003	FSTRECCT L4 R12 R8 TYPTELE2 TYP2540R TYP3289E TYP4245
DMKDMP	CC CORFPNT CPUID DAMAGRPT DMKPRGMC DMKSYSRV DMPCPUID	DMKRIODV DMKSYSSP DMPFLAG DMPPRFRG FXDLOG IONPSW L3 PROPSW	CHGSFB CORVM CPXSTOR DMKCPEID DMKRIOPR DMKSYSTP DMPFPRS	DMKSYSVM DMPGPRS DMPSYSRM GRLOG I PLPSW L5 RDCBLKPG	CLASFBA CPCREGO CUE DMKDMQGP DMKSCNRU DMKVFRSV DMPINREC DMPSYSRV HALFPAGE IPUADDR L8 RDCBLOK	DMKVRR DMPIPCS DMPTODCK HARDSTOP IPUADDRX MACRO	CPSPMODE C14 DMKDMQMC DMKSYSCK DMKVRRIS DMPKEY DUMPSAVE IDA LAP370E MCHEK RDEVBLOK	CORCP CPSTAT2 C15 DMKDMQPC DMKSYSCS DMKVRROP DMPKYREC	DMKSYSRC DMKVRSSV DMPLCORE EXTMODE INTREQ L1 PMAMODE	
DMKDMQ	ALARM CCC CORVM DE DMKDMPSW DMKVRR DMPSTAT IOBSIZE MONARDB RCUADD RDEVTYPC R15 SIGREST TYP3422	DMKVRRIS DMPUNI IOMASK MONCAD RCUBLOK	DMKOPRWT DMKVRROP DOUBLESP IONPSW MONCOM RCUCHA RSRTNPSW R3	DMKVRSSV DUMP LINE MONFLAG2 RCUPRIME	CPVR DMKDMPDS DMKRIOCN	BLANK CORCP CSW DMKDMPDVV DMKSCNRV DMPIPCS FFS LOCK PREFRG RCUTYPE R10 R6 SPOOLED UE	BREAKSUP CORDISA CTLREGS DMKDMPMA DMKSCNRU DMPMP FPREGS LOWCORE PROCIPL RDEVADD R11 R7 STORSIZE VMBLOK	BUSY CORFLAG CUE DMKDMPPA DMKSYSCS MAINPSA PSA RDEVBLOK R12 R8 TODCLOCK WAIT	DMKSYSSP DMPSMSG IFCC MCHEK RCHADD RDEVCUA R13 R9	CC CORTABLE C2 DMKDMPRY DMKSYSVM DMPSMSGL INTTIO MONAIOB RCHBLOK RDEVCUB R14 SENSE TYP3420
DMKDNC	RO R4	R1 R5	R10 R6	R11 R7	R12 R <b>8</b>	R13 R9	R14	R15	R2	R3
DMKDRD	ACORETBL CLASFBA DMKCVTBH	ADSPCH CLASURI DMKDMPAL	AFREE CONNECT DMKDMPAU		AFRET CORTABLE DMKDMPRC		APTRLK DEFER DMKDSPCH		ASYSVM DMKCKTUU DMKFREE	BRING DMKCVTAB DMKFREEP

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#### MODULE EXTERNAL REFERENCES (LABELS AND MODULES)

	DMKFRET DMKPSASP DMKSYM F0 L1 R10 R6 SAVEWRK2 SFBDUMP SFBLOK SFBLOK SFBTUSE SPRECNUM TRQBUSER TYP3330 VSPSFBLK	DMKPTRAN DMKSYSDU F1 L3 R11 R7 SAVEWRK3 SFBEOF SFBMISC1 SFBTYPE SPSIZE TRQBVAL TYP3340	DMKPTRLK DMKSYSOW F255	DMKPTRUL DMKVSDAD F256 OWNDRDEV R13 R9 SAVEWRK5 SFBFLAG SFBFLAG SFBOPEN	DMKRPAGT DMKVSDDL F4096 PSA R14 SAVEAREA SAVEWRK6 SFBFLAG2 SFBOR1G SKIP SWPKEY1	R15 SAVEREGS SAVEWRK7 SFBFLAG3 SFBPNT SPCHAR SWPKEY2	DMKSCHST DMKVSFNU F8 RDEVTYPC R2 SAVER0 SAVEWRK8	DMKSCNMU DMKVSGRI IOERETN RDEVTYPE R3 SAVER2 SAVEWRK9 SFBINUSE SFBSIZE SFBSIZE SPLINK	DMKSCNRD DMKVSUCR IOKEEP R0 R4 SAVER6 SFBCLAS SFBLAS SFBINVS SFBSTART	DMKSCNVU FFS LOCK R1 R5 SAVEWRK1 SFBCOPY SFBLAST SFBSYSID SPPREPAG TRQBLOK TYP2319
DMKDRE	DMKLOCRQ	CORSWPNT DMKDMPDV DMKPGTDS DMKRPAGT DMKVSGRI L3 R10 R6 SAVEWRK2	AFREE CORTABLE DMKDMPRC DMKPGTDT DMKRPAPT F0 MP R11 R7 SAVEWRK3 SFBLAST SFBLAST SKIP TRQBLOK TYP3350	DMKDMPSF DMKPGUDU DMKSCHST F1 OWNDLIST R12 R8	DMKPGUSD DMKSCNMU F256 OWNDRDEV R13 R9 SAVEWRK6 SFBORIG SFFILID	DMKPGUVG DMKSCNRD F4096 PSA R14 SAVEAREA	DMKFREE DMKPGUVR DMKSYM F5 RDEVBLOK R15 SAVEREGS SFBFILID SFBSEEN SPSIZE	DMKFRET DMKPSASP DMKSYSDU I OERETN RDEVTYPC R2 SAVER0		DMKLOCRD DMKPTRLK DMKVSFID LOCK
DMKĎSB	ADSPCH ASYSVM CPEXBLOK DMKIOESD FTRRSRL IOBLINK IOERBLOK IOERSIZE OPERATOR RCUTYPE RDEVDED RDEVSEL RO R4 TYP2314	IOERSNSZ PROCIPL RDCBLOK	AFREEP CC CPEXSIZE DMKLOKIO FTR70MB IOBRADD IOERCCRL IOERVSER PSA RDCFEAT RDEVFLAG RDEVSTAT R10 R6 TYP3340	DMKQCNWT IFCC IOBREL IOERCPID LOCK RCHADD RDCRRLSE RDEVFTR	IOBCAW IOBSIZE IOERCSW LOCKSAV RCHBLOK	I OBCC3 I OBSPEC I OERDATA LPUADDR RCUADD	IOBCSW IOBSPEC2 IOERDW LPUADDRX RCUBLOK RDEVCFLT RDEVPPAG	DMKFREE DMKSTKCP IOBFATAL IOBSTAT IOEREXT MP RCUCHA	IOBIOER IOBTIO IOERLEN NORET RCUPRIME RDEVCUB RDEVRDC	DMKFRET FTRRPS IOBIRA IOBUSER IOERLOC NOTUSED
DMKDSP	ACTIVTRQ ALOKVM ATTN CCTRPYP CPAPRPND CPEXBPNT CPEXR8 CPSHRLK	APSTAT1 AVMREAL CC3 CPCCHLK CPEXDEFR CPEXSIZE	AEXTSP APSTAT2 BALR2 CDC CPCREG0 CPEXFPNT CPEXTYPE CPSTATUS	CPCREG6 CPEXPRIO CPFRELK	AFREEP APSTAT4 BRING CLASTERM CPCREG8 CPEXPROC CPMCHSE CPSTAT3	AFRET APTRAN BUSY CODE CPDASAAV CPEXREGS CPMICON CPSTAT4		CONPNT	ALOKSY ARIODV CCTFLCNT CONSTAT CPEXADD CPEXR12 CPRUN CPSYSLK	ALOKTRL ASYSVM CCTRPYN CONTASK CPEXBLOK CPEXR13 CPSEGPRT CPTIDLE

#### MODULE

#### EXTERNAL REFERENCES (LABELS AND MODULES)

	DMKHPTEX DMKMCHSE DMKVATAT DMKVATAT DMKVAITA FRLKPROC INTPRL IOBRADD IPUADDR MICPTLB2 PCI PGWAITIM PROCIO PROTREI RDEVADD	DMKIOSER DMKMCTPR DMKMCTPR DMKVATBC DPOKTLB EMSPEXT F1 INTTIO IOBSIZE IPUADDRX MICRRBE PERADD PMAAVAIL PROCIPL PROTREIL RDEVAIOB RDEVTYPC R1 R5 SNARBACH TIMER TRAC10 TREXIN1 UC	DMKPERIL DMKREIPL DMKVATEX DPSTAT EMSPQUI F255 IOBBPNT IOBUSER IUCVBLOK MICSSKE PERBLOK PMASTAT PROPSW PSA RDEVBLOK RQLKBASE R10 R6 SNARBLOK TRACCURR	DMKIOSW1 DMKIOSW1 DMKSCHDL DMKSWAPD DMKVATMD DSPA EMSREC F3 IOBCSW IOERBLOK MICSTBVR PERCODE PREFIXA PROTBLOK PSADSPRQ RDEVCON RQLOCK R11 R7 SNARFG1	C8 DMKFREE DMKIUBRK DMKPMARW DMKSCHQ1 DMKVCRNT DMKVCRNT DSPB EMSREXT F4096 IOBFLAG IOERDATA IUCVMASK MICEVMA2 MICVIP PERCR9 PREFIXB PROTDPSW PSAEXT RDEVDED RUNCRINV R12 R8 STACKVM TRACFLG2 TRAPCR8 TREXT VECSTAT	DMKLOKCT DMKLPMASW DMKSCHQ2 DMKTRCEX DMKVIOCL DSPCH EMSRQUI IDLEWAIT IOBFPNT IOMASK LAP370E MICEVMA3 MICVPFR2 PERMODE PRNPSW PROTERR PSAEXTX RDEVFIOB RUNCRO R13 R9 SYNCMASK TRACFLG3 TRAPTT TRLLABEL VECUSER	DEFER DMKFRESW DMKLOKDF DMKPMAWT DMKSCHRL DMKTRCIO DMKVIOMK DSPRQ EXNPSW IFCC IOBIRA IONPSW LASTUSER MICIPTP2 MSSFINTR PGADDR PROBMODE PROTEXTL PWTPAGES RDEVSCHD RUNCR1 R14 SAS TEMPRO TRACPROC TRCFPRUN	DMKFRET DMKLOKRL DMKLOKRL DMKLOKRL DMKVRCIT DMKVMASH DSPRU EXOPSW INMSFBLK IOBLOK IONTWAIT LOCK MICISKE MVSA370E PGBLOK PROBSTRT PROTFLAG PXA RDEVSIZE RUNPSW R15 SIGDISP TEMPR10 TRACSTRT TRCRUN TRQBFPNT VMSIZE	DMKKTRCPG DMKVMCEX EMSINQSC EXTMODE INTEX IOBMISC IOOPSW LOCKSAV MICLRA2 OFF PGBSIZE PROBTIME PROTPSW QUANTUM RDEVSNRB RUNUSER R2 SIGMASK TEMPR6 TRACSVCR TRACSVCR TRACSVCR TRACSVCR	DMKFRET1 DMKLOKTR DMKLOKTR DMKVATAB DMKVATAB DMKWAIST EMSMASK FFS INTEXF IOBPAG IPLCCW1 LOKREQ MICPEND PAGEWAIT PGPNT PROB370E PROTRCNT QUANTUMR RDEVSTAT RUN370E R3 SIGXC TEMPSAVE
DMKEIG	CCC COMPSYS PSA R13 TERMSYS	CCHCMDV CSW RTCODE0 R14 TIOCCH	CCHDAV FFS RTCODE1 R15	CCHD I I FCC RTCODE2 R2	CCHLOG80 IGBLAME RTCODE3 R3		CCHREC IGVALIDB RTCODE5 R9	RO	COMPFES IOELPNTR R1 SAVEWRK1	COMPSEL IOERBLOK R12 SAVEWRK9
DMKENT	LOCK MNCHSIZ MNDVMORE MN602HLN MN603CQ3 PREFIXB RCUCHA	LPUADDR MNCHSIZE MNRDEVB MN602MLN MN603CQ4 PROCIO RCUPRIME RDEVQCNT R2	MNCHSZ MN602ADD MN602SAM MONCHPTR PROCIPL	MNCHDAT1 MNCUBSY MN602CUB MN603CB1	MNCHDAT2 MNDEVLEN MN602CUQ	MNCHDT11 MNDEVLST MN602DEV MN603CB3	MNDVBSY MN602DLN	MNCHLIST MNDVBSY2 MN602DVQ MN603CH MPUOPER RCUADD	DMKSYSAT MNCHSAMP MNDVCNT	MNCHSAM1 MNDVLEN MN602HDR MN603CQ2
DMKEPS	AFREE DMKFREE LOCK R14	AFRET DMKFRET NOTRESP R15	BLANKS DMKQCORD PSA R2	BUFFER DMKSCNFD RDEVBLOK R3		BUFNXT FFS R0 R5	BUFSIZE F2 R1 R8	CLASGRAF F4 R11 R9	CLASTERM F8 R12 SAVEAREA	DMKERMCP INHIBIT R13 SAVEREGS

MODULE	EXTER	RNAL REFE	RENCES (LA	ABELS AND	MODULES)					
	SAVER0	SAVER11	SAVEWRK1	SAVEWRK6	TYPBSC	UCASE	VMBLOK			
DMKERM	PLFLAG1 RDEVWTH R2 SAVER0	DMKPTRAN IOKEEP LANGSIZE PLFRET REPCNT R3 SAVER1	ALARM CLASTERM DMKQCNPL LANGADDR LANGVMB PLIST R0 R4 SAVER2 SPECIALV	DMKSYSRM LANGBLOK LANGVMBK PLLED R1 R5 SAVER3	LANGDBCS LOCK PLR2 R10 R6	NICBLOK PLSIZE R11 R7	BLANKS DFRET F1 LANGHIGH NICSIZE PSA R12 R8 SAVEWRK4 XRIGHT16	F2 LANGLOW NICWTH RDEVBLOK R13 R9	BUFCNT DMKCVTDB F255 LANGNTRY NORET RDEVNICL R14 SAVEAREA SAVEWRK6	F4 LANGNTSZ OPERATOR RDEVTYPC R15 SAVEREGS
DMKERP	AP DMKCKFES ERRBLOK RECNXT R3	CKPLOAD DMKCKFIP ERROBR RECPAG R4	CLASFBA DMKCKFIQ ERRSRDEV RECPAGFL R5	FFS	CPEXFPNT DMKCKFMX FLAG R1 R7	CPEXR6 DMKCKFVT FREESAVE R10 R8	DMKCKNIO	DMKCKFCL DMKCKNPW MP R14	DMKCKFCQ DMKCKNRD PSA R15	DMKCKFEP DMKCKP RECFLAG1 R2
DMKEXT	CPLOKFL CRESIMD DMKCLKSC DMKLOKDF DMKSTKMP EMSPSYNC F2 INTEX MSSFINTR PREFIXA RDEVBLOK RUNCRO R15 SENSE SM	ASYSOP CPAPRPND CPMFAWIA CSW DMKCVTAB DMKLOKIO DMKLOKIO DMKPMASW DMKTMRVT EMSPXEX F255 INTEXF NICBLOK PREFIXB RDEVFLAG RUNPSW R2 SIGDISP SYNCMASK TRACFLG2	CPRUN CUE DMKLDMPC2 DMKLOKSY DMKVSAER DMKVFRSV EMSREC F256 IOMASK NICNAME PROCIO RUEVHIO RUDUSER R3 SIGEMS	CO DMKDSPB DMKLOKTR DMKPSANX DPAPUOP EMSRSHD F3 IPUADDRX NICSIZE PROCIPL ROCIPL ROCIPL RO R4 SIGEXT TEMPR13 TRACSTRT TRQBUSER	DMKMCTFS DMKQCOCL DPSTAT EMSRXEX F4 LOCK NICUSER PSA RDEVPROC R1 R5 SIGQUI TEMPR2	CPSTAT2 C2 DMKDSPE DMKMCTMA DMKRNHND EMSPCLKC EXNPSW F5 LOCKSAV NOCODE QUANTUM RDEVPS R10 R6 SIGRES TEMPSAVE TRACO1	CPEXREGS CPSUPER C6 DMKDSPRU DMKMCTPR DMKSCHTQ EMSPEND EXOPSW F6 LPUADDR NORET QUANTUMR RDEVSTA3 R11 R7 SIGSAVE TIMEDISP TRAC13	CPSYSLK C8 DMKDSPWI DMKMHCIN DMKSCNEP EMSPEXT EXTMASK F8 MFAMASK PMAGUEST RCHBLOK RDEVTYPC R12 R8 SIGSENSE TIMER TRCEXT	CPEXSIZE CPUID DMKCLKAP DMKFREEP DMKMPOEX DMKSCNRD EMSPQUI F0 F9 MP PMAMODE RCHPROC RDEVUSER R13 R9	CPWAIT DMKCLKCC DMKHPTDI DMKPMAEW DMKSTKIO EMSPSHD F1 INMSFBLK MPUOPER PMASTAT RDEVBASE RESET R14 SAS SIGXC TRACEND TROBBPNT
DMKFCB	LINE	VERLEN								
DMKFMT	ATTN C2 FLAG L4 PROPSW R12 R8	BLANK DE FO L400 PSA R13 R9	BUSY D1 INTREQ L4096 RANGE R14 SCAN	CAW D2 IONPSW L5 READ R15 SENSE	CC D28 IOOPSW L6 REGSAV R2 SILI	CD D3 L1 L7 RESET R3 SKIP	CE D4 L1024 L8 R0 R4 SM	COUNT D5 L16 L80 R1 R5 UC	CSW D8 L2O MAXLEN R1O R6 UE	CUE EQCHK L3 NOPRINT R11 R7 ZERO
DMKFPS	AFRET C1	APSTAT2 C6	AVMREAL DMKFRET	CPCREGO DMKPSAFP	CPPTLBR DMKPSASP	CPSPMODE DMKSCHQ1	CPSTAT2 DMKSCHQ2	CPSTAT4 DMKSCNVU	CP370EON DMKTMRPT	CO EXTMODE

MODULE
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	F1 LAP370E OLDKEYOP RUNCR1 R2 STOACTV STOVPPG TRQBLOK	F15 LOCK PAGE4K RUNPSW R3 STOBLOK STOVPSG TRQBQUE	F240 L2 PREFIXB R0 R4 STOFLAG STO6CPG VECF	F4 MATCH PROBMODE R1 R5 STOLAST STO6CSG VECUSER	F4095 MICBLOK PROPSW R10 R6 STONEXT TEMPR12 VMBLOK	F4096 MICEVMA2 PSA R11 R7 STOSEGVR TEMPR14 VMSIZE	F6 MICPEND PURGESTO R12 R8 STOSHCR1 TEMPSAVE XKEYMODE	R13 R9 STOUSPT TIMEDISP	INTPRL MICVIP QUANTUMR R14 SAS STOUSPTL TIMER XRIGHT16	R15 SPMPFX STOVCR1 TRANMODE
DMKFRE	C6 DMKLOKSY ENDSIZES FREERO FRENAME FRSIZE LOCK MAXSIZE PRIMEHDR R1 R5 SIGXEX SYMUNLOK S6	PRIMEHI R10 R6 SIZE	CPEXRO DMKDMPC2 DMKPTRFR EXSUBTOP FREER12 FROSIZE FO LOKREQ MINLEAVE PRIMELO R11 R7 SPBOUND S15 TABWDTH\$	FREER14 FRPALLOC F4096 LPUADDR NUMCAPLS PROCIO R12 R8 SPNTR S18 TEMPR6	DMKPXB FRADDR FREER15 FRPMCALR HBOUND LPUADDRX NUMSTEAL PSA R13 R9 SPTIME S21	DMKFRTSN DMKSTKLF FRECALLR FREESAVE FRPMEXT HMAXSIZE	DMKSYSCS FREDPACA FREEWORK FRPMNAME HWORD LSPSIZE PMASTAT PSAEXTX R15 SIGDISP SUBSTLA S27 TIMEPOP	DMKLOKDF DSPRQ FREE FREEXT FRPMSIZE INCRBY1	CO DMKLOKIO ECPSOP FREEH FRELSNSS FRPMTAG IPUADDRX MAXCSIZE PREFIXA RQLOCK R3 SIGWAKE SUBTABMX S30	ECPSUBTB FREEP FRELSTTS FRPSIZE LASTUSER MAXHSIZ PREFIXB R0 R4 SIGXC
DMKFRT	ACORETBL CORTABLE DMKFRELS DMKSYSCS FREER14 FRPMTAG LSPSIZE PSA R15 STAKSIZE TRACSTRT XTNDLOCK	COUNT DMKFREMT DMKSYSRM FREER15 FRPRETRN LSPSIZES PSAEXT R2 SUBHEAD TRACSVCR	DMKVCNFT FREESAVE FRPSIZE MAXHSIZ PXA R3 SUBTABLE		DMKFRESV EXCLOCK FREEXT FRTAG PAGBMP R1 R5	EXSUBTOP FROSIZE F1 PREFIXA R10 R6 TABWDTH\$	BALRSAVE DMKFREHP DMKPXA FRPALLOC F4096 PREFIXB R11 R7 TRACCURR USPSIZE	DMKFRELN DMKPXB FRBYTES FRPCACHL LOCK PRIMEHDR R12 R8	DMKQCNFT FREERO FRPMEXT LOCKSAV PRIMEHI R13 R9 TRACFLG1	CORPGPNT DMKFRELP DMKQCOFT FREER13 FRPMSIZE LPUADDR PRIMELO R14 SPBOUND TRACPROC XPAGNUM
DMKGIO	ADSPCH CLASURO DEFER DMKUNTFR IOBFLAG IOBSTAT RDEVCUP R2 SAVER2	AFREEP CPEXADD DMKCCWTR DMKUNTRN IOBHVC IOERBLOK RDEVSTA5 R3 TYP3340	DMKDSPCH F0 IOBIOER IOERCSW	APTRAN CPEXFPNT DMKFREEP IFCC IOBIRA IOERDATA R1 R5 VMBLOK	DMKFRET IL IOBLINK	CCC CPEXRO DMKIOSQV IOBCAW IOBLOK IOERPNT R11 R7	CLASDASD CPEXR1 DMKPSAP0 IOBCC3 IOBMISC IOERSIZE R12 R8	CPEXSIZE DMKPTRAN 10BCLN 10BM1SC2	DMKSCNVD IOBCSW IOBSIZE PSA R14	C1
DMKGRA	AFREE CONTSKSZ DMKTBMZO	AFRET C1 F0	APTRAN DEFER F255	ASYSVM DMKFREE LOCK	BRING DMKFRERC PSA	CONADDR DMKFRET RO	CONCNT DMKPTRAN R1	CONDATA DMKTBMAO R10	CONTASK DMKTBMTO R11	CONTSIZE DMKTBMXO R12

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MODULE	EXTER	RNAL REFE	RENCES (LA	ABELS AND	MODULES)					
	R13 R9	R14 SAVEAREA	R15 SAVEREGS	R2 SAVER6	R3 SYSTEM	R4 VMBLOK	R5 XRIGHT16	R6	R7	R8
DMKGRC	IOBLOK NICQREP RDEVFTR2 RDEV14B R2	AFRET CONPARM DEFER DMKPTRUL IOERETN NICRFLG RDEVHT REW R3 SAVEWRK2 TEMPR9	LOCK NICWTH RDEVPSS R0 R4	APTRLK CONTASK DMKFRET FO NICADFF PSA RDEVPT R1 R5 SYSTEM VMBLOK	DMKGRTB F1 NICADVF	DMKGRT12 F10 NICAWSF RDFVADVF	CONTSKSZ DMKLOCKD F2 NICBLOK RDEVBLOK	DMKLOCKQ F4096 NICEWO	DMKPGUVG F8 NICHT RDEVEHLT RDEV12B R14	CONWORK2 DMKPGUVR IOBCSW NICPT
DMKGRD	CONWCC CRTWNG DMKGRFIN FTRNODRP GRCMXDLN GREFL GREIC IOBLINK NOTRESP RDEVBLOK RDEVBLOK RDEVBLOK RDEVHOLD RDEVSTA3 RO R4 SELRB	DMKCFMAT DMKGRGBR FTR3270E GRCSFIN GREFSMOR GREIS IOBLOK OPERATOR RDEVCON	CONPARM CPUVERSN DMKDSPCH DMKGRHIN F2 GRCSFSTA GREFTFMT GRERT IOBMISC2 PRIORITY RDEVCORD RDEVHT RDEVTRQ R10 R6 SELWRT TROBIOK	DMKFRET DMKGRISB F9 GRCVMRD GREGC GRESR IOBSIZE PSA RDEVCTL RDEVIOAT RDEVTYPE R11 R7 SF TRQINTEG	CONPNT CRTDIAG DMKGRCUP DMKGRTB GRCALRM GREBI GREGCMOR GREST IOBSPEC RB RDEVECOL RDEVIOCP RDEVUSER R12 R8 SILI	DMKGRT12 GRCCLRIN GREBT GREGGCLR GRESV IOBUNSL RDEVACTV RDEVEHLT RDEVIODE RDEVWIOB R13 R9 TRQBCHIO TYP3277	GRECR GREGGRUN GREWS IOBUSER RDEVADFF RDEVEWO RDEVNOW RDEVWTH R14 SBA TRQBCLIN TYP3278	CONTASK CRTFSSA DMKGRESM DMKIOSQR GRCECOL GREGL GREGL GRTBLOK LOCK RDEVADVF RDEVADVF RDEVADVF RDEV14B R15 SCRCMDR	CRTSIO DMKGRFBR D66LNCNT GRCEHLT GREC2 GREGN INHIBIT LOGDROP RDEVAINH RDEVFTR RDEVFTR RDEVFTR RDEV741D R2 SECUSER TRQBCRT	FFS GRCINH GREFI GREGTHLD IOBCAW LOGHOLD RDEVAIOB RDEVAIOB RDEVRUN
DMKGRE	F5 GREDNCTL GREFT GREST PSA	CONEWA CONSTAT CRTDIAG DMKGRFCF GREBI GREDNSEL GREFUTCP RB RDEVIOCP R1 R5 TRQBCRT	RDEVIODE R10 R6 TRQBFLAG VCONOBRK	CRTFSSA DMKGRISB GRECR GREDRSCP GREGC IC RDEVBLOK RDEVNOW R11 R7 TRQBFLG2	GREC1 GREDWBOK GREGL I DA RDEVCON RDEVPS R12	CRTUSEWA EXCOLOR GREDATNR GREFC GREGN IDAHEAD RDEVCORD RDEVPSS R13 R9 TRQBLOK	EXHILIGH GREDBCCW GREFI GREIC IDAHSTRT RDEVCTL RDEVSTA3 R14 SBA	EXPSS GREDCIOS GREFL GREIS IOBCSW RDEVECOL RDEVTFLG R15 SELRBP VCONANF2	CORFPNT DMKERMSG F1 GREDCK1D GREFR GRERT 10BL0K RDEVEHLT RDEVWSF R2 SELRMP	CONPNT CORTABLE DMKGRDBR F4096 GREDCVMP GREFS GRESR LOCK RDEVHSTR RDEV741D R3 SILI VCONCBRK
DMKGRF	ADSPCH	AFREE	AFRET	APTRAN	ASYSOP	ASYSVM	ATTN	BRING	BUFAPL	BUFFER

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	DMKGRTFD DMKQCOET FFS GREBT GREDIIOB GREFT IOBIOER IOBSPEC5 IOERSIZE RDEVAIOB RDEVDISB RDEVHOLD RDEVMDL RDEVSTA3 RDEVUSER R12 R8	CONCCW3 CONTGMXB CRTPPA1 DMKFREE DMKGRGCR DMKGRCFTO FTRNODRP GRECR GREDNCTL GREGC GRESV IOBLINK IOBSTAT LOCK RDEVAIRA RDEVAIRA RDEVAIRA RDEVAIRA RDEVAIRA RDEVSTA4 RDEVSTA4 RDEVWIOB R13 R9 TRQBFLG2	GREC1 GREGGLT IDA IOBLOK IOBUNSL LOGDROP RDEVAPLP RDEVAPLP RDEVAPLP RDEVNAB RDEVHT RDEVSTA5 RDEVWSF R14 SENSEID TRQBFLG3 TYP3277	C1 DMKGRCQY DMKGRGPF DMKHPTDI DMKSCHST F1 GREC2 GREDRIOB GREGL IFCC IOBWISC IOBUSER LOGHOLD RDEVIOAT RDEVERR RDEVSTA6 RDEVSTA6 RDEVSTA6 RDEVWTH R15 SILI TRQBIRA TYP3278	DMKGRGT1 DMKIOERR DMKSCNRU F4 GREDATNR GREDRSCP GREGN INTREQ IOBMISC2 IOERBLOK PREFIXA RDEVFCNS RDEVFCNS RDEVFCNS RDEVFS RDEV741D R2 STYPRDPT	CRTA10 DEFER DMKGRDCC DMKGRGTR DMK10EST DMKSYSNM F4096 GREDBCCW GREDBCCW GREDBCCW GREDBCCW GREDBUBU IOBRADD IOERCSW PRGC RDEVFLAG RDEVFLAG RDEVFLAG RDEVFLAG RDEVFLAG RDEVFLAG RDEVFLAT RDPTQRYG R3 SYSTEM TRQBLINE UC	DMKGRECL DMKGRHIN DMKGRHIN DMKTBMAI F8 GREDCIOS GREFI GREIC IOBCOPY IOBRCNT IOBRCNT IOERDATA PRTC RDEVCON RDEVFORC RDEVFORC RDEVFORC RDEVREAD RDEVTFLG	DMKGREFD DMKGRISB DMKOPEEM DMKTBMTI GRCMXDLN GREDCK1D GREFL GREIS IOBCSW IOBSENS IOERFLG3 PSA RDEVCORD RDEVFTR RDEVLGDT RDEVRUN RDEVTMCD R1 R5 TRQBCHIO	DMKPTRAN DMKPTRAN GRCRUN GREDCVMP GREFR IOBFATAL IOBSIZE IOERNUM RDEVACTV RDEVCPNA RDEVCPNA RDEVCPNA RDEVLOG RDEVSCHD RDEVTRQ R10 R6 TRQBCPQ TRQBUSER	DMKGRGBR DMKGRTAI DMKQCOCL DMKTBMZI GREBI GREDFIOB GREFS GRESR IOBFCNS IOBSPEC IOERREAD RDEVADVF RDEVCTL RDEVHIO RDEVLTRM RDEVSTA2 RDEVTYPE R11 R7 TRQBCRT
DMKGRG	DMKSCNRU GRCALRM GRCSFIN GREDCVMP GREFLTST GREGC INHIBIT IOBSPEC PFDATA PSA RDEVAPLP RDEVECOL RDEVIODE	CONPARM CPEXSIZE CRTFSSA DMKFREE DMKGRFCF DMKSTKCP GRCCLRIN GRCSFSTA GREDCTL GREFR GREIC INTREQ LOCK PFDCMD RCUBLOK RDEVBLOK RDEVBLOK RDEVBLOK RDEVEHLT RDEVMDL RDEVTMCD R1 R5 TFMPRO	CRTUSEWA DMKFREEP DMKGRHIN DMKTBLUP GRCCLROT GREBI GREDNSEL GREFSINT GREIS IOBCAW LOGDROP PFDCPAD4 RCUDVTBL RDEVCON RDEVENAB RDEVMORE	CPINIT CRTWNG DMKFRERC DMKGRISB EDIT GRCECOL GREBT GREDRD77 GREFSMOR GRERT IOBCOPY LOGHOLD PFDCPYSP RCUPRIME RDEVFLAG RDEVFLAG RDEVFLAG RDEVFLAG RDEVFLAG RDEVFLAG RDEVFLAG RDEVTAME RDEVTAME RDEVTAME RDEVTAME	CONTASK CPSTAT5 DMKCFMAT DMKGRTAB EWA GRCEHLT GRECR GREDRIOB GREFSM77 GRESR IOBCSW MNCLRESP PFKADDR RDEVCPNA RDEVCPNA RDEVCPNA RDEVCPNA RDEVCPNA RDEVCPNA RDEVTPE R12 R8 TREXFLAG TROBLOK	CRTAIO DMKCFMBK DMKCFMBK DMKGRTPF FO GRCHOLD GREC1 GREDRSCP GREFSREJ GRESV IOBLINK MNCOERD PFKFLAG RCUTYPE RDEVHOLD RDEVPS RDEVWIOB R13 R9 TREXMOR TYP3277	DMKGRDBR DMKGRT12 F1 GRCMORE GREDATNR GREDWBOK GREFSR77 GREWS IOBLOK NOMC PFKIMM RDEVACTV RDEVCUA RDEVHSTR RDEVREAR RDEVWSF R14 SAVEAREA TREXT TYP3278	CRTCARD DMKCNTED DMKGRDCC DMKQCNWT F2 GRCMXLEN GREDBCCW GREFEC GREFSWNG GRTABLEN IOBMISC NORET PFKLNG RDEVADVF RDEVDED RDEVHT RDEVRUN RDEVWTH R15 SAVER0 TRQBCHI0 TYP3284	CRTDIAG DMKCVTBD DMKGRDSR DMKRETGT F3 GRCNAC GREDCIOS GREFCCLR GREFSW77 HILIGHT IOBMISC2 NOTIME PFKRET RDEVIOAT RDEVDIIP RDEVIOAT RDEVSTAT RDEVSTAT RDIDX R2 SBA TRQBCLIN UCASE	DMKGRECL DMKRETPT F4095 GRCRUN GREDCK1D GREFI IC IOBSENS NOTRESP PFKTABLE RDEVAIRA RDEVDISA RDEVIOCP RDEVSTA3 RDFTQRYG R3 SILI

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MODULE

DMKGRH

ALARM CONDWC LOCK R10 SILI

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DMKGR I	F256 GREDRIOB IOBSIZE RDEVAIRA RDEVTEXT R11 R8	F4 IC IOBUSER RDEVAPLP	DMKQCOET F4096 IOBCAW IOERBLOK RDEVBLOK RDEVTMCD R13 SAVEAREA	RDEVCON RDEVTRQ R14	DMKFREEP DMKSCNRD GRACDAPL IOBFLAG IOERSIZE RDEVGRF1 RDEVTYPE R15 TAB	APUOPER CONCNT CONTINUE DMKFRET DMKTBLGR GRACDTXT IOBIOER LOCK RDEVHSTR RDEVUSER R2 TEMPRO TYP3278	DMKGRAOT DMKTBLGT GRADEV77 IOBIRA LOCKSAV RDEVIOAT	DMKGRDBR DMKTBLRG GRADEV78 IOBLINK LPUADDR RDEVIOCP RO R5	DMKGRFIN FAKEATT GRAFESC IOBLOK PRIORITY	CC1 CONPNT CRTAPL DMKLOKIO FO GREDIIOB IOBMISC PSA RDEVNOW R10 R7 TRQBDEV
DMKGRT	CLASTERM DMKBOXNS FFS PFDVAL	DMKCNTED FO PFKADDR	AFRET BOXLOGO CONCNT DMKDSPCH F1 PFKLNG RDEVBLOK R13 R9 TRQBLINA	F2 PFKTABLE RDEVHT R14 SAVEAREA	CONPARM DMKFRET F3	NICADFF PFTABS RDEVWTH R2 SAVER0	BUFFER CONTSIZE	DMKRGBFM NICHT	DEFER	PFDATA
DMKHPS	IDA IOBLOK IOERETN RDEVBLOK RDEVLIOB RDEVTYPE R14 SAVEAREA	DMKSTKCP IL IOBRADD IOERLEN	INTREQ IOBRCNT IOERSIZE RDEVCTRS RDEVMDL RDEVWTH R2 SAVER11		I OBCP I OBSPEC MP RDEVDIIP RDEVPS R0 R4		APTRLK CPEXADD DMKFRET F1 IOBFLAG IOBVADD PSA RDEVEWO RDEVSIZE R10 R6 SAVEWRK8 UE	DMKHPTRE F15 IOBFPNT IOERBLOK RDEVADD RDEVFIOB RDEVSTA3 R11 R7	F4 IOBIOER IOERCSW RDEVADFF RDEVFLAG	F4096 IOBLINK IOFRDATA
<b>ДМКНРТ</b>	F4 IOBIRA IOERBLOK RDEVAIOB	CCC CRTEXT DMKPTRAN F4096 IOBLINK	F7 IOBLOK IOERDATA RDEVATT	RDEVBLOK	AFRET CORFLAG DEFER DMKSCHDL IOBCAW IOBRCNT IOERLEN RDEVBUZY RDEVUSER	IOBCP IOBSIZE IOERSIZE RDEVDED	DMKFREE DMKSTKCP IOBCSW IOBSPEC	APTRLK CPEXBLOK DMKFREEP DMKSTKIO IOBFLAG IOBUNSL MP RDEVQREP R10	DMKFRET DMKVIOIN IOBFPNT IOBUSER PSA	BRING CPEXR11 DMKGRCSV FO IOBIOER IOBVADD RDEVADD RDEVSTAT R12

EXTERNAL REFERENCES (LABELS AND MODULES)

BALRSAVE BUFINLTH CC CONLNCNT CONPARM CONTASK PSA RDEVBLOK RDEVCTL R12 R14 R15 TRQBFLAG TRQBLINE TRQBLOK

CONDATA

IOBLOK

CONDLN

IOBMISC R1 R8

CONCCW1CONCCW2CONCCW4CONCNTCONCRTALRMCRTSIODMKGRTFOIOBCAWIOERDEVHOLDRDEVMORERDEVRUNRDEVTFLGROR2R3R4R5R6

MODULE	EXTE	RNAL REFE	RENCES (L	ABELS AND	MODULES)					
	R13 R9 TRQBLOK	R14 SAVEAREA TRQBSIZE	R15 SAVEREGS TYP3286	R2 SAVER11 UC	R3 SAVER5 VMBLOK	R4 SAVER8	R5 SAVEWRK2	R6 SAVEWRK3	R7 [.] SAVEWRK4	R8 TRQBLINA
DMKHPU	IL IOBSPEC RDEVBLOK R10 B6	CORCP DEFER DMKPTRUL IOBCP IOBUNSL RDEVBUZY R11 R7	I OBCSW I OBUSER RDEVDED R12 R8 SAVEWRK4	DMKSTKIO IOBFLAG IOBVADD RDEVFIOB R13 R9	DMKFREEP DMKVIOIN IOBFPNT IOERBLOK RDEVSCHD R14 SAVEAREA	F0 IOBIRA LOCK RDEVSTAT R15 SAVEREGS	DMKGRFIN F1 IOBLINK PSA RDEVSTA4 R2 SAVER1	ASYSVM CPEXR11 DMKPSAFC F4096 IOBLOK RDEVADD RDEVUSER R3 SAVER10 SAVEWRK9	ATTN CPEXSIZE DMKPSASC F7 IOBRADD RDEVAIOB R0 R4 SAVER13 SILI	DMKPTRAN IDA IOBSIZE
<b>ДМКНУС</b>	ECBLOK F4 IOBCSW MCHMODEL RCWHEAD R0 R4 UE	DMKCCWTR DMKFRET DMKFRET DMKPTRAN EXTVPORL F4095 IOBLOK MOD3090 RCWPNT R1 R5 VMBLOK VMCLNULL VMESTAT	DMKPTRUL FFS F4096 IOBMISC PARMSIZE RCWRCNT R10 R6 VMCF VMCF VMCMDLEV VMEXTCM VMMTEXT	DMKHVDAL DMKRPDEP F0 F5 IOBMISC2 PCI RCWTASK R11 R7 VMCFWAIT VMCCMND VMEXWAIT	DMKHVEAL DMKSCNVU F1 F6 IOBRADD PRGC RCWVCAW R12 R8 VMCLANY VMCLANY VMCONBUF VMFSTAT VMNPWOCL	CHC DMKCVTDT DMKHVFAL DMKSSVHV F16 F60 IOBSIZE PRTC RCWVCNT R13 R9 VMCLASSA VMCONLEN VMGPRS	DMKMHVSM DMKTMRPT F2 F8 IOERETN PSA RDEVBLOK R14 TEMPR6 VMCLASSB	DMKDSPB DMKPGSSS DMKUNTFR F20 IFCC IOKEEP RCWCCW RDEVSER R15 TEMPR8 VMCLASSC	BLANKS C1 DMKDSPCH DMKPRGSM DMKVMCFC F256 IL LOCK RCWCTL RDEVTYPC R2 TYP3480 VMCLASSD VMCLASSD VMCLASSD VMCXSTAT VMJSTAT VMVPOREL	DMKPSAFP DMKVSIEX F3 IOBCAW MCHAREA RCWGEN RDEVTYPE R3 UC VMCLASSE VMDVSTRT VMMCODE VMSEG
DMKHVD	ACNTUSER BRING CPUVERSN DMKDRDSY DMKPTRAN DMKUDRMD F256 MPUOPER NPRTBL PPMHP025 PPMPP PREFIXB RDEVTYPE R3	ACORETBL CLASFBA C1 DMKDSPCH DMKDSPCH DMKUDRRV F3 NICBLOK NPRVOL PPMHP03 PPMSCH PROC10 R0 R4 SAVEWRK3 SWPREF1 TYP3380	CLASGRAF DEFER DMKFREE DMKRPAPT DMKUDUMN F4 NICGRAF PAGCORE PPMHP032 PPMSEPP PROCIPL R1 R5 SAVEWRK4 SWPREF2 VMACCOUN	AFREE CLASTERM DMKACOQU DMKFRET DMKSCNVS F0 F4095 NICSIZE PMAAVAIL PPMHP034 PPMVDLE PSA R10 R6 SEGINV SYSTEM VMACOUNT VMNOECPS	AFRET CORDISA DMKCPEID DMKHPSDG DMKSNCP F1 F4096 NICTYPE PMASTAT PPMHP036 PPMVMSP1 RDEVBLOK R11 R7 SEGPAGE TYPBSC	APSTAT1 CORFLAG DMKCPEPP DMKIOEFM DMKSNTQN F16 F8 NPRCNT PPMAP PPMHPO4 PPMVMSP2 RDEVCODE R12 R8 SPMPFX TYP2314 VMAIP2	APTRAN CORTABLE DMKCPVAA DMKPGUPR DMKSYSRM F2 IOERETN NPRNAME PPMBSEPP PPMHP042 PPMVMSP3 RDEVFLAG R13 R9 SWPCHG1 TYP3277 VMASCCPD VMPSW	APTRLK CPSPMODE DMKCVTDB DMKSYSTZ F20 IOKEEP NPRPAGCT PPMHP05 PPMHP05 PPMHP05 PPMHP05 PPMHP05 RDEVNICL R14 SAVEAREA SWPCHG2 TYP3278	ASYSVM CPSTAT2 DMKDRDER DMKPSAP0 DMKUDRDS F24 I PUADDR NPRPNT PPMHP01 PPMHP01 PPMHP2 PPMVMSP5 RDEVOWN R15 SAVEREGS SWPCYL TYP3330 VMESTAT	DMKPSASP DMKUDRFU F255 LOCK NPRSTART PPMHPO2 PPMM21 PREFIXA RDEVTYPC R2 SAVERO SWPFLAG TYP3350 VMEXTCM VMRSTAT

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ZEROES

DMKHVE	R14	BRING CLASGRAF CORVM DMKDSPNP DMKPTRAN DMKSYSEA F1 IOKEEP NICD3275 NICSIZE	DMKPTRAQ DMKSYSER F2 LOCK NICD3276 NICTMCD PSAMSS RDEVMDL RDEVWTH R2	CPEXBLOK DMKFREEP DMKPTRLK DMKSYSTP F255 L1 NICECOL NICECOL NICWTH RDCBLOK	CPEXMISC DMKFRET DMKPTRUL DMKVMDIA F3 L2 NICEHLT NICEHLT NIC14AD RDCPAGAP RDEVNICL	DMKIOEFR DMKRPAGT DMKVSIEX F4 L4 NICLLEN PROTBLOK RDEVADVF RDEVPSS R1 R5	CONTINUE CPEXR1 DMKIOEHS	DMKSCNVD FFS F6 NICADVF NICPT PROTERR	C1 DMKLOCKQ	DMKSSSMQ FTR35MB F9 NICDTYPE NICRDED PROTPALT
DMKHVF	F7 LANGPAGE NLSVOL RDEVTYPC R2 SAVER0 SAVEWRK7	DMKRPAPT DMKVBMVR IOERETN LANGSIZE PREFIXA RDEVTYPE R3 SAVER1 SAVER1 SAVEWRK8 SFBFLASH	DMKVSWDC IOKEEP LANGVMBK PSA	DMKVSWOR LANGADDR LOCK PSALANG R1 R5 SAVER2 SFBCLAS SFBLAS SFBLDBEG SFBUSER SPSPLNKC	DMKPGUVG DMKSCNVS F0 LANGBLOK MATCH PSWCC2 R10 R6 SAVER6 SFBC0PY SFBLDMID SPCHAR	DMKPGUVR DMKSCNVU F1 LANGLANG NLSNAME PSWCC3 R11 R7 SAVEWRK1 SFBDEST SFBLOK SPCHAR1 SWPFLAG	APTRAN CPEXSIZE DMKPSAFP DMKSFBNS F256 LANGLOCK NLSNEXT RDEVBLOK R12 R8 SAVEWRK2 SFBDIST	DMKPSASP DMKSNTLA F4 LANGNEXT NLSPGCT RDEVCODE R13 R9 SAVEWRK3 SFBFILID SFBSHOLD SPCHAR3	F4095 LANGNTRY NLSSTRT RDEVFLAG R14 SAVEAREA SAVEWRK4 SFBFLAG	DMKVBMIN F4096 LANGNTSZ NLSTBL RDEVOWN R15 SAVEREGS SAVEREGS SAVEWRK5 SFBFLAG3
DMKIDR	ADSPCH DMKDSPCH IXIRA R1 R5 SAVEWRK2 SRTFLAG SRTSPND SSCTSFVM	IXNEXT R10 R6 SAVEWRK3 SRTGIND SRTVMADD	AFRET DMKFRET IXREGS R11 R7 SAVEWRK4 SRTIPND SSCBADDR UIUCGLBL	SRTNEXT	IXR12 R13 R9 SAVEWRK6 SRTPATH SSCIXADD	IXSIZE R14 SAVEAREA SAVEWRK7 SRTPREV SSCIXLST	CC0 DMKUDRIA LOCK R15 SAVEREGS SAVEWRK8 SRTRESID SSCRTADD ZEROES	SAVEWRK9 SRTRRPND	SRTRVPND	CC3 IXEXBLOK R0 SAVEWRK1 SRTBLOK SRTSIZE SSCTSFPT
DMKIDU	DMKDMPSI DMKRPAPT	BALRO DMKCKSCV DMKDRDDD DMKRSPSC	ALOCBLOK BALR1 DMKCKTUU DMKERMCP DMKSCNEP DMKVDHOR	BRING DMKDMPAL DMKFREE DMKSCNMU	DMKPGTDS	DMKDMPBG DMKPGTVR	CORSWPNT	CORTABLE DMKDMPDV DMKPSTIN	DMKDMPRC	DEFER DMKDMPSF DMKRIORN

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MODULE

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	RECCYL R12 R8	OWNDRDEV RECMAP R13 R9 SFBSYSID	RECMAX R14 SAVEAREA	RDEVALLN RECPNT R15 SAVEREGS SYSTEM	RDEVBLOK RECSIZE R2 SFBCDMP TEMPR1	RECUSED R3	RO R4 SFBFLAG5	R1 R5	R10 R6	RECBLOK R11 R7 SFBSIZE
DMKIMG	CLEAR RO R6	CURRSAVE R1 R7	EGPRO R12 R8	EGPR15 R13 R9	EGPR8 R14 SAVEAR	LOCCNT R15 SSAVE	MAINHIGH R2 STRTADDR	R3	NUCON R4	RENAME R5
DMKIOC	RCUBLOK RDEVMDL R12	CLASDASD RCUTYPE RDEVMOO4 R14 TYP3330	CLASFBA RCU2701 RDEVNAME R15	CLASTERM RCU2702 RDEVRDC R4	OBRDEVSH RCU2703 RDEVSADN R5	RDCBLOK	RDCDVCLS	RDCOBR	RDEVBLOK	PSA RDEVCUA R10 TYP2311
DMKIOE	DMKSYSTZ ERRIOB ERRSIZE F60 IOBQSTAT IOERCSW IOERSIZE IRMLMT MIHDEVT OBRRECN RDEVTYPC R2 SAVER1 SDRLNGTH	CPUID DMKIOFIN D2 ERRIOER ERRSRDEV F7 IOBRADD IOERDATA IOERTEMP IRMLMTCT MIHINT OBRSHOBR RDEVTYPE R3	DMKIOFM1 EQCHK ERRKEEP ERRSW2 F8 IOBSIZE IOEREXT IOERVSER IRMMAXCT MIHKEYN OBRSWSN	DMKCVTAB DMKIOFOB ERRBLOK ERRKEY ERRVOLID IFCC IOBSPEC5 IOERFLG2 IRMAND IRMOR MIHREC PSA R1 R5 SAVEWRK7 TYP2305 TYP3480	DMKIOFST ERRCCNT ERRMIOB FTREXTSN IOBCP IOBSTAT IOERFLG3 IRMBIT1 IRMRLADD MIHSIZE RDEVBLOK R10 R6	DMKDSPCH DMKIOFVR ERRCCW ERRMIOER F1 IOBFATAL IOERBLOK IOERFLG4 IRMBIT2	DMKFREE DMKIOGF1 ERRCORR ERRMSIZE F255 IOBFLAG IOERCCRA IOERFLM IRMBLOK LOCK MIHUSER RDEVFTR R12 R8 SDRCPID TYP3350	DMKFREEP DMKFREEP DMKFREEP ERRCPID ERROBR F256 IOBHVC IOERCCRL IOERLEN IRMBYT1 MIHCPID MIHVOL RDEVIRM R13 R9 SDRCTRS TYP3375	DMKFRET DMKSCNRD ERRFLAG ERRPARM F4 IOBIOER IOERCEMD IOERCEMD IOERPNT IRMBYT2 MIHCUA1 MP RDEVSER R14 SAVEAREA	CPEXR11 DMKIOCVT DMKSTKCP ERRHEADR ERRSDR F4095 IOBLOK
DMKIOF	CPUVERSN DMKIOEEP DMKIOESQ DMKRPAPT ERRSDR IOERDATA	DEFER DMKIOEES DMKIOEVQ DMKSTKCP FFS IOERFLG3 RDEVCTRS R1 SDRCPID SDRRDEV	DMKIOEIP DMKIOJBL ERRBLOK F15 IOERREAD RDEVTYPC R10	DMKERMSG DMKIOEIQ DMKIUACP ERRFLAG F255 IXBLOK RDEVTYPE R11 R7 SDRCTR8 SDRSHRT	DMKIOEMQ DMKPGUVG ERRIOB F4 IXIRA	DMKFREEP DMKIOEMX DMKPGUVR ERRIOER F4095 IXREGS RECFLAG1 R13 R9 SDRCUA SDRSIZE1	DMKFRET DMKIOENI DMKPTRAN ERRKEEP F7 IXSIZE RECNXT R14 SAVEAREA SDRFLCT	DMKIOENQ DMKPTRLK ERROBR IOERBLOK LOCK RECPAG R15 SAVEREGS SDRLNGTH SYSTEM	DMKIOECL DMKIOERP DMKPTRUL ERRPARM IOERCPID PREFIXA RECPAGFL R2	CPEXSIZE DMKIOECQ DMKIOERQ DMKRPAGT ERRPATH
DMKIOG	AFREE ARIOCT	ALLCHANS ASYSVM	AMCHAREA BRING	AP CHANID	APSTAT1 CLASFBA	APSTAT4 CLEAR	APTRAN CPEXSIZE	APTRLK CPSTAT5	APUOPER CPSTAT6	ARIOCH CPTCH

	DMKSCNRU F1 MCHCPEX MODEL145 MOD3083 MP	DMKERMSG DMKIOEMX	DMKIOENI DMKSIX60 F7 MCHFIX	DMKIOECE DMKIOETY DMKSYSCT G16CHAN MCHLEN1 MODEL158 MOD4321 POFFLINE RDEVBLOK	DMKIOECL DMKIOHFR DMKSYSEA IOELPNTR MCHMODEL MODEL165 MOD4331	DMKPGUVG DMKSYSER IPUADDR MCHPROCA MODEL168 MOD4341 PROCIPL RDEVTYPC RECPAGFL R14	DMKIOEEP DMKPGUVR ECSWLOG LOCK	DMKIOEES DMKPTRAN FFS LPUADDR MCNPSW MOD3032 MOD9081 RCHBLOK RECCCPD RECPAGFR R2	DMKCCHSZ DMKIOEFR DMKRPAGT FXDLOG LPUADDRX MODEL135 MOD3033 MOD9083 RCHDISA RECFLAG1 RECPAG1U R3 SAVEWRK7 ZERO	DMKIOEHS DMKRPAPT F0 MCHAREA MODEL138 MOD3081 MOD9190 RCHSTAT RECFLAG2 R0 R4
DMKIOH	ADSPCH CPUID DMKIOEES DMKPTRLK F1 IOBLOK PSA R12 R8	AFREE DE DMKIOEFR DMKPTRUL F10 IOBRADD RECFLAG1 R13 R9	DMKRIODV F6 IOBSIZE		DMKDSPCH DMK10EMX DMKRPAGT 10BCC2 10BUSER RECPAGFA R2	APTRLK DMKERMSG DMKIOENI DMKRPAPT IOBCSW LOCK RECPAGFL R3 TYPSRF	BRING DMKFREE DMKIOETY DMKSCNMU IOBFATAL MCHAREA RO R4 UE	CE DMKFRET DMKIOSQR DMKSCNRU IOBFLAG MCHMODEL R1 R5 VMBLOK	DMKSYSER I OB I RA	CLASSPEC DMKIOECL DMKPGUVR DMKSYSTZ IOBLINK MP R11 R7
DMKIOJ	RDCMDR R0 R6	ERRCCNT ERRSDR IOERBLOK OBRBLKLN OBRHAN OBRSNSCT RDEVBLOK R1 R7 TNSDEVAD TYP3203 TYP3203	I OERCSW OBRCCHS OBRIORTY OBRSSDR1 RDEVCTRS R10 R8	ERRCONT FTREXTSN IOERDATA OBRCORL	ERRCORR F15	OBRPBN OBRVOLN RDEVMOO4 R15 SDRCTRS TNSSWS3	ERRIOB F4 IOERSIZE OBRCUAIN OBRPGMN OBR33SNS RDEVRDC R2 SDRFLAGS TNSVOLID TYP3330 TYP3480	CLASTERM ERRIOER F7 IOERVSER OBRCUAPR OBRRECN PREFIXB RDEVTYPC R3 SDRLNGTH TNS3480S TYP3340 TYP3505 XOBRT3	ERRKEY F8 LOCK OBRDDCNT OBRSDRCT PSA RDEVTYPE R4	CPUID ERRMIOB IOBFATAL LPUADDR OBREOD OBRSDRSH RDCBLOK RECPAG R5 SDRSHRT TNS8809S TYP3370 TYP38003 XOBR150
DMKIOQ	AFREEP AVMREAL CPEXR15 DMKIOSMQ F0 IOBERP IOBWINI IOBSIOF IOBUSER PREFIXA RCHPEND RCUCHCNT RCUSHRD RDEVCUB	AFRET BUSY CPEXR6 DMKIOSNM F1 IOBFATAL IOBMQDTO IOBSIZE IOBVADD PREFIXB RCHPROC RCUCUBSY RCUSTAT RDEVCYL	IOBBPNT IOBFH IOBOFF IOBSNSIO IOBVCUE PROCIPL RCHQCNT	DMKSCHRT 10BCC1 10BFLAG 10BSPAG 10BSPEC LOCK PSA RCHRSTQ RCUF10B RCUTYPE	CPSPMODE DMKSCNRD 10BCC3 10BFLT 10BPATHF 10BSPEC2 LOCKSAV RCHADD RCHSTAT RCUPRIME RDEVADD	DMKSCNRU 10BCP 10BFPNT 10BSPEC3 LPUADDR RCHBLOK RCHTYPE RCUQCNT RDEVAOF	DE DMKSTKIO IOBCSW IOBHVC IOBQDTO IOBSPEC4 LPUADDRX RCHBUSY RCUADD RCURSTQ RDEVBLOK	DMKFREEP DMKSTKLF IOBCTRQ IOBIRA IOBRADD IOBSPM MP RCHCUTBL RCUBLOK RCUSCED RDEVBOF	DMKFRET DMKVIOIN IOBCUBSY IOBLINK IOBRELCU IOBSTAT MPGEND RCHFIOB RCUBUSY	DMKIOSCB FFS IOBCYL IOBLOK IOBRSTRT IOBUNSL MPUOPER RCHMPX RCUCHA RCUSENSE RDEVCUA

#### MODULE

#### EXTERNAL REFERENCES (LABELS AND MODULES)

MODULE					HODULLOJ					
	R11 R7	R12 R8	RDEVSCHD R13 SAVEAREA TIMEDISP	R14 SAVER11	R15 SAVEWRK1	R2	RDEVTYPC R3 SAVEWRK3	R4	R1 R5 SAVEWRK6	R10 R6 SAVEWRK7
DMKIOS	DMKTAPER F8 IOBCP IOBHIO IOBQSTAT IOBQSTAT IOBSPLT IOERBLOK IOERSIZE MP RCHBLOK RCHSCED RCUPRIME RCHSCED RCUPRIME RCU3880 RDEVCYL RDEVNRDY RDEVSENS RDEVTYPE R14 SAVEAREA SKIP	CPRUN CSW DMKDSPA DMKIOQDU DMKLOKSY DMKTPERP IFCC IOBCSW IOBHVC IOBMINI IOBRADD IOBSPM IOERCCRA IOERSNSS MPUOPER RCHSEL RCUQCNT RCHSEL RCUQCNT RCEVOED RDEVOED RDEVOFF RDEVSER RDEVUNSN R15	DMKIOQFC DMKRSPER DMKRSPER DMKRDSI IL IOBCUBSY IOBIMSTK IOBRCAW IOBSIOF IOBSTAT IOERCCW IPUADDR PCI RCHBUSY RCHSTAT RCURSTQ RCWCCW RDEVDISA RDEVDISA RDEVSTAT RDEV333V R2 SAVERETN SYSVIRT	DMKV101N INTREQ 10BCYL 10B10ER 10BPAG 10BRELCU 10BS1ZE 10BT10 10ERCP1D 1PUADDRX PREF1XA RCHCUTBL RCWSCED RCWTASK RDEVF10B RDEVPROC RDEVSTA2 RUNUSER R3 SAVER11 TEMPSAVE TRCTCH	DFRCC1 DMKFREE DMKIOQFX DMKSCNPH FFS IOBBPNT IOBERCNT IOBIOLOK IOBPATH IOBRES IOBSNSIO IOBUC IOERCSW LASTUSER PREFIXB RCHFIOB RCH370 RDEVAIOB RDEVFLAG RDEVGLAG RDEVGLAG RDEVGLAG RDEVGLAG RDEVSTA3 RO R4 SAVEWRK1 TIMEDISC TYP83C7 TYP8375	DMKFREEP DMKFREEP DMKSCNRD FTRRPS IOBCAW IOBERP IOBIRA IOBPATHF IOBSPEC IOBUNSL IOERDATA LOCK PRGC RCHMPX RCUBLOK RCUSENSE RDEVBLOK RDEVFTR RDEVGCNT RDEVSTA4 R1 SAVEWRK2	CPEXADD CPSTAT2 DMKBSCER DMKFRET DMKFRET DMKIOQQU F10 IOBCC1 IOBFATAL IOBLDRUN IOBPROC IOBRREL IOBSPEC2 IOBUSER IOEREXT LOCKSAV PRTC RCHPEND RCUBUSY	CPEXBLOK CPEXAL4 DMKCCHIS DMKIOQSK DMKIOQSK DMKIOQSK DMKSSUI1 F16 IOBCC2 IOBFH IOBLDSZ IOBPST IOBRSTRT IOBSPEC3 IOBVADD IOERFLG3 LPUADDR PSA RCHPROC RCUCHA RCUSTAT RDEVBZCH RDEVBZCH RDEVBZCH RDEVSTA7 R11 R7 SIGWAKE	CPSTAT6 DMKDADER DMK10QUE DMK10QUS DMKSTKCP F2 10BCC3 10BFLAG 10BL1NK 10BPVM 10BRTCT 10BSPEC4 10BRTCT 10BSPEC4 10BRCUE 10ERLEN MNCLSEEK PSA10SW RCHQCNT RCUSUB RDEVCUA RDEVCUA RDEVSTAD RDEVSTAD RDEVSTAD R12 R8 S1GXC	CPEXR5 CPTCH DMKDASER DMKIOQDH DMKLOKDF DMKLOKDF DMKSTKIO F256 IOBCLRIO IOBFPNT IOBLOK IOBGPNT IOBBOK IOBSPEC5 IOBVHIO IOERNOLG MNCOCYL RCHADD RCHRSTQ RCUFIOB RCUTYPE RDEVCUB RDEVSCHD
DMKIOT	DMKIOSRH DMKQVMCU	DMKQVMTS DMKSTKIO INTREQ IOBERP IOBMISC	DMKIOSRS	C2 DMKCPXZT DMKGRFIN DMKIOSRU DMKRIOCN DMKVIOIN IOBPNT IOBFLAG IOBPMINT	CPCREG8 CPSTATUS C6 DMKDIAIR DMKIOSCB DMKIOSST DMKRNHIN EMSMASK IOBCAW IOBFPNT IOBPVM	CPEXADD CPSTAT2 C8 DMKDMPC2 DMKIOSC1 DMKLOKIO		CPEXFPNT CPSUPER DEFER DMKDSPA DMKIOSEN DMKPMAEW DMKSCNRU F1 IOBCLRIO IOBIOER IOBRADD	CPEXR13 CPWAIT DFRCC DMKDSPCH DMKIOSER DMKPMAIO DMKSSUI1 F8 IOBCP IOBIRA IOBRCAW	CPEXSIZE CSW DFRCC1 DMKDSPQI DMKIOSRC DMKPTRAN

	I OERBLOK LPUADDR PROCIO RCUCUBSY RDEVAIOB RDEVDIIP RDEVPROC	RDEVATT RDEVDISA RDEVQBSY RDEVSTA3 RUNUSER R3 SAVEWRK2 SYSTEM	MICBLOK PSA RCUFIOB RDEVBLOK RDEVFIOB	RDEVFLAG RDEVQ10B	QUANTUMR RCUQCNT RDEVBZCH RDEVFTR RDEVRDY RDEVSTA7 R10 R6 SKIP TIMEDISP	RCUSCED RDEVCC3 RDEVIOER RDEVRSTA RDEVTYPC R11 R7 SM TIMER TRCSMINT	RDEVMID RDEVSCHD	RDEVCPIO RDEVNRDY RDEVSENS	RDEVOFF RDEVSPT RDEVUSER R14 SAS SPTINTAD	PRGC RCUBUSY RCUTYPE RDEVDED RDEVPIOB RDEVSTAT RDEV333V R15 SAVEAREA
DMKISM	AFREE F8 RCWPNT R14 SAVEAREA	CD IDA RCWRCNT R15 SAVEREGS	C1 IOBCAW RCWTASK R2 VMBLOK	DMKFREE IOBIRA RCWVCAW R3 XPAGNUM	DMKPTRAN IOBLOK RO R4	DMKPTRUL IOBMISC R1 R5	DMKUNTIS PSA R10 R6	F16 RCWCCNT R11 R7	F2 RCWCCW R12 R8	F4 RCWIO R13 R9
DMKIUA	ADSPCH CCTFLAG1 CCTRPYN CODE CPEXSIZE DMKIUJEP DMKSTKCP IUCBFAD1 IUSFLAG2 IUSSIZE MSGBLOK MSGSNDOP PDENT PROPSW R2 SAVERETN TRACIUCV	CCTRPYP CPEXADD C1 DMKIULEP F0 IUCLINK1 IUSINSTR IUSYMBK MSGERROR MSGTAG PDFLAGS PSA R3 SAVERO	CCTRPYPR CPEXBLOK DEFER DMKIUNEP F1 IUCVBLOK	CCTRPYTL CPEXFPNT DMKDSPCH DMKIUNIN F4 IUCVCPEX IUSIUCV2 IXEXBLOK MSGFLAG3 MSGUSED PDMSGCT R1 R5 SAVESIZE	DMKIUPEP F4095 IUCVMB IUSLEN1 IXIRA MSGFPNT MSGWHTRC PDSEVERD R10 R6 SAVEWRK4	CCTSNDN CPEXRO DMKFREEP DMKIUSEP F5 IUSAVE IUSLEN2 IXREGS MSGNOFL PDAPPC PDSINV R11 R7	CCTSNDP CPEXR10	CCTSNDPR CPEXR3 DMKIUCEP DMKPRGSM F60 IUSCPENT IUSPAGE2 LOCK MSGPURGE PDAPPEND	CCTRCVTL CCTSNDTL CPEXR4 DMKIUEEP DMKPSASP IETYPRNP IUSFCODE IUSFCODE IUSFCODE IUSFCODE IUSFCODE IUSFCODE PARMSS MSGANSLN MSGSCPID PDAPREQS PREFIXA R14 SAVEAREA	CC2 CPEXR5 DMKIUGEP DMKPTRAN IETYPRP IUSFLAGS IUSFLAGS IUSFLAGS IUSFLAGS PSGAUDIT MSGSIZE PDCPXQ PROBMODE R15
DMKIUB	DMKIOFSV DMKRPICN DMKVMGCN MSGANSLN MSGFLAG3	CCTSNDPR DMKCRMCN DMKIUAPD DMKRPIIL DMKVMGIL MSGAPPC	DMKIUAQU DMKRPIQS DMKVMGSV MSGAUDIT MSGID	C1 DMKCRMQS DMKIUNIN DMKRPIRM F0 MSGBLOK MSGMASK1 MSGTAG PDSTRECV R13	CCTRPYN DEFER DMKCRMSV DMKIUSET DMKRPISV IUCBFAD1 MSGCTLS MSGNORPY	CCTRPYP DMKAPSCN DMKFREE DMKMSGCN DMKVCPIL IUCCBFA1 MSGCTLT MSGCTLT MSGTGPID PSA R15	CCTRPYPR DMKAPSIL DMKFRET DMKMSGC2 DMKVCWCN IUCPNDHD MSGDESC MSGPRMD MSGWHTRC RECQMSG R2	CCTRPYTL DMKAPSSV DMKIDRCN DMKMSGSV DMKVCWQS IUCPNDTL MSGERROR MSGPRTY PDAPMSGP RPYQMSG R3	PDAPPCFL R0 R4	CCTSNDN DMKBIOIL DMKIOFCN DMKPTRAN DMKVCWSV LOCK MSGFLAG2 MSGSCCLS

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MODULE	EXTERNAL	REFERENCES	(LABELS	AND	MODULES)
MODULE	EXILINAL	ILI LILIOLO	(LADELO	AND	MODULLO)

HODOLL			(1000) (10							
DMKIUC	CONFSYNC CONVFIXL CPEXR14 DMKIUBTB F10 IUSAVE IXNEXT MSGSNDOP PDMSGLIM PDSTRECV RCBADLIM RC2MANY R2 SAVERO	CONFTPN CONVLUNM CPEXSIZE DMKIUGGP IUCBFAD1 IUSCCODE LOCK MSGTGPID PDPEND1 PDSTSEND	CONVMARE C1 DMKIUNIN IUCCBFA1 IUSCPENT MSGAPPC PDALLOCD PDPEND2 PDTGIUCV RCEXTLEN R0 R4 SAVEWRK2	CONILUNM CONVMODE DEFER DMKPSAFP IUCMXCN IUSFLAG2 MSGBLOK PDAPPC PDPRMD PDTGPID RCINVLUN R1 R5	CONVTOTL DMKDSPCH DMKDTRAN IUCPNDHD IUSIUCV MSGFLAGS PDAPPCFL PDPRTY PDVALID RCINVMOD R10 R6	CPEXADD DMKFREE DMKSCNAU IUCPNDTL IUSPAGE1 MSGFLAG2 PDAPSYCF PDSEND PENDCONN RCINVSRV R11 R7	CPEXBLOK DMKFREEP DMKSTKCP IUCTOTCN IUSPATH MSGNORPY PDCNTRL PDSEVERD PREFIXA RCNLOG R12 R8	CONNECT CPEXMISC DMKFRET DMKUDRIA IUCVBLOK IXBLOK MSGSCPID PDENT PDSTALLC PSA RCNOPATH R13 R9	CONEXTSZ CONPILEN CPEXRO DMKIDRFN DMKUDRRV IUCVCPEX IXEXBLOK MSGSNDAD PDFCNCD PDSTATE RCBADDIR RCNTRGIU R14	CONP2LEN CPEXR10 DMKIUAPD F0 IUCVMB IXIRA MSGSNDLN PDFLAGS PDSTCONN RCBADFCN RCPTHSVD R15 SAVEREGS
DMKIUE	DMKIUAQU F1 IUSPATH MSGARLST MSGFLAGS MSGPRMD PDAPPC PDTGIUCV RCPURGED R10 R6	DMKIUSPD F4095 LOCK MSGASNAX MSGFLAG2 MSGFLAG2 PDAPPCFL PSA RCRECVSH R11 R7 SAVEWRK5	MSGASNPX MSGFLAG3 MSGSCPID	DMKPSACC IUCVMB MSGABTOT MSGAUDT1 MSGFPNT MSGSNDAD PDFLAGS RCAPPC RCTINVLN R13 R9	IUSAVE MSGAIINV MSGAUDT2 MSGID MSGSNDLN PDFLAGS2 RCBUFBND RCTOTLEN R14 SAVEAREA	C1 DMKPSASC IUSCCODE MSGAITRN MSGAUDT3 MSGSNDOP PDLRCINV RCMSGLEN RCTTRUNC R15 SAVEREGS	IUSCPENT MSGANSLN MSGBLIST MSGMASK1 MSGTGCLS PDLRECL RCNEGLEN RECQMSG R2 SAVERO	DMKATSCF DMKPTRAN IUSFLAG2 MSGAPPC MSGBLOK MSGRORPY MSGTGPID PDSEVERD RCNODATA RPYQMSG R3 SAVEWRK1	DMKPTRUL IUSIUCV MSGARCAX MSGCTLT MSGPARTL MSGWHTRC PDSTATE RCNOPATH R0 R4 SAVEWRK2	IUSMSGBK MSGARCPX MSGDESC MSGPRM PDALLOCD PDSTCONN RCPROTCK R1 R5
DMK I UG	F6 LOCK	IUCMXCN MSGAPPC MSGFLAG3 MSGTGCLS	MSGFPNT MSGTGPID PDSIZE R1 R6	CCTSNDPR IUSAVE MSGAUDIT MSGID	CCTSNDTL IUSCCODE MSGAUDT1 MSGMASK1 PDAPPCFL	DMKFREE IUSCPENT MSGBLOK MSGNORPY	DMKFRET IUSFLAG2 MSGCTLS MSGPRMD PDENT PSA R14	DMKIUAPD IUSIUCV MSGCTLT MSGPURGE	RCNODATA R2	FO IUSPATH MSGFLAGS MSGSCPID
DMKIUJ	CC1 CPEXR10 DMKIUAPD IUCTOTCN IXBLOK MSGEBROR	CC2 CPEXR14 DMKIUBTB IUCVBLOK IXEXBLOK MSGFLAGS	AFREEP CCTMXPDS CC3 CPEXR3 DMKIUNIN IUCVCPEX IXIRA MSGFLAG2 MSGWHTRC PDFCNCD	CONEXT CPEXSIZE DMKPTRLK IUCVMB IXNEXT MSGFPNT	CONLENTH DMKCVTAB DMKPTRUL IUSAVE IXSIZE MSGID PDAPLOCK	CPEXADD DMKDSPCH DMKSCHST IUSCCODE LOCK MSGPURGE	CCTRCVHD CPEXBLOK DMKFREE DMKSTKCP IUSCPENT MSGAPPC MSGSCCLS PDAPPCFL	CPEXFPNT DMKFREEP F1 IUSFLAG2 MSGASVRD	CCTSNDHD CPEXMISC DMKFRET IUCPNDHD IUSIUCV MSGAUDT2 MSGSNDLN	CPEXRO DMKIUACU IUCPNDTL IUSPATH MSGBLOK

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MODULE	EXTER	RNAL REFE	RENCES (L	ABELS AND	MODULES)					
	PDSEND PDTGIUCV RCINVSCD R11 R7 SAVEWRK6 TRQREGS	RCINVSEV R12 R8	PDZERO RCMSGCT R13 R9 SAVEWRK8	PDSTALLC PENDCONN RCNONAPP R14 SAVEAREA SAVEWRK9 ZEROES	PREFIXA RCNOPATH R15 SAVEREGS	PSA RCSNDOP R2	PDSTCONN RCCOMSRV RCTRUNC R3 SAVER1 TRQBLOK	RCINVCNF RO R4 SAVEWRK1	PDSTSEND RCINVCON R1 R5 SAVEWRK2 TRQBUSER	RCINVREC R10 R6 SAVEWRK3
DMKIUL	DMKIUELR FO IUSPATH MSGAPRMD MSGAUDT3 MSGMASK1 MSGTGPID PDPRMD	MSGBLOK MSGPRM MSGWHTRC PDSEVERD RCNOPATH RPYQMSG R3 SAVEWRK1	IUCVBLOK MSGAALEN MSGARPAX MSGCTLS MSGPRMD PDAPPC PDTGIUCV	IUCVMB MSGAANAX MSGARPLE MSGERROR MSGPRTY PDAPPCFL PSA RCPRMLST R1 R5	PDCNTRL RCADDRCK RCPROTCK R10 R6 SAVEWRK4	C1 DMKPSAFP IUSCCODE MSGAATOT MSGATINV MSGFLAG2 MSGSCCLS PDENT RCANSBND RCPURGED R11 R7	IUSCPENT MSGALIST MSGATTRN MSGFLAG3 MSGSCPID PDFLAGS RCAPPC RCRECVSH R12 R8	DMKATSCF DMKPSASP IUSFLAG2 MSGANSAD MSGAUDIT MSGFPNT MSGSNDOP PDFLAGS2 RCIINVLN RCSNDLST R13 R9	MSGANSLN MSGAUDT1 MSGID MSGTAG PDLRCINV RCMSGLEN RCTOTLEN R14 SAVEAREA	DMKIUAQU DMKPTRUL IUSMSGBK MSGAPPC MSGAUDT2 MSGTGCLS PDLRECL RCNEGLEN RCTRUNC R15 SAVEREGS
DMK I UN	F4 IUSCPENT MICPEND MSGDESC MSGPRM MSGTAG PDFLAGS PREFIXA RCNOSEND R2	CCTSNDN DMKDSPCH IETYPMNP IUSFLAG2 MICVPSW MSGFLAGS MSGPRMD MSGTGCLS PDMSGCT PSA RCPRMLST R3	CCTSNDP DMKFREE IETYPMP IUSIUCV MSGALIST MSGFLAG2 MSGFRTY MSGTGPID PDMSGLIM RCANSBND RECQMSG R4	CCTSNDPR DMKFREEP IUCMSG IUSIUCV2 MSGANSAD MSGFLAG3 MSGPURGE MSGUSED PDPRMD RCAPPC R0 R5	IUCVBLOK IUSMASK MSGANSLN MSGFPNT MSGSCCLS PDAPMSGP PDPRTY RCBUFBND R1 R6	CCTICTRL CPEXADD DMKIUAPD IUCVCNT IUSMSGBK MSGAPPC MSGID MSGSCPID PDAPPC PDSEND RCMSGCT R11 R7	CPEXBLOK DMKIUAQU IUCVMB IUSPAGE1 MSGBLIST MSGKEY MSGSIZE PDAPPCFL PDSTATE	CPEXRO DMKIUBTB IUSAVE IUSPATH MSGBLOK MSGMASK1 MSGSNDAD PDCNTRL PDSTRECV RCNOPATH R13	CPEXR11	PDFCNCD PDTGPID RCNOPRTY R15
DMKIUP	DMKUDRFU IUCMXCN IUSCPENT MSGSIZE PDSEVERD R1 R5	AFREE CCTCLPS CCTSNDN DMKDSPCH DMKUDRMD IUCPNDHD IUSFLAG2 MSGUSED PDTGIUCV R10 R6 SAVEWRK4	CCTSNDP DMKFREE DMKUDRRV IUCPNDTL IUSIPSIZ PDAPPC PDTGPID R11 R7	CC2 DMKFREEP IUCBFAD1 IUCSIZE	IUCBFAD2 IUCVBLOK IUSIUCV2 PDAVAIL PREFIXB R13 R9	CCTICTRL CPEXADD DMKIUACU IUCBFLN1 IUCVCPEX IUSMXCN PDCNTRL PSA R14	CPEXBLOK DMKIUAPD	CPEXMISC DMKIUBTB IUCCBFA1 IUSAVE LOCK PDFCNCD	CPEXRO DMKIUNIN	CPEXR10
DMKIUS	ADSPCH CCTPNDSN	AFREE CCTRCVHD	AFREEP CCTRCVTL	AFRET CCTSNDHD	BALRSAVE			CCTFLCNT CC1	CCTMSGCT CC2	CCTPDSEG CC3

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MODULE	EXTERNAL	REFERENCES	(LABELS	AND	MODULES)

	MSGTGPID PDAPREQS PDPEND1 PDTGIUCV RCINVSND R0 R4	DMKFREE F0 IUSPATH MSGCTLT MSGUSED PDAPSNDQ PDPEND2 PDTGPID RCMSGCT R1 R5 SAVEWRK9 TRQREGSZ	R10 R6 SENDQMSG	DMKFRET IUCMSG MSGANSAD MSGERROR MSGPRTY PDALLOCD PDCNTRL PDSTALLC RCANSBND RCNONAPP R11 R7	DMKIUAPD IUCVBLOK MSGANSLN MSGFLAGS MSGPURGE PDAPLOCK PDENT PDSTATE RCBUFBND RCNOPATH R12 R8 TEMPSAVE	IUCVCPEX MSGAPPC MSGFLAG2 MSGSCPID PDAPMSGP PDFLAGS PDSTCONF RCCOMSRV RCSNDOP R13 R9	DMKIUERC IUCVMB MSGARPLE MSGFLAG3 MSGSIZE PDAPPC PDFLAGS2 PDFLAGS2 PDFLAGS2 RCSYCLVL R14 SAVEAREA TRQBLOK VMIDLE	PDLRCINV PDSTRECV RCINVCON RCTRUNC R15 SAVEREGS	IUSCPENT MSGAUDT1 MSGKEY MSGSNDLN PDAPPEND PDLRECL PDSTSEND RCINVREC RECQMSG R2 SAVERO	DMKSCHST IUSFLAG2 MSGBLOK MSGNOFL
DMKJRL	BLANK	PWDDATE PWDUSRID	CL DMKFREE	DMKFRET FO PWDIBLOK RDEVADD R10 R6	ALARM CLASTERM DMKGRTTI F4 PWDINVCT RDEVBLOK R11 R7 SNARVMB	DMKLOKSW F8 PWDITRQ	APUOPER DMKACOQU DMKQCNWT LOCK PWDLGCNT RDEVSNA R13 R9 TRQBIRA	AQCNWT DMKCVTAB DMKSCHST NORET PWDLOG RDEVSNRB R14 SAVEAREA TRQBLOK	ASYSOP DMKCVTBD DMKSCNAU PSA PWDLUNAM RDEVSTA3 R15 SAVEREGS TRQBSIZE	ASYSVM DMKCVTBH DMKSCNFD PWDALOG PWDSIZE RDEVTYPC R2 SAVER0 TRQBUSER
DMKLDOO	AFREE C2	AFRET DE	APOINT	BLANKS DMKCPE	CAW DMKPSA	CC DMKWRM	CLEAR EIGHT	CONTINUE FSIZE	CSW IOMASK	CTL I PLPSW
	LASTMSG R2 UE	PSW R3 WRITE	READ R4	RO R5	R1 R6	R10 R7	R12 R8	R13 R9	R14 SILI	R15 UC
DMKLNK	R2 UE ACIADDR ACITGRP BLANKS DMKCFQRD DMKLOCKQ DMKSCNVU FTR2311B F8 PSA RDEVFTR R0 R4 SAVER1	PSW R3 WRITE ACICLR1 ACIUDIR BRING DMKCVTBH DMKCVTBH DMKSCOLI FTR2311T	READ R4 ACICODE ACIUSRID CDDED DMKCVTHB DMKPMADT DMKSSSLN	R0 R5 AC1FCN AFREE CDVADD DMKEPSWD DMKPTRAN DMKSSSMQ FTR70MB JPSLNKDS RDCBLKMX RDEVSEL R11 R7 SAVER12	R1 R6 ACILINK AFRET CLASDASD DMKFREE DMKPTRUL DMKSTKCP F0 LINKJRL	R10 R7 ACIMODE APTRAN CLASFBA DMKFRET DMKFRET	R12 R8 ACIPARMS APTRLK CPEXBLOK DMKJRLSL DMKSCNAU DMKUDRFD F2 LOCK RDEVCUP RDEVCUP RDEVSTAT R14	R13 R9 ACIRGRP ARIODC CPEXR1 DMKLNMSG DMKSCNFD DMKUDRFU F4 L1 RDEVDED RDEVSTA5 R15 SAVEREGS SAVEREGS	R14 SILI ACIRUSR ARIODV C1 DMKLOCK DMKSCNRD DMKUDRRV F4095 MASKLINK RDEVDISA	ACISIZE ASYSVM DEFER DMKLOCKD DMKSCNVS DMKVDSLK F7 MSSPRES RDEVFLAG RDEVFLAG RDEVTYPE R3

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MODULE	FXTF	RNAL REFE	RENCES (1)	ABELS AND	MODULES)					
HODOLL	SAVERO	SAVER1	•	SAVER2		SAVEWRK1	SAVEWRK2	SAVEWRK4	SAVEWRK5	SAVEWRK6
DMKLOC	LOCK R14	LPUADDR R15	AFREEP, DMKDSPCH MP R2 SAVERETN	AFRET DMKFREE PSA R3 SAVEWRK1	AP DMKFREEP R0 R4 SCAN	ASYSLC DMKFRET R1 R5 SYSLOCS	CPEXADD DMKSTKMP R10 R6	CPEXBLOK DMKSTKOP R11 R7	CPEXFPNT DMKSYSLB R12 R8	CPEXPROC DMKSYSLR R13 R9
DMKLOG	AFRET CPIEXLOG DMKLOHRC DMKUDRRV JPSCBLOK NICSIZE RDEVSTA3 R15 SAVEREGS	DMKLOJEP F1 JPSFLAGS NOTIME	DMKLOKSW F2 JPSLOGDS PSA RDEVTYPE R3 SAVER10	DMKRPWEP F255 LOCK RDEVATSW R0 R4 SAVER11	DMKSCNFD F4 LOGONJRL RDEVBLOK R1 R5 SAVER2	BLANKS DMKERMCP DMKSYSJR F5 MASKLOG RDEVFLAG R10 R6 SAVEWRK1	ACIRGRP BUFSIZE DMKERMSG DMKSYSSP F6 MP RDEVNICL R11 R7 SAVEWRK2 TIMEDISP	DMKFREE DMKSYSSZ F7 NICBLOK RDEVPSUP R12 R8 SAVEWRK3	F8 NICFLAG RDEVSNA R13 R9	DMKJRLLO DMKUDRMD INHIBIT NICPSUP RDEVSNRB R14 SAVEAREA
DMKLOH	DMKBLDRT DMKLOCKQ DMKSCNRD FFS IXR11 NEWPAGES PROCIPL RDEVSNA R1 R5 SAVER3	DMKCFSAS DMKLOCRD DMKSCNRN FO LANGBLOK NEWSEGS PROTBLOK RDEVSNRB R10 R6 SAVER5 SAVER5 SAVEWRK8 TRQBLOK	DMKLOCRQ DMKSYSPC F255 LANGLANG NICATRB PROTSIZE RDEVSTA3 R11 R7 SAVER6 SNARBLOK	DMKCVTBH DMKLOKIO DMKTRQ80 F3 LANGNEXT NICBLOK PSA RDEVTFLG R12 R8 SAVER7	DMKLOKSW DMKUDRFU F4 LOCK NICSIZE PSALANG RDEVTRQ R13 R9 SAVEWRK1 SNAROUT	CPSTAT4 DMKFREE DMKPGTMV DMKUDRMD F4096 LOCKSAV NICSTAT RDEVAIOB RDEVTYPC R14	NICTRQ RDEVAIRA RDEVTYPE R15 SAVEREGS	CP370EAV DMKFRET DMKQCOSY DMKUDRXI IOBLOK LPUADDRX NICUSER RDEVBLOK RDEVUSER R2 SAVER0	DMKSCHRT DMKVSDFH IOBUSER L8 PREFIXA RDEVNICL	DMKBLDEC DMKLOCKD DMKSCNAU D8 IXBLOK MP PREFIXB RDEVPS R0 R4 SAVER2
DMKLOJ	ADSPCH ASYSLC CPEXBLOK CPSTAT2 DMKLNKSB DMKSCNVU DMKVDSAT	CPSTAT4 DMKLOKIO	AFREEP AVMREAL CPEXR11 DMKACON DMKLOKSW DMKSSSL2 DMKVDTPG	DMKLOMSG	DMKNEAVR	CPEXR8 DMKDSPCH DMKSCHRT	APSTAT1 CLASTERM CPEXSIZE DMKERMSG DMKSCNRU DMKSYSMU	CPMICAVL DMKFREE DMKSCNVD	CPMICON DMKFREEP DMKSCNVN	DMKSCNVS
	F8 MICEVMA3 MICVTMR RDEVADD RDEVPAG RDEV333V R3 SAVER2	LOCK MICFSSE MICWORK RDEVATSW RDEVPRDV RO R4 SAVER5	LOCKSAV MICPTLB MP RDEVBLOK RDEVSER R1 R5 SAVEWRK1	LPUADDR MICRSEG MSSPRES RDEVBPAG RDEVSIZE R10 R7 SAVEWRK5	MICBLOK MICSIO PAGPGSWP RDEVCUP RDEVSTAT R11 R8 SAVEWRK8	MICCREG MICSIZE PREFIXA RDEVDED RDEVSTA3 R12 R9 SAVEWRK9	MICCREGO MICSKYMD PREFIXB	MICDASA MICSTBVR PSA RDEVFLAG RDEVSYS R14 SAVEREGS SIGSTART	MICEVMA MICSTSM2 PSAEVMA RDEVFTR RDEVTYPC R15 SAVER10 SYSLOCS	MICEVMA2 MICVPSW PSAMSS RDEVOWN

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DMKLOK	ADSPCH CODE CPLOKFL DMKPXBND MFAMASK R12 SVMSTAY TRCLOK	MP R14		CPUID EMSMASK PREFIXA R2	APSTAT1 CPEXPROC C0 F1 PREFIXB R3 TRACEND XTNDLOCK	DMKCVTAB LASTUSER PSA R4	DMKDSPCH LOCK R0 R5	APSTAT5 CPEXR11 DMKDSPRU LOKSAVE R1 R6 TRACSTRT	APUOPER CPEXR14 DMKFREEP LOKSAV2 R10 R9 TRACSVCR	ASYSVM CPEXSIZE DMKPXA LPUADDR R11 SVMNOUPD TRAC12
DMKLOM	ADSPCH CLASTERM DMKQCNWT DMKSYSLW LOCK RDEVFLAG R12 R8 SCRCMDR	AFRET DMKCQHFI DMKSCNRD DMKSYSMU LOGMBLOK RDEVSNA R13 R9 SNARBLOK	DMKSYSNM LOGMLEN RDEVSNRB R14 SAVEAREA	R15	APUOPER DMKCVTBH DMKSCNVU DMKSYSTM LOGMTXT RDEVTYPE R2 SAVER2 TIMEDISP	DMKSTKCP DMKUDRFU MP	NORET	BLANK DMKERMSG DMKSYSDT F1 OPERATOR R1 R5 SAVEWRK7	BLANKS DMKFRET DMKSYSDW F3 PSA R10 R6 SAVEWRK8	CLASSPEC DMKLOKSW DMKSYSLG F4 RDEVBLOK R11 R7 SAVEWRK9
DMKMCC	ACORETBL AQCNWT CORTABLE DASDCL DMKMCDIN DMKMNIST DMKPRGMC DMKSYSBF F4 LOCK MONBUFAV MONIOBF NORET RDEVFLAG R11 R7 SILI TYP3480	ASYSVM CPCREG8 DEFER DMKMCDLI DMKMNITH DMKPRGMI	F8 MNBHDLEN MONCHPTR MONNEXT PAGEND	MONSIZE PERFCL RDEVSYS R14	DMKDSPNP DMKMCDTI DMKMNL DMKRSPMN DMKSYSRV IOBCAW	DMKMNLFI DMKSCHRT FFS IOBLOK MNDVSIZE	DMKMIADL DMKMNLIN DMKSCNFD FO IOBMISC MONAIOB MONDVLST MON1BUF PSA RDEVUSER R3 SAVEWRK1	APTRAN CLASTAPE CPEXSIZE DMKFREEP DMKFREEP DMKSCNRU F1 IOBSIZE MONARDB MONDVNUM MP RDEVBLOK R0 R4 SAVEWRK3 TRQBSIZE	C1 DMKFRET DMKMIARO DMKPGUVG DMKSTKCP F2 IOBUSER MONATRB MONFLAG1 MPFEAT RDEVDED R1 R5 SAVEWRK5	APUOPER CORFLAG C8 DMKLOKIO DMKMNIDK DMKPGC8 DMKSYSAT F3 IOERETN MONBUFAC MONFLAG3 MPGEND RDEVDISA R10 R6 SCHEDCL TYP3430
DMKMCD	ADSPCH BLANK DEFINTVL DMKFRET DMKSCNRU F3 MONFLAG1 RANGE R3 SAVEWRK1 ZERO		DMKMNISH DMKSYSAT F5 MONIOBF R1 R5	AFRET CONTINUE DMKCVTHB DMKMNIST DMKSYSMX F60 MONSLMT R10 R6 SAVEWRK6	DMKDSPCH DMKPRGC8 DMKSYSTE F7 MONUSER R11 R7	APSTAT1 CPEXADD DMKENTSK DMKPRGMC DMKSYSTS F8 MONUTRB R12 R8 TODATE	DMKENTUT DMKPRGTI ERROR F9 MP R13 R9	DMKERMSG	AUTGO CPEXSIZE DMKFREE DMKSCHRT F1 MONCHPTR PREFIXB R15 SAVEREGS VMBLOK	AUTOSPL C8 DMKFREEP DMKSCNFD F2 MONCOM PSA R2 SAVER12 X40FFS
DMKMCH	ACORETBL AMCHAREA CCS4HARD	AP	ADSPCH APSTAT1 CODE	AEXTSP APSTAT2 CONTINUE	AFREE APSTAT4 CORCFLCK	AFREEP APUOPER CORDISA	AFRET ASYSVM CORFLAG	ALARM AVMREAL CORIOLCK	ALOKSY BLANK CORPGPNT	ALOKVM CCS4DGRD CORSWPNT

EXTERNAL REFERENCES (LABELS AND MODULES)

ZEROES

MODULE

#### MODULE EXTERNAL

#### EXTERNAL REFERENCES (LABELS AND MODULES)

	F3 LPUADDR MCHFLAG1 MCHPDAR1 MCHRESEV MCH1MAIN MCH3SOFT	CPPTLBR C1 DMKDSPRU DMKPGSPO DMKVFRNM F4096 L3 MCHFLAG3 MCHFLAG3 MCHPDAR6 MCH0HDWR MCH1PROC MCH3SOLD MCH7RSRE MCPROGID	C13 DMKFREE DMKPMAMC EXDCCF F6 L8	DMKPMASW EXDCNO F7 MCCPUID MCHFLAG5 MCHP1IDE MCH0SFTR MCH3BCST MCH4REPA MCH7SUP	C7 DMKFRELO DMKFRER EXDINSTO INTMC MCFXDLOG MCHFLAG6 MCHFLAG6 MCHFIIKE MCHOTERM MCH3DATA MCH5IFSA MCH5IFSA MCH5IFSA MCH5IFSA MCH5IFSA MCH2CTYP MODEL165 OPERATOR PROCIO R11 R7 TIMEDISP	CPSTAT4 DMKACRCT DMKFRET DMKFRET EXDINTTO INTRC MCHAREA MCHAREA MCHFLAG7 MCHP1SDE MCH0USAD MCH3DGRD MCH3DGRD MCH7CHTM MCH7TRQ MCSWONE MOD3031	DMK10EMC DMKQCNWT EXDRESVD I PUADDR MCHCPEX MCHFSAR MCHP1SKE MCH1BUFF MCH3HARD MCH7EXIT	CPUMODEL DMKCFPRR DMKLOKDF DMKSCHST FFS IPUADDRXX MCHEK MCHLEN MCHP1STD MCH1COST MCH3INTE MCH3INTE MCH7VRTM MCS1PASS MOD3081 PAGREF QUANTUMR R14 SIGSTART TRACEND	CPWAIT DMKCVTAB DMKMCTPT DMKSTKOP F1 LOCK MCHFIX MCHLEN1 MCHP6CBA MCH1GERR MCH3PASS MCH7I0EM MCNPSW MCS2HARD MOD3090 PMAMODE RECMODE R15	MCH1IOTO MCH3PROT MCH7OPSW MCOLDPW MICBLOK MOD4331 PMAON RECOVRPT R2 SWPCHG1 TRACPROC TRQBUSER
DMKMC I	DMKSCNFD MP R13	DMKSTKOP NORET R14	AMCHAREA CPUVERSN F2 OPERATOR R15 SAVEWRK3	C14 F8 PSA R2	I PUADDR RECMODE R3	APSTAT4 DMKDSPCH I PUADDRX RECOVRPT R4 SAVEWRK7	LOCK RO R5	BLANKS DMKFREEP MCHAREA R1 R6	CPEXADD DMKMCHLM MCHMODEL R11 R9	
<b>DMKMCT</b>	CPWAIT DMKDMPAA DMKLOKPS	DMKLOKRL DMKSCNEP I PUADDRX	CSSFEAT DMKDMPSA DMKLOKRM DMKSTKMP LOCK MFASAVE PREFIXA R1 R7		DMKACRCO DMKDSPRQ DMKLOKTR	DMKACSCV DMKFREEP DMKOPRWT EMSPQUI	DMKLOKDF DMKPGSPO EXNPSW	CPSTAT5 DMKCFPRR DMKLOKDS DMKPMASW FFS MCHFLAG3	DMKQCNWT F255 MCHFLAG7	CPTERMLK DMKCVTBH
<b>DMKMHC</b>	ADSPCH AVMREAL C2 DMKSTKCP HCIRTN HCVMREQ MSFRSP R11 R8	AEXTSP CODE DMKDMPC2 DMKSTKMP HCMSFACT HCVRX MSFR01 R12 R9	DMKSTKOP HCMSFCW INMSFBLK PMAMODE R13	DMKDSPRU EXTMODE HCMSFDB	F1 HCMSFDBV LPUADDR PREFIXA R15	DMKFREEP F8	DMKFRET HCBLOK HCREG13	CPSPMODE DMKLOKDF HCBPNT HCRSCP MOD3081 R0 R4	DMKPTRUL HCFLAG HCSIZE MSFBLOK R1 R5	ASYSVM CPUID DMKSCHST HCFPNT HCVMBLK MSFDATA R10 R6 SCCBKMAX

MODULE	EXTER	NAL REFER	RENCES (LA	ABELS AND	MODULES)					
	SCCBLOK TRACCURR TRQBSIZE	TRACEND	SCCBRESP TRACFLG3 TRQBUSER	TRACPROC	SCCB0010 TRACSTRT VMBLOK	SIGSENSE TRACSVCR VMSIZE	SIGSTART TRAC17 ZEROES	SIGSTOP TRCMSSF	TEMPR1 TRQBIRA	TEMPR2 TRQBLOK
<b>DMKMHV</b>		C1 HCMSFCW MSFBLOK MSFR41 R15 SAVEREGS	AMCHAREA DEFER HCMSFDB MSFCFLGS MSF00 R2 SAVEWRK8 SCCB0100	DMKFREE HCMSFDBV MSFDATA PSA R3	HCSIZE MSFDB1 R0 R4 SCCBLEN	AVMREAL DMKMHCVM LOCK MSFDB2 R1 R5 SCCBLOK SYSCPWT	MCHAREA MSFDB3 R10 R6	CMDBEG DMKPTRLK MCHMODEL MSFINFO R11 R7 SCCBMODP VECSTAT	MOD3081 MSFLNG R12 R8	CORSHARE F0 MOD3090 MSFRSP R13 R9 SCCBRMAX XPAGNUM
DMKMIA	DMKRPAPT DMKSYSUR F5 MONCURBF MONSFB PREFIXB R15 SAVEREGS SFBFLAG SFBDRIG SFBDRIG SFBUSER	DMKPGUVG DMKRSPMN DMKVSDAD IOERETN MONDAS MONSPLCT PSA R2 SAVFR1	DMKSCNAU DMKVSGRI IPLPSW MONDASA MONUSER RDRCHN R3 SAVER2 SFBFLAG4 SFBFLAG4 SFBRECNO SPLINK	DMKLOCRD DMKPRGC8 DMKSCNFD EXHAUST LOCK MONDASB MON1BUF R0 R4 SAVEWRK1 SFBFNAME	DMKCKTUU DMKLOCRQ DMKPRGMC DMKSPLSP FFS MNBHDLEN MONEX NORET R1 R5 SAVEWRK3 SFBFTYPE SFBSTART SPOOLED	DMKSTKCP F0 MONAIOB MONFLAG1 OPERATOR R10 R6 SFBCLAS SFBINUSE SFBSYSID	DMKMCCCL DMKPTRLK DMKSYSAT F1 MONBUFAC MONFLAG2 OPNSFB R11 R7 SFBCOPY SFBLAST	DMKMNIDK DMKPTRUL DMKSYSBF F2 MONBUFIO MONFLAG3 PAGEND R12 R8 SFBDATE SFBDATE SFBLOK SFBTUSE	DMKMNISH DMKQCNWT DMKSYSCL F3 MONBUF1	ASYSOP CLCMD C1 DMKERMSG DMKRPAGT DMKSYSEN F4 MONCOM MONNEXT PREFIXA R14 SAVEAREA SFBFILID SFBOFORM SFBUFORM SYSTEM
DMKMID	ADSPCH CPEXBLOK DMKERMSG DMKSYSDW PREFIXB R15 SAVEREGS VMBLOK	DMKFREEP DMKSYSTE PSA R2	RESET R3	DMKSYSTS R0 R4	R1 R5	AQCNWT DATE DMKPRGMC F60 R10 R6 TEMPSAVE	DMKQCNWT LOCK R11 R7	AUTGO DMKDMPDT DMKSCHST NORET R12 R8 TODATE	CONTINUE DMKDMPTD DMKSTKCP NOTRESP R13 R9 TRQBLOK	CPEXADD DMKENTKC DMKSYSAT PREFIXA R14 SAVEAREA TRQBVAL
DMKMN I	DMKENTES DMKFREEP DMKMOOOO DMKPTRLK	ARIODV CLSUS CPEXR12 DMKCPEID DMKENTET DMKFREHI DMKM0040 DMKPTRUL DMKSYSRM IOBFLAG LPUADDR MNCOTT	DMKENTSC DMKFRELO DMKPGTPI DMKQCNWT DMKSYSRV IOBIOER I PUADDRX	DMKCPEPP DMKENTST DMKFRET DMKPGTSI DMKRIOCT DMKSYSTE IOBIRA MNCHSIZE MNDFVIFN	DMKENTTB DMKIOSQR DMKPGUVR DMKSCHRT DMKSYSTS IOBLOK MNCHSZ MNDFVLMP	APSTAT1 CC CORTABLE CRTEXT DMKCVTDT DMKENTTE DMKMIACC DMKPRGC8 DMKSCHST ERROR IOBMISC MNCHZMP MNDEVLST MN097CPP	CRTEXTSZ DMKDSPCH DMKENTTI DMKMNL DMKPRGMC DMKSTKCP FFS IOBMISC2 MNCLDAST MNDVCNT	DMKDSPNP DMKENTUT DMKPRGMI DMKSYSAP F1 IOBSIZE MNCLPERF MNDVHDRL	CPEXBLOK C8 DMKENTBS DMKERMSG DMKMNLIN DMKPRGMO DMKSYSAT F4095 IOBSTAT MNCLUSER MNDVLEN	DASDCL DMKENTEC DMKFREE DMKMONPR DMKPRGTI DMKSYSNP IOBCAW IOERSIZE MNCODASH MNDVMORE

nobocc					1100002207					
	MN097TTS	MN097LEN MN097UID MN600HLN MONFLAG2 NORET RCHADD RDEVBLOK R0 R4 TEMPR1 TRUN	MN097VR MN600MAX MONCHPTR MONFLAG3 PAGECUR RCHBLOK	MN097LSN MN098 MN600MXS MONIOBF PAGEND RCHCUTBL RDEVDISA R10 R6 TODATE VMBLOK	MN098LEN MN600NUM MONCODE MONNEXT PAGENXT	MN098UID MN600SER MONCOM MONSIZE PERFCL RCUBLOK	MN600ADD MN600TY MONCURBF MONSUSCT PREFIXA RCUDVTBL RDEVSER R13 R9	MN600CNT MONAIOB MONDVLST	MN600DEV MONARDB MONDVNUM MONUTRB PROCIO RCUSUB RDEVSYS R15 SAVEREGS	
DMKMNJ		MONBUF1 R12 R8	APTRAN DEFER DMKPTRAN DMKUDRFU MONCOM R13 SAVEAREA TRQBUSER		APUOPER DMKCVTBD DMKSCHST F0 MONSPLCT R15 SAVER2 VMBLOK	AQCNWT DMKDSPNP DMKSCNFD F1 NORET R2 SFBFILID	ASYSVM DMKERMSG DMKSYSAT F2 PSA R3 SFBLOK	AUTGO DMKFREE DMKSYSCL F5 R0 R4 SYSTEM	AUTOSPL DMKMOOTI DMKSYSMX F8 R1 R5 TRQBIRA	BLANKS DMKPRGMC DMKSYSTE LOCK R10 R6 TRQBLOK
DMKMNL	CACHSTOD CNTSTDAT CPEXR8 DMKFRET DMKSCNRD IOBIOER IOBUSER MN605BW MN605BW MN605RS2 M3880CUU M3880TOD RCHADD RCHADD RCUSUB RDEVCU2 RDEVSTAT R14	CACHNTIM CACHTRQP CNTSTOD CPEXSIZE DMKIOSQR DMKSTKCP IOBIRA IOERBLOK	CACHNTMI CACHWR1 CNTS2DAT DASDCL DMKMONPR F0 IOBLINK IOEREXT MN605DUO MN605SIO M3880M13 PSA RCHPROC RDEVADD RDEVDISA RDEVSTA6 R2	DMKCVTAB DMKPRGC8 F1 IOBLOK IOERSIZE MN605LN MN605SIR	CACHRCNT CACHWSD1 CPEXBLOK DMKDSPCH DMKPRGMC F15 IOBMISC LOCK MN605SPH M3880RCU PTRDVCT RCUBLOK RDEVBLOK RDEVBLOK	CACHRDVP CACHWSD2 CPEXMISC DMKENTGP DMKPRGTI F5 IOBPATHF MNCLDAST MN605NPO MN605SPO M3880RLN PTRENLN RCUCHA	CPEXRO DMKENTGQ DMKSCHST IOBCAW IOBPROC MN3880DV MN605NPR	CACHIOBP CACHRSD2 CNTSDATA CPEXR10 DMKENTIM DMKSCNEP IOBRADD MN38801L MN605PMC MN605SRH M3880SG PTRNTRY RCUCHC RDEVCUA RDEVCUA RDEVPPAG R11 R7	CPEXR11 DMKFREE DMKSCNPH IOBFATAL IOBSIZE MN605 MN605PM0	CACHKX CACHSTA1 CNTSSIZE CPEXR13 DMKFREEP DMKSCNRA IOBFLAG IOBSTAT MN605BD MN605BD MN605RS1 MONCODE M3880SSS PTRSIZE RCUPRIME RDEVCUP RDEVSIZE R13 R9
DMKMNT	AFREE ASYSOP CLASDASD CPUSER DMKCPIVP DMKSCNMI DMKVRRDD FTRTOMB I PUADDR NORET PSA	CPUVERSN DMKCPIWT DMKSCNPH	FF F256 LOCK	CUE DMKCPZMG	C2 DMKCPZVR DMKSCNVS FORMBLOK IFCC LPUADDR	DMKSYSOC FORMOPER INTREQ LPUADDRX PGIDMOD	BUSY CLASURO DMKALOCP DMKFREE DMKSYSOW FTRFH INTTIO		DMKIOTIN DMKSYSPU FTRRSRL	ARIODV CCC CPUMODEL DMKCPISH DMKLOKIO DMKTODTB FTR35MB IOOPSW MPUOPER PROCIPL RCUCHCNT

MODULE	EXTERNAL	REFERENCES	(LABELS	AND	MODULES)
HODOLL		ner enerozo	(		

	RDCLENGF RDEVADD RDEVCUA RDEVFTR3 RDEVPTHS	RDCBLKMX RDCPAGAP RDEVALT RDEVCUB RDEVMDL RDEVRDC	RCUPRIME RDCBLKPG RDCPAGCG RDEVAOF RDEVMDOO RDEVSER RDEVVMNT R11 R7 TEMPR15 TYP2305 TYP3851	RDCBLOK RDCPAGFA RDEVASGN RDEVCU11 RDEVMD13 RDEVSTAT	RDEVBLOK RDEVDISA RDEVMO00 RDEVSTA2	RDEVBOF RDEVDUPL RDEVMO04 RDEVSTA3 RSPXDST2 R14 SAVEAREA	RDEVCFLT RDEVEXTN RDEVNAME RDEVSTA5 RSPXDST3 R15	RDCFPNT RDCSIZE RDEVCKDX RDEVFLAG RDEVNATH RDEVSTA6	RDEVPPAG RDEVSTA7 RSPXSIZE R3 SM TRACSTRT TYP3480	RDCLENGC RDCTYPE RDEVCRDC RDEVFTR2 RDEVPRDV RDEVTYPC
DMKMON	ERROR IOBCYL IOBSPEC2 MNDEVLST MN000LEN MN10YCNT MN202PGR MN202PGR MN202VMR MN30XFLG MN300RSP MN305S0 MN700ADD MN700ADD MN700QDV MN802NPP MONBUFAC	DMKFRÉT DMKSCHPU FFS IOBFATAL IOBSTAT MNDVCNT MNDVCNT MN10VIO MN202XSWS MN202PRI MN30XLEN MN300SST MN305UID MN700CRS1 MN802NUM	DMKSCHQ1 F0 IOBFH IOERSIZE MNDVLEN MN099 MN10YLEN MN202VRL MN3002PST MN202VRL MN3005WCT MN700CHR MN700CHR MN700CHR MN700CHR MN700CHR MN700RS2 MN802PGR MN802PGR MN802PGR MONBUFIO MONEX MONSAVE1 MPUOPER PREFIXB RCHQCNT RDEVCUA	DMKM1AWO DMKSCHW1 F2 IOBFLAG IONTWAIT MNHCLASS MN099CNT MN202WS MN202QDP MN202VVT MN300UID MN500 MN700CYL MN700UID MN802PGW MONBUF1 MONFLAG1 MONSAVE2	MN099LEN MN20XNPP MN20YTTI MN202QDR MN305 MN5001NS MN5001NS MN5001NS MN700DIR MN802CLN MN802PRB MONCLASS MONFLAG2 MONSLMT OWNDVSER	MN20XPRC MN20YVTI MN202QDT MN204LEN MN305LEN MN500LEN MN500LEN MN700LEN MN802WID MONCLKSA MONFLAG3 MONFLAG3 MONFLCT PAGECYL PROCIPL RCUCHA RDEVFLAG R11 R7	DMKPRGC8 DMKSYSAT F4095 IOBMISC MNCLPERF MNHDRLEN MN202APR MN202APR MN202APR MN202APR MN202APR MN202APR MN202APR MN305PN MN5000VH MN7000PC MN802CTR MN802WI0 MONCLOCK MONIOBF	DMKPRGMC DMKSYSMX IDLEWAIT IOBMISC2 MNCLSYS MNHRECSZ MN10XADD MN20XQ1E MN2022IOC MN202RES MN300 MN305P0 MN500UID MN700PR0 MN500UID MN700PR0 MN802DEV MN802WPG MONCODE MONLOSLT PAGEWAIT PSA	DMKSCHAL DMKSYSNM IOBCAW IOBPAG MNCODA MN10XLEN MN202XQ1N MN202LEN MN300END MN305RSP MN500VAD MN305RSP MN500VAD MN700QCH MN802DLN MONLSTBK MONLSTBK MONLSTBK MONLSTBK ROEVQCNT RDEVQCNT R14	DMKSCHMF DMKSYSOW IOBCSW IOBSIZE MNCOSUS MNRDEVB MN10XUID MN20XQ2E MN202LPR MN202SFG MN300LEN MN300LEN MN300LEN MN300LEN MN300LEN MN700 MN700QCU MN802NAU MONARDB MONCRSLT MONNEXT MONUSER PGREAD RCHADD RCHADD RCHADD RCHADD RCHYPE RDEVSKUP R15 SAYEREGS SSBEPGS
DMKMOO	DMKFRESC	ALOKSP CPSTAT DMKDSPNP DMKFRETS DMKLOKTR	DMKHVCDI	ALOCPP APSTALOC CPSTAT5 DMKDSPPT DMKIOSNM DMKMNLMR	APSTAT1 CPTIDLE DMKENTIM DMKIOTCT DMKMONPR	ALOCPUSE APUOPER CPWAIT DMKENT62 DMKLOKCT DMKPAGCC	DMKPAGPS	ASYSVM DMKCVTAB DMKFRELN DMKLOKIO DMKPAGSS	ALOCTDSK ATMRSN DMKDSPCH DMKFRELS DMKLOKRL DMKPGTDM	DMKFRENP DMKLOKRM DMKPGTPB

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MODULE	EXTERNAL	REFERENCES	(LABELS	AND	MODULES)
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MODULL				ADELS AND	riodules)					
	DMKPRVIP DMKPRVPT DMKPTRFF DMKPTRSS DMKPXAOP DMKPXBFC DMKPXBFC DMKSCHQ3 DMKSCHQ3 DMKSCHQ3 DMKSCHQ3 DMKVFRNO DMKVFRNO DMKVFRNO DMKVFRNO DMKVFRNO DMKVSIT F5 LPUADDRX MN000CPA MN000CPA MN002QA MN002QA MN002SI MN002CI MN005CIU MN005CIU MN005CIU MN005CIU MN005CIU MN005CIU MN005CIU MN007B1 MN400 PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR MN400PDR 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MN40PDR MN40PDR MN40PDR MN40PDR MN40PDR MN40PDR	DMKPRVLC DMKPRVRR DMKPTRSW DMKPTRSW DMKPXA0S DMKSCHBS DMKSCHBS DMKSCHBS DMKSCHST DMKVFRNR DMKVFRNR DMKVFRNR DMKVSJHD F6 MNCHLIST MN0000VF0 MN0022IBM MN003CMG MN003CMG MN003CMG MN003CMG MN003CMG MN003CMG MN003CMG MN007DPB MN003CMG MN007DPB MN006DLN MN400ACT MN400VTT MN400VTT MN400VTT MN400VTT MN400VTT MN603LNM MN0UTRB PROCIPL RCUSUB	DMKPRVLP DMKPRVTC DMKPTTHL DMKPXAPC DMKPTTHL DMKPXAPC DMKSCHW1 DMKSCHW1 DMKSCHW1 DMKSCHW1 DMKSCWW1 DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKVFRNS DMKFRNS DMKVFRNS DMKVF	DMKPXARC DMKPXBMC DMKSCHW2 DMKSCHW2 DMKSCHW2 DMKSYSOW DMKVFRSV FRE F8 MNCHSAM2 MN0001SD MN000VRC MN000VRC MN002LEN MN000VRC MN002LEN MN0002LEN MN0002LEN MN0004HL MN005LCN MN0061L MN0005LCN MN0005LCN MN400FSP MN400TT1 MN400WSS MN600HLN MN400PSP MN400TT1 MN400WSS MN600HLN MONCLASS MPUOPER PSASVCCT RDEVADD RECCYL R3	DMKPRVMN DMKPRVTP DMKPTRN2 DMKPXACX DMKPXACX DMKPXACX DMKPXACX DMKPXACX DMKSCNRD DMKSCNRD DMKSCNRD DMKSYSSP DMKVSICI FREENUM IDLEWAIT MNCHSIZ MNDVLEN MN000LEN MN0002CSR MN002Q21 MN003CSR MN002LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN0005LEN MN005LEN MN005LEN MN005LEN MN0005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN005LEN MN0	DMKPRVMO DMKPRVXI DMKPTRPR DMKPXADL DMKPXAUS DMKPXAUS DMKSCHN2 DMKSELS1 DMKSYSSW DMKVSICT F1 IPLPSW MNCHSIZE MNDVMORE MN000PAD MN002Q1A MN002Q1A MN002Q1A MN002Q1A MN002Q1A MN002Q1A MN002Q1A MN002SQT MN005MAL MN007LSB MN400LEN MN400LEN MN400LEN MN400LEN MN400LEN MN400LEN MN400LEN MN400LEN MN400LEN MN400LEN MN400LEN MN400LEN	DMKPRVMS DMKPRVXS DMKPTRRC DMKPXAFC DMKPXAFC DMKPXAFC DMKPXAWC DMKSWABS DMKSYSSZ DMKSYSSZ DMKVSICW F2 IPUADDRX MNCLDAST MNCLDAST MNCLDAST MNCLDAST MNCOOPAS MN0002Q1B MN002SQ3 MN003LEN MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A MN005N0A	DMKPRVNC DMKPSANX DMKPSANX DMKPXAIP DMKPXAIP DMKPXBRC DMKSCHQT DMKSWAIL DMKTMRPT DMKVSISF F3 LOCK MNCOOPBP MNO000Q2E MNO01LEN MN002Q1C MN003 MN005PAL MN005PAL MN005PAL MN005PAL MN005PAL MN007PSB MN400MHL MN400QDR MN400VRC MN400VRC MN400VRC MN400ZDLN MN602DLN MN602DLN MONFLAG1 PGWRITE RCUADD	DMKPTRCS DMKPTRRW DMKPXAIS DMKPXBTL DMKVSBTL DMKVSTSI F4 LOCKSAV MNCLUSER MN000ATT MN000PBS MN000ATT MN0007S1 MN005ADR MN005ADR MN005SER MN007AS1 MN400WLH MN400WLH MN400VRL MN602HLN MN602HLN MN602HLN MONSACT PREFIXB RCUBLOK RDEVNAME R12 R8	DMKPRVPE DMKPTRFC DMKPTRSC DMKPXAMC DMKPXBDL DMKPXBUS DMKSCHQ2 DMKSYSMX DMKVFRNC DMKVSITC F4096 LPUADDR MNCODAS MN000CDC MN0000SRC MN0003PNS MN005CCN MN005SZ MN007AS2 MN007AS2 MN007AS2 MN007AS2 MN400RES MN400RES MN400RES MN400RES MN400RES MN400RES
<b>DMKMPO</b>	ADSPCH CPEXBLOK CPSUPER DMKPSAPO F16 MP R0 R4 SIGSTART VHD	CO DMKPSAST F2 PERGPRS R1 R5	AFREEP CPEXR12 DMKCFMBK DMKSTKCP F240 PREF1XB R10 R6 SIGXC WAIT		DMKDSPCH DUMPSAVE INTEX	DMKERMSG EXNPSW INTEXF PROPSW R13 R9		AVMREAL CPSTATUS DMKFREEP FFS IPUADDR QUANTUMR R15 SIGREST TREXCR9 Y4	CPCREGO CPSTAT2 DMKPERIL F1 LOCK RSRTOPSW R2 SIGSENSE TREXPERA Y6	R3 SIGSSS
DMKMSG				ALARM BUFFER DMKERMSG DMKQCORD		APSTAT1 CPEXADD DMKFRET DMKSCNFD	APTRLK CPEXBLOK DMKIUACP DMKSTKCP	DMKLOCK	DMKLOCKD	ASYSOP DMKCVTBD DMKLOKSW F0

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MODULE	EXTER	RNAL REFE	RENCES (LA	ABELS AND	MODULES)					
	F1 HILIGHT NORET R13 R9 SAVEWRK5	F15 IXBLOK NOTIME R14 SAVEAREA SAVEWRK6	F2 IXIRA NOTRESP R15 SAVEREGS SAVEWRK8	R2 SAVER1	F4 IXSIZE PSA R3 SAVER11 TIMEDISP	F5 LOCK R0 R4 SAVER2 VCONCTL		F7 MSGMSG R10 R6 SAVEWRK2 VCONOMSG	F8 MSGSMSG R11 R7 SAVEWRK3 VMBLOK	F9 MSGWNG R12 R8 SAVEWRK4 XRIGHT16
DMKMSW	AFREE CLASFBA EDIT IOERACT IOERIGN NORET RO R4 SAVER7	FFS I OERADR	IOERIND3 OPERATOR R10 R6	DMKCVTBH F4 IOERCNCL IOERIND4 PSA R11 R7	F6 IOERCSW	IOERLEN	I FCC I OERDATA I OERNUM RDEVNAME R14	INTREQ IOERDEC IOERPEND	CDC DMKQCORD IOBLOK IOERETRY IOERSTRT RDEVTYPC R2 SAVERO	I OBRADD I OERFLG1 LOCK
DMKNEA	DMKSCNMU F3 NICENAB NICSTAT RDEVDED R13 SAVEAREA	DMKSCNRD F4 NICEPAD NICTERM RDEVDISA R14 SAVEREGS	DMKERMSG DMKSCNVU F6 NICFLAG NICTMAT RDEVMAX R15	DMKFREE DMKSYSCK F8 NICPSUP NICTYPE RDEVNICL R2 SAVER9	LOCK NICRDED NICUSER RDEVSTAT R3 SAVEWRK1	DMKVDSDF NICBLOK NICRFLG NICVDEVB R0 R4	FFS NICDISA NICROPER NORET R1 R5	F1 NICDTYPE NICRSPL OPERATOR R10 R6 SAVEWRK4	DMKSCNAU F2 NICD3275 NICRUNN	F255
DMKNEM	R0 SAVEAREA	R1 SAVEREGS	R11 SAVER0	R12	R13	R15	R2	R3	R4	R5
DMKŇES	DMKSCNRU LOCK NICLBSC NICUSER RDEVASTB RDEVENAB RDEVENAB RDEVSTAT RDEVVM2 R15 SAVEREGS	LPUADDR NICLINE NORET RDEVAVM2 RDEVEPDV RDEVPDLY RDEVSTA4 RDEVWAIT R2 SAVER11	DMKCVTBH DMKQCOCL F1 MODESWT NICLTRC PSA RDEVBASE RDEVBASE RDEVEPLN RDEVPEND RDEVSTA6	RDEVTBTU RO R4 SAVER9	DMKCVTHB DMKRGBEN F255 NICCIBM NICQPNT RCHCUTBL RDEVCON RDEVFLAG RDEVFLAG RDEVFLAG RDEVTCTL R1 R5	DMKRIORN F3 NICDISA NICSESN RCUBLOK RDEVCTRS RDEVIRM RDEVRCVY RDEVTMCD R10 R6	DMKRNHND F4 NICENAB NICSIZE RCUDISA RDEVCUA RDEVLNCP RDEVRSVD RDEVTYPC R11 R7 SAVEWRK3	F6 NICEPAD NICSTAT RCUDVTBL RDEVDED RDEVMAX RDEVSADN RDEVTYPE R12 R8	RDEVDISA RDEVMDL RDEVSDR	DMKSCNRD F8 NICFLAG NICFYPE RDEVADD RDEVDISB RDEVNICL RDEVSLOW RDEVUSER R14 SAVEAREA

DMKNES	AFREE CACTLTR CSWLNCP DMKLOKSW DMKSCNRU LOCK NICLBSC NICUSER RDEVASTB RDEVASTB RDEVASTB RDEVNRDY RDEVSTAT RDEVVM2 R15 SAVEREGS SAVEWRK8	FFS LPUADDR NICLINE NORET RDEVAVM2 RDEVEPDV RDEVPDLY RDEVSTA4 RDEVWAIT R2 SAVER11	ALOKSP CLASSPEC DMKCVTBH DMKQCOCL F1 MODESWT NICLTRC PSA RDEVBASE RDEVEPLN RDEVPEND RDEVSTA6 RDIDX R3 SAVER2 TIMEDISP	APSTAT1 CLASTERM DMKCVTDB DMKQCPTO F2 NICBLOK NICPSUP RCHBLOK RDEVBLOK RDEVBLOK RDEVEPMD RDEVPTHS RDEVTBTU R0 R4 SAVER9 TYPBSC	RDEVFLAG	RDEVCTRS RDEVIRM RDEVRCVY	ARIOCU CONSYSR DMKFREE DMKRNHND F4 NICENAB NICSIZE RCUDISA RDEVCUA RDEVLNCP RDEVLNCP RDEVTYPC R11 R7 SAVEWRK3 TYP2700	RDEVSADN RDEVTYPE R12 R8	RDEVDISA RDEVMDL RDEVSDR RDEVUSC8 R13 R9	BLANKS CSWLMEP DMKLOKIO DMKSCNRD F8 NICFLAG NICTYPE RDEVADD RDEVDISB RDEVNICL RDEVSLOW RDEVUSER R14 SAVEWRK7
DMKNET	AFREE BSCFLAG1 CPSTAT5 DMKNESEP F255 NICBLOK NICFLAG	AFRET BSCHALT CRESIMD DMKNESWN F3 NICCIBM NICFMT	AP BSCSHUT DMKCFQRD DMKQCNWT F4 NICDISA NICLBSC	APSTAT1 CACTLIN DMKCVTBH DMKRGBEN F6 NICDISB NICLGRP	APUOPER CDCTLIN DMKCVTHB DMKRIORN F60 NICDTYPE NICLINE	AQCNWT CLASSPEC DMKERMSG DMKRNHND F7 NICDXSC NICDXSC NICNAME	ARIODV CLASTERM DMKFREE DMKSCNFD F8 NICD3284 NICPSUP	LOCK	BLANKS CONSYSR DMKIOESR DMKSCNRU MP NICEPAD NICRATTD	BSCBLOK CONTACT DMKLOKSW DMKSCNVU NICADVF NICEPMD NICRATTN

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	RDEVSTAT R14 SAVEAREA	R15	RDEVENAB RDEVUSER R2 SAVER2	NICRUNN PSA RDEVFLAG RDIDX R3 SAVER9 TYP3705	NICSESN RDEVAUTO RDEVLNCP R0 R4 SAVEWRK1 VMBLOK	RDEVMAX R1 R5	NICSTAT RDEVBLOK RDEVNICL R10 R6 SAVEWRK3	RDEVNRDY R11 R7	RDEVPEND R12 R8	NICTYPE RDEVDED RDEVRSVD R13 R9 SAVEWRK7
DMKNLD	DMKPTRAN DMKSCNRU FTRTYP1 IOBLINK IOBSTAT LOCK NICBLOK NICTERM RCUDVTBL RDEVDISA RDEVLCEP	CE DMKERMSG DMKPTRUL DMKSCNVS F0 IOBCAW IOBLOK IOBLOK IOBLOK IOBLOK IOBLOK IOBLOK IOCKSAV NICCIBM NICTYPE RCUSTAT RDEVENAB RDEVLNCP RDEVRCVY	DMKQCNWT DMKSCNVU F1 IOBCC1 IOBUNSL LPUADDR NICUSER RDEVADD RDEVADD RDEVEPDV	CLASSPEC DMKFREEP DMKGCOCL DMKSNTRN F256 IOBCC3 IOBMISC2 IOBUSER MP NICEPMD NOAUTO RDEVAIOB RDEVEPLN RDEVEPLN RDEVSTAT R10 R6 SAVEWRK3	DMKFRET DMKQCORD DMKSTKIO F3 IOBCP IOBRADD IOERBLOK NCPNAME NICFLAG NORET RDEVATT RDEVEPMD RDEVNCP	C1 DMKIOSQR DMKQCPRO DMKVDREL F4096 IOBCSW IOBRCAW IOERDATA NCPPAGCT NICNAME NOTRESP RDEVBASE RDEVFIOB RDEVNICL RDEVSTA4 R12 R8 SAVEWRK5	NCPPNT NICPSUP OPERATOR RDEVBLOK RDEVFLAG RDEVNRDY RDEVTFLG R13 R9	DMKRPAGT EDIT F8 IOBFPNT IOBRSTRT IOEREXT NICSIZE PSA RDEVCODE RDEVFTR RDEVOWN RDEVTMCD R14	DMKSCNFD ERRMSG IL IOBIOER IOERSIZE IOERSIZE NCPTBL NICSTAT RCUBLOK RDEVCUA RDEVIOER RDEVPEND	CCPTYPE DMKCVTHB DMKCPGUVR DMKSCNRD FFS INTREQ IOBIRA IOBSPEC IPLREQ NCPVOL NICSWEP RCUDISA RDEVDED RDEVIRM RDEVTHS RDEVTYPE R2
DMKNLE	F4 IOBCP IOBRCAW IOERDATA PSA	DMKFRET DMKQCNWT DMKVSGRI F4096 IOBCSW IOBRCNT IOERETN RDEVAUTO RDEVRSVD R12 R8 SAVEWRK6	DMK10SQR DMKQCORD EDIT F5 10BFLAG 10BRSTRT 10EREXT RDEVBLOK RDEVSTAT R13 R9 SAVEWRK7	DMKLOCRD DMKRNHIN ERRMSG F6 IOBIOER IOBSIZE IOERSIZE RDEVDED RDEVTYPC R14 SAVEAREA	RDEVDISA RDEVTYPE R15 SAVEREGS SAVEWRK9	DMKPGTCG DMKSCNFD F0 IL IOBLINK IOBSTAT LOCK RDEVFLAG RDEVUSER R2 SAVER2	RDRCHN R3 SAVEWRK1 SFBCOPY SFBOFORM	DMKDSPCH DMKPGUVG DMKSCNRU F2 IOBCAW IOBMISC IOBUNSL NORET RDEVMDL R0 R4 SAVEWRK2 SFBDATE	DMKPGUVR	DMKPTRAN DMKSYSDU F3 IOBCC3 IOBRADD IOERBLOK OPERATOR RDEVPEND R10 R6 SAVEWRK4 SFBDUMP
DMKNMT	FREELOWE R3	FSTFMODE R4	FSTFNAME R5	NUCON R6	RO R7	R1 R8	R12 R9	R14	R15	R2
DMKOPE	AFREE	AFRET	ALARM	AP	APSTAT1	APTRAN	APTRLK	APUOPER	AQCNWT	ASYSOP

#### MODULE

## EXTERNAL REFERENCES (LABELS AND MODULES)

	DMKERMSG DMKMCHST DMKRPAPT DMKUDRMD IOBLOK OPERATOR	DMKFREE DMKMNIST DMKSCNEP DMKUDRRV IOBUSER POWEROFF	DMKCPIBD DMKFRET DMKPGUVG DMKSCNMU EDIT IONPSW	CUE DMKCPICD DMKGRCCP DMKPGUVR DMKSCNVS FF LOCK PSA	DMKGRCHL DMKPTRAN DMKSTABD FFS LPUADDR PSALANG	C2 DMKCPITR DMKGRCMR DMKQCNWT DMKSYSAT F0 LPUADDRX PSASYSID	DE DMKCPIWC DMKGRCRU DMKQCORD DMKSYSUR F1	DMKGRCVM DMKQCOSY DMKSYSWM F4 NOAUTO RDEVATOF	DMKSYSWV F8 NORET RDEVBLOK RDEVTYPE R2	DMKCFMÉN DMKERMCP DMKLOGOP DMKRPAGT DMKUDRFU INTTIO NOTIME RDEVCODE
DMKOPR	ALARM CONTINUE DMKRIOCN NOAUTO R1 R7 UC	AVMREAL CPID DMKRIODV PSA R10 R8 XRIGHT16	BCTWAIT CPSTAT5 DMKSCNRU RDEVBLOK R12 R9	BUSY CPUID DMKVRRIS RDEVCORD R14 SENSE	CAW CPUVERSN FFS RDEVGRTY R15 SILI	CC CSW FO RDEVTYPC R2 SM	CD C2 F1 RDEVTYPE R3 TYP3066	CE DE INTTIO RDEVUSER R4 TYP3210	DMKDMPLK IONPSW	CLASTERM DMKIOTIN LOCK RO R6 TYP3278
DMKOVR	BLANKS R12 R8	CC R13 R9	CSW R14 SENSE	DEVICE R15 SILI	ERRMSG R2 WAIT	INBUFF R3	INFILE R4	R1 R5	R10 R6	R11 R7
DMKPAG	DMKSYSOC F4 IOBFATAL IOBOFF LPUADDR PAGEFBAT PAGESIZE PAGEXEHH PAGEXSKH PAGSWCSZ PAGSWTIC PXA RDEVCUP	ALOCPRD APUOPER CPEXRO DMKPGTPB DMKSYSOW F6 IOBFH IOBPAG L1 PAGEFSNS PAGELOCO PAGESK PAGEXSLT PAGSWDA1 PAGSXIDA1 RDCBLKAP RDEVECKD	F8 IOBFLAG IOBRADD L16 PAGEFTIC PAGESLOT PAGEXLR PAGEXLR PAGSWDA2 RDCBLKPG RDEVFIOB RDEVRDC R0 RL SSBEINVL	ALOCPSIO CC CPEXR5 DMKPTRPS D4 IDA IOBFPNT IOBSIZE L3 PAGEHEAD PAGENUME PAGESNS PAGEXLRD PAGEXSNS PAGEXLRD PAGEXSNS PAGEXSNS PAGEXSNS PAGESUDSZ PCIF RDCBLOK RDEVFLAG	FTRFH IOBBPNT IOBIRA IOBSPEC L4 PAGEIDA1 PAGESRCD PAGEXOP PAGEXCOP PAGEXCOP PAGEXCOP PAGEXSTR RDEVFTR RDEVFTR RDEVSIZE R10 R6	ALOCSW CLASDASD CPEXR9 DMKPTRWQ FTR70MB IOBCAW IOBLINK IOBSPEC2 L8 PAGEIDA2 PAGEPRI PAGESRCH PAGEXRW PAGEXST2 PAGSWHH PREFIXB RDEVADD RDEVFTR2 RDEVSTA4 R11 R7 SSBENUM	ALOCTMS CLASFBA DMKDSPCH DMKPTSAD F1 IOBCC3 IOBLOK IOBSTAT OWNDLIST PAGEIOB PAGESS PAGEXSHC PAGEXSHC PAGEXTIC PAGSWNOP PSA	F10 IOBCP IOBMINI IOBUSER OWNDRDEV PAGELOCA PAGERW PAGETYPE PAGEXSHH PAGSWARG PAGSWR PSAEXT RDEVBLOK RDEVLOW RDEVTYPC R13 R9 SSBHEADL	PAGELOCB PAGESECT PAGEXDED PAGEXSIZ PAGSWCC PAGSWRW PSTPINPP RDEVBUZY RDEVMDL RDEVTYPE R14 SILI	DMKIOSQR DMKSTKIO F3 IOBCYL IOBMISC2 LOCK PAGECYL PAGELOCD PAGESEEK PAGEXECC PAGEXECC PAGEXSKC PAGSWCHN PAGSWSCH PSTPOUPP RDEVCODE RDEVMD02
DMKPAH	ADSPCH CPEXADD	AFRET CPEXBLOK	ALOKRM CPEXBPNT	ALOKSP CPEXFPNT	APSTAT1 CPEXMISC	APUOPER CPEXR0	CC CPEXR5	CD CPEXR9	CE CUE	CORTABLE DE

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MODULE	EXTE	RNAL REFE	RENCES (LA	ABELS AND	MODULES)					
		IOBLOK	DMKFRET DMKSTKCP IOBMISC PAGEFBAT PAGETYPE PSA R14 SILI		DMKSTKOP IOBRADD PAGELOCA PAGEXLR	DMKPAGEX DMKSYSOW IOBSIZE PAGELOCD PAGEXRW RDEVBLOK R3 SSBLOK	F5 IOBSTAT PAGELOCN PAGEXSNS	I L LOCK PAGERW	DMKPAGXS IOBCAW LPUADDR PAGESEEK PAGSWCHN R1 R6 SWPRECMP	IOBCSW OWNDLIST PAGESK PAGSWEXT R10 R7
DMKPE I	PERWKFLG PEXELIM PEXMASK RANGE R3	DMKPETAB F2 PERBLIP PERSCADD PERSCADD PERWKFL2 PEXFLAGO PEXNEXT R0 R4	APTRAN DMKERMSG DMKPTRAN F240 PERBLOK PERHITS PERSCAN PERWKLEN PERWKLEN PEXFLAGT PEXPRINT R1 R5 SAVEWRK4	AQCNWT DMKFREE DMKQCNWT F3 PERBUF PERINTO PERSIZE PERWORK PEXFROM2 PEXRUN R10 R6 SAVEWRK5	F5 PERCHAIN PERLNEND PERSTPCT PERWRKCT PEXGPR PEXSECND R11 R7	PEROPTN PERSTPSP PEXBLOK PEXGREG PEXSIZE R12 R8	COUNT DMKLOCKQ EXTMODE F7 PERPASCT PERPASCT PERTBAK PEXBR PEXINST PEXINST PEXSTORE R13 SAVEAREA XPAGNUM	FLAGS F8 PERCTACT PERPASSP PERTBLEN PEXDATA PEXINTO PEXTERM R14 SAVEREGS	F0 LOCK	PERTOTAL PEXDLEN PEXLEN PSA R2 SAVEWRK1
DMKPEL	AFREE F6 PERCR9 PERTOTAL PEXFLAGO PEXNEXT PSA R2 SAVEWRK1	PEXPASS RANGE R3	PEXFROM PEXPRINT RO R4	DMKFREE PERBLOK PERON PEXBLOK PEXGPR PEXRANGE R1 R5 SAVEWRK4	R10 R6	PEXCHCMP PEXINST PEXSECND R11 R7	PERRNGTB PEXCMND PEXINTO	PERSTPCT PEXDATA PEXLEN PEXSTORE R13 SAVEAREA	PERSTPSP PEXDLEN PEXMASK	PEXELIM PEXMECMP PEXTHIRD R15
DMKPEN	AFREE DMKFRET F8 PERCNTCH PERSAVED PERWRKCT PEXBR PEXSTORE R2 SAVEWRK1	LOCK PERCOUNT PERSCAN PESBLOK PEXCMND PSA R3	AQCNWT DMKPELCR NORET PERCTACT PERSCDLN PESCHAIN PEXELIM R0 R4 SAVEWRK3	PERSIZE PESCOUNT	CL DMKSCNFD PERAPPND PERENDIT PERTBAK PESELIM PEXFLAGT R10 R6 ZEROES	PERBLOK PERFLAG PERTBLEN PESFLAG	DMKCVTBD F1 PERBUF PERGPRP PERTMPCH PESNAME PEXINST R12 R8	F2 PERCHAIN PERHITS PERTOTAL PESNEXT PEXLEN R13	DMKERMSG F3 PERCHANG PERMODE PERWKFLG PESSIZE PEXMASK R14 SAVEREGS	F4 PERCLEAR PERREGSV PERWKFL2 PEXBLOK PEXNEXT R15
DMKPEQ	PEXFLAGT PEXSTORE R13 R9	PEXTHIRD R14	LOCK PESNAME PEXGPR	CL FFS PERBLOK PESNEXT PESGREG RANGE RANGE R2 SAVER2 XRIGHT24	CLEAR F1 PERBUF PEXBLOK PEXINTO RESET R3 SAVEWRK1	DATAOUT F15 PERCHAIN PEXBR PEXNEXT R0 R4 SAVEWRK2	F2 PERCTACT PEXCMND PEXPASS R1 R5	F3 PERFLAG PEXDATA PEXRUN R10 R6	DMKERMSG F4 PERHITS PEXDLEN PEXSECND R11 R7 SAVEWRK5	F5 PERSAVED PEXFLAGO PEXSTEP R12 R8
DMKPER	AFREE	APTRAN	AQCNWT	BRING	C1	DEFER	DMKCFMBK	DMKCVTBD	DMKERMSG	DMKFREE

MODULE

#### EXTERNAL REFERENCES (LABELS AND MODULES)

HODOLL										
	F1 PERCR9 PERINST PEXDATA PEXINTO PEXTHIRD R2	F15 PERCTACT PEROPNOT PEXDINV PEXINTO1	F4095 PERDATON PEROPQU PEXDLEN PEXMASK R0 R4	LOCK PEREX PEROP1 PEXFLAGO PEXNEXT R1 R5	PERADDR PEREXADD PEROP2 PEXFLAGT PEXPASS R10 R6	PERBLIP PEREXMOD PERSTLEN PEXFROM	PERTBAK PEXGPR PEXSECND R12 R8	PERBUF PERGALT PERTBLEN PEXGREG	PERCDE PERGPRP PEXBLOK PEXGSUC PEXSUCC R14 SAVEAREA	F0 PERCHAIN PERHITS PEXBR PEXINST PEXTERM R15 SAVEREGS
DMKPET	AFREE DMKCFMBK DMKVSTPT PERDATON PERSEQP PEXSUCC R15 SAVEREGS VMBLOK	PEREX PERSEQT PEXGSUC PEXTERM R2	F1	F3 PEREXMOD PEXBLOK PEXMASK R0 R4	PEXBR PEXNEXT R1 R5	BUFCNT DMKLOCKD MP PERINST PEXCMND PEXPRINT R10 R6 SAVEWRK4	PERADDR PEROPNOT PEXDATA	PEXDLEN PEXSTEP R12 R8	PERBUF PEROP2 PEXFLAGO PEXSTEPN R13 R9	PERCHAIN PERSAVED PEXFLAGT PEXSTORE R14 SAVEAREA
DMKPGM	DMKPGTSU DMKSCNAU F0 PAGRBITS R0 R4 SAVER1 SAVEWRK5 SEGTABLE SSBEPGS SSBUPGS	ALOCPP ASYSVM CPEXSIZE DMKPGTDP DMKPGUAL DMKSTKCP F1 PAGTABLE R1 R5 SAVER11 SAVEWRK6 SSBALLOC SSBFLAG	DMKPGTMT DMKPGUPR DMKSTRAN F16 PAGTOT R10 R6 SAVER12 SAVEWRK7 SSBBPNT SSBFPNT SWPALLOC SYSPFLG2	DMKCVTAB DMKPGTMV DMKPTREP DMKSWAIS F2 PGREAD R11 R7 SAVER13 SAVER13 SAVEWRK8 SSBCODE SSBHEADL SWPCHG1	CORTABLE DMKDSPCH DMKPGTMX DMKPTRPS DMKSWAPR LOCK PGWRITE R12 R8 SAVER7 SAVEWRK9 SSBCYL SSBLOK SWPCODE	DMKFREE DMKPGTPB DMKPGTPB DMKSWMIG LOCKSAV PSA R13 R9 SAVER9 SEGFLAG SSBEFLG SSBEFLG SSBBFLG SSBBGREF SWPCYL SYSPPST	ALOKSP CPEXBLOK DMKFREEP DMKPGTPC DMKPTRWQ DMKSYSPG LPUADDR PSAEXT R14 SAVEAREA SAVEWRK1 SEGMIG SSBEINVL SSBNDLCT SWPFLAG	AP CPEXFPNT DMKFRET DMKPGTPM DMKPTTFT DMKSYSPM MP PSTPINPP R15 SAVEREGS SAVEREGS SAVEWRK2 SEGMIGPG SSBENTRL	DMKLOKSW DMKPGTPN DMKSYSPP NORET PSTPINSW R2 SAVERETN SAVEWRK3 SEGMIGPP SSBENTRP SSBENTOR SWPRECMP	APUOPER CPEXRO DMKPAGIO DMKPGTSF DMKSCHMS DMKTRQST PAGINVAL PXA R3 SAVERO SAVERO SAVEWRK4 SEGPAGE
DMKPGS	CPPTLBR DMKFRET DMKPTRSC DMKVFSRS IUCVBLOK PAGBMP PRNPSW R15 SAVEREGS	APTRAN CORPGPNT C1 DMKHVDIP DMKPTRUL DMKVMASH IUCVCPEX PAGCORE PROCIPL R2	DEFER DMKIUACU DMKPTSAD FFS KEEPSEGS PAGRBITS PSA R3 SAVER12	DMKBLDRL DMKLOCKD DMKPTSPW FO LASTUSER PAGREF RO R4 SAVER13 SAVEWRK9	DMKBLDRT DMKLOCKQ DMKPTSRS F1 LOCK PAGSHR R1 R5 SAVER2 SEGFLAG	DMKCVTAB DMKPGUAL DMKPTTFT F15 MICBLOK PAGTABLE R10 R6 SAVER3 SEGINV	CORDISA CPEXADD DMKDSPCH DMKPGUPR DMKSCHST F16 MICVTMR PAGTONLY R11 R7	DMKPGUSP DMKSTKCP F4096 MP PAGTOT R12 R8 SAVEWRK2 SEGPROT	DMKFRÉE DMKPTRAN DMKSWAPR F8 OLDVMSEG PAGTSWP R13 R9 SAVEWRK3 SEGTABLE	INTPR PAGACT PREFIXB R14 SAVEAREA SAVEWRK4 SHRBPNT

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	SPMPFX SWPCODE SWPVM TRQBTOD VMSIZE	SSBALLOC SWPCYL SWPVPAGE TRQBUSER XRIGHT16	SWPFLAG TEMPR1 TRQBVAL	SSBENTRY SWPKEY1 TIMER TRQREGSD	SWPPSTOR TREXANSI	SSBFLAG SWPRECMP TREXIN1 VMABLOK	SSBHEADL SWPSEGNO TREXNSI VMAFPNT			SWPALLOC SWPTRANS TRQBLOK VMBLOK
DMKPGT	RDEVCODE RECUSED R4	ALOCPREC AP CPEXADD DMKFREEP DMKSYSVM	APSTALOC CPEXBLOK DMKQCNWT F1 RDEVTYPE R1 R6	AQCNWT CPEXR11 DMKSTKCP LOCK	ALOCALOC ALOCMAX ALOCRDEV ASYSVM CPEXSIZE DMKSYSDP MP RECBLOK R11 R8 TYP3380	ALOCRECS BALRSAVE CPID	ALOCPAGE ALOCRSET BALRO DMKDMPAL DMKSYSPG OPERATOR RECMAP R14	ALOCSW BALR1 DMKDMPRC DMKSYSPP	ALOCCYL1 ALOCPG ALOCTMS BALR15 DMKDMPSW DMKSYSPS PSTRCPG\$ RECPNT R2 SYSPBPNT XSWFULL	ALOCPMAX ALOCTMSI BALR8
DMKPGU		DMKFRET LOCK RDEVRDC RECUSED R3 SSBENTRL	ALOCPNT	ALOCPP APSTALOC DMKPGTMS OWNDLIST RDIDX R1 R5 SSBENUM	DMKPGTPC	ALOCPS ASYSVM DMKPGTPU PSA RECBLOK R11 R7	ALOCPUSE BALRSAVE	ALOCRDEV BALR1 DMKPGTTU	RDCPAGAP RECMAX R14 SSBCYL	ALOCPAGE ALOCRSET CPSTAT5 DMKPTRPS RDEVALLN RECPNT R15 SSBEFLG SWPFLAG
DMKPIA	CC	LOCK	SILI							
DMKPIB	CC	LOCK	PIB	SILI						
DMKPMA	ACORETBL AVMREAL CORFLAG CO DMKDSPB DMKSCHST EXNPSW F1 INTPRL MCOPSW PAGCORE PMAON RDCBLKMX RUNCR1 R15 SAVEREGS SIGEMS TIMER TROBFPNT TYP3380 Y6	EXOPSW F2 INTRC MFAMASK PAGE4K PMASTAT RDCBLOK RUNPSW R2 SAVERO SIGXC TRACCURR	CDCONF CORTABLE C15 DMK5RET DMKSCNRD EXTMODE F4095 IPUADDR MICBLOK PAGINVAL PREFIXA RDEVBLOK RUNUSER R3 SAVER1 SPMPFX	C2 DMKPGUVG DMKSCNRU FAILSTAD F4096 LOCK MICEVMA3 PAGPGSWP PREFIXB	CDDEF CPSPMODE C6 DMKPRGCT DMKSCNVD FFS F8 LPUADDR MICEVMA4 PERADD PRNPSW RDEVMDL R1 R5 SAVEWRK1 SYNCMASK	DAMAGRPT DMKPRVLG DMKSCNVU FPRLOG GRLOG LPUADDRX MICIOSW PERCODE PROCIO RDEVMD00 R10 R6 SAVEWRK2	DMKPSAPO DMKTMR FTR35MB INTEX	CPSUPER DMKDMPCP DMKPTRAN DMKTMRSP FTR70MB INTEXF L8 MICSIZE PMAAVAIL PROPSW RDEVRDC R12 R8 SAVEWRK8 TEMPR2	DMKVRR FXDLOG INTMC MCHEK MSSFMASK	DMKSCHRT EMSMASK FO INTPR MCNPSW

DMKPRG	ADSPCH CPCREGO CP370EON DMKDSPB DMKQCNWT DMKVATSX INTPR PERADD PRNPSW PROTREI RUNPSW R2 SSEB TEMPR10 TRACFLG1 VECAVAIL Y4	DMKDSPCH DMKREIPL DMKVAULA INTPRL PERADDR	DMKSCHQ1 DMKVFRIN LAP370E PERBLOK PROB370E PROTTRAN R0 R4 SSEF TEMPR13	C6 DMKFPS DMKSCHQ2	ERRMSG LPUADDR PERDATON PROTBLOK QUANTUM R10 R6 STOBLOK TEMPR15	C8 DMKPERIL DMKTMRPT EXTMODE MONCLASS PERFLAG	FF MONCODE PMAGUEST PROTFLAG RAPF R12 R8 STOSHCR1 TIMEDISP	DMKCFMBK DMKPRVLG	DMKVATPF F0 MVSA370E PREFIXA	DMKPTRAN
DMKPRV	DMKPRWTB DMKSTKDE DMKVAULA F15 F8 MCHMODEL PERBLOK PROCIPL RCUCHC RUNCR1 R3 SHRTABLE	DEFER DMKMHVSM DMKPRWTP DMKTMR DMKVAURN F16 INTPR MICBLOK PERCR9 PROPSW RCUCHD R0 R4	DMKTMRCC DMKVFRSV F240 INTPRL	DMKDSPB DMKMPOSP DMKPSAFP DMKTMRSP DMKVSIEX F4 IPUADDR MNCLINST PERFLAG RCHBLOK RCUSUB R10 R6 SWPSHR TREXNSI	CPSPMODE CSSFEAT DMKDSPCH	CPCREGO CPSTATUS CO DMKDSPRU DMKPMAWD DMKPSASP DMKTRCPB	C1 DMKFREEP DMKFREAF DMKPTRAN DMKTRCPV EXTRACTL F5 LPUADDR MPFEAT PREFIXB RCHSTIDC	CPSTAT4 C14 DMKHVCAL DMKPMAXS DMKPTTDM DMKVATAB FFS F6 L2 OLDKEYOP PRNPSW RCUBLOK RDEVPS R14 SEG1M TEMPSAVE	APTRAN CPEXREGS CPUID C15 DMKIUAEP DMKPRGSM DMKSCNVU DMKVATAT F0 F60 L4 PAGE2K PROBMODE RCUCHA RDEVSTA3 R15 SHRFLAG TRANMODE VECSTAT	CPUMCELL C6 DMKLOKDF DMKPWIP DMKSTKCP DMKVATEX F1 F7 MCHAREA PERADDR PROCIO RCUCHB
DMKPRW	ACORETBL CORFLAG CP370EON DMKPRVXS F15 MNCOSIM R0 R5 SWPFLAG XPAGNUM	CORSHARE	AP CORSWPNT DEFER DMKPSAPO F4 PERADDR R10 R7 SWPKEY2	APSTAT1 CORTABLE DMKDSPA DMKPSASP F4095 PERBLOK R11 R8 SWPREF1	APSTAT2 CPPTLBR DMKDSPCH DMKPTRAN F4096 PERCR9 R12 R9 SWPSHR	APTRAN CPSEGPRT DMKPMAWD DMKSYSRV F60 PERGPRS R14 SEGPROT TRANMODE	DMKPMAXS DMKVATAB F8 PERSALT R15 SHRFLAG	AVMREAL CPSPMODE DMKPRGSM DMKVAULA INTPR PREFIXB R2 SHRNOPRT VMSIZE	DMKPRVXI EXTMODE LOCK PRNPSW R3 SHRTABLE	BRING CPSTAT4 DMKPRVXR F0 MNCLINST PSA R4 SWPCHG1 XKEYMODE
DMKPSA	DMKCPE DMKLOKSY	CORSWPNT DMKDMPDK DMKLOKVM	APSTAT1 CORTABLE DMKDSPCH DMKMPORS DMKRIOCH	DMKEXTSP DMKPRVMA	DMKFREE DMKPTRAN	BALR1 CPSPMODE DMKFREEP DMKPTRLK DMKRIODC	DMKFRET DMKPXA	DMKPXADL	BALR7 CP370EON DMKLOKRM DMKPXADQ DMKRIOPU	DMKLOKSP DMKPXATL

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MODULE

F15

F2

LOCK

R13

PAGREF

R12

PAGSHR

PSAEXT PREFIXA PREFIXB R12 PSA RO R1 R11 SEGFLAG SEGINV SEGPROT SWPVPAGE TRANMODE TRAPCP SEGTABLE SHRFLAG SHRNOPRT SHRTABLE VMBLOK XKEYMODE X2048BND ZEROES SAS R2 R7 SWPFLAG SPMPFX SWPPAG ACORETBLAFREEAFRETALOCBLOKALOCCHNALOCCSRALOCCYL1ALOCCYL2ALOCFLGALOCFLGALOCMAPALOCMAXALOCNAGALOCPAGEALOCPAVLALOCPAVLALOCPMAXALOCPNTALOCPUSEALOCCYL1ALOCCYL2ALOCFLGALOCRMEVALOCSWALOCTMSIALOCUSEDAPAPSTALOCAPSTALOCAPSTAT1APUOPERASYSVMCORCPCORFLAGCORTABLECORVMCPUIDDMKCVTBDDMKDSPNPDMKERSGDMKFREEDMKFRETDMKMHCCPDMKPGTCPDMKPGTDLDMKPGTPBDMKPTFFDMKPGTPNDMKPGTPTDMKPGTSDDMKPGTSLDMKPGTSLDMKPGTSTDMKPGTSUDMKPTRFRDMKPTFTDMKSYSSGDMKSYSSVDMKSYSSZDMKPGTSLDMKPGTSLF1F4F4096F8HCBLOKHCMSFCWHCMSFDBHCRMAPHCRSCPHCSIZELOCKMPPREFIXBPSAPSTRCBYTPSTRCPAGPSTRCPG\$RECBAKRECBLOKRECCYLRECMAPRECMAXRECPNTRECSIZERGUSEDR0R1R10R11R12R13R14R15R2R3R4R5R6R7R8 DMKPST R1 R5 R13 R14 R15 R2 R3 R4 R5 R6 R7 R8 R9 SAVEAREA SAVEREGS SAVEWRK1 SAVEWRK2 SAVEWRK4 SAVEWRK9 SCCBIBLK SCCBIMAP SCCBINUM SCCBKMAX SCCBLEN SCCBLOK SCCBRESP SCCB0010 SYSPALOC SYSPATYP SYSPCNT SYSPCYL1 SYSPCYL2 SYSPFLG SYSPFLG2 SYSPFPNT SYSPLIST SYSPPAG SYSPPST SYSPVLEN SYSPVLST SYSPVOL SYSPXST 13<br/>CCBKMAXSAVEAREA<br/>SAVEAREA<br/>CCBKMAXSAVEAREA<br/>SCCBLEN<br/>SCCBLEN<br/>SYSPFLG2SAVEAREA<br/>SCCBLOK<br/>SYSPFLG2SAVEAREA<br/>SCCBLOK<br/>SYSPFLG2SAVEAREA<br/>SCCBLOK<br/>SYSPFLG2SCCBKESF<br/>SYSPFLG2SCCBKESF<br/>SYSPFLG2STOFICEACORETBL<br/>ALOKSP<br/>ALOKSP<br/>BALRSAVE<br/>DCRFREE<br/>CORFFRET<br/>DMKFRETD<br/>DMKFRETD<br/>DMKPTSAD<br/>DMKPTSAD<br/>DMKPTSAD<br/>DMKSYSCS<br/>DMKSYSCS<br/>F2AFREE<br/>AFREE<br/>CORFICEAFREE<br/>AFREE<br/>APAGCP<br/>BALR2<br/>CORENT<br/>CORFONT<br/>CORFONT<br/>CORENT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br/>CORFONT<br DMKPTR R12 R13 R14 R15 R2 R3 R4 R5 R14R15R2R3R4R5R6R7SAVEAREASAVEPROCSAVEREGSSAVERTNSAVER0SAVER1SAVER12SAVER13SAVER7SAVEWRK1SAVEWRK2SAVEWRK3SAVEWRK4SAVEWRK5SAVEWRK6SAVEWRK8SEGPAGESHRFLAGSHRNOPRTSHRSEGCTSHRSGPRTSHRTABLESIGEMSSIGEXTSIGXCSPECIALVSSBALLOCSSBEPNTSSBCODESSBCYLSSBEFLGSSBEINVLSSBENUMSSBEPGSSSBEXTNDSSBFLAGSSBFPNTSSBHEADLSSBLCKSSBNDLCTSSBPSTORSSBTRANSSSBUPGSSSBVMSCBSSBVPAGESWPALLOCSWPAPPSWPCHG1SWPFLAGSWPFLAG2SWPKEY1SWPPAGSWPPSTORSWPRECMPSWPSHRSWPTABLESYSTEMTEMPR1TEMPR2TEMPR3TIMERVFAULTVMBLOKXKEYMODE R8 R9 SAVER2 SAVER3 SAVER7 SAVEWRK9 SEGINV SEGPAGE SIGQUI SIGRES SIGXC SSBENTRL SSBENTRY SSBENUM SSBOLD SSBNPGS SWPCODE SWPCYL SWPFLAG SWPTRANS SWPVPAGE SYSTEM XPAGNUM XTNDLOCK ZEROES ALOKSP AP APSTAT1 APSTAT2 CORFLAG CORFLUSH CORFPNT CORPGP CPEXBLOK CPEXFPNT CPEXMISC CPEXRO DMKPTS ACORETBL ADSPCH AFREEP ALOKRM APSTAT2 AVMREAL CODE CORBPN CORSWPNT CORTABLE CORVM CORPGPNT CORSHARE CORSWAP CORBPNT CORCP CPEXADD CPPTLBR CPSTAT4 CPXSTOR DMP DMKPTRU1 DMKSTKCP DMKVATSI FF DMKCVTAB DMKDSPCH DMKFREEP DMKPAGQ DMKPTRFQ DMKPTRQ2 DMKPTRSC FREESAVE FO F1 MNCLSWAP MNCODLSO MP F1 F16 PAGACT KEYREF LOCKSAV LPUADDR

PAGTABLE PREFIXA

R15

R14

PREFIXB PSA

R3

R2

RO

R4

F4096

DMKRIOUC DMKRSPAC DMKRSPLP DMKRSPPR DMKRSPPU DMKRSPRD DMKRSPSP DMKSYSAC DMKSYSCS DMKSYSLC

F8

F60

DPMICON

DPOKTLB

MP

LOCK

DPSEGPRT DUMPSAVE EXTMODE

R14

R6

R7

APUOPER ASYSVM

CPEXSIZE

PAGINVAL

KEYCHG

R11

R7

CPEXR11

PAGCORE

F6

R10

R6

R1

R5

PAGCORE PAGSHR

R15

EXTERNAL REFERENCES (LABELS AND MODULES)

F240

DMKSYSOP DMKSYSSP DMKSYSVM DMKTMRSN DPAPUOP

	R8 SHRTABLE TEMPR4 XKEYMODE	TRACCURR	SSBFPNT	SAVEREGS SSBLOK TRACFLG1	SSBOLD	SEGPAGE SWPFLAG TRACSTRT	SHRFLAG SWPREF1 TRACSVCR	SWPREF2	SHRSEGCT SWPVPAGE TRCPTSRS	TEMPRO
DMKPTT	CPEXR7 DMKPTRF1 FF PREFIXA R15 SAVEREGS SHRNOPRT	BALRO CORSWPNT CPEXSIZE DMKPTRF2 F1 NOINVPTE PSA R2 SAVER1	DMKPTRN2 F16 NORESTR RUN370E R3 SAVER2 SHRTABLE	C1 DMKPTRQC F4096 NOSIGP R0 R4 SAVER3	F8 OPPRSTRT R1 R5 SAVEWRK1 SIGQUI	DMKPTSAE I PUADDRX PAGCORE R10 R6	DMKLOKDF DMKSTKCP LOCK PAGE2K R11 R7	CPEXFPNT DMKPGUVG	LOKSYS	DMKPTRFQ
DMKPXA	CPXTRANS PSTPINSW TRLLOCK		DSPB PSTPOUSW VMBLOK	DSPCH PXAEND	DSPRQ RQLOCK	DSPRU SPBOUND	DSPWA SUBHEAD	FREE SUBSTLA		PSTPINPP TRLLABEL
DMKPXB	CPXTRANS PSTPINSW TRLLOCK	DSPA PSTPOUPP TRLSTL	DSPB PSTPOUSW VMBLOK	DSPCH PXAEND	DSPRQ RQLOCK	DSPRU SPBOUND	DSPWA SUBHEAD	FREE SUBSTLA	NUMPOOLS SUBTABLE	
DMKQCN	DEFER DMKQCOCS F9 MNCOWRIT NICSIZE PLERRMSG PLNCB PLSVR2 RDEVECOL R0 R4 SAVER11 SAVER11 SAVEWRK7	CONTGMXD DFRET DMKQCONQ GRAFDEV MP NICWTH PLFLAG1 PLNOAUTO PLSVR20 RDEVEHLT R1 R5 SAVER2 SCRCMDR	CONFSS CONTSIZE DMKCVTDT DMKQCQED GRTSALEN MSGCPIO NOMC PLFRET PLNORESP PLSVR21	DMKDSPCH DMKSCNAU IDAHEAD MSGEMSG NORET PLHILITE PLNORET PLSVR22 RDEVNICL R11 R7 SAVER4 TIMEDISP	CLASTERM CONNCB CONUSER DMKFREE DMKSCNVU IDAHSTRT MSGIMSG NOTIME PLIMSG PLNOTIME PLSVR23 RDEVSNA R12 R8 SAVER7	CONOUTPT CONVDEVB DMKFRERC FFS INHIBIT MSGSCIF NOTRESP PLINHIBT PLOPERTR PLVIRDVD	CONWORK1 DMKFRET F0 LOCK MSGVMI0 OPERATOR PLIST PLPRIOR PLVMGNI0 RDEVTYPE R14 SAVEAREA	DMKLOKSW F2 LOGDROP NICBLOK PLALARM PLLED PLR2 PSA RDEVWTH R15 SAVEREGS	CPID DMKMSGIU F255 LOGHOLD NICCORD PLDFRET PLLOGDRP PLSECUSR RDEVBLOK RESET R2	F4095 MNCLRESP NICHT PLDIAG PLLOGHLD PLSIZE RDEVCORD RTYPE R3 SAVER1
DMKQCO		CONSYNC CORTABLE DMKERMSG		CPEXBLOK DMKFREEP	CONTSKSZ CPEXREGS DMKFRET	CONDATA CONPNT CONUSER CPEXR12 DMKGRDIC	CPEXSIZE DMKLOKSW	CONFLG2 CONRESP CONWORK1 C1 DMKPTRAN	BALRSAVE CONADDR CONFSOP CONRETN CONWORK2 DEFER DMKPTTFT DMKVSTVP LOCK PLEDIT	CONADDR2 CONFSS CONSPLT CONWRTRD DMKCNSIC DMKQCPTO

MODULE

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MODULE

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		PLINHIBT PLSVR2 RDEVBLOK R0 R4 SAVER1 SAVEWRK5 VCONCNT2	PLSVR21 RDEVCON R1 R5 SAVER11 SECUSER	PLSVR22 RDEVDROP R10 R6 SAVER2 SNARBLOK	R11 R7 SAVER4	PLUCASE RDEVNICL R12 R8 SAVER8 SNARINN	PLVIRDVD RDEVSNA R13 R9	R14	PLWRTRD RDEVSTA2 R15 SAVEREGS	R2 SAVERETN SAVEWRK3
DMKQCP	ADSPCH CONTASK DMKCVTBD DMKSCHRT OPERATOR RETDLEN R3 TRQBIRA	DMKSCHST	DMKDSPCH DMKSCNRD RDEVBLOK R1 R5	DMKSCNRN	DMKFREEP DMKSTKCP RDEVSNA R11 R8	DMKSYSNM RDEVSNRB R12	AQCNWT CPEXSIZE DMKPTRLK DMKVCTLO RDEVSTA3 R13 SAVEREGS VMBLOK	DMKPTRUL LOCK RDEVTYPC R14	CLASSPEC DMKACODS DMKQCNWT MP RDEVTYPE R15 SNARBLOK	DMKCVTAB DMKSCHDL NORET RETBUF R2
DMKQCQ	AFREE CONDATA CONLNCNT CONSPLT CONWORK1 F10 NOTRESP PLNOAUTO PSA RTYPE R3 SAVER4 TEMPRO	AFRET CONDCNCL CONLNRES CONSTAT CONWORK2 F2 PLALARM PLNORESP RDEVAPLP R0 R4 SAVEWRK1 TEMPR1	CONDIAG CONNEWL CONTASK DMKFREE F256 PLDFRET PLNORET RDEVBLOK R1 R5	DMKFRET F3 PLDIAG PLOPERTR RDEVSNA R10 R6 SAVEWRK4	CONDRFMT CONPARM CONTGMXD DMKTBLSF F9 PLFLAG1 PLPRIOR RDEVSNRB R11 R7	CONPARM2 CONTSIZE DMKVCSWT GRTHILEN PLHILITE PLSVR2	CONFLAGS CONPNT CONTSKSZ	CONRESP CONUSER D66LNLEN LOCK PLLED PLSVR22	CONFSS CONRETN CONVDEVB FO NORET PLLOGDRP PLVIRDVD RDEVTYPC R15 SAVER2	CONCNT CONLED CONSFCNT CONWORK F1 NOTIME PLLOGHLD PLVMGNIO RDEVTYPE R2 SAVER3 SNARTTY XRIGHT16
DMKQVM	ARIODC CLASTERM CORTABLE CPSTATUS C15 DMKFREEP DMKSCHQ2 F1 IOBLOK MICSPT QUANTUMR RCUBLOK RDEVACTV RDEVDED RDEVTFLG R13 R9	CPCREGO CPSTAT2 C3 DMKFRERC DMKSCHRT F15 IOBRADD MPFEAT RCHADD RCUBUSY RDEVA10B	ARIOUC CLASURO CPCREG8 CPSTAT4 C5 DMKFRET DMKSCHST F4096 IOBSIZE MVSA370E RCHBLOK RCUCHA RDEVAIRA RDEVDRAN RDEVTPE R15 SAVEREGS	RCHBMX RCUCUBSY RDEVALT RDEVFLAG RDEVUSER R2 SAVEWRK2	CRTEXT C8 DMKPMARW DMKSCNRD F8 IOERBLOK PMAAVAIL RCHBUSY RCUPRIME RDEVBLOK RDEVPREP RSRTNPSW R3 SPMPFX TRQBQUE	DMKSCNRU IOBBPNT IOEREXT PMAMODE RCHSCED RCUSCED RDEVBUZY RDEVSCHD R0 R4 SPXCR6	CUE DMKDSPB DMKPSAPO	CONTSKSZ CPEXSIZE CO DMKDSPQI DMKQCNWT DMKSTKCP IOBCSW LOCK PMASTAT RCHSTAT RCHSTAT RCUSTAT RDEVCON	CPQVMCU C1 DMKDSPVM DMKRIOCH DMKVATBC IOBFPNT MICBLOK PSA RCHTYPE RCUSUB RDEVCTL	ARIOCU CLASGRAF CORSWPNT CPSPMODE C14 DMKERMSG DMKSCHQ1 FXDLOG IOBLINK MICEVMA2 QUANTUM RCUADD RCUTYPE RDEVCUA RDEVSTA4 R12 R8 TIMER VHD ZEROES
DMKREI	ADSPCH BUFSIZE	AFREE DMKCFGIR	AFRET DMKCVTAB	APTRLK DMKDSPCH	BLANKS DMKFREE	BUFCNT DMKFRET	BUFFER DMKPTRLK	BUFIN DMKPTRUL	BUFINLTH DMKSCHST	

#### EXTERNAL REFERENCES (LABELS AND MODULES)

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	DMKUDRMD PROTRCNT R15 SAVEREGS ZEROES	PROTREI R2	DMKUDRX I PSA R3 TRQB I RA	RO R4	PROTATRQ R1 R5 TRQBSIZE	R10 R6	PROTBLOK R11 R7 TRQBUSER	PROTBUFF R12 R8 TRQBVAL	PROTERR R13 R9 VCONCTL	PROTFLAG R14 SAVEAREA VMBLOK
DMKRET	BALRSAVE R1 R7	PSA R11 R8	RETBUF R12 VMBLOK	RETDATA R14 ZEROES	RETDLEN R15	RETLEN1 R2	RETLEN2 R3	RETNXTD R4	RETNXTS R5	RO R6
DMKRGA	BSCFLAG1 BSCPCCW2 BSCUP CONADDR CONFSS CONFSS CONFSS CONFSS CONFSS CONFSS CONFOR CONFSS CONFOR CONFSS CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFOR CONFO	DMKCVTBH DMKLOKSW DMKRGBSL DMKSCNRU IDAHEAD IOBIRA IOBUSER NICADVF NICDXSC NICPOLL NICRUNN NICUSER RDFVCON	BSCRCVD BSCSEND BUFFER CONCCW2 CONNCB CONTASK CRTEXTSZ DMKDSPCH DMKQCOCL DMKRGBSN DMKSCNVU IDAHSTRT IOBLINK IOBVADD NICALRM NICALRM NICALRM NICALRM NICALRM NICSELT NICSELT NICSELT NICSELT NICSELT NICSELT NICSELT NICSELT NICSELT NICSELT NICSELT NICSELT NICSELT NICSELT NICSELT TRUBLOK VCONBRK	CRTFSII DMKERMSG DMKQCOET DMKRGBUP DMKSTKCP IDAHWRK1 IOBLOK IOERBLOK NICAPL NICFLAG NICGRY NICFLAG NICYDEVB RDEVDISB RDEVDISB RDEVTYPC R15 SAVER2 TIMEDISP	BSCREGEN BSCSIZE CC CONCMD1 CONPNT CONTSKSZ CRTFSSA DMKFREE DMKQCPTO DMKRGC DMKSTKIO IDAHWRK2 IOBMISC IOEREXT NICATRB NICFMT NICCATTD NICSIZE NICWSF RDEVENAB RDEVTYPE R2 SILI TRQBCHIO TROBPOLL	BSCINDEX BSCRESP BSCSIZE1 CD CONCNT CONRESET CONUSER CRTPPA1 DMKFREEP DMKRGBCL DMKRGBCL DMKRGBCL DMKRGDOB DMKVI0IN INHIBIT IOBMISC2 IOERSIZE NICBLOK NICHOLD NICRATTN NICSTAT NICS275 RDEVFLAG RDEVWAII R3 SKIP TROBCPO	BSCRPTR BSCSPTR CE CONCNTL CONRESP CONWRT DE DMKFRET DMKFRET DMKRGBCO DMKRGEIO FTRDIAL IOBCAW IOBRADD LOCK NICCORD NICCORD NICCORD NICCORD NICCORD NICCTERM ONEENT RDEVFTR RO R4 SVMUNLOK TRQBCRT TRQBUSER	BSCOPIED BSCRSTRT BSCTMRQ CLASTERM CONDATA CONRETN CPEXADD DMKBLDVM DMKGRCQR DMKRGBEN DMKRGESK F1 10BCC3 10BS1ZE LOGDROP NICCPNA NICLGRP NICCENA NICCENA NICCENA NICCENA NICCENA RDEVMAX R1 R5 TEMPR1 TRQBDEV TRQBVAL	BSCRVI BSCRVI BSCTSTRQ CLEAR CONDCNT CONSTAT CPEXBLOK DMKCFMBK DMKCFMBK DMKRGBMT DMKRGBMT DMKSCHRT F2 IOBCSW IOBSPEC LOGHOLD NICCIAG NICMORE NICTMCD RDEVADVF RDEVNICL R10 R6 TEMPR15 TRQBFLG2 TRQNAME	DMKCFMEN DMKIOERN DMKRGBPL DMKSCHST F6 IOBFATAL IOBSTAT LPUADDR NICDISB NICNAME NICRFLG1 NICTRQ RDEVBLOK RDEVNRDY R11 R7 TEMPR2
DMKRGB	BSCSEL CONADDR CONDLE CONFSS CONPPA1 CONTASK CRTEXT DMKFREEP DMKIOSOR	CRTEXTSZ DMKFRET DMKLOKSW	BSCRCVD BSCSIZE CONCCW2 CONDLN CONLED CONRETN CONTSKSZ CRTFSII DMKGRAOT DMKQCOET DMKTBLGT	CONCCW3 CONDRFMT CONLNCNT CONSBADR CONUSER CRTFSSA DMKGRCUP DMKGRCUP	CONLNRES CONSTAT CONWCC CRTPPA1 DMKGRCWC DMKRGATM DMKVI0IN	CONCMD1 CONEWA CONNCB CONSTX CONWORK2 DE DMKGRTB DMKRGD01	BSCSCAN BSCUP CONCNT CONEWRT CONEWRT CONSTX1 CONWSF DMKCFMAT DMKGRTFD	BSCSCCW1 CC CONCNTL CONFLAGS CONPARM CONSYNC CPEXADD DMKCVTAB DMKGRTFM	CONPARM2 CONSYNXP CPEXBLOK DMKDSPCH DMKGRT12	BSCSCCW3 CONACTV CONDCNCL CONFSOP CONFNT CONSYN1 CPEXSIZE DMKFREE DMKIOSHA DMKSTKCP F256

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MODULE

DM F3

MODULE	EXTER	RNAL REFER	RENCES (LA	BELS AND	MODULES)					
	IOBIOER IOBUNSL NICADFF NICDIAG NICEWO NICQRY NICSTAT NIC14B RDEVDISB RDEVWAII R3 SCRCMDR TRQBLINE VCON3270	NICTEXT NIC3274 RDEVENAB RO R4 SILI TRQBLOK	NICFMT NICRATTN NICTMCD NIC3276 RDEVFLAG R1 R5 TAB	NICTRQ PRIORITY RDEVFTR R10 R6 TIMEDISP	NICAPL NICD3276 NICHT NICREAD NICTYPE PSA RDEVMAX R11 R7 TRQBCHI0 TRQBUSER	IOERSIZE NICATRB NICD3277 NICMORE NICRFLG NICUSER RDEVAIOB RDEVNICL R12 R8 TRQBCPQ	NICNTRL NICRUNN NICUSEWA RDEVBLOK RDEVNRDY R13 R9 TROBCRT	LOGDROP NICBLOK NICD3284 NICPOLL NICSELT NICVDEVB RDEVBSC RDEVPEND R14	NICQDONE NICSIO NICWSF RDEVDED RDEVRSVD R15 SAVEREGS TRQBFLG2	NICEHLT NICQPNT NICSIZE NICWTH RDEVDISA RDEVSTAT R2 SAVER2
DMKRGC	BSCRVI BUFCNT CONCCW3 CONRESET CONUSER DMKCVTHB DMKGRTP6 DMKRGBC0 DMKTBMAI F255 IOBLOK MNCLRESP NICCARD NICCARD NICC3277 NICMORE NICRFLG1	BSCSCCW1 BUFFER CONCNT CONRESP CRTFSII DMKFREE DMKGRT12 DMKGBMT DMKTBMTI F3 IOBMISC2 MNCOERD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD NICCORD N	DMKRGBRE DMKTBMXI F4095 IOBREMOT MP NICCPNA NICECOL NICRUNN NICWSF PFDCPYSP RDEVFTR R13 R9 TREXMOR VCONBRK	BSCSEND BUFSIZE CONDATA CONSTAT CRTPPA1 DMKFRET DMKRGBUP DMKTBMZI F6 IOBSIZE NICADVF NICDIAG NICEHLT NICQDONE NICCHLT NICQDONE NICCHLT NICSIZE NICWTH PFKADDR RDEVMAX R14 SAVEAREA TREXT VCONCTL	BSCSENSE CC CONDLE1 CONSTX1 C1 DMKGRAOT DMKGRAOT DMKVIOIN HILIGHT DMKVIOIN HILIGHT NICDISA NICENAB NICCAINH NICDISA NICCENAB NICCENAB NICSTAT NIC3275 PFKFLAG RDEVNICL R15 SAVERO	INHIBIT IOBSPEC5 NICALRM NICDISB NICFLAG NICFLAG NICTEXT NOMC PFKIMM RDEVSTAT R2 SAVER2 TRQBFLG2	FTRDIAL IOBCAW IOBUNSL NICAPL NICAMSG NICFMT NICRATTN NICTMCD NORET PFKLNG RDEVWAII R3 SILI TRQBLINA	CONADDR CONLABEL CONTINUE DMKCFMBK DMKGRTAC DMKRGAIN DMKTBLGT F0 IOBCSW IOBUSER NICDTYPE NICHOLD NICRDED NICTRQ NOTIME PFKRET R0 R4 SYSTEM TRQBLINE	CONTSIZE DMKCNTED DMKGRTAI DMKRGASP DMKTBLRG F1 IOBIRA IOBVADD NICAWSF NICDXSC NICHT NICREAD NICTYPE NOTRESP PFKTABLE R1 R5 TAB	BSCIGN BSCRSTRT BSCUP CONCCW2 CONPNT CONTSKSZ DMKCVTBD DMKGRTPF DMKRGBCL DMKTBLUP F2 IOBLINK LOCK NICBLOK NICBLOK NICCSLOG NICCSER PFDATA PREFIXA R10 R6 TEMPRO TRQBPA1R
DMKRGD	AP CONDATA DLE IDAHSTRT RTNNOCTL R4 XRIGHT16	BALRSAVE CONDLE1 ETB IOBLOK R0 R5 XRIGHT24	BLANK CONFLAG ETX LOCK R1 R6	BLKLEN CONFLG2 F1 MP R10 R7	BSCBLOK CONFSS F255 NICAWSF R11 R8	CC CONLABEL IDA NICBLOK R12 R9	CD CONNCB IDAENTRY NICRFLG R14 SILI	CONCCW1 CONTASK IDAEWRD1 ONEENT R15 STX	CONCCW2 CONWORK IDAHCURR PSA R2 VMBLOK	CONCNT CONWSF IDAHEAD RTNBLOCK R3 WR
DMKRGE	AFREE CONTASK IOBIOER IOBVADD	BALRSAVE DE I OB I RA I OERBLOK	DMKFREE IOBLINK	BSCBLOK DMKSTKIO IOBLOK IOERDATA	BSCSENSE DMKVIOIN IOBREMOT IOERLEN	F1	CE IDAHEAD IOBSPEC LOCK	CONADDR IDAHWRK1 IOBSPEC5 L1	CONFLG2 IDAHWRK2 IOBUNSL L2	CONNCB IOBCSW IOBUSER NICBLOK

# EXTERNAL REFERENCES (LABELS AND MODULES)

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	NICVDEVB R13 R9 XRIGHT16	PSA R14 SAVEAREA	RDEVATT R15 SAVEREGS	RDEVBLOK R2 SKIP	RDEVSNA R3 UC	RO R4 VCONCTL	R1 R5 VCONRBYT	R10 R6 VCONRFLD	R11 R7 VCONRIND	R12 R8 VMBLOK
DMKRND	DUMP R15	I NPUT R2	ON R3	RO R4	R1 R6	R10 R7	R11 SAVEAR	R12	R13	R14
DMKRNH	DMKIOSQR DMKSCNRU F60 IOBFLAG IOBRSTRT IPLREQ NICDISA NICNTRL NICUSER RDBUFLN RDEVLCEP	CONRETN CONTCMD CSETDSM	CTRMLTR DMKCVTBH DMKNLDR DMKVSTPT IL IOBIRA IOBSPEC LOGDROP NICENAB NICQPNT NORET RDEVAUTO RDEVMAX RDEVSTA4 R13 R9	CKPBITS CONACTV CONDEST CONRTRY CONTSKSZ DE DMKCVTDT DMKNLEMP EDIT INHIBIT IOBLINK IOBSTAT LOGHOLD NICCRNT OPERATOR RDEVBLOK RDEVNCP RDEVTBTU R14	DFRET DMKDSPCH DMKQCNWT ERRMSG INTREQ IOBLOK IOBUNSL MP NICERLK NICSESN PCI RDEVBUZY RDEVNICL RDEVTYPC R15 SAVEREGS	DMKQCOCL F1 IOBCAW IOBMISC IOBUSER NICATOF NICFLAG NICSIZE PREFIXA RDEVCKPT RDEVNRDY RDEVWAIT R2 SAVER0	DISCNCT DMKFREE DMKQCOET F16 IOBCC1 IOERBLOK NICATTN NICLINE NICSTAT PRGC RDEVCON RDEVPEND	DMKFREEP DMKQCPTO F255 IOBRADD IOERDATA NICBLOK NICLTRC NICTELE PRIORITY RDEVDED RDEVRCVY R0 R4 SAVER2	CONSYSR CRESCND DMKCFMAT DMKFRET DMKRIORN F256 IOBCP IOBRCAW IOEREXT NICCIBM NICMTA NICTERM PRTC RDEVDISA RDEVRSVD R1 R5 S111	F4 IOBCSW IOBRCNT IOERSIZE NICDED NICNAME NICTYPE PSA RDEVFLAG RDEVSCHD R10 R6 SYMUNIOK
DMKRPA	ACORETBL BRING CPEXBLOK DMKPGUPR FFS NORLSE R13 SAVER1 SWPFLAG	AFREEP CORCFLCK CPEXFPNT DMKPGUSP F1 PAGCORE R14 SAVER2 SWPRECMP	CPEXMISC	DMKPTRUL IOERETN PAGREF R2	AP CORIOLCK CPEXSIZE DMKPTRWQ LOCK PREFIXB R3 SAVEWRK2 SYSTEM	CPPTLBR DMKPTTFT LOCKSAV PSA R5	DEFER	DMKFREEP DMKSWAPR MICBLOK R1 R9	APUOPER CORTABLE DMKPAGIO DMKVATSI MICVTMR R11 SAVEAREA SWPCHG2	DMKPGSPR DMKVMI MP R12
DMKRPD	AP DMKUDRFU R14 VMBLOK	APTRAN FO R15	APTRLK LOCK R2	BLANKS MP R3	BRING PSA R4	C1 R0 R5	DEFER R1 <b>R6</b>	R11	DMKPTRAN R12 SAVEREGS	DMKPTRUL R13 SAVEWRK2
DMKRPI	ACICODE R11	ACIDEFR R12	ACIPARMS R13	AP R15	DMKIUACP R5	LOCK SAVEAREA	MP SAVEREGS	RCNOSEND SEVER	RÛ	R1
DMKRPW	ACICODE R13	ACIDEFR SAVEAREA	ACI PARMS SAVEREGS	AP	LOCK	MP	RO	R1	R11	R12

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MODULE	EXTE	RNAL REFE	RENCES (L	ABELS AND	MODULES)					
DMKRSE	DMKRSFPR F5 IOBFATAL IOBSTAT IOERECSW IOERNUM PRGC	DMKRSFSD F7 IOBFLAG IOERACT IOERERP IOERPEND PRTC RDEVRSTR R11 R7	F8 IOBIOER IOERBLOK IOERETRY IOERPNT PSA RDEVSPL R12 R8 SFBLOK	DMKRSP83 IFCC IOBLOK IOERCCRA IOEREXT IOERREAD RDEVAIOB	INTREQ IOBMISC2 IOERCCRL IOERFLG1 IOERSIZE RDEVBACK RDEVSTA4 R14 SAVEAREA SFBRECER	F1 IOBCAW IOBRADD	LOCK RDEVBUZY RDEVTYPC R2	F20 IOBCC3 IOBRCNT IOERDATA IOERIGN LPUADDR RDEVDELP RDEVTYPE R3 SAVEWRK2 TYPPUN	F3 IOBCSW IOBRSTRT IOERDEPD IOERIND3 MP RDEVFLAG R0 R4	R1 R5 SAVEWRK5 TYP1443
DMKRSF	ADSPCH DMKFRET IOBFATAL IOERBLOK IOERSIZE RDEVTYPE R4 SAVEWRK1 VMBLOK XOBRRT1 XOBRT3	IOBFLAG IOERCCRA IOERXERP RO R5 SAVEWRK2	R1 R6 SAVEWRK4	IOBIRA IOERCPID MP R10 R7 SAVEWRK6	NOTUSED R11 R8	IOBMISC IOERDATA PSA R12 R9 SKIP	RDEVBLOK R13 SAVEAREA TYP3203 XOBRFCB	R14 SAVEREGS TYP3262	IOERLEN RDEVIOER R15 SAVER6 TYP3289E XOBRMIS1	DMKFREE 10BERP 10BUSER 10ERPNT RDEVPROC R3 SAVER7 TYP4245 X0BRM1S2 X0BRT2
DMKRSP	DMK10E1D DMKPGUVG DMKSCNDC DMKSYSHE DMKSYSWM F2 10BCP 10BM1SC2 10ERCCRA 10ERSIZE RDCBL0K RDEVDFCB RDEVL0AD RDEVSPAC RECMAP RSPCLPRT RSPRPAG2	DMKAPZNO DMKDSPCH DMKDSPCH DMKDSUR DMKSQUR DMKSCNRD DMKSYSHL DMKTCSCO F255 IOBCSW IOBRADD IOERCCRL IOKEEP RDCPAGAP RDEVDISA RDEVMAXP RDEVSPL RECPNT RSPDEFER RSPRSTRT RSPXDST1	DMKERMSG DMKIDEIQ DMKIDEIQ DMKSCNRU DMKSCNRU DMKSCSHT DMKTCSET F3 IOBRCAW IOERCSW LOCK RDEVALGN RDEVDRAN RDEVDRAN RDEVDRAN RDEVDRAN RDEVSTAT RECSIZE RSPDPAGE RSPXFCB	DMKFREE DMKIOEMQ DMKPTSAD DMKSEPSP DMKSYSOC DMKTCSTR F4 IOBFATAL IOBFATAL IOBFATAL IOBRCNT IOERDATA LPUADDR RDEVBACK RDEVEXTN RDEVSTA2 RECUSED RSPDPAG2 RSPSFBLK RSPXFILE	DMKFREEP DMKIOEMX DMKRIOCN DMKSEPTL DMKSYSOW DMKTMRPT F4095 IOBFLAG IOBRSTRT IOERDEPD MP RDEVBLOK RDEVFLAG RDEVFLAG RDEVSTA4 RDEVFLAG RSPFFLDC	DMKCSOSD DMKFRET DMKFRET DMKFRAGT DMKSPAGT DMKSYSRM DMKURSTA F4096 IOBIOER IOBSIZE IOERDERD NPRCNT RDEVENTAS RDEVPTHS RDEVTERM RSPBF1IO RSPFLAG2 RSPSFLAG2	DMKIOCVT DMKIOJBL DMKRSERR DMKSPKDL DMKSYSSP DMKVRR F8 IOBIRA IOBSPEC IOERERP NPRNAME RDEVCFCB RDEVTOER RDEVTOER RDEVTYPC RSPBF1VL RSPIMIDL RSPVPAGE	DMKCVTDT DMKIOECL DMKIOSQR DMKSQDC DMKSTKCP DMKSYSTP FFS IFCC IOBLINK IOBSTAT IOERETN NPRPNT RDEVCLAS RDEVISPL RDEVRDC RDEVTYPE RSPBF2DC RSPLCTL RSPVPAG2	DMK10ECQ DMKLOKIO DMKRSQFR DMKSYSVM F0 IOBCAW IOBLOK IOBLOK IOBUNSL IOEREXT NPRTBL RDEVDED RDEVLDBG RDEVLDBG RDEVRSTR RECBLOK RSPBF2IO RSPMISC RSPXAUTO	RECCYL RSPBF2VL RSPRPAGE RSPXBLOK

MODULE	EXTE	RNAL REFE	RENCES (LA	ABELS AND	MODULES)					
	R9 SFBFLAG SFBLOK SFBSIZE SPNXTPAG TYP3211 URSBACK		SAVEREGS SFBFLAG3 SFBPNT SFBTICER SPRECNUM TYP3800 URSFILE	SFBFLAG4 SFBRECER SFBTUSE SPRMISC	SFBRECNO SFBTYPE SPSIZE TYP38008	SFBRECOK SFBUHOLD SYSTEM	SFBRECS		SFBLDBEG SFBRSTRT SKIP TYPPUN	SFBFCBXL SFBLDMID SFBSHOLD SPLINK TYP3203 UE ZEROES
DMKRSQ	AFREE F255 RECBLOK R11 R7 SFBLOK SPRMISC	AP IOBLOK RECCYL R12 R8 SFBPNT SPSIZE	BRING LOCK RECMAP R13 R9 SFBRECER SYSTEM	CD MP RECPNT R14 SAVEAREA SFBRECOK TEMPR1	CLASFBA PSA RECSIZE R15 SAVEREGS SFBRECS VMBLOK	DMKFREE RDCBLOK RECUSED R2 SAVER2 SILI	RDCPAGAP RSPLCTL R3	DMKPGUVR RDEVBLOK R0 R4 SAVEWRK3 SPLINK	RDEVRDC R1 R5 SAVEWRK4	DMKSCNDC RDEVTYPC R10 R6 SFBFLAG SPPREPAG
DMKRST	DMKSCNFD F24 IOBLINK PSA RSPERR R1 R5	DMKPGTSG DMKSPKDL F6 IOBLOK RDEVBLOK RSPFLAG2 R10 R6 SFBFTYPE SILI		DMKPGUVG DMKSPLOR IL IOBRADD RDEVDRAN	DMKPGUVR DMKSTKCP IOBCAW IOBRCNT	DMKUDRFU IOBCC1 IOBRSTRT RDEVNRDY RSPSIZE R14	DMKURSTA IOBCP IOBSIZE RDEVSPL RSPSWAP R15	DMKFRET DMKPTRUL DMKVSGAI IOBCSW IOBSTAT RDEVSTAT RDEVSTAT RSPVPAGE R2 SAVEWRK2 SFBSIZE	DMKVSGRI IOBFATAL IOERETN RDEVTYPE RSPVPG2 R3	DMKLOCRD DMKRPAPT F0
DMKSÅD	ADMKCPE CCO CLASURO CPUVERSN DE DMPLCORE EXOPSW IOOPSW PMAAVAIL PSA R3 SFBLOK SVCOPSW	DMPABEND DMPPGMAP EXTMODE I PLPSW	APUOPER CC2 COLON CSW DMPCKCOM DMPPGMP2 FPRLOG LINE PMASTAT R1 R5 SFBRECNO TRACSTRT	DMPPRFRG GRLOG MCHEK PREFIXA R10 R6 SFBTIME_	CD CPABEND CURNTPSW DMPCPUTM DMPSYSRM HEADER MCNPSW PREFIXB R11 R7 SFBUSER	BLANK CDC CPID CO DMPCRS DMPSYSRV IFCC MCOPSW PREFIXVL R12 R8 SILI TYPPRT	BUSY CE CPSPMODE C1 DMPFPRS DMPTODCK INTPR MICBLOK PRGC R13 R9 SM UC	C14 DMPGPRS	CPUID C2 DMPINREC EIGHT IOMASK	CCC CLASTAPE CPUTIMR DATE DMPIPCS EXNPSW IONPSW NOTUSED PRTC R2 SFBDIST SVCNPSW
DMKSAV	ALARM CLASFBA DMKCKN DMKCKPNC DMKCVTBH D2 IOOPSW PRNPSW R13 R9	AP CPID DMKCKNND DMKCKPND DMKERP D4 LOCK PSA R14 SILI	AVMREAL CSW DMKCKP DMKCKPRS DMKOPRWT EXNPSW L3 PSTARTSV R15 SK1P	BUSY CUE DMKCKPLD DMKCKPR2 DMKSYSAL F1 L4 QDISK R2 SM	CAW C2 DMKCKPLE DMKCKPST DMKSYSNL F2 L5 REGSAV R3 TEMPR2	CC DE DMKCKPLF DMKCKPT DMKSYSNU F3 L7 R0 R4 TEMPR4	CCO DMKCKD DMKCKPLH DMKCKW DMKSYSRS F4 L8 R1 R5 TEMPSAVE	CE DMKCKF DMKCKPLM DMKCPICD DMKSYSTP F4096 MCNPSW R10 R6 TYP2305	CKPLOAD DMKCKH DMKCKPLN DMKCPINT DMKSYSTZ INTREQ MP R11 R7 TYP2314	CLASDASD DMKCKM DMKCKPLW DMKCVTBD DMKSYSVL IONPSW NDISK R12 R8 TYP3330

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MODULE	EXTER	RNAL REFE	RENCES (L	ABELS AND	MODULES)					
	TYP3340	TYP3350	TYP3375	TYP3380	UC	UE	VMBLOK			
DMKSBL	AP R13	CL R14	F1 R15	LOCK R3	MP R4	PSA R6	RO R7	R1 R8	R11 SAVEAREA	R12 SAVEREGS
DMKSCH	ADSPCH ASYSVM CPEXPROC DMKDSPNP DMKSYSSP F8 MNCOAEL PSA R12 R8 TEMPR13 TRACEND TRACEND TRLCT XCDISP	DMKDSPN2 DMKTRQRT IPUADDRX MNCOAQ PSAEXT R13 R9 TEMPR14	MNCODQ PSAEXTX R14 SIGWAKE TEMPR15 TRACPROC	CPRUN DMKFREXX FFS LOCKSAV MP PXA R15 SIGXC TEMPR4	Q1DROP R2 SLNONPOS TEMPR7 TRACSVCR TRQBQUE	CPUID DMKPTRRC F16 LPUADDRX NOSLBORO RUNUSER R3 STARTIME TEMPR9	F2 MICBLOK PREFIXA R0 R4 TEMPR0 TEMPSAVE TRAC09 TRQBVAL	APUOPER CPEXADD C1 DMKPTSRS F24 MICEVMA2 PREFIXB R1 R5 TEMPR10 TIMEDISP TRCDROP TSEND ZEROES	DMKSTKCP F256 MICSPT PROBSTRT R10 R6 TEMPR11	CPEXMISC DMKDSPCH
DMKSCN	AP BALR1 CLASURI F4095 NICSIZE RCHCUTBL RCUSUB RDEVFLAG RDEVTYPC R5 TYPSDLC VCONNICB	RCUTYPE RDEVFTR RDIDX R6 TYPTELE2	RCUBLOK RDEVADD RDEVMOUT R0 R7 TYP2700	ARIOCT BLANKS C1 F7 OWNDRDEV RCUCHA RDEVAOF RDEVNICL R1 R8 TYP3210 VCONREMF	LOCK OWNDVSER RCUCHCNT RDEVBLOK RDEVPS R11 R9 TYP3705	RCUCHD RDEVBOF RDEVPTHS R14 TYPCTCA	MPGEND PROCIPL RCUDISA RDEVCUA RDEVSER	FTR3088 MPUOPER PSA RCUDVTBL RDEVCUB RDEVSIZE R2 TYPPRT	FO NICBLOK RCHADD RCUPRIME RDEVDED RDEVSTAT R3 TYPPUN	CLASTERM F255 NICRSPL RCHBLOK RCUSTAT RDEVDISA
DMKSCO	CLASDASD RCUPRIME R11 R9 ZEROES	RCUSUB R12	FTR2311B RCUTYPE R13 SAVEREGS	FTR2311T RDEVBLOK R2 SAVER0	LOCK RDEVCUA R3 SAVER11	PSA RDEVCUB R4 SAVER3	RCHBLOK RDEVLNKS R5 SAVER6	RCÚBLOK RO R6 SAVER8	RCUCHA R1 R7 TYP2311	RCUCHC R10 R8 VMBLOK
DMKSEG	ACORETBL CORLCNT DMKMCHST DMKSYM F2 PSA R13 R9 TYP3330	DMKPGTPG DMKSYSNU F4 RDEVBLOK R14 SAVEAREA	DMKPGUBN DMKSYSRM F4096	DMKPMA12 DMKSYSRV IOERETN RDEVTYPC R2	BRING CPSTAT4 DMKPTRAN DMKSYSSP LOCK RDEVTYPE R3 SWPCHG1 VMBLOK	DMKPTTAL DMKSYSVL NEWPAGES	DMKRPAPT DMKVMI	DEFER	F1 PMAAVAIL R11 R7	DMKCKP DMKSCNVS F16
DMKSEL	ACORETBL ALOCRWRT CONTROL CORSWPNT CPEXR2 DMKDSPRU	ALOKRM CORBPNT CORTABLE CPEXSIZE	AEXTSP ALOKSP CORCP CORVM CPPTLBR DMKFREEP		AFREEP ALOKVM CORFLUSH CPEXBLOK CPXSTOR DMKLOKDF	AFRET APSTAT1 CORFPNT CPEXFPNT C1 DMKLOKSW	CPEXMISC DMKCVTAB	APUOPER CORPGPNT CPEXRO DMKDSPCH	ALOCPSIO ASYSVM CORSHARE CPEXR11 DMKDSPNP DMKPGTPG	CHG CORSWAP CPEXR13 DMKDSPN2

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	DMKPTRF2 DMKPTRSS DMKSTKCP F2 MICBLOK PAGTSWP R0 R4 SAVER2 SHRNOPRT SSBEINVL	DMKPTRNS DMKPTRSW DMKSWAPO F4096 MICVTMR PGWRITE R1 R5 SAVEWRK1 SHRSGPRT SSBENTRL SSBVPAGE SWPKEY2	DMKPTRS2	DMKPTRPO DMKPTRU1 DMKSYSSZ IPUADDRX OPPRSTRT PREFIXB R11 R7 SAVEWRK3 SIGEMS SSBFLAG SWPAPP SWPPSTOR	DMKPTRPS DMKPTRWQ DMKVATSI	DMKPTRQC DMKPTSAD FF PAGINVAL PSA R13 R9 SAVEWRK9 SIGRES SSBFPNT SWPCHG2 SWPREF1	DMKPTRFA DMKPTRQ2 DMKPTSAE FFS LASTUSER PAGRBITS PSAEXT R14 SAVEAREA SEGFLAG SIGXC SSBHEADL SWPCODE SWPREF2 UNCHG	DMKPTRRC DMKPTSMW FO LOCK PAGREF PSTPOUPP R15 SAVEREGS SEGINV SSBBPNT	DMKPTRFN DMKPTRRF DMKPTTFT F1 LOCKSAV PAGSHR PXA R2 SAVER11 SEGPAGE SSBCPG SSBNPGS SWPFLAG SWPFLAG SWPSHR VMBLOK	DMKPTRFQ DMKPTRSC DMKPTTPM F16 LPUADDR PAGTABLE RUNUSER R3 SAVER13 SAVER13 SHRFLAG SSBEFLG SSBTRANS SWPFLAG2 SWPFLAG2 XKEYMODE
DMKSEP		R13 R9 SAVEWRK8 SFBRECNO SKIP	FORMNARR 10BFATAL 10ERETN RDEVSEPF R14 SAVEAREA SAVEWRK9	DMKFRET DMKSEQLA FORMNTRY IOBFLAG IOKEEP	R2 SAVER10 SFBDEST	BLANK DEFER DMKPGUVG DMKSYSFL FORMSEND IOBLINK PSA RSPXSEQ R3 SAVER8 SFBDIST SFPBLOK TYP3203 ZEROES	DMKPGUVR DMKSYSSF FORMUSER IOBLOK	DMKTBLSF F0 IOBMISC RDEVEXTN R1 R5 SAVEWRK2 SFBFLAG2	BOXLOGO DMKCVTBD DMKPTRUL DMKTCSSP F4 IOBMISC2 RDEVFLAG R10 R6 SAVEWRK4 SFBLOK SFPLEN TYP3800	BRING DMKCVTBH DMKRPAGT FFS F9 IOBRSTRT RDEVLOAD R11 R7 SAVEWRK5 SFBOFORM SFPTITLE TYP38003
DMKSEV	CCC CCHSTG I FCC RTCODE3 R2	CCHCHNL CCHUSV IGBLAME RTCODE4 R3	CCHCMDV COMPFES IGPRGFLG RTCODE5 R4	CCHCNTB COMPSEL IGTERMSQ RTCODE7 R9	CCHCPU COMPSYS IGVALIDB RO SAVEAREA	CCHDAV CSW INTERCCH R1 SAVEWRK1	CCHDI FFS IOERBLOK R12 SAVEWRK9	CCHINTFC F7 PSA R13 TERMSYS	CCHLOG70 F8 RTCODE1 R14 TIOCCH	CCHREC HIOCCH RTCODE2 R15 XRIGHT16
DMKSFB	APTRAN DMKPSAFP R0 R4 SAVEWRK1 XPAGNUM	APTRLK DMKPSASP R1 R5 SFBFILID ZEROES	ARSPPR DMKPTRAN R10 R6 SFBFLAG	ARSPPU DMKPTRUL R11 R7 SFBINUSE	BLANKS DMKVSFID R12 R8 SFBLOK	BRING DMKVSFNS R13 R9 SFBPNT	C1 DMKVSFNU R14 SAVEAREA SFBSIZEB	R15	DMKLOCRD LOCK R2 SAVERO SFBUSER	DMKLOCRQ PSA R3 SAVER2 VMBLOK
DMKSIX	CCHCHNL CCHUSV IGBLAME RTCODE5 R9	CCHCMDV COMPFES IGPRGFLG RO SAVEAREA	CCHCNTB COMPSEL IGTERMSQ R1 SAVEWRK1	CCHCPU CSW IGVALIDB R12 SAVEWRK9	CCHDAV FFS IOERBLOK R13 TERMSYS	CCHDI F1 PSA R14 TIOCCH	CCHINTFC F7 RTCODE1 R15 XRIGHT16	CCHLOG60 F8 RTCODE2 R2	CCHREC HIOCCH RTCODE3 R3	CCHSTG IFCC RTCODE4 R4
DMKSNC	APTRAN C1 F256 PSA	APTRLK DEFER F4096 RDEVBLOK	ASYSVM DMKERMSG IOERETN RDEVCODE	BRING DMKPTRAN LOCK RDEVFLAG	CCPADDR DMKPTRUL NCPNAME RDEVOWN	NCPPAGCT	CCPNAME DMKSCNVS NCPPNT RDEVTYPE	NCPSTART	CCPSIZE FO NCPTBL R1	CLASFBA F1 NCPVOL R10

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DMKSND	ADSPCH BUFIN CPEXR2 DMKFRET R0 R4 SAVEWRK7	AFREE BUFNXT CPEXR8 DMKLOKSW R1 R5 SAVEWRK8	AFREEP BUFSIZE CPEXSIZE DMKSCHRT R10 R6 TIMEDISP	DMKSCNAU R11 R7	DMKSCNFD R12 R8		F2 R14 SAVEAREA	DMKERMSG LOCK R15 SAVEREGS	MP R2	BUFFER CPEXR12 DMKFREEP PSA R3 SAVER9
DMKSPC	BUFCNT R15	BUFFER R9	BUFNXT SAVEAREA	DMKCFYSP SAVEREGS	DMKCQQ I D	RO	R1	R11	R12	R13
DMKSPK	ADSPCH BRING DMKCKSPL DMKPGUSD DMKSCNDC F4 IOBSTAT RDCPAGXT RECSIZE R3 SFBDIST SFBDIST SFBMISC1 SFBTYPE TYPRDR	DMKPGUSR DMKSTKCP F8 IOBUSER RDEVBLOK R0 R4 SFBDUMP	DMKPGUVG DMKSYSFL IOBCAW IOERETN RDEVFTR R1 R5	DMKVSGRI IOBCP LOCK RDEVMDL R10 R6 SFBFIRST	DMKFREE DMKPTRAN FFS IOBCYL MP RDEVRDC R11 R7 SFBFLAG SFBFLAG SFBRECER	FTR70MB IOBFATAL OWNDLIST RDEVTYPC R12 R8	DMKFRET DMKPTRUL FO IOBFLAG PSA RDEVTYPE R13 R9 SFBFLAG4	DMKIOSQR DMKRPAGT F10 IOBIRA RDCBLKAP READ R14	DMKRSPDL F2 IOBLOK RDCBLKPG RECBLOK R15 SAVEREGS	DMKRSPHQ F3 IOBMISC2 RDCBLOK RECPNT R2 SAVER11 SFBLOK
DMKSPL	APSTAT1 ASYSVM DMKAPZNO	DMKSCNAÚ DMKVSGRI F4096 IOBVADD PLIST RSPRPAGE R14 SAVEAREA SFBCOPY SFBLAST	APTRLK BLANKS DMKCKTSU DMKLOKSW DMKSPKDL FFS F8 LOCK PLR2 RSPSFBLK R15 SAVEREGS SFBDATE SFBLOK SFBSTART SHQSHOLD SPCOPYFG	APUOPER BRING DMKCKTUU DMKPGTSG DMKSTKIO FORMBLOK IOBCSW MP PLSIZE R2 SAVER11 SFBDEST SFBNOHLD SFBSTCPY SHQUSER	DMKPGUVG DMKSYSFL FORMNTRY IOBIRA NOCOPY PRTCHN RSPVPAGE R3 SAVER7 SFBDIST SFBDIST SFBDFORM SFBSYSID SKIP SPFLAG1 TYPRDR	DMKPTRAN DMKUDRFU FORMOPER IOBLINK NORET PSA R0 R4 SAVER8 SFBFILID SFBORIG	DMKQCNPL DMKUDRMD FORMSEND IOBLOK NOTRESP RDEVBLOK R1 R5 SAVER9 SFBFLAG SFBFLAG SFBPNT SFBTYPE SPCHAR SPLINK TYP3210	R10 R6 SAVEWRK1 SFBFLAG2	DMKV101N F0 IOBSPEC PLFLAG1 RDRCHN R11 R7 SAVEWRK2 SFBFLASH SFBFLASH SFBREQUE SFBUHOLD SPCHAR2	DMKRPAPT DMKVSDAD F1 IOBUNSL PLFRET RSPDPAGE R12 R8 SAVEWRK8 SFBFNAME SFBRSTRT SFBUSER
DMKSPM	ACORETBL ASYSVM CPEXR12	ADSPCH AVMREAL CPEXR14	AFREE BLANKS CPEXSIZE	AFREEP CORCP CPPTLBR	AFRET CORFLAG CPSPMODE	AP CORTABLE CPSTAT2	APSTAT1 CORVM CPSTAT4	APSTAT2 CPEXADD CUE	APUOPER CPEXBLOK DMKDMPCP	

#### EXTERNAL REFERENCES (LABELS AND MODULES)

R11R12R13R14R15R2R3R7R8R9SAVEAREASAVEREGSSAVEROSAVER2SAVEWRK3SAVEWRK4SAVEWRK5SAVEWRK6SAVEWRK8SAVEWRK8SAVEWRK9SYSTEMTYP3375TYP3380VMBLOKVMBLOKSAVEWRK6SAVEWRK8SAVEWRK8SAVEWRK9SYSTEM

R4 SAVER6 TYP2314

R5 R6 SAVEWRK1 SAVEWRK2 TYP3330 TYP3350

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## EXTERNAL REFERENCES (LABELS AND MODULES)

HODGEL					,					
	IOBIRA MICEVMA2 PMASTAT R14 SAVEAREA	DMKFREEP DMKVIOIN IOBLINK MICEVMA3 PREFIXA R15 SAVEREGS TREXINST	F1 IOBLOK MICPMAMP PREFIXB R2 SAVER11	F16 IOBSIZE	F3 IOBSPEC MICSTSM2 R0 R4		F4096 IOBUNSL MPFEAT R10 R6 SWPKEY1	IOBCSW LOCK NORET R11 R7 SWPKEY2	DMKSTKCP IOBCUBSY LPUADDR PMAAVAIL R12 R8 TIMEDISP	IOBCUE MICBLOK PMAMODE R13 R9
DMKSPR	AFREE DMKLOKSW R11 R9 TIMEDISP		APSTAT1 DMKSCNAU R13 SAVEREGS	R14	AQCNWT MP R15 SPT I SSUR	ASYSVM NORET R2 SPTOSFID	BLANK OPERATOR R3 SPTOUSER	R4	DMKCVTBD R0 R5 SVMNOUPD	R1 R6
DMKSPS	DMKIOSQR DMKRPAGT DMKVSETR IOBMISC PRTCHN RDRCHN R3 SFBCONV SFBMISC1 SFBUHOLD	DMKLOCRD DMKRPAPT DMKVSGRI IOBSIZE PSA R0 R4 SFBDEST SFBDFORM SFBUSER SFDBUFR2 SPTENTPT SPTIOBAD SPTOFLOW SPTRUN	DMKLOCRQ DMKRSPFL F0 IOBSTAT RDEVBLOK R1 R5 SFBDUMP SFBPNT SFBXAB SPTBUFV1 SPTFILES SPT ISSUR	DMKLOKSW DMKSCNAU F1 IOERSIZE RDEVFLAG R6 SFBFILID SFBRECNO SILI SPTBUFV2 SPTFLAG	DMKPGTDG DMKSPKDL IL LOCK RDEVSPT R11 R7 SFBFLAG SFBFLAG SFBRECS SPCPTRAP SPTCAN SPTFLAG1 SPTFLDPRT SPTSHOLD	IOBCSW MP RDEVSYS R12 R8 SFBFLAG4 SFBSHOLD SPLINK SPTCLAS SPTFLAG2 SPTFLAG2 SPTFLDRDR SPTPUR	DMKPGUSD DMKSYSDU IOBFATAL NORET RDEVTYPC R13 R9 SFBFNAME SFBSIZE SPNXTPAG SPTCLASS SPTCLAS3 SPTLINK SPTRADDR	DMKPGUVR DMKSYSOW IOBFLAG OWNDLIST RDEVTYPE R14 SAVEAREA SFBINUSE SFBSTART SPPREPAG SPTCODE SPTFORM SPTLKLST SPTRDEV SPTSPID2	SFBSYSID SPRMISC SPTDEST SPTFRMST SPTLOAD SPTRDR	DMKVSDDL IOBLOK PCHCHN RDIDX R2 SFBCLAS SFBCLAS SFBLOK SFBLOK SFTDESTS SPTINTAD SPTMSGAD SPTTREAD SPTTM
DMKSPT	DMKVSFNS F6 IOKEEP RDEVSPT R11 R8 SFBCLAS SFBUHOLD SPTDEST	DMKVSFNU F7 LOAD RDEVSTAT R12 SAVEAREA SFBDEST SILI	DMKLOCRQ DUMP F8 LOCK RDEVSYS R13 SAVERECS SFBFLAG SPTBLOK SPTENTPT SPTLOAD SPTREW	FLAGS IOBCAW OFF RDEVTYPC R14 SAVER2 SFBFLAG4	F0 IOBCSW PSA RDEVTYPE R15 SAVEWRK1 SFBINUSE SPTBUFR2	SFBLOK SPTBUFV1 SPTFLAG2	DMKSCNFD F2 IOBIRA RDEVDED REWIND R3 SAVEWRK4 SFBOFORM SPTBUFV2 SPTFLAG3 SPTOFLOW	RO R4 SAVEWRK7 SFBSHOLD SPTCAN SPTFORM SPTPRT	DMKSPSIO F4 IOBMISC RDEVFLAG R1 R6	F5 IOBUSER RDEVPEND R10 R7 SAVEWRK9 SFBTUSE SPTCLASS SPTINTR SPTRADDR
DMKSRM	AFREE BLANKS DMKFRET	AFRET BRING DMKPAGDP	ALOCPG CC3 DMKPGMST	ALOCPP C1 DMKPGTDF	ALOCPS DEFER DMKPGTDK	ALOCSW DMKCVTBD DMKPGTDL	AP DMKCVTDB DMKPGTDM	APTRAN DMKDSPQS DMKPGTDP	AQCNWT DMKERMSG DMKPGTPL	ASYSVM DMKFREE DMKPGTPN

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# MODULE EXTERNAL REFERENCES (LABELS AND MODULES)

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		DMKSCNFD F3 OFF R2 SAVEWRK1	F4 PSA R3	DMKSWABT F8 R0 R4 SAVEWRK4		DMKSWAQ2 IOKEEP R10 R6 SYSPEXT	DMKSCHIB DMKSYSSW LMSG R11 R7 SYSPEXTN SYSPEXTN SYSXRWCT		DMKSCHQB F0 L4 R13 R9 SYSPFPNT VMBLOK	F1 MP R14 SAVEAREA
DMKSSP	I OOPSW RCUADD RDEVBLOK	ARIOCT CE C2 DMKRIOPU L3 RCUBLOK RDEVCKDX RDEVSTA3 R14 SAVEAREA TYP3210	ARIOCU CLASDASD DE DMKRIORD L5 RCUCHA RDEVCLAS RDEVTYPC R15 SCAN TYP3277	DMKCPINT DMKSYSNU MAXLEN RCUCHB RDEVCUA	DMKCVTBH		DMKRIOCH FTREXTSN PSA RCUDVTBL	FTRUCS RCHBLOK	INPUT RCHCUTBL RCUTYPE	IONPSW
DMKSSS	DMKCVTBH	DMKPGUVR DMKVDAS2 L4 RCUTYPE RDEVOWN R1 R5 SAVER2	AFREEP ASYSVM CPEXRO DMKERMSG DMKPTRAN DMKVIOMK MP RDEVADD RDEVSEL R10 R6 SAVEWRK1 SYSVIRT	DMKRPAGT	R12 R8 SAVEWRK3	F8 RCHBLOK RDEVCUA RDEVSTAT R13 R9	DMKSCNVS IOERETN RCUADD RDEVDED	CPEXSIZE DMKLOKIO DMKSCNVU LOCK RCUBLOK RDEVDISA	APTRLK CPEXADD C1 DMKLOKSW DMKSSTFV LOCKSAV RCUCHA RDEVFLAG RDEVTYPE R2 SAVERETN SAVEWRK7 ZEROES	DMKSTKCP LPUADDR RCUPRIME RDEVFTR RDEVUSER R3
DMKSST	F8 RDEVDED RDEVTYPC R3 SAVER3	IOERETN RDEVDISA	DMKSSSNS LOCK RDEVFLAG R1 R5 SAVER6	LOCKSAV	APTRLK DMKPGTPG FFS LPUADDR RDEVNRDY R11 R7 SAVEWRK2 TYP3330	FO MP RDEVOWN R12 R8	ASYSVM DMKPGUVR F1 MSSPRES RDEVSEL R13 R9 SAVEWRK4	F16 PSA RDEVSER R14 SAVEAREA	BRING DMKRPAGT F4 PSAMSS RDEVSTAT R15 SAVEREGS SAVEWRK6	F4096 RDEVBLOK RDEVSYS R2 SAVER2
DMKSSU	ADSPCH CPEXBLOK DMKFRET IOBBPNT IOBLINK LOCK R0 R4 SAVEWRK6 TRQBTOD			DMKSCHST IOBCTRQ IOBRSTRT MP R11		DMKSSSMQ IOBFATAL IOBSPEC RDEVBLOK R13	IOBSTAT RDEVCFLT R14	IOBFLT IOBUNSL RDEVFIOB R15 SAVEWRK3		R3

MODULE
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# EXTERNAL REFERENCES (LABELS AND MODULES)

DMKSSV	AFRET CPEXBLOK DMKFRET DMKSSSHR LOCK RDEVSEL R14 SAVEAREA	DMKLOKIO DMKSSSMQ LOCKSAV RDEVSER R15	DMKSSSNS LPUADDR	DMKSSSVM MP RDEVVMNT R3	DMKSSTBL MSSPRES RO R4		ARIODC C1 DMKSCNRD DMKSTKCP PSA R10 R6 TIMEDISP	FO PSAMSS R11 R7	BLANKS DMKCVTBH DMKSSSCA F4 RDEVBLOK R12 R8	DMKSSSCV F8
DMKSTA	DMKPSAST DMKSELSB	CORTABLE DMKCPISW DMKFREHI DMKPTRFN DMKSELSG DMKSYSTR F4096 PRIMEHDR R11 R8	DMKCPITR DMKFREHP DMKPTRF1 DMKSVCHI DMKVRSSV F8	DMKFRELO DMKPTRF2 DMKSVCLO EXTMODE INTPR PRIMELO R14	CPSTAT4 DMKCPJTA	DMKFREPP DMKPTRN2 DMKSYSFP FFS L2048 PROPSW R2	DMKSYSNP F1 MCHEK PSA R3	DMKPAGHI	CORFREE DMKCPEND DMKDSPNP DMKPAGLO DMKSELCP DMKSYSRM F255 OLDKEYOP R0 R5 TRACSVST	DMKDSPN2 DMKPRGIN DMKSELSA DMKSYSRV F4 PAGESIZE R1 R6
DMKSTD	R12									
DMKSTK	AEXTSP CPEXPROC IOBUSER PSAEXT R15 SWTHSAVE	I PUADDRX PSAEXTX R2	AP CPEXTYPE LOCK PXA R3 XCDISP	APSTAT1 CPRUN LOKREQ RQLOCK R4 XCPEND	APUOPER CPSTATUS LPUADDR RO R5	CPEXBLOK CPUID LPUADDRX R1 R6	CPEXBPNT CPWAIT MP R10 R7	CPEXDEFR DMKLOKSY PREFIXB R11 R8	CPEXMISC IOBFPNT PSA R12 SIGDISP	CPEXPRIO IOBLOK PSADSPRQ R14 SIGXC
DMKSTP	CPEXRO DMKFREHI DMKPTRES DMKSCHBS DMKSCHNS DMKSCHSM	DMKPTRPR DMKSCHCA DMKSCHCB DMKSCHST DMKSWAOP F1 PAGERATE R0 R4 SAVER1 STPDRP SYSPOVFL SYSXSIZE SYSSIZE	AP CPEXR12 DMKPAGPS DMKPTRRC DMKSCHCO DMKSCHCO DMKSCHCO DMKSCHCO DMKSCHCO DMKSCHCO DMKSCHCO TR SCHCO SPACE SYSPALL SYSPPCNT TEMPRO	DMKPGMEP DMKSCHCU DMKSCHCU DMKSCHS2 DMKSCHS2 DMKSWART F3 PGREAD R10 R6 SAVER13 SYSPALOC	APUOPER DMKCVTAB DMKPGTDF DMKPTRSC DMKSCHDL DMKSCHUC DMKSCHUC DMKSYSNM IDLEWAIT PGSRATIO R11 R7 SAVEWRK1 SYSPEXT SYSPTOTL TEMPR10	ASYSVM DMKDSPNP DMKPGTMX DMKPTRSS DMKSCHET DMKSCHQT DMKSTDAT DMKSYSRM IONTWAIT PGWAITIM R12 R8 SAVEWRK2 SYSPEXTN	DMKSCHQ1 DMKSTKCP DMKSYSSW LOCK PGWRITE R13 R9 SAVEWRK4 SYSPFLG	CD DMKDSPQS DMKPGTQT DMKPTTLH DMKSCHIS DMKSCHQ2 DMKSCHQ2 DMKSYSSZ MP PREFIXA R14 SAVEAREA SAVEWRK6 SYSPFPNT SYSXLONG TEMPR4 TRQBTOD	CPEXADD DMKFREE DMKSCHAL DMKSCHAL DMKSCHAL DMKSCHRL DMKSCHRL DMKTRQIL NOALCARY PREFIXB R15 SAVEREGS SAVEWRK8	DMKSCHAP DMKSCHLI DMKSCHSC DMKSWABT DMKTRQTI NOSLBORO PROCIPL R2 SAVERETN SAVERETN SAVEWRK9 SYSPLIST SYSXRWCT TEMPR6
DMKSTR	ACORETBL APUOPER CORTABLE	ASYSVM	AFREEP CORBPNT CPEXBLOK	ALOCBLOK CORFLAG CPEXFPNT	ALOCFLG CORFPNT CPEXMISC	ALOCPG CORFREE CPEXRO	ALOCPP CORIOLCK CPEXR12	ALOKRM CORLCNT CPEXR13	ALOKSP CORPGPNT CPEXR4	APSTAT1 CORSWPNT CPEXR9

	CPEXSIZE DMKPAGIO DMKPTRPR FO PAGCORE PSA R3 SAVER1 SEGFLAG SSBENTRY SWPRECMP XMIGSWT	DMKPAGQ DMKPTRRQ F16 PAGINVAL R0 R4 SAVER11 SEGINV SSBENUM	F4096 PAGPGSWP R1 R5 SAVER2 SEGMIG SSBFLAG	DMKPTSAE F8 PAGRBITS R10 R6 SAVER3	DMKPTRAQ DMKPTTCL LOCK PAGREF R11 R7 SAVEWRK1 SEGMIGPP SSBLOK	DMKBLDRL DMKPTREP DMKPTTDM LOCKSAV PAGSWP R12 R8 SAVEWRK2 SEGPAGE SSBTRANS SWPVPAGE	DMKPTRER DMKSCHDL LPUADDR PAGTABLE R13 R9 SAVEWRK3 SEGPLEN	DMKPTRFN DMKSCHMS MICBLOK PAGTBSIZ R14 SAVEAREA SAVEAREA SAVEWRK4 SPECIALV SWPCYL	DMKPTRFQ DMKSTKCP MICVTMR PAGTONLY R15 SAVEREGS	DMKLOKSW DMKPTRN2 DMKSYSCS NEWPAGES PGREAD R2 SAVERO SEGENQ SSBENTRL SWPPAG XKEYMODE
DMKSVC	R4	DMKFREEP DMKSVDIN FRPSIZE LPUADDRX R0 R7	DMKFRELP DUMPSAVE FRTAG PREFIXA R1 R8 SAVEWRK2	FO PREFIXB R11 SAVEAREA	CPSTAT2 DMKFRET FRPALLOC F4095 PRIMEHDR R12 SAVEPROC SVCOPSW	INTSVC	APTRAN CPEXBLOK C1 DMKLOKSY FRPMEXT INTSVCL PRIMELO R14 SAVERTN SYSTEM VMBLOK	DEFER DMKPTRAN	FRPMSIZE LOCK	
DMKSVD	ADSPCH CODE C8 DMKLOKDF DMKTRCSV F4 NORET QUANTUMR R12 R8 TRACEND TRACEND TREXT ZEROES	AFREE CPCREGO DEFER DMKLOKSY DMKVAULA F60 PERBLOK RUN R13 R9 TRACFLG1 TRQBLOK	AFRET CPCREG8 DFRET DMKPERIL DMKVAURN INTSVC PERDATON RUNCRO R14 SAS TRACPROC TRQBQUE	DMKPRGRF DMKVERD INTSVCL	AP CPEXBLOK DMKCVTBH DMKPTRAN DMKVERO IOMASK PERMODE RUNPSW R2 SVCOPSW TRACSVCR WAIT	APSTAT1 CPEXREGS DMKDSPB DMKQCNWT EXTMASK LAP370E PREFIXA RUNUSER R3 SVCREGS TRAC02 XRIGHT24	EXTMODE LOCK	APUOPER CPSTATUS DMKDSPRU DMKTMRPT FFS LPUADDR PSA R1 R5 TIMEDISP TRAPCR8 Y2	AQCNWT CO DMKFREE DMKTRCIT FO MCHEK PSASVCCT R10 R6 TIMER TRCSVC Y4	BRING C1 DMKFRET DMKTRCPB F1 MP QUANTUM R11 R7 TRACCURR TREXIN1 Y6
DMKSWA	ACORETBL AP CORFPNT CPEXRO DMKPGUAL DMKPTSAD MICBLOK PREFIXA R13 R9 SAVEWRK8 SSBENUM SSBNPGS SWPCHG2 TRACCURR	APSTAT1 CORLCNT CPEXSIZE DMKPGUPP DMKPTTFT MICVTMR PSA R14 SAVEAREA SEGINV SSBEPGS SSBOLD SWPCYL	DMKPGUSW DMKSYSCS MNCLSWAP PSAEXT R15 SAVEREGS SEGPAGE SSBEXTND SSBPGSP SWPFLAG	DMKDSPCH DMKPTRAN DMKSYSSZ MNCODEOS PSTPOUSW R2 SAVER11 SSBBPNT	DMKFREE DMKPTRAQ F1 PXA R3 SAVER2 SSBCODE SSBFLUSH SSBSWAP SWPSEGNO	BRING CORVM DMKFREEP DMKPTROQ F8 MNCODSOS R0 R4 SAVESIZE SSBCYL SSBFPNT SSBTRANS SWPTABLE		CORBPNT CPEXBLOK DMKPAGIO DMKPTRSW LOCK PAGCORE R10 R6 SAVEWRK3 SSBEINVL SSBLOK SSBVMSCB	CORFLAG CPEXFPNT DMKPGTSB DMKPTRS2 LOCKSAV PAGINVAL R11 R7 SAVEWRK5 SSBENTRL SSBENTRL SSBENTRL SSBVPAGE TIMEDISP	SSBNDLCT SWPCHG1

MODULE	EXTERNAL	REFERENCES	(LABELS	AND MODULES)

DMKSWM	DMKSTRAN LPUADDR R1 R5 SAVEWRK5	ALOKRM CORVM DMKFRET DMKPTROQ DMKSWAIS PAGCORE R10 R6 SAVEWRK6 SSBENTRL SSBUPGS	DMKPTRPS DMKSWAOS PAGINVAL R11 R7 SAVEWRK7	DMKPGMUS DMKPTRSW DMKSYSCS PAGREF R12 R9 SAVEWRK8 SSBEPGS	APUOPER CPEXFPNT DMKPGTPM DMKPTRS2 DMKSYSSW PSA R13 SAVEAREA	ASYSVM CPEXMISC DMKPGTSF DMKPTSAD F1 PSAEXT R14 SAVEREGS SEGMIG	DMKPTSAE F6 PSTPINSW R15 SAVEWRK1 SEGPAGE SSBHEADL	CORFPNT CPEXSIZE DMKPGUSW DMKPTTFT F8 PXA R2 SAVEWRK2 SSBALLOC	CORPGPNT DMKERMSG DMKPTREP DMKSCNAU LOCK REQUSER R3 SAVEWRK3 SSBCYL SSBNDLCT	DMKFREE DMKPTRFN DMKSCNFD LOCKSAV R0 R4 SAVEWRK4 SSBEFLG	
DMKSYM	DMKDSPN2 DMKFREAP DMKFREAP DMKFREMX DMKGRCQY DMKIOSMQ DMKLOKPS DMKLOKPS DMKPGTDM DMKPGTDM DMKPRVLP DMKPRVLP DMKPRVLP DMKPRVLP DMKPRVLP DMKPRVLP DMKPRVLP DMKPRVLP DMKPRVLP DMKPRVLP DMKPRVLP DMKPRVLP DMKPRVLP DMKPRVLP DMKPRVLP DMKSPRQ DMKSCHTQ DMKSCHTQ DMKSCHTQ DMKSCHTQ DMKSCHTQ DMKSCHTQ DMKSCHTQ DMKVCNFT DMKVCNFT	DMKCPEID DMKCPEID DMKDSPQS DMKFREE DMKFREE DMKFRENP DMKIOSNM DMKLOKRL DMKMCTPT DMKPTTD DMKPTACD DMKPMAXS DMKPRVCS DMKPRVCS DMKPRVCS DMKPRVCS DMKPTRF2 DMKPTRF2 DMKPTRF2 DMKPTRF2 DMKPTRF2 DMKSCHN2 DMKSCHN2 DMKSCHN2 DMKSCHN2 DMKSCHN2 DMKSCHN2 DMKSCHN2 DMKVCFIL DMKVCFIL DMKVRIC	DMKCPEML DMKCPEML DMKDSPRQ DMKFREEA DMKFREEA DMKFREEP DMKIOSQR DMKLOSQR DMKPGTPG DMKPGTFG DMKPRGCT DMKPRVCT DMKPRVCT DMKPRVCT DMKPRVCT DMKPRVCT DMKPTRFA DMKPTRFA DMKPTRFA DMKPTRFA DMKRGDOB DMKRSCHW2 DMKSCHW2 DMKSCHW2 DMKSCHW2 DMKSCHW2 DMKVCRT DMKVCRMT DMKVCRMT DMKVCRMT	DMKDSPRU DMKFREEP DMKFRERC DMKGRHIN DMKIOSQV DMKLOKSW DMKNSWR DMKPGTPL DMKPGTPL DMKPRGC8 DMKPRVDI DMKPRVMO DMKPTRFC DMKPTRFK DMKPTRFK DMKPTRFK DMKPTRFK DMKPTRFK DMKRGD0I DMKRSCNRU DMKSVCNS DMKSVCNS DMKSVCNS DMKSYSRV DMKKTRQMD DMKVATEX	DMKCPEPP DMKCPSPWA DMKCPSPWA DMKFREHI DMKFRESC DMKIOTCT DMKLOKSY DMKOPRWT DMKPRGIN DMKPRGIN DMKPRGIN DMKPRGIN DMKPRGIN DMKPRVEK DMKPTRLX DMKPTRLX DMKPTRLX DMKPTRLX DMKPTRLX DMKPTRLX DMKPTRLX DMKPTRLX DMKRSN DMKSCHPU DMKSCHPU DMKSYSW DMKVCVER DMKVCVER DMKVCVER DMKVCVER DMKVCVER DMKVCVER DMKVCVER DMKVCVER DMKVCVER DMKVCVER DMKVCVER DMKVCVER DMKVCVER	DMKCVT DMKCVT DMKFRELG DMKFRESV DMKFRESV DMKFRESV DMKHVCAL DMKIOKTR DMKLOKTR DMKPAGIO DMKPGTPT DMKPRGMC DMKPRGMC DMKPTRAN DMKPTRFN DMKPTRNF DMKFTRNF DMKFTRNF DMKRTRQL DMKCCRD DMKSCHQ1 DMKSCHQ1 DMKSYSCS DMKVSC DMKVSC	DMKDADER DMKCADER DMKERTIM DMKFRELN DMKFRELN DMKFRELN DMKHVCDI DMKLOKCT DMKLOKCT DMKLOKVM DMKPGSS DMKPGSS DMKPGSS DMKPTRF DMKPTRCO DMKPTRRCO DMKPTRRCO DMKPTRRCO DMKPTRRCO DMKPTRRCO DMKSCHQ2 DMKSCHQ2 DMKSCHQ2 DMKSCHQ2 DMKSYSLC DMKVFRCH DMKVSPWA	DMKDASER DMKDSPEC DMKEXTIN DMKFRELO DMKFRELO DMKFRETL DMKIOEFM DMKLOEFM DMKPGTM DMKPGTM DMKPGTM DMKPRGSM DMKPRGSM DMKPRGSM DMKPRGSM DMKPRCS DMKPTRFQ DMKPTRFQ DMKPTRFQ DMKPTRFQ DMKRTRFQ DMKSCHRL DMKSCHRL DMKSCHRL DMKSCHRL DMKSCHRL DMKSCHRL DMKSCHRL DMKSCHRL DMKSCHRL DMKSCHRL DMKSCHRL DMKSCHRL DMKSCHRL DMKSCHRL DMKSCHRL DMKSCHRL DMKSCHRL DMKSCHRL DMKSCHRL DMKUNTFR DMKVAULA DMKVOIN DMKVSICW	DMKDGDDK DMKDGDDK DMKCDSPIT DMKEXTSP DMKFRELP DMKFRTRS DMKIOERR DMKIOERR DMKPGTDF DMKPGTDF DMKPGTDF DMKPRVCC DMKPRVCC DMKPRVCC DMKPTRFR DMKPTRFR DMKPTRFR DMKFTRFR DMKSCHDL DMKSCHRT DMKSCHDL DMKSCHRT DMKSCHDL DMKSCHRT DMKSCHDL DMKSCHRT DMKSCHZ DMKSCHZ DMKSCHZ DMKSCHZ DMKVAURN DMKVMASH DMKVSIEX	DMKDIDEP DMKDSPNP DMKFRS DMKFRELS DMKFRTTR DMKIOSHA DMKLOKFR DMKNCTMA DMKPGTDK DMKPGTDK DMKPGTDK DMKPGTDK DMKPRVLG DMKPRVLG DMKPRVLG DMKPTRPL DMKPTRFO DMKPTRFO DMKPTRFO DMKPTRFQ DMKPTRFQ DMKRSPEX DMKSCHIB DMKSCHIB DMKSCHST DMKSCHST DMKSTKMP DMKSYSOP DMKTRKFP DMKVCNEX DMKVCNEX DMKVSISF	
DMKTAP	ADSPCH CPEXADD DMKFREE DMKTAQRE F5 IOBFLAG	AFREE CPEXBLOK DMKFREEP DMKTAQRP F6 IOBHVC	DMKFRET	AFRET CPEXREGS DMKIOERR FFS IFCC IOBIRA		CCC CPEXR12 DMKIOSQR F0 IOBCC3 IOBLOK	CD CPEXSIZE DMKMSWR F1 IOBCP IOBMISC		CHC DE DMKPTRUL F16 IOBERP IOBRCAW	CONTINUE DMKDSPCH DMKSTKCP F3 IOBFATAL IOBRCNT	

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MODULE	EXTE	RNAL REFE	RENCES (LA	ABELS AND	MODULES)					
	IOBRSTRT IOERCAN IOEREXT IOERSIZE RDEVFTR R10 R6 TYP2401	IOERCCRA IOERFLG1 IOERMSG IOERSTAT	IOBSPEC IOERCCRL IOERFLG2 IOERMSW IOERSTRT RDEVNRDY R12 R8 TYP2420	I OERFLG3 I OERNUM LOCK	R14	IOBUNSL IOERCPEX IOERFSR IOERPEND PRGC RDEVSTMD R15 SAVEREGS TYP8809	IOERIGNR IOERPNT PRTC RDEVTYPE R2	IOERACT IOERDATA IOERIND3 IOERRBK PSA REWIND R3 SAVEWRK1 UE	IOERBLOK IOERDW IOERIND4 IOERREAD RDEVBLOK R0 R4 SAVEWRK4 XRIGHT16	IOERERG IOERINFO IOERREW RDEVCUB R1 R5 SILI
DMKTAQ	AFREE DMKFREE F5 IOBFLAG IOERCAN IOERFLG1 IOERNUM IOERWRK R0 R4 SILI TYP8809	AFRET DMKFRET F8 IOBIOER IOERCCRA IOERFLG2 IOERORA LOCK R1 R5 SKIP ZEROES	AP DMKIOEST IDA IOBLOK IOERCCRL IOERFLG3 IOERPEND MP R10 R6 TRACK7	IFCC IOBRCAW IOERCLN IOERFSR	CCC DMKSTKCP IL IOBRCNT IOERCPEX IOERIGNR IOERREAD PRGC R12 R8 TYP2415	IOERIND3	IOERDATA IOERIND4 IOERSIZE PSA R14	IOERINFO IOERSTRT RDEVBLOK R15	CPEXBLOK F2 IOBERP IOERBLOK IOERENG IOERLOC IOERSUPP RDEVTYPE R2 SAVEWRK1 TYP3422	F4 IOBFATAL IOERBSR IOEREXT IOERMSW IOERVLD REWIND R3
DMKTCS	DMKSNTON HLDAREA HLDCHAR2 IDA IOBSIZE NPRVOI	DMKTCTET HLDCCW1 HLDCHAR3 IOBCAW IOBSTAT PSA RDEVSTA2 RSPXCHR R10 R6	F0 HLDCCW2 HLDCPY IOBFATAL IOERETN RDEVBLOK RDEVTYPC RSPXCHR1 R11 R7 SAVEWRK5 SFBUSER	F1 HLDCCW3 HLDFCB IOBFLAG IOKEPP RDEVCODE RDEVTYPE RDEVTYPE R12 R8	DMKPTRAN F2 HLDCCW4 HLDFLAG IOBIRA LOCK RDFVCURP	F256 HLDFLSHC IOBLINK MP RDEVEXTN RSPDPAGE RSPXCMOD R14 SAVEAREA	DMKSCNAU F3 HLDCCW6 HLDMCHR IOBLOK NPRNAME RDEVFLAG RSPLCTL RSPXFCB R15 SAVEREGS SAVEREGS SAVEWRK9 SPCHAR1 SPNXTPAG	DMKSCNRD F4095 HLDCCW7 HLDMDFY IOBMISC NPRPNT RDEVFSEP RSPRPAGE RSPXMCHR R2 SAVER11	DMKSCNVS F4096 HLDCHARS HLDSIZE IOBMISC2 NPRSTART RDEVIMAG RSPRPAG2 RSPXVTRC R3 SAVEWRK1 SFBFILID SPCHAR3 SYSTEM	HLDADDR1 HLDCHAR1 HLDSTCPY IOBRSTRT NPRTBL RDEVOWN RSPVPAGE R0 R4 SAVEWRK2
DMKTCT	ADSPCH C1 DMKPGUVR F2 HLDCCW6 IOBFATAL LOCK RSPLCTL RSPXFCB R2 SAVER11 SILI	F3 HLDCHARS IOBFLAG PSA RSPRPAGE	DMKRPAGT HLDADDR1 HLDCHAR1 IOBIRA RDEVBLOK RSPRPAG2 R0 R4	DMKSCNAU HLDAREA HLDCHAR2 IOBLINK RDEVEXTN RSPSFBLK R1 R5	RSPXBLOK R10 R6 SAVEWRK3	DMKTCSML HLDCCW1 HLDHLPU IOBMISC RDEVPRFG RSPXCHR R11 R7 SAVEWRK4	FTR4WCGM HLDCCW2 HLDMNAM IOBMISC2 RDEVPURG RSPXCHR1 R12 R8	F0 HLDCCW3 HLDSFID IOBRSTRT RDEVSEPF RSPXCHR2 R13 R9 SFBFILID	RDEVSTA2 RSPXCHR3 R14 SAVEAREA	F16 HLDCCW5 IOBCAW IOBSTAT RDEVTYPE RSPXCMOD R15

MODULE EXTERNAL REFERENCES (LABELS AND MODULES)

DMKTDKALOCELOKALOCCUSLALOCCUSLALOCCUSLALOCCUSEALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSKALOCTOSK<												
DMKTEEBLANKS R10CODE R11 R12DEVICE R12 R12 R11 R11 R12DMKTEDAT R13 R14 R14 R15DMKTESUB R14 R15 SUBSTATDMKTESUB R15 R16 R17 SUBSTATDOCK R16 R4 R15 SUBSTATPROBMODE R4 R5 R16 SUBSTATR1 R6 R6 R5 R10 SUBSTATR1 R9 R5 SUBSTATR1 R9 SUBSTATR1 R9 SUBSTATR1 R10 R11 SUBSTATR1 R10 R10 SUBADDRR1 R10 R10 SUBADDRR1 SUBSTATR1 R10 SUBADDRR1 SUBADDRR1 R10 R10 SUBADDRR1 SUBADDRR1 SUBSTATR1 SUBSTATR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBSTATR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDRR1 SUBADDR<	<b>DMKTDK</b>	CLASFBA DMKPGT4A PSA R10 R6	CLEARBIT DMKPGT5A RDCBLKAP R11 R7	DMKCPXCK DMKPGT7A RDCBLOK R12 R8	DMKPGTAE DMKPGT8A RDEVBLOK R13 R9	DMKPGTAN DMKSYSTD RDEVLNKS R14 SAVEAREA	DMKPGTAT F255 RDEVRDC R15 SAVEREGS	DMKPGTAW F256 RDEVTYPC R2 SAVER0	DMKPGTAO LASTALOC RDEVTYPE R3 SAVER1	DMKPGTA4 LOCK R0 R4 SAVER8	DMKPGTA5 L1 R1 R5	
R10R11R12R13R14R15R4R5R6R8SUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTSUBVIRTS	DMKTED	CODE										
ATTAR1R3R3R3R3R9SUBADDRSUBADDRSUBSTESUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLGSUBCSFLG	DMKTEE	R10 R9	R11 SUBADDR	R12 SUBBDTE	R13 SUBCSFLG	R14 SUBCSWPT	R15 SUBFBYTE	R4 SUBLEVEL	R5 SUBLIST	R6 SUBREAL	R8	
R3R4R5R8DMKTESCSW R2MKTEDATR0 R3R1 R4R1R10 R5R11 R6R11 R7R12 R6R13 R7R13 R9R14 R9R15DMKTHIADSPCH CPEXADD CPEXADD DMKSCHS1AFREE DMKRER DMKSCHS1AFREE DMKSCHQ2 DMKSCHQ1AFREE DMKSCHQ2 DMKSCHQ2 DMKSCHQ1AFREE DMKSCHQ2 DMKSCHQ1AFREE DMKSCHQ2 DMKSCHQ1AFREE DMKSCHQ2 DMKSCHQ1AFREE DMKSCHQ2 DMKSCHQ1AFREE DMKSCHQ2 DMKSCHQ1AFREE DMKSCHQ2 DMKSCHQ1AFREE DMKSCHQ2 DMKSCHQ1AFREE DMKSCHQ2 DMKSCHQ1AFREE DMKSCHQ2 DMKSCHQ1AFREE DMKSCHQ1 DMKSCHQ1 DMKSCHQ1 DMKSCHQ1 DMKSCHQ1 DMKSCHQ1 DMKSCHQ1 DMKSCHQ1 DMKSCHQ1R11 R12 R11 R12 R11 R12 R11 R12 R11 R12 R11 R12 R11 R12 R11 R12 R11 R12 R11 R12 R11 R12 R11 R11 R12 R11 R12 R11 R12 R11 R11 R12 R11 R12 R11 R12 R11 R11 R12 R11 R12 R11 R11 R12 R11 R12 R11 R11 R12 R11 R12 R11 R11 R12 R11 R11 R12 R11 R11 R11 R12 R11 R11 R11 R12 R11R14 R15 R14 R14 R15 R14 R17 R16 R17 R14 R17 R16 R17 R16R11 R12 R11 R11 R11 R12 R11 R11 R11 R12 R11R14 R12 R12 R17 R17 R14 R14 R17 R17 R17 	DMKTEF	R14 SUBFBYTE	R15 SUBLEVEL	R4 SUBLIST	R5	R8	R9	SUBADDR	SUBBDTE	SUBCSFLG	SUBCSWPT	
R2R3R4R5R6R7R8R9VMBLOKDMKTH1ADSPCHAFREEAFREEAFRETAPSTAT1APUOPERAQCNWTASYSVMBLANKSCONTINUEDMKERNSGDMKFREEDMKFREEDMKFREEDMKFREEDMKREPDMKRCVTBDDMKSCHC0DMKSCHCUDMKSCHC1DMKSCH1DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2DMKSCH2R1R11R12R13R14R15R2R3R4R5R6R7R8R9SAVEWRK3SAVEWRK5SAVEWRK5SAVEWRK6SAVEWRK8SAVEWRK6SAVEWRK8SAVEWRK8SAVEWRK5SAVEWRK6SAVEWRK8SAVEWRK6SAVEWRK6SAVEWRK8SAVEWRK5SAVEWRK6SAVEWRK8SAVEWRK5SAVEWRK5SAVEWRK6SAVEWRK8SAVEWRK5SAVEWRK5SAVEWRK6SAVEWRK8SAVEWRK5SAVEWRK5SAVEWRK6SAVEWRK8SAVEWRK5SAVEWRK5SAVEWRK5SAVEWRK5SAVEWRK5SAVEWRK5SAVEWRK5SAVEWRK5SAVEWR	DMKTEM					R10	R11	R12	R13	R14	R15	
CPEXADDCPEXREGSCPEXREGSCPEXREGSCPEXREGSCPEXREGSCPEXREGSCPEXREGSDMKCRETDMKCVTBDDMKCVTBDDMKSCHCDDMKSCHCDDMKSCHCDDMKSCHCDDMKSCHCDDMKSCHCDDMKSCHCDDMKSCHCDDMKSCHLDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTDDMKSCNTD<	DMKTES										R15	
BALR2BRINGCHGREGSCPCREGOCPEXADDCPEXADDCPEXBLOKCPEXREGSCPRUNCPSTATUSCOC1DEFERDMKCVTABDMKDSPADMKDSPADMKDSPCHDMKDSPRUDMKFREEDMKFRETDMKFRTTDMKFRTTDMKLOKDFDMKLOKSYDMKVARSXDMKVARSXDMKVARSXDMKVARSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKVATSXDMKSCHSTDMKSCHSTDMKSCHSTDMKSCHSTDMKSCHSTDMKSCHSTDMKSCHSTDMKSCHSTDMKSCHSTDMKSCHSTPERADDRPERBLOKPERBLOKPERADDRPERBLOKPERBLOKPERBLOKPERADDRPERBLOKPERBLOKPERBLOKPERADDRPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOKPERBLOK	DMKTHI	CPEXADD DMKERMSG DMKSCHLI DMKSTKOP I PUADDR PROCIO R14 SAVEAREA	CPEXBLOK DMKFREE DMKSCHNS DMKSWART I PUADDRX PROCI PL R15 SAVEREGS	CPEXREGS DMKFREEP DMKSCHQT DMKSYSMP LOCK PSA R2 SAVER11	CPEXSIZE DMKFRET DMKSCHQ2 DMKTMRPT MPUOPER RUNUSER R3 SAVEWRK1	DFRET DMKQCNWT DMKSCHRL DMKVFRSV NORET R0 R4 SAVEWRK2	DMKCVTBD DMKSCHCA DMKSCHS1 F1 PAGELOAD R1 R5 SAVEWRK3	DMKCVTBH DMKSCHCO DMKSCHS2 F3 PAGERATE R10 R6 SAVEWRK4	DMKDSPCH DMKSCHCU DMKSCNFD F4 PGSRATIO R11 R7	DMKDSPNP DMKSCHEL DMKSCNRD F60 PREFIXA R12 R8	DMKDSPN2 DMKSCHIS DMKSCNVU F8 PREFIXB R13 R9	
CC3 CHNGMSG CPID CPSTAT5 C1 DATE DEFER DMKCQYID DMKCQYIT DMKCVTDT DMKDMPDT DMKDMPTD DMKERMCP DMKFREE DMKFRTLT DMKMCHST DMKPRGIN DMKPTRAN DMKQCORD DMKQCOSY DMKSCHST DMKSYSDW DMKSYSTI DMKSYSTZ DMKTMRFR DMKTRQMD EDIT EXTMODE FFS F0 F1 F2 F3 F4 F5 F60 F7 F8 LOCK LPUADDR LPUADDRX L8 MCHEK MP NOAUTO NORET NOTIME POWEROFF PREFIXA PREFIXB PRNPSW PROCIPL PSA R0 R1 R11 R12 R13 R14 R15	DMKTMR	BALR2 C1 DMKLOKDF DMKSTKDE F60 PERCR9 R13	BRING DEFER DMKLOKSY DMKSTKIO F7 PERSALT R14	CHGREGS DMKCVTAB DMKPRGSM DMKVATEX LOCK PROBSTRT R15	CPCREGO DMKDSPA DMKPSAFP DMKVAURN LPUADDR PROBTIME R2	CPEXADD DMKDSPCH DMKPSASP EXTMASK MICBLOK PSA R3	CPEXBLOK DMKDSPRU DMKPTRAN F0 MICEVMA2 R0 R4	CPEXREGS DMKFREE DMKSCHQ1 F1 MICSPT R1 R5	CPRUN DMKFRET DMKSCHQ2 F4 MP R10 R6	CPSTATUS DMKFRTLT DMKSCHRT F4095 PERADDR R11 R7	CO DMKFRTRS DMKSCHST F5 PERBLOK R12	
	DMKTOD	CC3 DMKDMPDT DMKSCHST F1 LPUADDRX PRNPSW	CHNGMSG DMKDMPTD DMKSYSDW F2 L8 PROCIPL	CPID DMKERMCP DMKSYSTI F3 MCHEK PSA	CPSTAT5 DMKFREE DMKSYSTZ F4 MP R0	C1 DMKFRTLT DMKTMRFR F5 NOAUTO R1	DATE DMKMCHST DMKTRQMD F60 NORET R11	DEFER DMKPRGIN EDIT F7 NOTIME R12	DMKCQYID DMKPTRAN EXTMODE F8 POWEROFF R13	DMKCQYIT DMKQCORD FFS LOCK PREFIXA R14	DMKCVTDT DMKQCOSY FO LPUADDR PREFIXB R15	

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м	ODULE	EXTER	RNAL REFER	RENCES (LA	ABELS AND	MODULES)					
		STARTIME UCASE	SYSTEM VMBLOK	TEMPSAVE ZEROES	TODATE	TRQBIRA	TRQBLOK	TRQBSIZE	TRQBTOD	TRQBUSER	TRQBVAL
D	МКТРЕ	IOERFLG3 IOERSPID	IOBCP IOBRCNT IOERCPEX IOERFSR	DMKMSWR IOBDYNP IOBRSTRT IOERCSW IOERIND3 IOERVSER	AFRET CPEXBLOK DMKSTKCP IOBERP IOBSPEC5 IOERDATA IOERINFO LOCK RDEVSTAT R3 VMBLOK	F1 IOBFATAL IOBSTAT IOERDW IOERLOC PRGC	F6	CCC CPEXR12 F8 IOBIOER IOERBLOK IOEREXT IOERNOLG PSA R10 R7	I FCC I OBLOK I OERBSR I OERFIXP	CDC DMKDSPCH IL IOBPATHF IOERCAN IOERFLG1 IOERPEND RDEVBLOK R12 SAVEAREA	IOBASNCT IOBPROC IOERCCRA IOERFLG2 IOERSIZE RDEVIOER R13
D	MKTRA	AFREE DMKFREE F1 R0 R5 TREXBRAN TREXBRAN		F3 R11 SAVEAREA	AVMREAL DMKLOCKQ F8 R12 SAVEREGS TREXCTL XRIGHT16	LOCK R13	MICBLOK R14 SAVEWRK1	CPSTAT2 DMKTRCIT MICVTMR R15 SAVEWRK2 TREXPRNT	NORET R2 SAVEWRK7	C1 FFS PSA R3 TIMER TREXSIZE	DMKERMSG FLAG2 RUN R4 TREXANSI TREXT
D	MKTRC	FFS IOBRADD R11	DMKPSASP FO IOBVADD R12	TREXLONT	DMKPTRLK F16 LOCK R14	DMKPTRUL F2 NORET R15 SAVEREGS SAVEWRK5 TREXBUFF	DMKQCNWT F60 PERMODE R2 SAVER0 SAVEWRK6 TREXCSW	CPCREGO DMKNEMOP DMKVAURN F8 PSA R3 SAVER1 SAVER1 SAVER1 TREXCTL1 TREXRUNF	DMKVSTPT INTSVCL R0 R4 SAVER2 SAVER2 SAVEWRK8 TREXCTL2	EXTMASK 10BCSW R1 R5 SAVER4 SAVEWRK9 TREXFLAG	EXTMODE IOBLOK R10 R6 SAVER5 SVCNPSW TREXINST
D	MKTRD	CO DMKLOCKQ DMKVAURN F255 IOBSTAT RCWVCNT R14 SAVEAREA SAVEAREA SAVEWRK7 TREXCCWI	C1 DMKNEMOP DMKVSTPT F3 LOCK RDEVBLOK R15 SAVEREGS SAVEREGS SAVEWRK8 TREXCTL1	R2 SAVER0 SAVEWRK9 TREXCTL2 TREXSIZE	DMKCCWSB DMKQCNWT FFS F60 PSA RDEVSNRB R3 SAVER1 SNARBLOK TREXFLAG	DMKSCNRD F0 F8 RCWCCW R0 R4 SAVER2 SNARLUN TREXINST	DMKSCNRN F1 IDA RCWGEN R1 R5 SAVER8 TRANMODE TRANMODE TREXIN1	F15 IOBCAW RCWPNT R10 R6 SAVEWRK1	DMKFREEP DMKSCNVU F16 IOBCSW RCWRCNT R11 R7 SAVEWRK2 TREXBRAN TREXLCNT	F2 IOBLOK RCWTASK R12 R8 SAVEWRK4 TREXBUFF	CSW DMKLOCKD DMKSYSRM F240 I OBRADD RCWVCAW R13 R9
D	MKTRK	ADSPCH DMKCCWSB FFS IOBCC3 IOBRCAW	AFREE DMKDSPCH F0 IOBCP IOBRCNT	AFREEP DMKFREE F1 IOBCSW IOBRSTRT	AFRET DMKFREEP F3 IOBFATAL IOBSIOF	F4096	BRING DMKIOSQR F6 IOBHVC IOBSPEC	CC DMKIOSQV F8 IOBIOER IOBSTAT	CD DMKPSAPO IL IOBIRA IOBUSER	C1 DMKPTRAN IOBALTSK IOBLOK IOERADR	

## EXTERNAL REFERENCES (LABELS AND MODULES)

HODOLL					,					
	IOERRDRO PSA RCWINVL R11 R7		IOERWRK	IOERDATA LOCK RCWCCW RCWREL R14 SAVEAREA SAVEWRK6	L1 RCWCNT RCWTASK R15 SAVEREGS	RDEVBLOK R2 SAVER1	L3 RCWCTL RDEVIOER R3 SAVER7	IOERFLG3 L4 RCWFLAG R0 R4 SAVER8 SKIP	IOERLOC L8 RCWGEN R1 R5 SAVEWRK1 UC	IOERPNT PCI RCWHEAD R10 R6 SAVEWRK2 VMBLOK
DMKTRM	BLANK RDEVPTTC R4	FF RDEVTFLG R5	F7 RDEVTMCD R8	LOCK RO SAVEAREA	PSA R1 SAVEREGS	RDEVATOF R11	RDEVBLOK R12	RDEVCORR R13	RDEVFLAG R2	RDEVIDNT R3
DMKTRP	DMKSTKCP F3 HALF2 NAMPROC 03DISP PREFIXA R14	DMKTRT F4 IOKEEP NAM2WORD 03ENTRY PREFIXB R15 SAVEREGS SELDISP	F8 LOCK NORET O3HEXLTH PSA R2	DMKUDRFU HALF1 NAME ORIGSEL O3MCHFLD RESET R3 SAVEWRK1 SELENTRY	NAMEENV 02ENTRY 03NUMSZ R0 R4 SAVEWRK2 SELFORW	DMKPTRAN DMKUDRRV HALF1EN1 NAMEMIN O2LENGTH O3NUMOO R1 R5 SAVEWRK3	HALF1EN2 NAMENTRY 02NAME 03SIZE R10 R6 SAVEWRK4 SELSIZE	DMKQCNWT F0 HALF1RLN NAMEROUT 02SIZE 03TABLE R11 R7	F1 HALF1SZE NAMESIZE 02TABEND 03TRPEND R12 R8	F2 HALF1VAL NAMNOPRO O2TABLE O4TABLE R13 R9 SAVEWRK8
DMKTRQ	ACTIVTRQ DMKDSPCH DMKSCHRL NOALCARY R11 R7 TRQBLOK	DMKFREE DMKSCHTQ PREFIXB R12 R8	AFREE DMKFRET DMKSTPX PSA R13 R9 TRQBTOD	F1 PSAEXT R14 SAVEAREA	LOCK PSAEXTX R15 TEMPSAVE	LOCKSAV PXA R2 TIMEDISP	APUOPER DMKSCHAL LPUADDR RQLOCK R3 TIMER VMBLOK	ASYSVM DMKSCHDL MICBLOK R0 R4 TMRDRP	C1 DMKSCHQ1 MICEVMA2 R1 R5 TRQBBPNT	MICSPT R10 R6
DMKTRR	INTTCP NAMESIZE 03DISP	INTTMACH NAM2NUM 03ENTRY 04FORMAT R10 R6 SELSIZE	NAM2WORD	HALF1VAL INTTVT ORIGSEL O3MCHFLD	INTTYPE 02ENTRY 03NUMSZ 04SIZE R13 R9	INTBLK NAME O2LENGTH O3NUMOO O4TABEND R14 SELDATA SFBMISC1	NAMEENV 02NAME 03SIZE		RESET R3 SELENTRY SPLINK	INTNULL NAMEROUT 02TABLE 04ENTRY R0 R4
DMKTRT	ADDSFB BFFPEND CPEXSIZE DMKLOCRD FFS INTCODE PLIST R10 R7	ADSPCH BFFREAL DMKCKSPL DMKLOCRQ F0 INTCTRL PLNORET R11 R8	AFREE BFFSTAT DMKCKTUU DMKPGTSG F1 INTLTH PLR2 R12 R9	DMKQCNPL	DMKRPAGT F4096 INTTYPE PREFIXA R15	ASYSVM BRING DMKDSPCH DMKRPAPT HALF1 LOCK PREFIXB R2 SELECT	OPNSFB PSA R3	CPEXBLOK DMKFREE DMKSTKCP	DMKFREEP DMKTRUST HALF1VAL PLFRET RO R5	CPEXR1 DMKFRET DMKVSDAD

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MODULE	EXTERNAL	REFERENCES	(LABELS	AND	MODULES)
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HODOLL				0000 7000	110001220/					
	SFBCOPY SFBMISC1 SFBUSER TRACSTRT TYP1403	SPFILID	SFBDIST SFBORIG SPLINK TRAPCR8 ZEROES		SFBFILID SFBRECSZ SPPREPAG TRAPOK	SFBSIZE	SFBFNAME SFBSTART SPSIZE TRAPVT	SFBFTYPE SFBTIME SYSTEM TTCODE	SFBLAST SFBTYPE TRACCURR TTENTRY	SFBLOK SFBUFORM TRACEND TTLENGTH
DMKTRU	ADDSFB BFFENTRY DEFER DMKFRET DMKQCNWT DMKVSGRI PREFIXB R15 SFBCLAS SFBCLAS SFBUSER TRAPDATA	DMKRSPTR F0 PSA R2 SFBCOPY SFBOFORM SPFILID	DMKCKSPL DMKLOCRQ DMKSTKCP F1 RDRCHN R3 SFBDATE	DMKPGTSG DMKSYSRM F2 R0 R5 SFBDIST SFBRECSZ SPRECNUM		DMKPGUVG	DMKPGUVR DMKTRXEX LOCK R11 R9 SFBFLAG SFBSYSID SYSTEM	DMKERMSG DMKPTRAN DMKTRXLK NORET R12 SAVEAREA SFBFNAME	DMKPTRLK DMKTRXTT OPNSFB R13 SAVEREGS SFBFTYPE SFBTYPE	BFFCCPD C1 DMKFREEP DMKPTRUL DMKTRXVT PREFIXA R14 SAVEWRK1 SFBLOK SFBLOK SFBUFORM TRAPCR8 ZEROES
DMKTRX	F4095 MP R14	AFREE CPCREGO DMKDSPA DMKPTRAN F4096 PREFIXA R15 TRAPDATA	AFREEP CPEXADD DMKDSPB DMKPTRUL HALF1SZE PSA R2 VMBLOK	DMKDSPCH		AP CPEXRO DMKERMSG DMKSYSRM INTTVT R1 R5 ZEROES	APSTAT1 CPEXSIZE DMKFREE FFS LOCK R10 R6	APTRAN CO DMKFREEP FO LOCKSAV R11 R7	APTRLK C1 DMKFRET F1 LPUADDR R12 R8	APUOPER DEFER DMKLOKDF F2 MONCODE R13 R9
DMKTTX	AFREE CONACTV CONESCP CONRETN DMKCVTBD DMKTBMMI DMKTTZLF MP PSA RDEVNOLF R0 R4 SAVER3	DMKTBMMO DMKVSTVP PFDATA RDEVAPLI	CONSYNC DMKFRET DMKTBMNI F0 PFDTEXT RDEVAPLO RDEVPSUP R10 R6	DMKTBMNO F1 PFKADDR RDEVASTB RDEVSCRL R11 R7	CONTSIZE DMKRETGT	DMKTBNAI F2 PFKIMM RDEVBLOK	CONUSER DMKTBLCI DMKTBNAO F3 PFKLNG	CONCNT2 CONPFDEL CONWRTRD DMKTBLCO DMKTBNBE INHIBIT PFKRET RDEVCORD RDEVTTYB R15	DMKTBLPI DMKTBNEA IOBLOK PFKTABLE RDEVNDLF	CONPNT DEFER DMKTBLPO
DMKTTY	RDEVACTV	ALARM CONCCW3 CONOUTPT CONWRTRD DMKTTZLF RDEVAPLO RDEVPSUP R12 R8	DMKFREE DMKVSTVP	F8 RDEVBLOK RDEVTFLG R14	CONCNT2 CONSPLT DMKTBLPO INHIBIT RDEVCON RDEVTMCD R15	CC CONDATA CONSTAT DMKTBMMO IOBLOK RDEVCORD RDEVTTYB R2 SAVEWRK1	RDEV3101 R3	RO R4	CONADDR2 CONFLAGS CONTSIZE DMKTBNEA NOAUTO RDEVNOCR R1 R5 SKIP	CONFLAG2 CONTSKSZ DMKTBNEB PSA
DMKTTZ	AP	сс	CD	CONDATA	CONTASK	LOCK	MP	SILI	SKIP	VMBLOK

MODULE	EXTE	RNAL REFE	RENCES (LA	ABELS AND	MODULES)					
DMKUCB	AP	СС	LOCK	MP	SILI					
DMKUCC	AP	cc	LOCK	MP	SILI					
DMKUCS	CC	LOCK	SILI							
DMKUDR	DMKSYSOW DPLS1Z2 10BLOK	DMKLOCKD DMKSYSPL DPLVADD IOBMISC OWNDRDEV R13 R9 SAVEWRK9	F0 IOBMISC2 OWNDVSER R14 SAVEAREA SILI	DPLEND F4096 IOBSIZE	DMKPGUVR DPLENDFG F8 IOBSTAT RDEVBLOK R2 SAVERO SYSTEM	DMKPTRAN	DMKRPAGT DPLIST IOBCP IOERETN R0 R4 SAVEWRK1	DPLLOW IOBFATAL LOCK R1 R5 SAVEWRK2	DMKSCNVU DPLNGTH IOBFLAG NORET R10 R6 SAVEWRK4	
DMKUDU	F240 R11 R7 SYSLOCS UDEVAD UIPARMSZ UMDISKRP	BUFFER DMKLOCKD DMKSYSUD F3 R12 R8 SYSTEM UDEVCNT UIPL UMDISKWP URPAGDEV		UCNTRL UDEVF UIPLPRMS UNOUPF	DPLIST IOKEEP R15 SAVEREGS UCNTRLSZ UDIRAD ULOCDVAD UOBJVMBK	DPLSIZE LOCK R2 SAVER3 UCURPASS UDIRF UMACAD UOP USCRCPO	DMKPTRAN DPLVADD PSA R3 SAVEWRK2 UDASDDEV	UDASDDIR UDISPMAC UMDISKAD UPRIOR USCRINR	BLANKS DMKCPVAC DMKRPAPT F0 R1 R5 SWPFLAG UDASDIPL UFLAGS UMDISKMD UPRIVLGE USCRSTA UWORK2	UIPARMS UMDISKMP
DMKUNT	F16 IOBCAW IOBSIZE IOERLEN RCWCTL RCW2311 RDCDIAGN R10 R6	CLASFBA DMKFRETL DMKSYSRV F240 IOBCC3 IOBSPEC2 LOCK RCWFLAG RDCALTCL	DMKFRET1 DMKTRKFP F255 IOBCLN IOBSTAT PRGC RCWGEN RDCALTRK RDCALTRK R12 R8	DMKVIOIN F4 IOBCSW IOBUNREL PRTC RCWHMR RDCBLKAP	DMKPTRUL D4 F6 IOBFLAG IOBUSER PSA RCWIO RDCBLKCE RDEVBLOK R14 SAVEAREA	DMKPTTFT FFS F7 IOBIRA IOBVADD RCWADDR RCWADDR RCWPNT RDCBLKCG	DATACHK DMKSCNVD FTR35MB F9 IOBLINK IOERBLOK RCWCCNT RCWRCNT RDCBLKFA RDEVMDL R2	RCWCCW RCWSHR RDCBLKMA RDEVRDC R3 SAVER8 TYP3330	CD DMKDSPCH DMKSTKIO F1 IFCC IOBMISC IOERDATA RCWCNT RCWTASK RDCBLOK R0 R4 SAVEWRK1 TYP3340	CDC DMKFREEP DMKSYSRC F15 IOBALTSK IOBRES IOERFLG2 RCWCOMND RCWVCAW RDCDIAG R1 R5 SAVEWRK2 TYP3350
DMKURS	F1 RCHBLOK RDEVCLAS	AFRET DMKCVTBH I PUADDR RCHPROC RDEVCUA RDEVCVLY	I PUADDRX RCUBLOK RDEVDFCB	AP DMKFRET LOCK RCUCHA RDEVDRAN RDEVPTHS		MP RCUPRIME RDEVFLAG	NORET RCUSUB		OPERATOR RDEVBLOK	RDEVCFCB RDEVLDMD

MODULE	EXTE	RNAL REFE	RENCES (LA	ABELS AND	MODULES)					
		RDEVXSEP RSPXFORM R11 R8 SFBDIST TYP3211 URSUSER		RSPXPMNT R13 SAVEAREA SFBLOK	RSPXRECT R14 SAVEREGS SFBPNT	RSPXSEQ R15 SAVEWRK3 SFBRECNO URSDEV	RSPXDST1 RSPXSETU R2 SAVEWRK5 SFBTYPE URSFILE VMSYSOP	RSPXDST2 RSPXSFIL R4 SAVEWRK6 SFBUSER URSOPER ZEROES	RSPXDST3 R0 R5 SAVEWRK8 TYPPRT URSPATH	RSPXDST4 R1 R6 SFBCLAS TYPPUN URSSTACK
DMKUSP	AFREE APSTAT1 DMKLOCKD LOCK RECSIZE R3 SAVEWRK2 SYSPFPNT		LPUADDR R1 R5 SAVEWRK4	ARSPPU DMKPAGSK MP R10 R6	DMKFREE DMKPAGXS	DMKFREHI DMKPAGXZ PAGEXSIZ R12 R8		DMKFRENP DMKVSPWA RECBAK R14 SAVEAREA	ALOKSP DMKFRET IOBLOK RECBLOK R15 SAVEREGS SFBSIZE	AP DMKFRTTE IOBMISC RECPNT R2 SAVEWRK1 SYSPALOC
DMKUSQ -	DMKFREE DMKPTRRC DMKSCNVD DMKVCTLO LOGDROP PFKSIZE RDEVLTRM RUNUSER R3 SAVEWRK1	DMKACOTM DMKFREEP DMKFTSPW DMKSSSVM DMKVDREL LOGHOLD PFKTABLE RDEVPRFG R0 R4	DMKBLDRL DMKFRET DMKQCNWT DMKSSULO DMKVMASH MICSIZE PFKTBLSZ RDEVPS R1 R5 SAVEWRK5	ASYSLC CPEXFPNT DMKCFPRR DMKHPTDI DMKQCOCL DMKSSVUS FFS MSSPRES	DMKCVTBD DMKLOCKD DMKRPIRA DMKSTKCP F0 NORET	DMKLOCKQ DMKSCHDL DMKSYSDW F1 NOTIME PSAMSS RDEVSTA3 R12 R8	DMKCVTDT DMKLOKSW DMKSCHRT DMKSYSNM F15 OPERATOR RDEVAIRA	F4 PFKADDR RDEVBLOK RDEVTYPC R14 SAVEAREA	DMKDSPCH DMKPGSPO DMKSCNRD DMKTRCND LASTUSER PFKCOUNT RDEVFCNS RDEVTYPE R15	DMKACODS DMKERMCP DMKPGSPP DMKSCNRN DMKVATBC LOCK PFKDWDS RDEVLOGC RDEVLOGC RDEVUSER R2
DMKVAT	ADSPCH ASYSVM CPEXADD CPSTAT4 DMKFREEP F0 LOCK MICFSSE OFF PSA R2 SAVEREGS STOFLAG STOFLAG STOFLAG XPAGNUM	CPXSTOR DMKFRET F1 LOCKSAV MICIPTP2 ON PURGESTO R3 SAVERETN STOFSTUS	CPEXREGS CP370EON DMKLOKDF F16 LOKSAVE MICLCTL2 PAGE2K	C1 DMKPRGSM F2 LPUADDR	F240 MCHAREA MICPTLB2 PAGTABLE R10 R6 SEGPROT STONXTUS	F255 MCHMODEL MICSTBVR PGADDR R11 R7 SPMPFX STOPAGVR STOVLEN	BALR5 CPPTLBR DMKDSPCH DMKPTSPW F4096	APSTAT2 BRING CPSEGPRT DMKDSPRU DMKSTKCP F8 MICDASA MICVPFR2 PGBSIZE R13 R9 STOACTV STOSEGVR STOYPSG VMBLOK	APTRAN CONTINUE CPSPMODE DMKERMSG DMKSTKDE INTPR MICEVMA2 MOD3081 PGPNT R14 SASPF STOBLKLN STOSHCR1 STOGCPG VMSIZE	CPSTAT2 DMKFREE EXTMODE LAP370E MICEVMA3 NOPTLB PREFIXB R15 SAVEAREA
DMKVAU	AFREE CPEXADD DEFER FO MP	ALOKSY CPEXBLOK DMKDSPRU F1 PREFIXB	AMCHAREA CPEXREGS DMKFREE F2 PSA	CPPTLBR	APSTAT1 CPSEGPRT DMKPRGSM LOCK R1		APTRAN CPSTAT4 DMKPTSAE LPUADDR R11	APUOPER CPXSTOR DMKSTKDE MCHAREA R12	BALR4 CP370EON DMKVATBC MCHMODEL R13	DMKVATMD

MODULE	EXTE	RNAL REFE	RENCES (LA	ABELS AND	MODULES)					
	R15 SAVEREGS STOFLAG VMBLOK	R2 SAVERO STOFSTUS XKEYMODE		R4 SAVER2 STONXTUS	R5 SAVER3 STOSEGCT	R6 SAVEWRK1 STOSEGVR	R7 SAVEWRK2 STOSHCR1		R9 STOACTV STOVLEN	SAVEAREA STOBLOK TREXADD
DMKVBM	AFREE DMKBLDRT LOCK R11 SAVEREGS WAIT	AP DMKFREE MP R12 SAVERO	APSTAT1 DMKPTRAN NEWPAGES R13 SAVER1		APTRLK DMKSYSSZ PREFIXA R15 SAVEWRK2		ASYSVM FO PSALANG R3 SSBHEADL	BRING F256 R0 R4 SYSTEM	C1 F4096 R1 R5 VMBLOK	DEFER IOKEEP R10 SAVEAREA VMSIZE
DMKVCA	ADSPCH BALR3 CPEXR0 DMKFRET F2 IOBFLAG IOBUNSL MP RCWCTL R14 SAVEAREA SILI VMBLOK	AFREE BALR9 CPEXR12 DMKSCHDL F240 IOBIOER IOBUSER PCI RCWFLAG R15 SAVEREGS SKIP ZEROES	AFREEP BUSY CPEXR13 DMKSCNVU F7 IOBIRA IOBVADD PCIF RCWINVL R2 SAVERETN TEMPRO	IDA IOBLINK IOERBLOK PRGC RUNUSER R3	DMKSTKIO  L  OBLOK	ATTN CE DE DMKSYSRM INTREQ IOBRCS IOERCSW PSA R1 R5 SAVER11 TEMPR4	BALRSAVE CPEX DMKDIBSM DMKVI0IN IOBCAW IOBRSTRT IOERDATA RCWADDR R10 R6 SAVER12 TEMPR5	CPEXADD DMKDSPCH FFS IOBCC1 IOBSIZE	BALR15 CPEXBLOK DMKFREE FREESAVE IOBCC3 IOBSPEC IOERSIZE RCWCNT R12 R8 SAVEWRK2 UC	BALR2 CPEXFPNT DMKFREEP F1 IOBSCSW IOBSTAT LOCK RCWCOMND R13 R9 SAVEWRK6 UE

	VMBLOK	ZEROES								
DMKVCB	ADSPCH CE DMKFREE F240 IOERCCW PSA R10 R6 SAVEWRK2	AFREE CONTINUE DMKFREEP IDA IOERCSW RCWADDR R11 R7 SAVEWRK3	AFREEP CPEXADD DMKFRET IOBCSW IOERDATA RCWCCW R12 R8 SAVEWRK4	DMKLOKSW IOBIRA IOERLEN RCWCNT R13 R9	IOBLINK IOERSIZE RCWCOMND R14	DMKSCNVU IOBLOK LOCK RCWCTL R15 SAVEREGS	DMKSTKCP IOBSIZE MP RCWFLAG R2	AQCNWT DE DMKSTKIO IOBUSER NORET RCWINVL R3 SAVER11 UC	ATTN DMKCVTBH DMKV101N 10BVADD PC1 R0 R4 SAVER5 VMBLOK	BLANKS DMKDSPCH FFS IOERBLOK PCIF R1 R5 SAVEWRK1 ZEROES
<b>ДМКУСН</b>	DMKFREE DMKSCNAU F0 MP RCHBLOK RCUPRIME RDEVCUB RDEVRSVD RDEVTPC R14 SAVEAREA	DMKFRERC DMKSCNEP F1 RCHCUTBL RCUSTAT RDEVCUP RDEVSCHD RDEVSCHD RDEVTPE R15	RCUSUB RDEVDED RDEVSPL RDEVUSER R2 SAVER10	DMKLOCKD DMKSCNRA F2 NOTERM RCHDISA RCUTYPE RDEVDISA RDEVSTAT	DMKSCNRU F3 OFFLPROC RCHPROC RDEVADD RDEVDRAN RDEVSTA2 R0 R4 SAVER2	DMKSCNVU F4 OPERATOR RCHSTAT RDEVALT RDEVENAB	DMKVDREL LOCK POFFLINE RCUBLOK RDEVATT RDEVFLAG RDEVSTA5 R10 R6	DMKVDSAT LOCKSAV PREFIXB RCUCHCNT RDEVBLOK RDEVOWN RDEVSTA6 R11 R7	DMKCVTBH DMKPTRUL DMKVDTPG LPUADDR PROCIO RCUDISA RDEVBUZY RDEVPPAG RDEVSYS R12 R8	FFS LPUADDRX PSA RCUDVTBL RDEVCUA
DMKVCN	ACORETBL APTRLK BUSY	ADSPCH AQCNWT CC	AFREE ATTN CD	AFREEP BLANKS CE	AFRET BRING CLASGRAF	ALARM BUFCNT CLASTERM	ALTWRT BUFIN CMDREJ	ANYWRITE BUFINLTH CONDLN		APTRAN BUFSIZE CONTGMXB

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# DULE EXTERNAL REFERENCES (LABELS AND MODULES)

	VCONPLST VCONREMF	DMKDSPCH DMKPTRLK DMKV101N F0 F8 IDAHSTRT IOBLOK IOERSIZE NICHT NICWTH PLLED RDEVFTR2 RDEVFTR2 RDEVFTYPE R15 SK1P TYP3278 VCONCNT VCONFSOP VCONPLSZ	DMKPTRUL DMKV10MK F1 IDA IDAHWRK1 IOBREMOT LOCK NICLGRP NIC3274 PLR2 RDEVHT RDEVWTH R2 SNARBLOK TYP3284 VCONCNT2 VCONFSS VCONPPA1 VCONRFLD	UC VCONCOMD VCONIDAP VCONRBUF	DMKFRERC DMKQCNPL EDIT F255 IDAEWRD1 IL IOBSPEC5 NICAPL NICRDED NORET PRIORITY RDEVNRDY RO R4 SNARTTY UE VCONCTL VCONLED VCONRBYT VCONRINX	ERASWRT F256 IDAEWRD2 INHIBIT IOBUSER NICAWSF NICRFLG NOTIME PRTC RDEVSNA R1 R5 SNARVMB VCONADDR VCONNCB VCONRCNT VCONRLN	R10 R6 TWOENTS VCONBFSZ VCONEWA VCONNICB VCONRD VCONRMAX VCONWRRM	NICDTYPE NICTEXT PCI RDEVAPLP RDEVSTAT R11 R7 VCONBUF VCONEWRT VCONRDEV VCONRMCT	NICTMCD PCIF RDEVATT RDEVTEXT R12 R8 TYPTTY VCONCAW VCONEXTN VCONOPT VCONRDSZ	DMKPTRAN DMKTBLSF FTRAWSF FTRAWSF F4096 IDAHPAGE IDBIRA IOERDATA NICD3278 NICTYPE PLFLAG1 RDEVBLOK RDEVTMCD R13 R9 TYP3066 VCONFLAG VCONFLAG VCONPLF VCONREMD
DMKVC P	SNARDIAL	DMKCFMBK DMKVCQAT DMKVCXIO IOBREMOT MP RDEVHT RDEVTYPE R13 R9 SAVEWRK5	DMKVCXSA IOBSIZE PSA RDEVLLEN RDEVUSC8 R14 SAVEAREA	DMKFREE DMKVCUIL DMKVIOIN IOBSPEC RDEVADVF	FTRAWSF IOBSPEC5 RDEVASTB RDEVMDL	APSTAT1 CPEXR11 DMKFRET DMKVCVEB F0 IOBUNSL RDEVATT RDEVPSS RDEVWTH R3 SAVER1 SAVEWRK9 SNAROUT VMCF	APUOPER CPEXR12 DMKGRCQY DMKVCVKS F1 IOBUSER RDEVAVM2 RDEVPT R0 R4 SAVER11 SNARBACH SNARPCT VMCFWAIT	R1 R5 SAVESIZE SNARBLOK SNARPVL	DMKLOKSW DMKVCVLY IOBIRA IXBLOK	DMKVCXFU IOBLINK IXSIZE RDEVEHLT
DMKVCQ	ADSPCH CONRETN DMKVCVCE F8 RDEVSNRB R12 R8	AFREE CONSTAT DMKVCVIN IXEXBLOK RDEVTAPL R13 R9 SAVEWRK4 SNARINN	AFRET CONTASK DMKVCVIX IXSIZE RDEVTFLG R14	DMKVCVND LOCK RDEVTMCD R15 SAVEREGS	CONADDR CONTSKSZ DMKVCVUT MP RDEVTYPE R2 SAVERETN SNARDIAL SNARPVL	PSA RDEVVM21 R3 SAVERO	CONCNTL DMKDSPCH DMKVCXIO RDEVAPLI RO R4 SAVER2 SNAREXWT TYP3277	CONDATA DMKFREE DMKVCXSA RDEVAPLO R1 R5 SAVER7 SNARFG1 TYP3278	CONOUTPT DMKFRET F0 RDEVBLOK R10 R6 SAVESIZE SNARFG2 VCONBRK	DMKVCSWT F4
DMKVCR	ADSPCH CONADDR CONDIAG CONPFDEL	AFREE CONADDR2 CONDWC CONPFWRT	CONESCP	AP CONCCW2 CONFLAGS CONRD	BUFCNT CONCCW3 CONFLG2 CONRESP	BUFFER CONCNT CONFSOP CONRETN	BUFINLTH CONCNTL CONFSS CONRMOD	BUFSIZE CONCNT2 CONNCB CONSPLT	CLASGRAF CONCNT3 CONOUTPT CONSTAT	CONDATA

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MODULE

MODULE	EXTERNAL	REF
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# EXTERNAL REFERENCES (LABELS AND MODULES)

	DMKSCHRT DMKVCVLD IDAEWRD1 PSA RDEVTMCD R13 R9 SAVEWRK5 SNARFG1 SNARFG1 SNARPASS TYP3277	I DAÈWRD2 RB RDEVTRQ R14 SAVEAREA SAVEWRK6 SNARFG2	DMKTTYOP DMKVCXFU IDAHEAD RDEVAIRA RDEVTYPC R15 SAVEREGS SAVEWRK7 SNARFG3 SNARFFIM UCASE	DMKVCQAT DMKVCXGF IDAHSTRT RDEVAPLP RDEVTYPE R2 SAVERETN SAVEWRK8 SNARFORC	DMKVCQSR DMKVCX10 IDAHWRK1 RDEVBLOK	DMKVCXOR IDAHWRK2 RDEVCON R0 SAVER2 SNARBACH SNARIF SNARVDEV	DMKVCVEB DMKVCXSA INHIBIT RDEVREAD R1 R5 SAVESIZE	EDIT IXEXBLOK RDEVSNRB R10 R6 SAVEWRK1 SNARCONQ SNARINN TEMPR14	RDEVTEXT R11 R7 SAVEWRK2 SNARDIAL SNARMDE TEMPR7	DMKRGESK DMKVCV1X IDAENTRY MP RDEVTFLG R12 R8 SAVEWRK4 SNARD1S SNAROUT TRAVSARP VMGEN10
DMKVCS	ADSPCH CONDATA CONFSOP CONRESP DMKFRET DMKVCVEB LOCK RDEVLLEN R13 R9 SAVEWRK5 SNARDIS SNARPCT VCONCTL VMVTERM	AFRET CONDCLR CONFSS CONRETN DMKLOKSW DMKVCVER LOGDROP RDEVSNRB R14 SAVEAREA SAVEWRK6 SNAREXWT SNARPRMT VCONOPT ZEROES	DMKVCVIX LOGHOLD RDEVTFLG R15 SAVEREGS SAVEWRK8	CONMORE CONSTAT DMKSCHRT DMKVCVLD MP RDEVTRQ R2 SAVERETN SAVERETN SAVEWRK9 SNARFG2 SNARTTY	APSTAT1 CONDRFMT CONNCB CONSYNC DMKTTXIN DMKVCVUT NCAUTO RDEVTYPC R3 SAVER11 SNARBACH SNARFG3 SNARVMB VMDVSTRT	CONOUTPT CONTASK DMKTTYOP DMKVCXFU PRIORITY R0 R4 SAVER2	CONTSKSZ DMKVCQAT DMKVCXSA PSA R1 R5 SAVESIZE SNARBTCT SNARINN TRACFLG3	CONEWRT CONPARM2 CONWRT DMKVCQRE FO RDEVAIRA R10 R6 SAVEWRK1	CONWSF DMKVCQSR F1 RDEVBLOK R11 R7 SAVEWRK2 SNARD1AL SNAROUT TRAVSARP	CONPPA1 DMKDSPCH DMKVCVCE IXEXBLOK RDEVCON R12 R8 SAVEWRK4
<b>DMKVCT</b>	ADSPCH DMKDSPCH DMKVCQSR RDEVAIRA RDEVTRQ R3 SAVER11 SAVER11 SAVEWRK9 SNARFG3 VMBLOK	DMKVCVCE DMKVCXOX RDEVATOF R0 R4	FO	AP DMKGRCSV DMKVCVIN F8 RDEVFLAG R10 R6 SAVEWRK2 SNARBACH SNAROUT	IXEXBLOK	DMKVCVLD IXSIZE RDEVQREP R12 R8 SAVEWRK4	LOCK RDEVSIZE R13 R9	DMKVCXD2 MP RDEVSNA R14 SAVEAREA SAVEWRK6 SNARDIS	SAVEWRK7 SNARFG1	R2
<b>ДМКУС</b> И	ADSPCH BUFINLTH CPEXREGS DMKFREE DMKVCVER IXSIZE PFKLNG RDEVSNRB R13 R9 SAVEWRK9 SNARMDE VCONBRK	CPEXR11 DMKFREEP DMKVCVLD LOCK PFKRET RDEVTEXT R14	MP PFKTABLE RDEVTFLG R15 SAVEREGS	PFDATA PFTAB RDEVTMCD R2 SAVER13 SNARCPFD SNARPFK1	AP CONPARM DMKCFMAT DMKRETGT DMKVSTVP PFDCMD PFTABS RDEVTYPE R3 SAVER2 SNARDIAL SNARVDEY VCONRBUF	R4 SAVEWRK1	SNARFG2 TIMEDISP	R10 R6 SAVEWRK5 SNARFORC TYP3277	DMKCVTBD DMKVCRNR F4 PFKFLAG RDEVPTTC R11 R7 SAVEWRK6 SNARFSS TYP3278	IXBLOK PFKIMM RDEVREAD R12 R8 SAVEWRK7 SNARINN UCASE

MODULE	EXTE	RNAL REFE	RENCES (LA	ABELS AND	MODULES)					
	VMCF VMOSTAT	VMCFWAIT VMPA2APL	VMDVSTRT VMPFUNC	VMGRFTAB VMPROT	VMLOGON VMPXINT	VMMCPENV VMQSTAT	VMMHLITE VMRSTAT	VMMLEVEL VMTERM	VMMLINED VMVTERM	VMMLVL2
DMKVCV	IXSIZE RDEVCON R10 R6 SAVER9 SNARCNTX SNARNXT TRACFLG3	IDAHEAD LOCK RDEVLLEN R11 R7 SAVEWRK1 SNARCONQ SNAROUT	I DAHPAGE MP RDEVSNRB R12 R8 SAVEWRK2 SNARCPT SNARSPT TRACPROC	DMKVCXFU IDAHSTRT ONEENT RDEVTEXT R13 SAVEWRK3 SNARDIAL SNARTTY TRACPSAF TRAMSGLM TRAVCSET TRQBIRA	CRTEXTSZ FFS IXBLOK PREFIXA RDEVTFLG R14 SAVEAREA SAVEAREA SNARDIS SNARVDEV	R15 SAVEREGS SAVEWRK5 SNARFG1 SNARVMB TRACSVCR TRASEND1 TRAVMADR TRQBSIZE	DMKFREE F1 IXIRA RCMSGCT RDEVTRQ R2 SAVERO SAVEWRK7 SNARFG2 TEMPR3 TRAEDCHR	RDEVUSER R3 SAVER1 SAVEWRK8 SNARINN TRACCURR TRAFUNCT TRATIMER TRAVSAPA	R0 R4 SAVER6 SAVEWRK9 SNARLUN TRACEND TRAINSTR TRAINSTR	IDAEWRD1 IXR13 RDEVBLOK R1 R5 SAVER7 SNARBLOK SNARNOPR TRACEVCS TRAIPRCD TRAUDATA
DMKVCW	DMKVCXGO RDEVQREP R12 R8 SAVEWRK7 SNARFG2 SNASTATS	DMKQCPTO DMKVCXOX RDEVSIZE R13 R9 SAVEWRK8 SNARFG3	RDEVSNA R14 SAVEAREA SEVER SNARINN TIMEDISP	DMKSTKCP LOCK	DMKSYSLU MP RDEVTMCD R2 SAVEWRK1 SNARACO SNARNXT TRACFLG3	PREFIXA RDEVUSER R3 SAVEWRK2 SNARBLOK SNAROUT	PSA R0 R4 SAVEWRK3 SNARCPT SNARRSE TRASEVER	RDEVBLOK R1 R5 SAVEWRK4 SNARDIAL SNARSIZ	RDEVCON R10 R6 SAVEWRK5 SNARDIS SNARSPN	RDEVPTTC R11 R7 SAVEWRK6 SNARFG1 SNARVMB
DMKVCX	CPEXREGS DMKFRET DMKVCRNR IXSIZE RDEVEHLT R11 R7	LOCK RDEVQREP R12 R8 SAVEWRK9 SNARFG3	C1 DMKIUACP DMKVCVEB PREFIXA RDEVSIZE R13 R9	PSA RDEVSNA R14 SAVEAREA SNARBLOK SNARINN	DMKACOSA DMKQCOCL DMKVCVLD RCMSGCT RDEVSNRB R15 SAVEREGS SNARCNTX SNARCNTX	DMKQCPTO FO RDEVADVF RDEVTFLG R2 SAVER1 SNARCPT SNAROUT	APUOPER CONPNT DMKBOXNS DMKRGEIO IXBLOK RDEVAIRA RDEVTRQ R3 SAVER9 SNARDIAL SNARSIZ TRCVCS	DMKSCHRT IXIRA RDEVBLOK R0 R4 SAVEWRK1	DMKSTKCP IXREGS	IXR12 RDEVECOL R10 R6 SAVEWRK5
DMKVDA		DMKFRÉT DMKSCNRN DMKSYSOW F1	DMKSCNRU	ALOKSP CLASDASD CPEXSIZE DMKLOCKQ DMKSCNVS DMKVDBMD F7 OPERATOR	CPSTAT5 DMKLOKIO DMKSCNVU DMKVDCPS F8	CRTEXT DMKLOKSW DMKSCONP DMKVDCSC IOBLOK	DMKMNLIN DMKSSSAS	DMKCVTAB DMKPMACD DMKSSSMQ DMKVDERR IOBSIZE	DMKCVTBH DMKQCNWT DMKSSSVA	DMKSCHST DMKSTKCP

# EXTERNAL REFERENCES (LABELS AND MODULES)

	RDEVPREP	RCHDED RDEVBLOK RDEVFLAG RDEVFS RDEVTYPC R12 R8 SAVEWRK2 TRQBLOK TYP3066	RDEVFTR RDEVSER	RDEVLOG RDEVSPL	RDEVMOUT RDEVSTAT	RCUPRIME RDEVCUP RDEVNATH RDEVSTA3 RDEV333V R2 SAVERO SAVERO SAVEWRK7 TRQBUSER TYP3330	RDEVDED RDEVOWN RDEVSTA4 RDIDX R3 SAVER11 SAVEWRK8	RCUTYPE RDEVDISA RDEVPEND RDEVSTA5 R0 R4 SAVER2 SAVERRK9 TYPCTCA VMBLOK	RC100 RDEVDISB RDEVPPAG RDEVSTA6 R1 R5 SAVER4 SYSVIRT TYPSDLC	RDEVPRDV
DMKVDB	APSTAT1 OPERATOR R13 R9 VMBLOK	APUOPER PSA R14 SAVEAREA	AQCNWT RDEVBLOK R15 SAVEREGS	R2	DMKCVTBH RDEVSTA5 R3 SAVER13	DMKLOKSW R0 R4 SAVEWRK1	DMKQCNWT R1 R5 SAVEWRK3	FFS R10 R6 SAVEWRK5	LOCK R11 R7 SAVEWRK9	NORET R12 R8 TIMEDISP
DMKVDC	ADSPCH CLASDASD CPEXRO DMKSCNFD F4 IOBIRA IOEREXT RC32 RDEVFTR RDEVTYPE R3 SAVER10 SAVEWRK9	CPEXSIZE DMKSCNRU F6 IOBLOK IOERSIZE RC40 RDEVOWN R0 R4 SAVER13	RDEVADD	DMKVIOMK F8 IOBSIZE IPUADDRX RDEVBLOK	FFS F9 IOBSPEC LOCK RDEVCUP RDEVRSVD R11 R7	ASYSVM CLASTERM DMKFREEP FTRVIRT IOBCC3 IOBSTAT MPUOPER RDEVDED RDEVSPL R12 R8 SAVEWRK3 VMBLOK	BLANK CLASURI DMKFRERC FO IOBCP IOBTIO PREFIXB RDEVDISA RDEVSTAT R13 R9 SAVEWRK5 ZEROES	BLANKS CLASURO DMKFRET F1 IOBCSW IOBUSER PROCIO RDEVDRAN RDEVSTA5 R14 SAVEAREA SAVEWRK6	DMKIOSQR F2 IOBFLAG IOERBLOK PSA RDEVENAB RDEVSYS R15 SAVEREGS	F3 IOBIOER IOERDATA RANGE RDEVFLAG RDEVTYPC R2 SAVERO
DMKVDD	AFREE BRING DMKFREE DMKPTRUL DMKSTKIO IOBIRA LPUADDR PLR2 RDEVFLAG RDEVUSER R3 SAVER2 SPNXTPAG	R4 SAVEWRK1	DMKFRET DMKQCNWT	I OBRADD OPERATOR RANGE	DMKLOCKQ DMKSCNAU DMKVDREL IOBSIZE	IOBSPEC PLFLAG1 RDEVATT RDEVSER R12 R8			DMKPGUVG	
DMKVDE	ADSPCH CL DMKCVTBD DMKLOKSW F1 IOBCP IOBSPEC LOCKSAV RDEVCKDX RDEVMDL	AFREE CLASDASD DMKCVTBH DMKSCNRD F2 IOBCSW IOBSTAT LPUADDR RDEVCMDK RDEVMD02	DMKDSPCH DMKSCNRN F3 IOBFATAL IOBTIO PSA	DMKSCNRU F4 IOBFLAG IOBUSER RANGE RDEVCUA	DMKFREE FFS F5 IOBIOER IOERBLOK RCUBLOK RDEVCUB	APSTAT1 CPEXBLOK DMKFREEP FTRFH F6 IOBIRA IOERDATA RCUCACH RDEVCUP RDEVPRDV	DMKFRET FTRRPS F8 IOBLOK IOEREXT RCUPRIME RDEVDISA	FTRVIRT IOBCAW IOBMISC IOERSIZE RCUSUB RDEVFTR	BLANKS CPEXR5 DMKLOCKD FTR35MB IOBCC1 IOBMISC2 IOERSNSZ RCUTYPE RDEVFTR2 RDEVFTR2	FTR70MB 10BCC3 10BS1ZE LOCK RDEVBLOK RDEVBLOK RDEVLNKS

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MODULE	EXTER	RNAL REFE	RENCES (LA	ABELS AND	MODULES)					
	RDEVSTA6 R12 R8 SAVER6 TIMEDISP	R13 R9 SAVER7	RDEVTYPC R14 SAVEAREA SAVEWRK1 TYP2305	RDEVTYPE R15 SAVEREGS SAVEWRK2 TYP3340		RESET R3 SAVER10 SAVEWRK4 UC	RO R4 SAVER11 SAVEWRK5 VMBLOK	R1 R5 SAVER13 SAVEWRK9 X40FFS	R10 R6 SAVER2 SILI ZEROES	R11 R7 SAVER4 SKIP
DMKVDF	APSTAT1 DMKLOCKD PSA R0 R4 TIMEDISP	APUOPER DMKLOKSW RANGE R1 R5 TYP2305	BLANK DMKSCNRN RDEVADD R10 R6 VMBLOK	BLANKS DMKSCNRU RDEVBLOK R11 R7		CLASFBA F2 RDEVLNKS R13 R9	F5 RDEVSTA5 R14	DMKCVTBD F6 RDEVTYPC R15 SAVEREGS	F8 RDEVTYPE R2	LOCK
DMKVDG	DMKPGTPN DMKSYSOW F1 PSA RDEVCUP RDEVOWN RECBAK R13 R9	ARIODV DMKPGTAN DMKPGTTM DMKSYSPE F10 RDCBLKFA RDEVECKD RDEVPRDV RECBLOK R14 SAVEAREA SAVEWRK5	ALOCPUSE CLASFBA DMKPGTAW DMKPGTTU DMKSYSPG F4096 RDCBLKMA RDEVFI0B RDEVRDC RECCYL R15 SAVEREGS	ALOCPAGE ALOCRDEV CPSTAT5 DMKPGTA0 DMKPGT4A DMKSYSPM F9 RDCBLOK RDEVFLAG RDEVSER RECPNT R2 SAVER10 SYSPBPNT	DMKDMPAL DMKPGT54 DMKPGT54 DMKSYSSW IOBLOK RDCPAGAP RDEVFTR RDEVSIZE RECUSED R3 SAVER2	ALOCPG ALOCRSET DMKDMPDV DMKPGTA5 DMKPGT7A DMKSYSSZ IOBMISC RDCRECSZ	ALOCPM ALOCRWRT DMKDMPRC DMKPGTDK DMKPGT8A DMKVDHBB IOBSIZE RDEVADD RDEVLIOB	DMKERMSG DMKPGTDL DMKPGT90 DMKVDHFR LOCK RDEVALLN	ALOCPNT ALOCTDSK DMKFREE DMKPGTDM DMKSCNRU DMKVDHPG OWNDLIST RDEVBLOK RDEVMDL	DMKFRET DMKPGTPL DMKSYSDP FTR70MB OWNDRDEV RDEVCODE RDEVMD02 RDIDX R12 R8
DMKVDH	ALOCTDSK DMKSYSSW RDEVFLAG RECPNT R15 SAVEREGS	ALOCUSED DMKSYSSZ	ALOCPAVL APSTALOC DMKSYSTD RDEVSER RECUSED R3 SAVEWRK2	FFS RDEVSYS RO R4 SYSPALOC	ALOCPNT	ALOCPUSE DMKDMPAL F4 RECBAK R10 R6 SYSPCNT	ALOCCYL2 ALOCRCUU DMKFREE LOCK RECBLOK R11 R7 SYSPCYL1 TYP3350		ALOCMAP ALOCSW DMKPGTCP PSTRCPG\$ RECMAP R13 R9 SYSPDASD TYP3380	ALOCMAX ALOCTDK DMKPGTGC RDEVBLOK RECMAX R14 SAVEAREA SYSPFLG2
DMKVDR	DMKVSUCO IDAHSIZD IOBLOK NICDISB NICSIZE RCWHEAD RDEVMOUT	ASYSVM CLASURI	FFS IDAHWRK1 IOBSIZE NICFLAG NICUSER RDEVADD RDEVOVLY	AFREEP BRING CPEXADD DMKACODV DMKIOSRW DMKSCNVU FO IOBCAW IOBUSER NICFMT NICVDEVB RDEVATT RDEVOWN R1	DMKLOKIO DMKSSSRL F1 IOBFLAG LOCK NICQRY NORET	DMKCFREP DMKPTRAN DMKSSSVM F255 IOBIRA LOCKSAV	CPEXR1 DMKCVTAB DMKPTRUL DMKSSULO	CPEXR8 DMKCVTBH DMKPTSPW DMKSTKCP IDAEWRD1 IOBLDRUN MSSPRES	DMKDSPCH DMKPTTFT DMKTDKRL IDAHEAD IOBLDSZ NICADVF NICRFLG RCWCCNT RDEVFLAG	APUOPER CLASTAPE CRTEXT DMKFREE DMKQCNWT DMKVCBRS IDAHPAGE IOBLDTXT NICBLOK NICROPER RCWCCW RDEVLNKS RDEVUSER R15

MODULE	EXTER	RNAL REFE	RENCES (LA	ABELS AND	MODULES)				•	
	R2 SAVER9 TRQBLOK TYP3211 VCONNICB VCONWBUF		TRQBSIZE TYP3800		TRQBUSER TYP38008	TRQBVAL VCONBFSZ	TWOENTS VCONBUF	SILI TYPCTCA VCONCTL	SAVEREGS SYSTEM TYP1052 VCONEXTN VCONSIZE	TROBIRA TYP2305 VCONNCB
DMKVDS	RDEVSPL R0 R4 SAVER13 SAVEWRK8 TYPBSC TYP3704	CPEXR12 DMKFREEP DMKFREEP DMKSPKDLK F0RMBLOK F8 RCUBLOK RDEVDISA RDEVNICL RDEVSTAT R1 R5 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER2 SAVER3 SAVER3 SAVER3 SAVER3 SAVER3 SAVER3 SAVER3 SAVER3 SAVER3 SAVER3 SAVER3 SAVER3 SAVER3 SAVER3 SAVER3 SAVER3 SAVER3 SAVER3 SAVER3 SAVER3 SAVER3 SAVER3 SAVER3 SAVER3 SAVE SAVE SAVE SAVE SAVE SAVE SAVE SAVE	AFREEP CLASSPEC CPEXR8 DMKFRERC DMKSYSCK FORMUSER LOCK RCUSHRD RDEVDRAN RDEVNRDY RDEVSTA3 R10 R6 SAVER8 SNARBLOK TYPFBA TYP3800 VCONREMF	CPEXSIZE DMKFRET DMKSYSCO FTRRSRL LOCKSAV RCUTYPE RDEVENAB RDEVOWN RDEVSYS R11 R7 SAVEWRK1 SNARVMB TYPPUN TYP38003	CRTEXT DMKLOKIO DMKSYSPR FTR3088 LPUADDR RC32 RDEVEPLN RDEVPEND RDEVTFLG R12 R8 SAVEWRK2 TRQBCRT TYP1052 TYP38008	CRTEXTSZ DMKNEADF DMKSYSPU FTR3270 NICBLOK RC40 RDEVFLAG RDEVFLAG RDEVFS RDEVTMAT R13 R9 SAVEWRK3 TRQBIRA TYP2305 VCONBRK	DMKSCHST DMKSYSTD FTR4WCGM NICDTYPE RDEVATT RDEVFTR RDEVFTR RDEVRCVY RDEVTYPC R14	DMKSCNRD DMKTDKGT F0 NICD3277 RDEVBLOK RDEVLNCP RDEVTYPE R15 SAVEREGS SAVEWRK5 TRQBSIZE TYP3210 VCONNICB	DMKDSPCH DMKSCNRU DMKTDKRL F16 NICD3278 RDEVCTL RDEVLNKS RDEVSNA RDEVUSER R2	DMKSCNVU DMKZTDDF F240 NICSIZE RDEVDED RDEVMAX RDEVSNRB RESET R3 SAVER10 SAVER10 SAVEWRK7 TRQBVAL TYP3278
DMKVDT	FFS IOBLOK NOADD RCUTYPE RDEVMOUT RDEVTYPC R15 SAVEREGS	F15 IOBPATHF NORET RC32 RDEVOWN RDEVUSER R2 SAVER1	DMKLOKSW F16 IOBRADD OPERATOR RDEVADD RDEVPEND	F240 IOBSIZE PSA RDEVATT RDEVPPAG R0 R4 SAVER2	DMKSCNEP F7 IOBSTAT RCHBLOK RDEVBLOK RDEVPRDV R1 R5 SAVER3	DMKSCNRA IOBCAW IOBUSER RCUBLOK RDEVCU2	RDEVDED RDEVSTAT R11 R7 SAVER8	DMKSCNRU IOBFLAG LOCK RCUPRIME RDEVDISA	IOBIRA LOCKSAV RCUSIZE RDEVFLAG RDEVSYS R13 R9 SAVEWRK2	DMKSYSCK IOBLINK LPUADDR RCUSUB RDEVFTR3 RDEVTMAT R14 SAVEAREA
DMKVER	ADSPCH CLASFBA DDRSIZE DMKSCNRD F24 MDRSENS MP OBRHAN OBRVOLN RDEVFTR R12 R8 SAVEWRK5 TYP3330	DEFER DMKSCNVU F4 MDRSIZE NORET OBRHSIZE	DMKVAURN F4095 MDRSIZE1 OBRCCHS OBRKEYN OBR3SIZE RDEVSER R14	DMKCVTHB EXTMODE F7 MDRSWS3 OBRCPIDN OBRLSIZE OBR33SNS	FTR2311B F8 MDRVOL OBRCUA OBRLSKN OPERATOR RDEVTYPC R2 SAVERETN	OBRPGMN PREFIXB RDEVTYPE R3	FTR35MB LOCK MIHKEYN OBRCUAPR OBRRECN PROCIPL R0 R4 SAVEWRK1	OBRSENSN PSA R1 R5		F15 MDRREC MIHVOL OBRFBSNS OBRSWSN RDEVDED R11 R7
DMKVFC	ACORETBL	ADSPCH	AFREE	AFREEP	AFRET	AP	APSTAT1	APUOPER	AQCNWT	ASYSVM

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MODULE	EXTER	RNAL REFE	RENCES (L	ABELS AND	MODULES)					
	DMKVFRSV HCMSFDB PSA R3 SAVEWRK1	CORFLAG DMKCVTBD DMKPTTFT FFS HCSIZE R0 R4 SAVEWRK2 SCCB0120 ZER0ES	CORTABLE DMKCYTBH DMKQCNWT F1 IPUADDR R5 SAVEWRK3 VECAVAIL	DMKDSPCH DMKSCNFD F24 I PUADDRX R10 R6 SAVEWRK4	CPCREGO DMKDSPNP DMKSTKOP F4096 IREALVF R11 R7 SAVEWRK5 VECINST	F5 LOCK R12 R8		CPEXREGS DMKFREEP DMKVFRIP HCCVU NORET R14 SAVEAREA SCCBLEN VECSTAT	CPEXSIZE DMKFRET DMKVFRNA HCDVU PREFIXB R15 SAVEREGS SCCBLOK VECUSER	DMKMHCCP
DMKVFD	DMKPTRFR F24 PSA R3	ADSPCH ASYSVM DMKCVTDB DMKPTSAD F3 R0 R4 SAVEWRK5	AFREE CORCP DMKCVTHB DMKPTTFT F4096 R1 R5 SAVEWRK7	AFREEP CORFLAG DMKCVUFP DMKPTTPM LOCK R10 R6 VECAVAIL	AFRET CORFPNT DMKDSPCH DMKQCNWT LOCKSAV R11 R7 VECOPVF	ALOKRM CORTABLE DMKDSPNP DMKSCNFD LPUADDR R12 R8 VECSAOK	DMKERMSG DMKSTKOP MP R13	AP CPEXBLOK DMKFREE DMKVFRSS NOINVPTE R14 SAVEREGS VECUSER	DMKFREEP DMKVFRSV	APUOPER CPEXSIZE DMKFRET DMKVFSOS PREFIXB R2 SAVEWRK3 X40FFS
DMKVFE	NOINVPTE R14 SAVEAREA	ASYSVM CPEXSIZE DMKPTRFR F16 NORET R15	AFREE BUFIN DMKCVTBH DMKPTSAD F24 PREFIXB R2 SAVERO VECOPVF	AFREEP BUFNXT DMKCVTDB DMKPTTFT F3 PSA R3 SAVER1 VECSAOK	AFRET CORCP DMKCVTHB DMKPTTPM F4096 R0 R4 SAVER10 VECSTAT	ALOKRM CORFLAG DMKDSPCH DMKQCNWT F8 R1 R5 SAVEWRK1 VECUSER	ALOKSP CORFPNT DMKDSPNP DMKSCNFD LOCK R10 R6 SAVEWRK2 VMBLOK	AP CORTABLE DMKERMSG DMKSTKOP LOCKSAV R11 R7 SAVEWRK4 X40FFS	APSTAT1 CPEXADD DMKFREE DMKVFRSS LPUADDR R12 R8 SAVEWRK5 ZEROES	APUOPER CPEXBLOK DMKFREEP DMKVFRSV MP R13 R9 SAVEWRK6
DMKVFR	PAGCORE R13	ALOKSY CPEXBLOK DMKSCHDL PREFIXB R14 SAVEREGS TEMPR9 ZEROES	AP CPEXREGS DMKSTKDE PROCIPL R15 SAVEWRK1 TRANMODE	DMKVFSOS PSA R2 SAVEWRK2	APSTAT4 CO F1 RUNCRO R3 SAVEWRK8 VECAVAIL	APUOPER C1 F4095 R0 R4 SEGINV VECF	ASYSVM DMKDSPA LOCK R1 R5 TEMPRO VECOPVF	BALRSAVE DMKDSPCH LPUADDR R10 R6 TEMPR4 VECSAOK	BALR15 DMKDSPRU LPUADDRX R11 R7 TEMPR5 VECSTAT	CPCREGO DMKLOKDF MP R12 R9 TEMPR6 VECUSER
DMKVFS	ACORETBL CORFLAG DMKDSPCH DMKVFRSA R0 R4 VMBLOK	CORIOLCK	AFREE CORLCNT DMKFREEP DMKVFRSV R10 R7	AFREEP CORPGPNT DMKPTREP DMKVFRVL R11 R8	ALOKRM CORTABLE DMKPTSAD F4096 R12 R9	DMKPTSAE LOCK R13	APSTAT1 CPEXADD DMKPTTCL LOCKSAV R14 SAVEREGS	APUOPER CPEXBLOK DMKPTTFT LPUADDR R15 SAVEWRK1	ASYSVM CPEXREGS DMKSTKOP PREFIXB R2 VACOVFR	CLEAR CPEXSIZE DMKVFRCH PSA R3 VECUSER
DMKVIO	CSW DMKFRET	AFREEP CE CPEXFPNT CUE DMKHPTDI DMKSTKIO F16	C1 DMKHVCAL	CPEXR11 DE DMKHVCVI	ATTN CLASGRAF CPEXR12 DEFER DMKIOSCB DMKTRDWT F4096	CPEXR13 DFRCC1 DMKPSAP0	BALRSAVE CLASTERM CPEXR4 DFRCC3 DMKPTRAN DMKUNTFR IFCC	CLASURI CPEXSIZE DMKCCHRF	BUSY CLASURO CPSPMODE DMKDSPCH DMKSCNVD DMKVDRES INTREQ	DMKFREEP DMKSCNVU

MODULE
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HODOLL			(20020 (2		,					
	IOBCAW IOBHIO IOBRESRV IOBTIO IOERDATA RCWCCW R1 R6 TYPCTCA	IOBCC2 IOBIOER IOBSENSE IOBUNREL IOERECSW RCWHEAD R10 R7 TYP3340	IOBUNSL	IOBCSW IOBLOK IOBSIZE IOBUSER IOERSIZE RCWRCNT R12 R9 TYP3705	IOBCUBSY IOBMISC IOBSPEC IOBVADD LOCK RCWTASK R14 SM UC	IOBCUE IOBMISC2 IOBSPEC2 IOBVDVIO PCI RCWVCAW R15 SPMPFX UE	IOBDIAG IOBPST IOBSPEC3 IOBVHIO PCIF RDEVBLOK R2 TEMPSAVE VMBLOK	R3	IOBFLAG IOBREMOT IOBSPEC5 IOERBLOK PSA RDEVSTA3 R4 TREXCTL2 XTNDLOCK	IOBSTAT IOERCSW RCWCCNT RO R5 TREXT
DMKVMA	CORTABLE DMKCFMRU DMKPTRAQ F16 PAGTABLE PROTRELL R15 SEGINV	BALRO CORVM DMKCVTBH DMKPTRSC F256	DMKPTRUL F4095 PROTBEG PSA R3 SEGPLEN	DMKDSPNP	CPEXREGS DMKERMSG DMKPTTFT LOCK	DMKREIPL LOCKSAV	CPPTLBR DMKFREEP DMKSTKCP LPUADDR PROTPALT R11 R7	CPSTAT4 DMKFRET DMKSTKMP PAGACT PROTPGAD R12 R8	FFS PAGCORE PROTRCNT R13 R9	ASYSVM CORSWPNT DMKCFMBK DMKLOKSY F1 PAGINVAL PROTRE1 R14 SAVEAREA SWPTABLE VMASHRBK
<b>DMKVMC</b>	ACORETBL BRING DEFER DMKPSASP DMKSTKOP R11 R7 SAVEWRK2 TRQBVAL	CORCP DMKCVTAB DMKPTRAN F0 R12 R8	AFREE CORFLAG DMKDSPCH DMKPTRLK F1 R13 R9 SAVEWRK6 TRQREG0	F7 R14	DMKFREEP DMKPTSPW LOCK R15 SAVEREGS	DMKSCHDL LPUADDR R2	APTRAN CPEXRO DMKLOKSW DMKSCHST PSA R3 SAVER2 TRQBIRA	APTRLK CPEXR11 DMKPSAFC DMKSCNAU R0 R4 SAVER5 TRQBLOK	APUOPER CPEXSIZE DMKPSAFP DMKSTKCP R1 R5 SAVER6 TRQBTOD	ASYSVM C1 DMKPSASC DMKSTKMP R10 R6 SAVEWRK1 TRQBUSER
DMKVMD	AFREE COLON DMKPTRUL F1 IOERETN R13 R9 SAVEWRK8 SFBTIME	AFRET C1 DMKSCNFD F10 IOKEEP R14 SAVEAREA SAVEWRK9 SFBUFORM	F2 LOCK R15 SAVEREGS SFBDATE		BLANK DMKCVTHB DMKVMEDP F4 RESET R3 SAVEWRK2 SFBFNAME VMBLOK	DMP1PCS F4096 R0 R4 SAVEWRK3	BRING DMKFREE DUMP F5 R1 R5 SAVEWRK4 SFBLOK XPAGNUM	BUFCNT DMKFRET ERRCODE F6 R10 R6 SAVEWRK5 SFBNORET X40FFS	BUFFER DMKPSAFP FFS F8 R11 R7 SAVEWRK6 SFBOFORM	BUFNXT DMKPTRAN FO HEADER R12 R8 SAVEWRK7 SFBSIZE
DMKVME	DMPCRS	DMKQCNWT DMPDMPID DMPVMTYP MODESET R13 R9 SAVEWRK8 SFBLAST	AFRET DEFER DMKLOCRQ DMKRPAGT DMPFPRS F0 NORET R14 SAVEAREA SAVEWRK9 SFBLOK SFBLOK SFBTYPE	DMKPGTSG DMKRPAPT DMPGPRS F1 PREFIXB R15	DMKPGUVG DMKRSPDP DMPINREC F4095 PSA R2 SAVEWRK1 SFBCOPY	DMKPGUVR DMKSPKDL DMPIPCS F4096 RDRCHN R3	DMKPSAFP DMKVSDAD DMPPGMAP F5 R0 R4 SAVEWRK3 SFBDIST SFBRECNO	DMKPSAPO DMKVSGRI DMPPSW F6 R1 R5 SAVEWRK4 SFBDUMP SFBRECSZ	DMKPTRAN DMPCKCOM DMPSYSRM IOERETN R10 R6 SAVEWRK5 SFBFILID	CPUID DMKFREE DMKPTRLK DMPCPUID DMPSYSRV IOKEEP R11 R7 SAVEWRK6 SFBFLAG SFBSTART SPSIZE

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MODULE	EXTE	RNAL REFE	RENCES (L	ABELS AND	MODULES)					
	SWPFLAG ZEROES	SWPKEY1	SWPKEY2	SYSTEM	TODATE	TYP1403	VMBLOK	VMSIZE	XKEYMODE	XPAGNUM
DMKVMG	AFREE R1 R6	AFRET R11 R7	DMKFREE R12 R9	DMKFRET R13 SAVEAREA	DMKIUACP R14 SAVEREGS	R15	LOCK R2 UFLAGS	PREFIXA R3 VGPTR	PSA R4 VMBLOK	RO R5 ZEROES
DMKVMI	ATTN CLASURI LOCK R2 SKIP TYP2540R	BUSY CSW PSA R3 SM UC	CAW DE RO R4 TAB UE	CC EXTMODE R1 R5 TYPCTCA VMBLOK	CD FLAG2 R10 R6 TYPRDR	CE IL R11 R7 TYPUNSUP	CLASDASD INTTIO R12 R8 TYP2401	CLASFBA I PLADDR R13 R9 TYP2415	CLASSPEC I PLCCW1 R14 SENSE TYP2420	CLASTAPE IPLPSW R15 SILI TYP2501
DMKVRR	PREFIXB RCUSTAT RDEVENAB	CLASDASD DMKBLDVM DMKFREEA DMKSCHDL	DMKCPIBD DMKFREEP DMKSCHRT FFS IOBPMINT MICBLOK	DMKFRET DMKSCHST F1 IOBRADD MICDASA NORET PROCIPL RDEVALT RDEVPTHS RDEVSTAT R11 R7	DMKLOGOP DMKSCNVU F4 IOBSIZE MICEVMA2 OPERATOR PSA RDEVBLOK RDEVBLOK	DMKPMAIO DMKSTABD F4096 IOBSPEC4 MICEVMA3 PMAAVAIL RCUBLOK RDEVCUA RDEVCUA RDEVPTH2 RDEVSTA5 R13	ARIOCT CORTABLE DMKDMPRY DMKPMATM DMKSTKIO INTTIO IOERBLOK MICFSSE PMAMODE RCUCACH RDEVCUB RDEVSTA6 R14 SAVEREGS TRQBVAL	DMKDSPQI DMKPTRFR DMKSYSND IOBBPNT IOERDATA MICPMAMP PMAON RCUDISA RDEVCUP RDEVPTH4 RDEVTPC R15	DMKVATOF IOBCSW IOERSIZE MICPMAV PMASTAT RCUOWNER RDEVDED	DMKERMSG DMKQVMTS DMKVDSAT IOBFPNT IOMASK MICPMMSK PREFIXA RCUPRIME RDEVDISA RDEVPTH6 RDEV333V R3
DMKVRS	DMKVRROP I OBSPEC4 RCUOWNER	CORFLAG DMKDMPMP FFS IOMASK	F1 IONPSW RCUTYPE	DMKDSPIS F4096 I00PSW RDEVATT RDEVSTA6 R2	INTTIO LOCK RDEVBLOK RDEVUSER R3 TREXIN2	I OBCSW MP RDEVDED		IOBLOK PSA RDEVFTR R1 R7	CAW DE DMKSCNRU IOBPMINT RCUBLOK RDEVPTHS R10 R8 TREXVAT	RCUCACH
DMKVSC	F4096 IOERSIZE RDCBLKMX	AP CPEXFPNT F8 LOCK RDCBLOK RDEVSTA6 R2 SYSVIRT VMBLOK	BALRSAVE DMKFRET 1DA L2 RDEVALT RDEVALT RDEVTYPC R3 TEMPR15 XRIGHT16	DMKSTKCP 10BFLAG MP RDEVBLOK	CC FTR35MB IOBFPNT PSA RDEVCMDK R1 R5 TYP2314	CD FTR70MB IOBLOK RCUBLOK RDEVCUA R10 R6 TYP3330		CLASFBA F10 !OBSIZE RCUPRIME RDEVMDL R12 R8 TYP3350	F15 I OERBLOK	RCUTYPE
DMKVSD	ACORETBL BLANKS	ADDSFB CHGSFB	AFREE CORCP	AFRET CORFLAG	AP CORTABLE	APSTAT1 CORVM	APTRLK DMKCKSPL	APUOPER DMKDSPNP	ARSPRD DMKFREE	ASYSVM DMKFRET

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## EXTERNAL REFERENCES (LABELS AND MODULES)

	DMKVSETR RHTINDEX R12 R8	FFS RHTLEN R13 SAVEAREA	F1 RHTNUMEN R14 SAVEREGS SFBFILID	LOCK RHTPAGNO R15 SAVERO	R2 SAVER1	DMKRSPCR NOCOPY RHTSFB1 R3 SAVER2 SFBLOK TEMPSAVE	PSA RHTSPU1 R4 SAVER7 SFBPNT	RDRCHN R0 R5	DMKSYSHT RHTBLOK R1 R6 SAVEWRK4 SFBSYSID	RHTECNT R11 R7 SAVEWRK6
DMKVSE	ADSPCH C1 DMKPTRAN DMKSTKCP MP RHTPAGNO R12 R8 SFBFLAG4 SFBFLAG4 SPULAST	DMKSYSHE OWNDLIST RHTRSRV R13 R9	DMKPTRPS DMKSYSHT OWNDRDEV RHTSFB1 R14	DMKPTRUL DMKSYSOW PSA RHTSPRF R15 SAVEREGS SFBPNT	RDRCHN RHTSPU1 R2 SAVER1	DMKRSPPR	DMKRSPPU DMKVSGUM RHTFLAG R0 R4 SAVEWRK1 SFBUSER	DMKLOCRD DMKRSPSC F0 RHTINDEX R1 R5	R10 R6 SAVEWRK7 SPUBLOK	DMKMCHST DMKSCNRD LOCK RHTNUMEN R11 R7
DMKVSF	AP PSA R15 SAVEREGS SPUFIRST	RHTBLOK R2 SAVER0	DMKRSPSM RHTVIRT R3 SAVER2	RO R4	DMKVSDFH R1 R5 SFBFILID	DMKVSETR R10 R6 SFBLOK	F4095 R11 R7 SFBORIG	F4096 R12 R8 SFBPNT	LOCK R13 R9 SFBSIZEB	MP R14 SAVEAREA SPUBLOK
DMKVSG	DMKVSEER PSA R13 R9	DMKFREE DMKRSPPU DMKVSELK RHTBLOK R14 SAVEAREA SAVEAREA SAVEWRK8 SPUIND	AFREEP CPEXREGS DMKFREEP DMKSCNAU DMKVSETR RHTFLAG R15 SAVEREGS SAVEREGS SAVEWRK9 SPULASGN SWPCODE	DMKFRET DMKSCNMU FFS RHTRSRV R2 SAVER0 SFBFILID	DMKMCHST DMKSCNRD FO RHTSPRF R3 SAVER7	IOERETN RHTSPU1 R4 SAVEWRK1 SFBPNT SPUMAPSZ	DMKPTRLK DMKSYSOW LOCK R0 R5 SAVEWRK2 SFBTYPE	DMKPTRPS DMKSYSSP MP R1 R6	APUOPER DMKCVTBH DMKPTRUL DMKSYSWI OWNDLIST R11 R7 SAVEWRK5 SPECIALV SPUSIZEB	DMKRSPFC DMKVSDFH OWNDRDEV R12 R8 SAVEWRK6 SPUBLOK
DMKVSI	DE DMKPMACR	DEFER DMKPSAPO	DMKPTRAN	DMKCCWTR DMKSCHDL DMKVI0IN F240 IOBLINK IOBSPEC2 IOERCSW	DMKV10MK F4095 10BLOK 10BSPEC3	DMKFREE DMKSTKCP DMKVSCVR F8 IOBMISC IOBSPEC4 IOERLEN R0 R4	APTRAN CE CPSPMODE DMKFREEP DMKSTKIO DMKVSJEX INTREQ IOBPVM IOBSPM IOERSIZE R1 R5 TYPCTCA	DMKFRET DMKTRDSI DMKVSPEX IOBCAW IOBRCAW IOBSTAT	CUE DMKHPSQV DMKUNTFR DMKVSWTO IOBCC3	DMKVCAST
DMKVSJ	ADSPCH BUSY CLASURO	AFREEP CAW CODE	ALOKSP CCC CPEXADD	ALOKSY CC3 CPEXBLOK	AP CDC CPEXMISC	APSTAT1 CE CPEXREGS			AVMREAL CLASTERM CPSTAT2	BRING CLASURI CSW

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	DMKSTKIO FO IOBRADD IOBVHIO PSAMSS R10 R6	DMKTRDSI IFCC IOBRCAW LOCK RCUBLOK R11 R7	R12 R8	DMKVIOCL IOBCUBSY IOBSPEC LPUADDR RDEVAIOB R13 R9	DMKVIOIN IOBFPNT IOBSPEC3 MP	DMKPSAPO DMKVIOMK IOBHIO IOBSPEC4 MSSPRES	DMKPTRAN DMKVIOXK IOBIRA IOBSTAT PREFIXA RDEVPS R2	DMKSCNRU DMKVSICH IOBLOK IOBUSER PREFIXB RDEVSTA3 R3 TRACCURR	I OBPATHF I OBVADD PSA R0 R4	DMKHPSHT DMKSTKDE DMKVSIFT IOBPROC IOBVCUE PSADSPRQ R1 R5 TRACFLG2 TREXT
DMKVSP	F1 F8 PRTC R2 SAVER7 SFBFLAG3	DMKSPLCV DMKVSWOR F15 IDA PSA R3 SAVER8 SFBFLNMT SFBRECSZ SPNXTPAG TYP3210 UE	DMKVSXCL F2 IL R0 R4 SAVER9 SFBHOLD SFBSIZE SPPREPAG TYP3211 VBFBLOK VSPCCW	DMKV10MK DMKVSXSE F256 INTREQ R1 R5 SAVEWRK2 SFBLAST SFBLAST SFBSTART SFBSTART SFBSTART SFBSTART VBFBUF	DMKVSQPD DMKVSXSI F3 IOERETN R10 R6 SAVEWRK6 SFBLDBEG SFBSYSID SPTIME TYP3289E VBFCCW1 VSPDPAGE	DMKCVTBH DMKPTRAN DMKVSRGC DMKVSXSR F4 LOCK R11 R7 SFBCLAS SFBCLAS SFBLDMID SFBTIME SYSTEM TYP3800 VBFCOUNT	DMKVSRMD DMKVSXTR F4096 MP R12 R8 SFBFCB SFBLOK SFBLOK SFBLOK SFBTYPE TEMPR3 TYP38003 VBFRADD VSPFLAG1	DMKERMSG DMKRPAGT DMKVSTOP DMKXADVS F5 PCI R13 R9 SFBFILID SFBFILID SFBPNT SILI TYPPRT TYP4245 VMBLOK	DMKRPAPT DMKVSVLD FFS F6 PCIF R14 SAVEAREA SFBFLAG SFBFLAG SFBPURGE SKIP TYPPUN TYP4248	CD CPEXSIZE DMKFREEP DMKSCNVU DMKVSWDC F0 F7 PRGC R15 SAVEREGS SFBFLAG2 SFBFLAG2 SFBFLAG2 SFBRECER SPFILID TYPTIMER TYP5ACCW VSPBUFBK VSPLCTL
DMKVSQ	FORMSEND F4096 R13 R9 SFBFCB SFBFCB SFBPNT SFPCLASS SPNXTPAG TYP3210	SFBFCBNL SFBPURGE SFPEND SPPREPAG TYP3211 VBFLGLFT	DMKSTKCP F0 LOCK R15 SAVEREGS SFBFCBXL SFBFCENU SFBECNUM TYP3800 VBFRADD1 VSPDPAGE	SFBFLAG2 SFBSTART SFPOPTS SPRMISC TYP38003 VBFRADD2	DMKPGUVG DMKSYSSF F24 PSA R3 SAVER2 SFBFLAG3 SFBSYSID SFPTITLE SPSIZE TYP4248	FFS F255 R0 R4 SAVER7 SFBFLAG4 SFBTIME SFPTOP SPTIME VBFBLOK VBFVSQR0		DMKPTRUL FORMFLAG F3 R10 R6 SAVER9 SFBLAST SFBVLEN SKIP TEMPR1		BRING DMKFREE DMKRPAPT FORMNTRY F4095 R12 R8 SFBCLAS SFBCLAS SFBDISC1 SFPBOT SPLINK TYPPRT VBFDCUSD VSPBUFBK VSPSFBLK
DMKVSR		TEMPR1 VBFBUF1 VBFVSQR2	APTRLK F255 TEMPR12 VBFBUF2 VBFWORK VSPIDASW	ASYSVM F256 TEMPR4 VBFCOUNT VMBLOK VSPIDAW2	VMSIZE	CLASURI F4095 TYP3211 VBFRADD VSPBIGBF VSP5ACCW	F4096 TYP3800 VBFRADD1 VSPBUFBK	MP TYP38003 VBFRADD2	DMKPSASC PSA TYP4248 VBFRADD8 VSPCCW	SAVEAREA VBFBLOK

# EXTERNAL REFERENCES (LABELS AND MODULES)

DMKVSTADSFEDADSFEDAFREEAFREEAFREEAFREEAFREEAFREEAFREEAFREEAFREEDMKREUNDMKRENDOCLASURDCLASURDCLASURDCLASURDCLASURDCCLASURDCPEXADDDMKVSREEDMKVSREPDMKRPGUNDMKPGUNDMKPGUNDMKRPGUNDMKRPGUNDMKRSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMSGUEDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMKSPGUDMSGUEDMSGUEDMSGUEDMSGUEDMSGUEDMSGUEDMSGUEDMSGUEDMSGUEDMSGUEDMSGUEDMSGUEDMSGUE				•							
R11R12R13R15R6R7R8R9SAVEAREASAVEREASAVEREADMKVSVAPDMKVSVPFFSSAVERKISFPLAGSFPLAGSFPLAGSFPLAGF256F3F4F4095F4096R2R3R4R5R6R1R10R12R13R14F4095F4096DMKVSWAFREEAFRETAPDMKFADD2VBFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPUFTICVSPU	DMKVST	CPEXBLOK DMKFREE DMKVSPWA F60 R11 R7 SAVER9 SYSTEM	CPEXFPNT DMKFREEP DMKVSQPD F7 R12 R8 SAVEWRK2 TYPPRT	CPEXR1 DMKFRET FFS LOCK R13 R9 SFBFILID TYP1052	CPEXR11 DMKPGUVR F1 R14 SAVEAREA SFBLOK TYP3210	CPEXR8 DMKPTRUL F2 OPNSFB R15 SAVEREGS SFBRECNO TYP3211	CPEXSIZE DMKRPAGT F3 PRTCHN R2 SAVERO SFBRECSZ TYP3800	DMKCKSPL DMKSCNVD F4 PSA R3 SAVER1 SFBSTART TYP38003	DMKCVTBH DMKSPKDL F4096 R0 R4 SAVER2 SILI TYP4248	DMKDSPCH DMKSPLOV F5 R1 R5 SAVER7 SKIP VMBLOK	DMKERMSG DMKSTKCP F6 R10 R6 SAVER8 SAVER8 SPLINK
LOCK R2 R2 R3 R4 WBFDATAPSA R0 R4 WBFADD1R0 WFFADD2R1 R1 R1 WFRADD2R1 WFFADD2R10 WFFR2 VSPCCWR11 R12 WSPCCWR12 R1 R1 R13 R14 R14 R15 R2 R7 R7 R7 R7 R7 R7 R7 R7 R7 R7 R7 R7 R7 R7 R8 R7 R7 R8 R7 R8 R7 R8 R7 R8 R7 R8 R7 R8 R7 R8 R7 R8 R7 R8 R9 SFBEDF SFBFLD DMKVSDLD SFBEDF SFBFLCF SFBFLD CT DMKVSDLD SFBFLCF SFBFLC7 SFBFLC7 SFBFLC7 SFBFLC7 SFBFLC7 SFBFLC7 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC9 SFBFLC9 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFLC8 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL28 SFBFL	DMKVSU	R11	R12	R13	R15	R6	R7	R8	R9	SAVEAREA	SAVEREGS
DMKKPAGTDMKVSDADDMKVSDADDMKVSDADDMKVSSKDFF3LOCKMPPSAR0R1R13R13R14R15R2R5R6R5R7R8R9SAVEREASAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVEREASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVERASSAVE	DMKVSV	LOCK R2	MP R3	PSA R4	RÓ R5	R1 R6	R10 R8	R12 R9	R13 TYP38003	R14 VBFBLOK	R15
CMDREJ DMKRPAGTC1DATACHK DMKSCNDCDE DMKYMASHDEFER DMKVMASHDMKVVTDTDMKKPREC DMKVSRGCDMKVSACC DMKVSWDCDMKVPSACC DMKVSWOTDMKVSWOT DMKVSWOTPCIF PCIFPRGCPSA RECPNTRDCBLOK RECSIZERDCBLOK RDCVENDENDRVSWDC IDAILINTREQ IDAINTREQ IDAINTREQ IDAINTREQ IDAIOEKYSWC DMKVSWDCDMKVSWOT DMKVSWOT DMKVSWOT RDEVENDCPCIF PCIFPRGCPSA RECPNTRDCBLOK RECSIZERDCBLOK RDCVENDERDCVBLOK RDEVENDCRDEVYTPC RECSIZERECLOK 	DMKVSW	DMKRPAGT RO R7 SFBDUMP SFBOPEN	DMKSPKDL R1 R8 SFBEOF SFBRECER	DMKVSDAD R11 R9 SFBFILID SFBRECNO	DMKVSDDL R12 SAVEAREA SFBFLAG SFBSIZE	DMKVSFNU R13 SAVEREGS SFBFLAG2 SFBSTART	DMKVSXRD R14 SAVER6 SFBHOLD SFBSYSID	F3 R15 SAVER7 SFBINUSE SFBUHOLD	LOCK R2 SAVER8 SFBLOK SFBUSER	MP R5 SAVER9 SFBMON SPLINK	PSA R6 SFBCLAS SFBNOHLD
DMKDSPDMKDSPCHDMKDSPWADMKVMASWDSPRQDSPWAIOBFPNTIOBLOKIOBUSERLOCKLPUADDRPREFIXAPSAPSAEXTPXARQLKBASERUNPSWR0R1R10R11R12R13R14R15R2R3R4R5R6R7R8R9TIMEDISPTRLANCHRTRLCTTRLINQTRLLABELTRLLOCKTRLSTLVMAFFVMBLOKVMBTRLVMCPWAITVMDEFSTKVMDSTATVMEPRIORVMFTRLVMIHISTVMLKHISTVMLOCKVMOSTATVMPATHVMPENDVMRLPROCVMRSTATVMRUNVMSHRVMSHRPRCVMSTKCPUVMXUNSTKWAITENDXTNDLOCKACNTBACKACNTBLOKACNTDATAACNTNEXTACNTSIZEACNTUSERACORETBLADDSFBAFREEAFRETALARMALOCBLOKALOCMPAGALOCNPAGALOCNPAGALOCRECSAPSTAT1APTRLKAPUOPERAQCNWTARIODVARSPPRARSPPUASYSVMBUIFVERCKPBLOKCKPNAMECKPMAXCKPSIZECLASFBACLASSPECCLASTERMCORCPCORFLAGCORTABLECORVMCPSTAT5C1	DMKVSX	CMDREJ DMKRPAGT F0 PCIF RECMAP R15 SAVEREGS SFBLOK SPRECNUM TYP3800	C1 DMKSCNDC F1 PRGC RECPNT R2 SAVER6 SFBOPEN SPSIZE TYP38003	DATACHK DMKTMRPT F3 PSA RECSIZE R3 SAVER7 SFBRECER SYSTEM TYP4245	DE DMKVMASH F4095 RDCBLOK RECUSED R4 SAVER8 SFBRECS TEMPRO TYP4248	DEFER DMKVSRGC IDA RDCPAGAP RO R5 SFBEOF SFBEOF SFBRECSZ TEMPR1 TYP5ACCW	DMKCVTDT DMKVSRMD IL RDEVBLOK R1 R6 SFBFLAG SFBFLAG SFBTYPE TYP3203 UC	DMKFREE DMKVSWDC INTREQ RDEVRDC R11 R7 SFBFLAG3 SILI TYP3211 UE	DMKPSACC DMKVSWFC IOERETN RDEVTYPC R12 R8 SFBLAST SKIP TYP3262 VMBLOK	DMKPSASC DMKVSWOC LOCK RECBLOK R13 R9 SFBLDBEG SPLINK TYP3289E VSPBUFBK	DMKPTRAN DMKVSWOT PCI RECCYL R14 SAVEAREA SFBLDMID SPNXTPAG TYP3505 VSPBUFSZ
AFRET ALARM ALOCBLOK ALOCMAP ALOCNPAG ALOCRECS APSTAT1 APTRAN APTRLK APUOPER AQCNWT ARIODV ARSPPR ARSPPU ASYSVM BRING BUFFVER CKPBLOK CKPNAME CKPRMAX CKPSIZE CLASFBA CLASSPEC CLASTERM CORCP CORFLAG CORTABLE CORVM CPSTAT5 C1	DMKWA I	DMKDSP LPUADDR R11 R7 VMAFF VMLOCK	DMKDSPCH PREFIXA R12 R8 VMBLOK VMOSTAT	DMKDSPWA PSA R13 R9 VMBTRL VMPATH	DMKVMASW PSAEXT R14 TIMEDISP VMCPWAIT	DSPRQ PXA R15 TRLANCHR VMDEFSTK	DSPWA RQLKBASE R2 TRLCT VMDSTAT	IOBFPNT RUNPSW R3 TRLINQ VMEPRIOR	IOBLOK RO R4 TRLLABEL VMFTRL	IOBUSER R1 R5 TRLLOCK VMIHIST	LOCK R10 R6 TRLSTL VMLKHIST
	DMKWRM	AFRET AQCNWT CKPSIZE	ALARM ARIODV CLASFBA	ALOCBLOK ARSPPR CLASSPEC	ALOCMAP ARSPPU CLASTERM	ALOCNPAG ASYSVM CORCP	ALOCRECS BRING CORFLAG	APSTAT1 BUFFVER CORTABLE	APTRAN CKPBLOK CORVM	APTRLK CKPNAME CPSTAT5	APUOPER CKPRMAX C1

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	DMKRPASV DMKRSPPC DMKSYSDT DMKWRNSB IOKEEP NICTYPE PSA RDEVENAB RDEVENAB RDEVSEP RECBUFF RHXTABLE RO R4 SAVEWRK2 SFBFLAG2	DMKRSPAC DMKRSPPR DMKRSYSHE DMKWRNWM LOCK NPRCNT RDEVALLN RDEVALLN RDEVSER RECCYL RSPXAUTO R1 R5 SAVEWRK3 SFBFLAG4 SFBRECER SHQBLOK	DMKRSPCP DMKRSPPU DMKSYSHL DUMP NICBLOK NPRNAME RDEVAUTO RDEVFLAG RDEVSPL RECFSZ RSPXBLOK R10 R6 SAVEWRK4 SFBFNAME SFBFSNAME SFBRECS SHQBSIZE	DMKRSPCR DMKRSPRD DMKSYSHT FF NICDISA NPRPNT RDEVBLOK RDEVFSEP RDEVSTAT RECPNT RSPXDEST R11 R7 SAVEWRK6	DMKRSPCU DMKRSPSC DMKSYSLG FFS NICENAB NPRTBL RDEVCKP RDEVIMAG RDEVTYPC RECSIZE RSPXFCB R12 R8 SAVEWRK8 SFBINUSE SFBSIZE	DMKRSPCV DMKRSPST DMKSYSOW F0 NICFLAG OPERATOR RDEVCKPT RDEVCKPT RDEVTYPE RECUSED RSPXFLAG R13 R9 SFBBCONV	DMKRSPDC DMKRSPTP DMKSYSSP F1 NICLGRP OWNDLIST RDEVCLAS RDEVNCP RDEVXSEP RHTBLOK RSPXFOLD R14 SAVEAREA SFBCONV SFBSCONV SFBSCONV SFBSCONV SFBSCONV	DMKRSPDL DMKRSPWC DMKSYSWA F2 NICSIZE OWNDRDEV RDEVCODE RDEVCODE RDEVCODE RDEVCIE RDEVCIE RDEVCIE RDIDX RHTECNT RSPXFORM R15 SAVEREGS SFBDATE	F256 NICSTAT PCHCHN RDEVDISA RDEVPRFG RDRCHN RHTVRFY RSPXINDX R2 SAVER2 SFBEOF SFBPNT SFBTUSE SWPCHG1	DMKRSPHQ DMKSNTQN DMKSYSWM F4096 NICTERM PRTCHN RDEVDRAN RDEVDRAN RECBLOK RHXADDR
DMKWRN	DMKRIOPU	APTRAN DMKCKSPL DMKRPAGT DMKRSPRD DMKVSGRI R0 R4 SFBFLAG4 SHQCKPT	APTRLK DMKERMSG DMKRPAPT DMKRSPSC F0 R1 R5 SFBFLAG5 SYSTEM	ASYSVM DMKFRET DMKRSPCP DMKRSPSM F1 R10 R6 SFBINVS VMBLOK	BRING DMKPGUVG DMKRSPCU DMKRSPST LOCK R11 R7 SFBLOK ZEROES	CHGSHQ DMKPGUVR DMKRSPDL DMKRSPSW PSA R12 R8 SFBORIG	DMKRSPHQ DMKRSPTP RDEVBLOK R13	R14 SAVEREGS	C1 DMKRIODV DMKRSPPC DMKVSDAD RDIDX R15 SFBCDMP SFBSYSID	DMKRSPPR
DMKXAB	AFREE DEFER DMKPSAFP FO R10 R6 SAVEWRK5 SFBLOK SPLINK	SFBPNT		APTRLK DMKFRET DMKPTRLK F16 R13 R9 SAVEWRK8 SFBTUSE SYSTEM	ARSPPR DMKLOCRD DMKPTRUL F4096 R14 SAVEAREA SAVEWRK9 SFBTYPE TYPPRT	DMKRPAGT IOERETN R15	DMKRPAPT LOCK R2 SAVEWRK1 SFBFILID	CHGSFB DMKPGUSD DMKSCNVU PSA R3 SAVEWRK2 SFBFLAG SFBFLAG SFBXAB XPAGNUM	CLASURO DMKPGUVG DMKVSFID R0 R4 SAVEWRK3 SFBFLAG4 SFBFLAG4 SFBXABL X2048BND	C1 DMKPGUVR FFS R1 R5 SAVEWRK4 SFBINUSE SFLAG
DMKXAD	AP FO R14 SAVEAREA SPLINK	BRING LOCK R15 SAVEREGS SPNXTPAG	CLASURO MP R2 SAVEWRK1 SPPREPAG	DEFER PSA R3 SAVEWRK2 SYSTEM	DMKPGTSG R0 R4 SAVEWRK3 TYPPRT	DMKPGUVG R1 R5 SFBFLAG4 VMBLOK	R10 R6	DMKRPAGT R11 R7 SFBXAB	DMKRPAPT R12 R8 SFBXABER	R13 R9
DMKXST	AFREE ALOCPAGE AP DMKPGTPN DMKSYSSZ MP RECSIZE R2	APSTALOC DMKPGTPT	ALOCCHN ALOCPMAX DMKCVTBD DMKPGTPU F4096 PSTRCBYT R0 R4	DMKERMSG	ALOCPUSE DMKFREE	ALOCCYL2 ALOCRDEV DMKMHCCP DMKPGTSL HCMSFCW RECBLOK R11 R7	ALOCRWRT DMKPGTDL	ALOCMAP ALOCSW DMKPGTDM DMKPGTSU HCRMAP RECMAP R13 R9	ALOCMAX ALOCTMSI DMKPGTPB DMKSYSPP HCSIZE RECMAX R14 SAVEAREA	DMKPGTPI DMKSYSSW LOCK RECPNT R15

## MODULE EXTERNAL REFERENCES (LABELS AND MODULES)

SAVEWRK1 SAVEWRK2 SAVEWRK3 SAVEWRK4 SAVEWRK5 SAVEWRK6 SAVEWRK7 SAVEWRK9 SCCBIMAP SCCBINUM SCCBLEN SCCBLOK SCCBRESP SCCB0010 SYSPALOC SYSPCYL1 SYSPCYL2 SYSPFLG SYSPFLG2 SYSPLIST SYSPXST

DMKZTD	ADSPCH CPEXADD CPEXSIZE F0 IOBLINK PSA RDEVRDC R13 R9 SAVEWRK6	F8 IOBLOK RDCBLKAP RDEVSTAT R14 SAVEAREA	DMKDSPCH IOBCAW IOBMISC RDCBLOK RDEVSTA3 R15 SAVEREGS	AFRET CPEXRO DMKFREE IOBCC3 IOBSIZE RDEVBLOK RDEVTYPC R2 SAVER8 TYP2305	CPEXR11 DMKFRET IOBCSW IOBUSER RDEVCPEX	IOBCYL LOCK RDEVCPIO R1 R5 SAVEWRK2	CPEXR4 D8 IOBFATAL L16 RDEVECKD R10 R6 SAVEWRK3	L8 RDEVFTR R11 R7 SAVEWRK4	
						TYP3340	TYP3350	TYP3375	TYP3380

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ABORT 000003 DMKNLD DMKNLE DMKRNH	
ACCNT 000005 DMKCKF DMKCKP ACCNTERR 000005 DMKCKF DMKCKP ACCTACNO 000003 DMKHVD	
ACCTBLOK 000005 DMKACO DMKCKF DMKHVD DMKSPL ACCTDIST 000002 DMKHVD DMKSPL	
ACCNTERR 000005 DMKCKF DMKCKP ACCTACNO 000003 DMKHVD ACCTBLOK 000005 DMKACO DMKCKF DMKHVD DMKSPL ACCTDIST 000002 DMKHVD DMKSPL ACCTUSER 000003 DMKHVD ACCTUSER 000001 DMKHVF ACIADDR 000001 DMKLNK ACIALTU 000001 DMKHVF ACIALTU 000001 DMKHVF ACICLR1 000003 DMKCSP DMKCSX DMKLNK ACICLR1 000003 DMKCFC ACICODE 000009 DMKCFC DMKCSP DMKCST DMKCSX DMKHVF DMKLNK DMKLOG DMKRPI DM ACIDEFR 000002 DMKRPI DMKRPW ACIDEL 000001 DMKLSP DMKCST DMKCSX DMKHVF DMKLNK DMKLOG DMKUSQ ACIFCN 000010 DMKCFC	
ACIAUSR 000001 DMKHVF ACICLR1 000003 DMKCSP DMKCSX DMKLNK ACICMD 000001 DMKCSC	
ACICMD 000001 DMKCFC ACICODE 000009 DMKCFC DMKCSP DMKCST DMKCSX DMKHVF DMKLNK DMKLOG DMKRPI DM ACIDEFR 000002 DMKRPI DMKRPW	KRPW
ACILINK 000001 DMKLNK	
ACIMODE 000007 DMKCSP DMKCSX DMKLNK ACINOAC 000001 DMKHVF ACINODE 000002 DMKCST	
ACIPARMS 000027 DMVCEC DMVCSP DMVCST DMVCSV DMVUVE DMVINV. DMVIOC DMVDDI DM	KRPW DMKUSQ
ACTEANS 000027 DMKLOG ACTRASS 000001 DMKLOG ACTRGRP 000009 DMKCFC DMKCSP DMKCST DMKCSX DMKLNK DMKLOG DMKUSQ ACTRUSR 000008 DMKCFC DMKCSP DMKCST DMKCSX DMKHVF DMKLNK DMKLOG DMKUSQ ACTSTZE 000025 DMKCFC DMKCSP DMKCST DMKCSX DMKHVF DMKLNK DMKLOG DMKUSQ ACTSTZE 000025 DMKCFC DMKCSP DMKCST DMKCSX DMKHVF DMKLNK DMKLOG DMKUSQ ACTSTZE 000002 DMKCSP DMKCSX	
ACISPOOL 000002 DMKCSP DMKCSX ACISTCP 000001 DMKCFC ACITAG 000001 DMKCST	
ACITGRP 000004 DMKCSP DMKCSX DMKLNK ACITUSR 000002 DMKCSP	
ACTUSRID 000006 DMKCSX DMKLNK	
ACNTBACK 000004 DMKACO DMKWRM ACNTBLOK 000018 DMKACO DMKCKF DMKHVD DMKJRL DMKWRM ACNTCODE 000018 DMKACO DMKCKF DMKHVD	
ACNTCONT 000002 DMKACO DMKCKF ACNTDATA 000028 DMKACO DMKCKF DMKCQU DMKHVD DMKWRM ACNTDEVC 000004 DMKACO DMKCKF ACNTDEVM 000004 DMKACO DMKCKF	
ACIWUSR 000001 DMKHVF ACNTBACK 000004 DMKACO DMKWRM ACNTCODE 000018 DMKACO DMKCKF DMKHVD DMKJRL DMKWRM ACNTCODE 000018 DMKACO DMKCKF DMKHVD ACNTCONT 000002 DMKACO DMKCKF ACNTDATA 000028 DMKACO DMKCKF ACNTDEVC 000004 DMKACO DMKCKF ACNTDEVM 000004 DMKACO DMKCKF ACNTDEVT 000002 DMKACO DMKCKF ACNTDEVT 000002 DMKACO DMKCKF ACNTNCYL 000004 DMKACO DMKCKF ACNTNEXT 000012 DMKACO DMKCKF ACNTNUM 000006 DMKACO DMKCKF ACNTNUM 000006 DMKACO DMKCKF ACNTPGRD 00002 DMKACO DMKCKF ACNTRFLG 00004 DMKACO DMKCKF ACNTRTL 000008 DMKACO DMKCKF ACNTRTL 000004 DMKACO DMKCKF	
ACNTNETT 000004 DMKACO DMKCKF DMKWRM ACNTNUM 000006 DMKACO DMKCKF DMKHVD DMKWRM ACNTPGRD 000002 DMKACO DMKCKF	
ACNTRFLG 000002 DMKACO DMKCKF ACNTRMID 000008 DMKACO DMKCKF ACNTRMTE 000004 DMKACO DMKCKF	
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ACNTTIME 000002 DMKACO DMKCKF ACNTUSER 000008 DMKACO DMKCKF ACNTUSER 000008 DMKACO DMKCKF DMKHVD DMKWRM ACNTVTIM 000002 DMKACO DMKCKF	

**CP** Label-to-Module Cross-Reference

LABEL	COUNT	REFERENC	ES								
ACNTVTT ACNTVVT ACOACCL ACOACCM ACOCHK ACOCKIT ACORETBL	000002 000002 000001 000002 000001 000005 000201	DMKACO DMKACO DMKACO DMKACO DMKACO DMKACO DMKATS DMKCPY DMKHVE DMKPRW DMKSPM	DMKCKF DMKCKF DMKBLD DMKDGD DMKIDU DMKPSA DMKSTA	DMKCCW DMKDGF DMKMCC DMKPST DMKSTR	DMKCDB DMKDRD DMKMCH DMKPTR DMKSWA	DMKCDM DMKDRE DMKMHV DMKPTS DMKSWM	DMKCDS DMKFRT DMKMNI DMKPTT DMKUDU	DMKCFQ DMKGRE DMKPAG DMKQVM DMKQVM DMKUNT	DMKCFU DMKHPT DMKPGM DMKRPA DMKVCN	DMKCPP DMKHPU DMKPGS DMKSEG DMKVDR	DMKCPU DMKHVD DMKPMA DMKSEL DMKVFC
ACORETN ACRLOCK ACTIAC ACTIBF1R ACTIBF1V ACTIBF2R	000002 000007 000004 000008 000005 000006	DMKVFD DMKACO DMKACR DMKACO DMKACO DMKACO DMKACO DMKACO	DMKVFE DMKACS	DMKVFS DMKCCH	<b>ДМКУМА</b> <b>ДМКСРО</b>	DMKVMC DMKMCH	DMKVRR DMKMCT	DMKVRS DMKVS I	DMKVSD	DMKWRM	
ACTIBF2R ACTIBF2V ACTIBLOK ACTICL ACTICLAS ACTICNT ACTIDCUR ACTIDCUR ACTIDISP ACTIFLAG	000004 000002 000005 000017	DMKACO DMKACO DMKACO DMKACO DMKACO DMKACO	DMKCKF								
ACTILIMT ACTIPCH ACTISFB ACTISFCK	000003 000004 000005 000009 000008	DMKACO DMKACO DMKACO DMKACO DMKACO DMKACO DMKACO	DMKCKF DMKCKF DMKCKF DMKCPP	DMKDSP	DMKEXT	DMKPMA	DMKQVM	DMKTRQ			
ACTIVTRQ ACTSFB ADDSFB	000018 000004 000026	DMKAPT DMKCKR DMKACO DMKVST	DMKCKS DMKCKS DMKCKS DMKWRM	DMKMIA	DMKNLE	DMKSPL	DMKSPS	DMKTRT	DMKTRU	DMKVME	DMKVSD
ADTRANS	000001	DMKSAD DMKACO DMKCCW DMKCRH DMKCRD DMKDRD DMKDRD DMKIOE DMKIOE DMKIID DMKPAH DMKSVD DMKSVD DMKVCB DMKVCB DMKVCA DMKVSQ	DMKACR DMKCFG DMKCFB DMKCSB DMKDRE DMKGRT DMKIOC DMKIOC DMKNCCP DMKSEP DMKSEP DMKSEP DMKSEP DMKSCON DMKVCN DMKVCC DMKVFS DMKVST	DMKALG DMKCFM DMKCPO DMKDAD DMKDSB DMKHPS DMKIOH DMKLOJ DMKRCJ DMKREI DMKSND DMKTAP DMKTRT DMKVCP DMKVCP DMKVIO DMKVIO DMKVIO	DMKAPS DMKCFO DMKCFO DMKDAS DMKDSP DMKIOS DMKLOK DMKRON DMKRGA DMKSPK DMKTCS DMKTCS DMKTCQ DMKVDR DMKVDR DMKVMA DMKZTD	DMKAPT DMKCFQ DMKCPS DMKDAU DMKENT DMKLOM DMKLOM DMKPRG DMKSPM DMKSPM DMKSPM DMKTCT DMKVCR DMKVCR DMKVDS DMKVMC	DMKAPY DMKCFR DMKCFW DMKDGD DMKHVC DMKHVC DMKHUA DMKMCD DMKPRV DMKSPS DMKTHI DMKVCS DMKVCS DMKVSE	DMKATS DMKCKF DMKCGF DMKGIO DMKHVD DMKHVD DMKNCH DMKMSG DMKPRW DMKRSF DMKSSS DMKTMR DMKVCT DMKVCT DMKVCT DMKVSG	DMKBIO DMKCKV DMKCPZ DMKDIA DMKGRD DMKHVE DMKNLD DMKNLD DMKNLD DMKRSP DMKSSU DMKSSU DMKVSI	DMKCAC DMKCNS DMKCQC DMKDIB DMKGRE DMKHVF DMKIUN DMKMCT DMKNLE DMKRST DMKRST DMKSTR DMKSTR DMKVAT DMKVAT DMKVFD DMKVSJ	DMKCCH DMKCQT DMKCQT DMK0ID DMKGRF DMKIDR DMKIUP DMKMHC DMKPAG DMKQCN DMKSCH DMKSCH DMKSCC DMKTRK DMKVCA DMKVCA DMKVFE DMKVSP
ADTRANS AEXTSP	000015 000055	DMKPRV DMKACO DMKFRE DMKSTK	DMKCCH DMKIOS	DMKCFP DMKMCH	DMKCLK DMKMCT	DMKCPI DMKMHC	DMKCPO DMKMOO	DMKCPP DMKPTR	DMKCPS DMKPTT	DMKCPU DMKSCH	DMKDSP DMKSEL

LABEL	COUNT	REFERENC	ES								
AFREE	000745	DMKACO DMKACS DMKCCS DMKCFP DMKCKV DMKCQT DMKCST DMKCST DMKDSB DMKCST DMKLOB DMKIUJ DMKLOH DMKNLE DMKNLE DMKSGL DMKSEL DMKSCD DMKTTY DMKVCB DMKVCR DMKVCR	DMKACR DMKCFQ DMKCCW DMKCFQ DMKCNS DMKCPZ DMKCSU DMKDEI DMKCSU DMKDEI DMKHPS DMKIOF DMKIOF DMKIOF DMKIOF DMKSND DMKSTP DMKSTP DMKSTP DMKVCH DMKVCA DMKVSD	DMKACS DMKAPZ DMKCDB DMKCFR DMKCPB DMKCQC DMKCQC DMKCQV DMKDEX DMKEPS DMKHPT DMKHPT DMKHOG DMKIUP DMKMCC DMKPAG DMKPAG DMKSVK DMKSVK DMKSVK DMKVCN DMKVCN DMKVFD DMKVSG DMKZTD	DMKALG DMKATS DMKCCM DMKCFI DMKCRM DMKCQG DMKCRM DMKCSW DMKDGD DMKERM DMKHPU DMKIUS DMKHPU DMKIUS DMKMCD DMKPEI DMKSPL DMKSPL DMKSPL DMKVCP DMKVCP DMKVFE DMKVFE DMKVSI	DMKALO DMKBIO DMKCFC DMKCFJ DMKCPJ DMKCQH DMKCSB DMKCSB DMKCSB DMKJGF DMKHVC DMKHVC DMKHVC DMKJRL DMKMSH DMKSPM DMKSPM DMKSPM DMKSPM DMKVCQ DMKVCQ DMKVCSP	DMKAPI DMKBLD DMKCFD DMKCFU DMKCPN DMKCQI DMKCQI DMKCSC DMKCSC DMKCSY DMKDIA DMKHVD DMKHVD DMKHVD DMKHC DMKMSW DMKLD00E DMKKSF DMKSPR DMKSPR DMKVCR DMKVCR DMKVCR DMKVCR DMKVCR	DMKAPS DMKBSC DMKCFF DMKCFF DMKCFO DMKCPO DMKCPO DMKCAP DMKCSF DMKCAD DMKLDIB DMKGRC DMKHVE DMKHVE DMKHVE DMKKPER DMKKPER DMKKPER DMKKPS DMKKSPS DMKTAQ DMKTRT DMKVCT DMKVCT DMKVMC DMKVST	DMKAPT DMKCAC DMKCFH DMKCFS DMKCPS DMKCQQ DMKCSO DMKDAS DMKDID DMKGRF DMKIUB DMKHVF DMKIUB DMKNES DMKNES DMKRSQ DMKSPT DMKSPT DMKCQQ DMKSPT DMKVCU DMKVCU DMKVCU DMKVCU DMKVCW DMKVSW	DMKAPU DMKCAO DMKCFJ DMKCFT DMKCPT DMKCQR DMKCQR DMKDAU DMKDAU DMKDAU DMKACAU DMKACAU DMKIDR DMKIDC DMKIDC DMKNET DMKNET DMKKEI DMKKREI DMKKRK DMKVCV DMKVCV DMKVDS DMKVME DMKVSX	DMKAPW DMKCCH DMKCCFM DMKCFU DMKCQS DMKCQS DMKDRE DMKDRE DMKDRE DMKIDG DMKILOG DMKNLD DMKNLD DMKNLDG DMKNLD DMKSCH DMKSCS DMKTTX DMKVCA DMKVCA DMKVCM DMKVDT DMKVMG DMKWRM
AFREEP	000287	DMKXAB DMKACO DMKCDS DMKCPI DMKCPI DMKDIA DMKGRI DMKNCC DMKMCC DMKMCC DMKSVC DMKSVC DMKSVC DMKVDC DMKVMA DMKZTD	DMKVSD DMKXST DMKACS DMKCPJ DMKCPJ DMKCPJ DMKDIB DMKHPT DMKIUA DMKMCD DMKMCD DMKQCP DMKSEL DMKSWA DMKUNT DMKVDD DMKVMC	DMKZTD DMKALG DMKCFM DMKCFM DMKCSO DMKDID DMKHPU DMKIUC DMKNLD DMKSWM DMKSWM DMKSWM DMKVDE DMKVRR	DMKAPS DMKCFO DMKCFN DMKCSR DMKDRD DMKHVC DMKHVCI DMKNLE DMKRGA DMKSPK DMKVAT DMKVAT DMKVSE	DMKAPT DMKCFQ DMKCFO DMKCSW DMKDSB DMKHVE DMKIUN DMKMCT DMKPGM DMKRGB DMKSPL DMKTHI DMKVCA DMKVDS DMKVSG	DMKATS DMKCFR DMKCFP DMKCSX DMKDSP DMKHVF DMKIUP DMKMHC DMKPGS DMKRGC DMKSPM DMKTPE DMKVCB DMKVFC DMKVSI	DMKBLD DMKCKF DMKCPS DMKDAD DMKEXT DMKIOE DMKIOE DMKIIS DMKMIA DMKPGT DMKRNH DMKSSS DMKTRD DMKVCN DMKVFD DMKVSJ	DMKCCH DMKCKV DMKCPU DMKDAS DMKFRE DMKIOF DMKIOC DMKMID DMKPRV DMKRPA DMKSSU DMKTRK DMKVCP DMKVFE DMKVSP	DMKCCS DMKCPV DMKCAU DMKGIO DMKIOQ DMKIOQ DMKLOJ DMKMNI DMKPTR DMKRSE DMKSTP DMKTRT DMKVCU DMKVFS DMKVSQ	DMKCCW DMKCPB DMKCPW DMKDGD DMKIOS DMKIOS DMKNLOK DMKMNL DMKPTS DMKSTR DMKSTR DMKTRU DMKVCX DMKVIO DMKVST
AFRET	000986	DMKZID DMKACO DMKATS DMKCDM DMKCPN DMKCPN DMKCQH DMKCSB DMKCSB DMKCSS DMKDGF DMKIOF DMKIOF DMKIOF DMKIOH DMKMON	DMKACR DMKBIO DMKCDS DMKCFS DMKCPO DMKCQI DMKCSC DMKCSY DMKDIA DMKFPS DMKIOH DMKIOH DMKIOJ DMKMSG	DMKACS DMKBLD DMKCFC DMKCFP DMKCQP DMKCQP DMKCQO DMKDIB DMKFRE DMKFRE DMKFRE DMKIOQ DMKIUP DMKLOM DMKLOM	DMKAPS DMKBSC DMKCFD DMKCFS DMKCQQ DMKCSP DMKDAS DMKDID DMKGIO DMKHPU DMKIOS DMKIUS DMKNEA	DMKAPT DMKCAC DMKCFF DMKCFT DMKCQR DMKCQQ DMKDAU DMKDAU DMKDIF DMKGRA DMKHVC DMKIOT DMKJRL DMKMCD DMKNES	DMKAPV DMKCAO DMKCFG DMKCFU DMKCPU DMKCQS DMKCQS DMKDEF DMKDRD DMKDRD DMKIUA DMKLUA DMKLD00E DMKLUA	DMKAPW DMKCCH DMKCFH DMKCPW DMKCQT DMKCQT DMKDEG DMKDRE DMKDRE DMKLNE DMKLNK DMKLNK DMKNLD	DMKAPX DMKCCS DMKCFM DMKCPZ DMKCQU DMKCQU DMKCSU DMKDSB DMKDSB DMKHVF DMKIUC DMKLNM DMKLNA DMKNLE	DMKAPY DMKCCW DMKCCPI DMKCQC DMKCQY DMKCQY DMKDSY DMKDSP DMKIDR DMKIUE DMKIUE DMKLOC DMKMNI DMKOPE	DMKAPZ DMKCDB DMKCPJ DMKCQG DMKCQG DMKCSW DMKCSW DMKDGD DMKEPS DMKIOE DMKIOE DMKIOE DMKLOG DMKLOG DMKPAH

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LABEL	COUNT	REFERENC	CES								
		DMKPEI DMKQCN DMKSPS DMKTRK DMKTRK DMKVCR DMKVCR DMKVCR DMKVCR DMKVSI DMKACR DMKACRJ DMKACRJ DMKACRJ DMKACR DMKACR DMKACR DMKACO DMKACO DMKACDH DMKACO DMKACDH DMKACO DMKACO DMKACC DMKKOO DMKKOO DMKKOO DMKKOO DMKCKF DMKCKF DMKCKF	DMKPEL DMKQCO DMKRSF DMKSPT	DMKPEN DMKQCP DMKRSP DMKSRM	DMKPET DMKQCQ DMKRST DMKSSS	DMKPGM DMKQVM DMKSEL DMKSSU	DMKPGS DMKREI DMKSEP DMKSSV	DMKPGU DMKRGA DMKSND DMKSVC DMKTPE DMKTTX DMKVCH DMKVCR DMKVRR DMKVRR DMKVRR	. DMKPMA DMKRGB DMKSPK DMKSVD	DMKPST DMKRGC DMKSPL DMKSWA	DMKPTR DMKRNH DMKSPM DMKSWM
		DMKTAP	DMKTAQ DMKTRP	DMKTCS DMKTRQ DMKUSQ DMKVCT	DMKTCT DMKTRT DMKVAT DMKVCU	DMKTH I DMKTRU	DMKTMR	DMKTPE	DMKTRA DMKUDR DMKVCN	DMITEC	DMKTRD DMKUNT
				DMKIRQ			DMKTMR DMKTRX DMKVCB DMKVCW DMKVDT DMKVMG DMKWRM	DMKTTA	DMKVCN	DMKUDU DMKVCP DMKVDC DMKVFD	DMKVCQ
		DMKVCR	DMKVCS	DMKVCT	DMKVCU	DMKVCA DMKVCV	DMKVCW	DMKVCX	DMKVDA	DMKVDC	
		DMKVDE	DMKVDG	DMKVDH			DMKVDT	DMKVER	DMKVFC		DMKVFE DMKVSG
		DMKVIO	DMKTKP DMKUSP DMKVCS DMKVDG DMKVMA DMKVSP DMKACR	DMKVDH DMKVMC DMKVSQ DMKCCH	DMKVMD DMKVST DMKCKH	DMKVME DMKVSW DMKCKM		DMKWRN	DMKVSC DMKVSC DMKXAB DMKCLK DMKERM	DMKVSD DMKZTD	DMKV3G
ALARM	000104	DMKACO	DMKACR	DMKCCH	DMKCKH	DMKCKM	DMKCKN	DMRGKP	DMKCLK	DMKCNS	DMKCPI
		DMKCPJ	DMKCQP DMKMCH	DMKDAD	DMKDAS	DMKDAII	DMKDMQ	DMKDSB	DMKERM	DMKGRD	DMKGRH DMKRGB
			DMKMCH DMKSAV	DMKMCT DMKTTY	DMKM I D DMKUDR	DMKMSG DMKURS	DMKMSW DMKVCN	DMKOPE DMKVCS	DMKOPR DMKVER	DMKGRD DMKPGT DMKWRM	DIMIKIGD
ALL	000039	DMKDMQ					Britte	2			
ALL ALLCHANS	000009	DMKACR	DMKACS DMKPGT	DMKCPP DMKSTP	DMKIOG	DMKPMA					
ALOCALOC	000010		DMKPGT	DMKSTP							
ALOCBLOK	000127	DMKALO	DMKATS	DMKCKF	DMKCKT	DMKCKV	DMKCPX	DMKCPZ	DMKDAD	DMKDAS	DMKDAU
		DMKDMP	DMKIDU DMKSTP	DMKMOO DMKSTR	DMKPAG DMKSWA	DMKPGM DMKSWM	DMKPGS DMKTDK	DMKPGT DMKUSP	DMKPGU DMKVDG	DMKPST DMKVDH	DMKPTR DMKWRM
		DMKSEL			DINKSWA				DMKVDG	DPIKYDII	DEIKAINE
ALOCCHN	000044	DMKMOO	DMKPGT DMKPGT DMKPAG DMKDMP	DMKPST DMKPGU	DMKSTP	DMKTDK DMKSEL DMKPST DMKPGU	DMKUSP DMKVDG DMKTDK DMKPST	DMKVDG	DMKVDH	DMKXST	
AL OCCSR	000019	DMKMOO	DMKPGT		DMKPST	DMKSEL				DMKXST	
ALOCCYL 2	000040	DMKMOO	DMKDMP	DMKPGT DMKPAG	DMKPST DMKPGU DMKPGT	DMKPGU	DMKPST	DMKXST DMKVDG DMKTDK	DMKVDH DMKVDG	DMKXST DMKVDH	DMKXST
ALOCCYL1 ALOCCYL2 ALOCDLOC	000006	DMKPGU									
AL OCDOWN	000007 000010			DMKVDG DMKVDG							
ALOCDU ALOCFLG	000074	DMKATS	DMKCKF	DMKDAD	DMKDAS	DMKDAU	DMKMOO	DMKPAG	DMKPGM	DMKPGS	DMKPGT
		DMKPGU	DMKPST	DMKDAD DMKSTR DMKSTP	DMKDAS DMKVDG DMKSWM	DMKVDH	DMKXST				
ALOCMALC ALOCMAP	000009 000050			DMKSTP	DMKSWM	DMKPGT	DMKPGU	DMKPST	DMKTDK	DMKVDG	DMKVDH
		DMKCKF DMKATS DMKPGU DMKMOO DMKALO DMKWRM	DMKXST								
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ALOCMGIN ALOCMGOU	000004	DMKPGM	DMKSTP	DMKSWM							
ALOCNPAGE ALOCPAGE ALOCPAVL ALOCPG ALOCPM	000032	DMKALO	DMKCKT	DMKSWM DMKCKV	DMKIDU	DMKPGT DMKVDG	DMKPST DMKVDH	DMKVDG DMKXST	DMKVDH	DMKWRM	DMKXST
ALOCPAGE	000027	DMKPGT		DMKPST	DMKUSP		DMKVDH DMKVDH				
ALOCPAVE	000021	DMKMOO	DMKPAG	DMKPST DMKPST DMKPGM	DMKUSP DMKSTP DMKPGT	DMKVDG DMKPGU	DMKSRM	DMKXST DMKSTR	DMKVDG		
ALOCPM	000005	DMKPAG	DMKVDG								
ALOCPMAX	000012		DMKCKI DMKPGU DMKPGT DMKVDG DMKVDG DMKVDG DMKCKF DMKVDG DMKMOO DMKRCM	DMKPST DMKDAD DMKVDH DMKPAG		DMKVDH DMKDAU	DMKXST DMKDMP	DMKIDU	DMKMOO	DMKPAG	DMKPGU
ALOUFINT	000038	DMKPST	DMKVDG	DMKVDH	DMKDAS DMKXST DMKPGM		DIAKDIA			DIANIAO	
ALOCPP	000027	DMKATS	DMKMOO	DMKPAG	DMKPGM	DMKPGS	DMKPGT	DMKPGU	DMKSRM	DMKSTR	DMKVDG
ALOCPRD	000013				DMKSTP DMKUSP						
ALOCPS	000022	DMKDAD	DMKDAS	DMKDAU	DMKPAG	DMKVDG DMKPGT	DMKPGU	DMKSRM	DMKSTP	DMKVDG	
ALOCPPP ALOCPRD ALOCPREC ALOCPSIO ALOCPUSE ALOCPUSE ALOCPWRT ALOCRCUU ALOCRCUU	000016	DMKPAG	DMKPGT DMKDAS DMKPGM DMKPGT	DMKDAU DMKPTR DMKPGU DMKSTP	DMKSEL	DMKSWA DMKSTP	DMKSWM DMKVDG	DMI/MDU	DMIXYET		
	000033		DMKPGI	DMKPGU	DMKPST DMKSWA	DMKSIP	DMKVDG	DMKVDH	DMKXST		
ALOCRCUU	000006	DMKALO	DMKSEL DMKPST DMKIDU	DMKVDH							
ALOCRDEV	000025	DMKALO	DMKIDU	DMKVDH DMKMOO DMKCKV	DMKPGT DMKDMP	DMKPGU DMKIDU	DMKPST DMKPGT	DMKTDK DMKPGU	DMKVDG DMKUSP	DMKVDH DMKVDG	DMKXST DMKWRM
ALOCRECS ALOCRSET			DMKCKT DMKPGU	DMKVDG	DMKDMP	DMKIDU	DHKPGI	DINKEGU	DHKUSP	DINKYDG	
ALOCRWRT	000005	DMKPAG DMKMOO DMKDAD DMKPAG DMKPAG DMKALO DMKALO DMKALO DMKCKF DMKPGT DMKPST	DMKPGU DMKSEL	DMKVDG DMKVDG	DMKXST						

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AL AL	LOCSREC LOCSW	000002 000035	DMKPGU DMKMOO	DMKPAG	DMKPGM	DMKPGT	DMKPGU	DMKPST	DMKSRM	DMKSWM	DMKVDG	DMKVDH
A I		000006 000008 000009	DMKXST DMKTDK DMKMOO DMKPAG	DMKVDH DMKTDK DMKPGT	DMKZTD DMKVDG	DMKVDH						
AL AL AL	LOCTDSR LOCTMS LOCTMSI LOCUSED LOKDS LOKFR LOKRM	000009 000013 000025 000007	DMKPGT DMKMOO DMKAPI	DMKPGT DMKPGT DMKCFQ	DMKVDG DMKPGU DMKDSP	DMKXST DMKPST DMKMCT	DMKTDK	DMKVDG	DMKVDH	DMKXST	DMKZTD	
			DMKFRE DMKCDS DMKSFL	DMKFRT DMKDGD DMKSTR	DMKMOO DMKFRE DMKSWA	DMKPAG DMKSWM	DMKPAH DMKUSP	DMKPGM DMKVFD	DMKPTR DMKVFE	DMKPTS DMKVFS	DMKPTT	DMKRPA
AL	LOKSP	000116	DMKACR DMKCPM DMKDIA	DMKACS DMKCPN DMKDIB	DMKAPI DMKCPO DMKDID	DMKCCH DMKCPS DMKD I F	DMKUSP DMKCCS DMKCPT DMKDSB	DMKCDS DMKCPW DMKDSP	DMKCFQ DMKCPZ DMKEXT	DMKCFR DMKCSO DMKFRE	DMKCKV DMKDE I DMKFRT	DMKCNS DMKDGD DMKGR I
			DMKIOQ DMKNLD DMKRSP DMKSWM	DMKIOS DMKPAG DMKSCH DMKTRQ	DMKIOT DMKPAH DMKSEL DMKUSP	DMKLOH DMKPGM DMKSSS DMKVCH	DMKLOJ DMKPTR DMKSST DMKVDA	DMKMCC DMKPTS DMKSSU DMKVDD	DMKMCT DMKPTT DMKSSV DMKVDE	DMKMNT DMKRGA DMKSTK DMKVDR	DMKMOO DMKRPA DMKSTR DMKVDS	DMKNES DMKRSE DMKSWA DMKVDT
AL	LOKSY	000069	DMKVFD	DMKTKQ DMKVFE DMKCCH DMKIOT	DMKVFS DMKCCS DMKLOK	DMKVSJ DMKCCW			DMKDSP DMKPAG	DMKVDR DMKEXT DMKPRG	DMKFRE DMKPRV	
AL	LOKTRL	000003	DMKIOS DMKPTT DMKVMA DMKAPI	DMKSCH DMKVSJ DMKDSP	DMKSEL DMKWA I	DMKMCH DMKSVC	DMKSVD	DMKTMR	DMKTRX	DMKVAT	DMKVAU	DMKVFR
AL AL AL	LOKVM LPRTBLK LTBLOK	000006 000008 000006 000016	DMKAPI DMKAPS DMKHVF DMKHVF	DMKCPP DMKAPU DMKIUC	DMKDSP DMKAPW DMKSPL	DMKMCH DMKAPY	DMKSEL DMKAPZ	DMKCKH				
	LTBLOK LTSIZE LTUSER LTWRT MCHAREA	000008	DMKHVF DMKVCN DMKACR	DMK I UC DMKCCH	DMKSPL DMKCFU	DMKCPO	DMKCPP	DMKCPU	DMKDID	DMKHVC	DMKIOG	DMKIOH
	NYWRITE		DMKMCH DMKQCN DMKACO	DMKMC I DMKQCQ DMKACR	DMKMHC DMKVCN DMKACS DMKAPY		DMKPRV DMKALO	DMKVAT	DMKVAU	ΠΜΚΔΡΤ	DMKAPU DMKCCF	DMKAPV
			DMKAPW DMKCCO DMKCFO	DMKAPX DMKCCS DMKCFP	DMKCCT DMKCFQ	DMKAPZ DMKCDS DMKCFR	DMKBIO DMKCFC DMKCFS	DMKAPI DMKBLD DMKCFF DMKCFT	DMKCAO DMKCFG DMKCFU	DMKCCD DMKCFH DMKCFV	DMKCFJ DMKCFW	DMKCCH DMKCFM DMKCFY
			DMKCKD DMKCKW DMKCPS DMKCSF	DMKCKF DMKCNT DMKCPT DMKCSO	DMKCKH DMKCPB DMKCPU DMKCSP	DMKCKM DMKCPE DMKCPV DMKCSQ	DMKCKN DMKCPI DMKCPW DMKCSR DMKENT	DMKCKP DMKCPJ DMKCPX DMKCST DMKERP	DMKCKR DMKCPM DMKCPY DMKCSU	DMKCKS DMKCPN DMKCRM DMKCSV	DMKCKT DMKCPO DMKCSB DMKCSW	DMKCKV DMKCPP DMKCSC DMKCSX
			DMKDEX DMKIOG DMKLOJ	DMKDGD DMKIOH DMKLOK	DMKDRE DMKIOJ DMKLOM	DMKDSB DMKIOQ DMKMCC DMKNLD	DMKENT DMKIOS DMKMCD DMKOPE	DMKERP DMKIOT DMKMCH DMKPET	DMKEXT DMKLNM DMKMCI DMKPGM	DMKCSV DMKHPS DMKLOC DMKMNT DMKPGS	DMKCSW DMKHPT DMKLOG DMKMON	DMKIOE DMKLOH DMKMOO
			DMKMPO DMKPSA DMKRGC	DMKMSG DMKPST DMKRGD	DMKNET DMKPTR DMKRNH	DMKPTS DMKRPA	DMKPTT DMKRPD	DMKQCN	DMKQCO DMKR PW	DMKQCP	DMKPGT DMKRGA DMKRSF	DMKPRW DMKRGB DMKRSP
			DMKRSQ DMKSPS DMKSWA DMKUCC	DMKSAV DMKSRM DMKTAQ DMKURS	DMKSBL DMKSSS DMKTCS DMKUSP	DMKSCH DMKSST DMKTMR DMKVAU	DMKSCN DMKSSU DMKTOD DMKVBM	DMKSND DMKSSV DMKTRX DMKVCA	DMKSPK DMKSTA DMKTTX DMKVCB	DMKSPL DMKSTK DMKTTY DMKVCH	DMKSPM DMKSTP DMKTTZ DMKVCN	DMKSPR DMKSVD DMKUCB DMKVCP
			DMKVCQ DMKVFE DMKVSP	DMKVCR DMKVFR DMKVSQ	DMKVCS DMKVRS DMKVSR	DMKVCT DMKVSC DMKVST	DMKVCU DMKVSD DMKVSU	DMKVCV DMKVSE DMKVSV	DMKVCW DMKVSF DMKVSW	DMKVER DMKVSG DMKXAD	DMKVFC DMKVSI DMKXST	DMKVFD DMKVSJ DMKZTD
AF	PAGCP POINT PSTALOC	000013 000001 000009	DMKCPI DMKLDOOE DMKMOO	DMKCPY DMKPGT	DMKPTR DMKPGU	DMKRST DMKPST	DMKSVC DMKVDH	DMKXST				

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APSTAT1	001111	DMKACO DMKCCW DMKCFQ DMKCFQ DMKCPW DMKDAU DMKDAU DMKDSP DMKLOK DMKKNI DMKNET DMKRGB DMKSFL DMKSTR DMKYCP DMKVCP DMKVCP	DMKACR DMKCDB DMKCFJ DMKCPJ DMKCPY DMKDEI DMKLOS DMKLOM DMKNLD DMKNLD DMKNLD DMKSA DMKSPM DMKSYC DMKTRX DMKVCS DMKVCS	DMKACS DMKCDM DMKCFV DMKCPZ DMKDGD DMKEXT DMKIOT DMKMCC DMKMCC DMKMCC DMKPST DMKSPR DMKSPR DMKSVD DMKVCT DMKVCT	DMKALG DMKCDS DMKCFY DMKCQP DMKDGF DMKFRE DMKFRE DMKFRC DMKMCD DMKMCD DMKPAG DMKPTR DMKSPS DMKSVA DMKUSP DMKVCU DMKVDR	DMKAPI DMKCFC DMKCKD DMKCQQ DMKDIA DMKFRT DMKIUE DMKMCH DMKPAH DMKPAH DMKPSS DMKSSS DMKSSS DMKSSS DMKVCV DMKVCV DMKVDS	DMKATS DMKCFF DMKCKF DMKCKP DMKCQU DMKDIB DMKGII DMKMPO DMKMPO DMKPGM DMKSST DMKTCS DMKVAT DMKVCW DMKVSD	DMKBLD DMKCFG DMKCKM DMKCPS DMKCQY DMKDID DMKHPS DMKJRL DMKMSG DMKPGS DMKQCN DMKSCH DMKSSU DMKVCX DMKVCX DMKVCX DMKVSG	DMKCAO DMKCFM DMKCKV DMKCPT DMKCSF DMKDIF DMKHVD DMKMSW DMKMSW DMKPMA DMKQCO DMKSSV DMKSSV DMKTHI DMKVBM DMKVFC DMKVFC DMKVSJ	DMKCCH DMKCFO DMKCFU DMKCSO DMKDMQ DMKHVE DMKLOH DMKNEA DMKNEA DMKNEA DMKSEL DMKSEL DMKSEL DMKTMR DMKVCB DMKVDB DMKVFD DMKVFD DMKVFD	DMKCCS DMKCFP DMKCNS DMKCSX DMKDSB DMKIOJ DMKNID DMKNES DMKRGA DMKSND DMKSND DMKSTP DMKVCH DMKVCC DMKVCC DMKVFE DMKWRM
APSTAT2	000054	DMKATS DMKPTS	DMKVFS DMKBLD DMKRPA	DMKVMA DMKCPP DMKSEL	DMKVMC DMKDSP DMKSPM	DMKVRR DMKFPS DMKVAT	DMKMCH DMKVAU	DMKV3G DMKMPO DMKVMA	DMKPGS	DMKPRV	DMKPRW
APSTAT3 APSTAT4	000004 000063	DMKDSP DMKACR DMK I OG	DMKEXT DMKAPI DMKLOK	DMKLOK DMKCCH DMKMCH	DMKCLK DMKMC I	DMKCPO DMKMCT	DMKCPP DMKPRV	DMKCPU DMKVCH	DMKDSP DMKVFR	DMKENT	DMKEXT
APSTAT5 APTRAN	000011 000326	DMKEXT DMKACO DMKCFC DMKCNS DMKCQY DMKGRA DMKHVF DMKLNK DMKLNK DMKRPD	DMKLOK DMKAPT DMKCFD DMKCSC DMKCSC DMKGRC DMKIDU DMKMCC DMKPMA DMKRSP	DMKATS DMKCFF DMKCSO DMKCSO DMKGRF DMKIOF DMKHV DMKPRG DMKRST	DMKBIO DMKCFG DMKCPM DMKDGD DMKGRT DMKIOG DMKMIA DMKPRV DMKSEG	DMKBLD DMKCFH DMKCPP DMKDGF DMKHPS DMK10T DMKMNJ DMKPRW DMKSEP	DMKCCH DMKCFM DMKCPS DMKDRD DMKHPT DMKIUA DMKNLD DMKPTR DMKSFB	DMKCCW DMKCFS DMKCPU DMKDRE DMKHPU DMKIUB DMKNLE DMKQCN DMKSNC	DMKCDB DMKCFU DMKCPW DMKDSP DMKHVC DMKIUC DMKOPE DMKQCO DMKSPK	DMKCDM DMKCFV DMKCPY DMKERM DMKIVD DMKIUE DMKPEI DMKRGC DMKSPL	DMKCDS DMKCKV DMKCQS DMKGIO DMKIVE DMKIUL DMKPER DMKRPA DMKSPT
		DMKSRM DMKTRC DMKVAU DMKVME DMKWRN	DMKSSS DMKTRD DMKVBM DMKVSE DMKXAB	DMKSST DMKTRK DMKVCH DMKVSG	DMKSSV DMKTRP DMKVCN DMKVS1	DMKSVC DMKTRU DMKVCX DMKVSJ	DMKSVD DMKTRX DMKVDR DMKVSP	DMKSWA DMKTTX DMKVER DMKVSQ	DMKTCS DMKUDR DMKV10 DMKVSR	DMKTCT DMKUDU DMKVMC DMKVSX	DMKTMR DMKVAT DMKVMD DMKWRM
APTRLK	000166	DMKACO DMKCFQ DMKCFX DMKCSY DMKHVD DMKIUL DMKOPE DMKSPK DMKTRP DMKVSD	DMKAPS DMKCFR DMKCQH DMKDGD DMKHVE DMKLNK DMKPMA DMKSPL DMKVSE	DMKAPT DMKCFS DMKCQI DMKDIB DMKHVF DMKMCC DMKQCP DMKSPT DMKTRX DMKVSG	DMKAPY DMKCKV DMKCQS DMKDRD DMKIDR DMKMHV DMKREI DMKSSS DMKVSQ	DMKATS DMKCPI DMKCQY DMKDRE DMKIOF DMKMIA DMKRPD DMKSST DMKVCH DMKVSR	DMKBIO DMKCRM DMKCRM DMKGRC DMKIOG DMKNIO DMKRSP DMKSVC DMKVCN DMKWRM	DMKCCW DMKCPO DMKCSC DMKHPS DMKIOH DMKRST DMKTAP DMKVDR DMKVDR DMKVRN	DMKCFC DMKCPP DMKCSR DMKHPT DMKHOT DMKMSG DMKSEP DMKTCS DMKVMC DMKXAB	DMKCFG DMKCPU DMKCST DMKHPU DMKIUE DMKILD DMKSFB DMKTCT DMKVMD	DMKCFH DMKCPW DMKCSW DMKIVC DMKIUJ DMKNLE DMKSNC DMKTRC DMKVME
APUOPER	000867	DMKAGO DMKCCW DMKCFV DMKCPO DMKCQU DMKDIF DMKIOG DMKLOJ DMKNID	DMKVSE DMKCCB DMKCCP DMKCPP DMKCQY DMKDSB DMKIOQ DMKLOK DMKLOK	DMK430 DMKCCM DMKCKF DMKCPS DMKCSO DMKDSP DMKI0S DMKLOM DMKMNJ	DMK43Q DMKCDS DMKCKV DMKCPT DMKCSX DMKENT DMK10T DMKMCC DMKMNT	DMKVSR DMKAPI DMKCFF DMKCLK DMKCPU DMKDEI DMKEXT DMKIUA DMKMCD DMKMON	DMKATS DMKCFG DMKCRS DMKCPV DMKDGD DMKFRE DMKIUE DMKMCH DMKMOO	DMKBLD DMKCFO DMKCFV DMKCPW DMKDGF DMKFRT DMKIUL DMKMCI DMKMPO	DMKCAO DMKCFP DMKCPJ DMKCPY DMKDIA DMKGRI DMKJRL DMKMCT DMKMSG	DMKCCH DMKCFQ DMKCPZ DMKDIB DMKHPS DMKLOG DMKMHC DMKMSW	DMKCCS DMKCFR DMKCPN DMKCQP DMKDID DMKHVE DMKLOH DMKLOH DMKNEA

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LABEL	COUNT	REFEREN	CES								
AQCNW	Г 000299	DMKNES DMKPRV DMKRGA DMKSPL DMKSTR DMKVCP DMKVDE DMKVDE DMKCFD DMKCFJ DMKCPJ DMKCPJ DMKCPJ DMKCPJ DMKCPJ DMKCPJ DMKCPJ DMKCPJ DMKCPJ DMKCPJ DMKCPJ DMKCPJ DMKCPJ DMKCPJ DMKCPJ DMKCPJ	DMKNET DMKRGB DMKSPM DMKSVC DMKTRX DMKVCS DMKVCS DMKVDF DMKCFF DMKCFF DMKCQC DMKCQY DMKDIF DMKNID DMKOPE DMKQCN DMKSVD	DMKNLD DMKPSA DMKRNH DMKSPR DMKVDU DMKVCT DMKVOR DMKVCR DMKVRR DMKCPO DMKCSB DMKCSB DMKCSB DMKCSB DMKMN I DMKPE I DMKQCP DMKCKN	DMKOPE DMKPST DMKRPA DMKSPS DMKVCU DMKVCU DMKVCS DMKVCSD DMKCFM DMKCFM DMKCFF DMKCSF DMKCSF DMKCSF DMKCSF DMKRNJ DMKPEN DMKTRA DMKVDA	DMKPAG DMKPTR DMKRSE DMKSSS DMKUSQ DMKVCV DMKVCV DMKVCT DMKCAO DMKCAO DMKCFP DMKCSO DMKCSO DMKCSO DMKCSO DMKRSC DMKRSC DMKRCD DMKTRC	DMKPAH DMKPTS DMKRSP DMKSST DMKVCS DMKVAT DMKVCW DMKVFC DMKCCH DMKCCFS DMKCPT DMKCQP DMKLNM DMKMSW DMKPER DMKRNH DMKTRD DMKYDD	DMKPGM DMKSAD DMKSAD DMKTCT DMKVAU DMKVCX DMKVFD DMKCFU DMKCFU DMKCFU DMKCFU DMKCFU DMKCAS DMKLOM DMKNEA DMKNEA DMKSPL DMKSPL DMKTRP DMKVDR	DMKPGS DMKQCN DMKSCH DMKSSY DMKVBM DMKVBM DMKVFE DMKVFE DMKCCM DMKCFV DMKCCV DMKCCQR DMKCQU DMKMCC DMKNES DMKPGM DMKSPM DMKTRU DMKVDT	DMKPMA DMKQCO DMKSEL DMKSTK DMKVCB DMKVCB DMKVDB DMKVFR DMKCPW DMKCQS DMKCFW DMKCQS DMKCEI DMKNET DMKNET DMKSPR DMKVER	DMKPRG DMKQVM DMKSND DMKSTP DMKTOD DMKVCH DMKVCH DMKVFS DMKCFC DMKCPB DMKCPY DMKCQT DMKCT DMKNLD DMKNLD DMKNLD DMKSPS DMKUSQ DMKVFC
ARDCBI	_OK 000006	DMKVFD DMKCPW	DMKVFE	DMKVME	DMKWRM	DARYDD	DIANUDD	BHILLER	UNIXYDY	DINKYEN	DINITIO
AR I OCI AR I OCI	000002	DMKCKD DMKACR DMKCQP DMKSSP	DMKQVM DMKACS DMKENT	DMKCCH DMKGRG	DMKCKD DMKIOG	DMKCKM DMKI OQ	DMKCPM DMK10S	DMKCPO DMKMNI	DMKCPP DMKMNT	DMKCPS DMKQVM	DMKCPV DMKSCN
ARIOC	Г 000030	DMKACR DMKCPS	DMKVCH DMKACS DMKCPV	DMKAPI DMKENT	DMKCCH DMK I OG	DMKCFP DMKI OQ	DMKCKD DMK10S	DMKCKM DMKMNT	DMKCPM DMKPMA	DMKCPO DMKSCN	DMKCPP DMKSSP
ARIOCU	J 000034	DMKVCH DMKACR DMKCPS	DMKVRR DMKACS DMKCPV	DMKCAC DMKCPZ	DMKCAO DMKCQP	DMKCCH DMKD I F	DMKCKD DMK I OQ	DMKCKM DMK I OS	DMKCPM DMKMNI	DMKCPO DMKMNT	DMKCPP DMKNES
AR I ODO AR I ODV	C 000012 / 000060	DMKQVM DMKDID DMKACR DMKCKN DMKGRG DMKQVM DMKVRS	DMKSCN DMKDSP DMKACS DMKCKV DMKLNK DMKSCN	DMKSSP DMKLNK DMKALO DMKCPI DMKLOJ DMKSPS	DMKVCH DMKLOJ DMKCCH DMKCPJ DMKMNI DMKSSP	DMKVRS DMKMNL DMKCFS DMKCPP DMKMNL DMKSSS	DMKQVM DMKCFU DMKCPZ DMKMNT DMKSSV	DMKSCN DMKCKD DMKCSO DMKMOO DMKVCH	DMKSSS DMKCKF DMKDID DMKNES DMKVDA	DMKSSV DMKCKH DMKD1F DMKNET DMKVDG	DMKVRS DMKCKM DMKDSP DMKPGU DMKVDT
ARIOPE ARIOPE ARIORE ARIOU ARPCHI	R 000006 J 000007 D 000005	DMKCKF DMKCKF DMKCKF	DMKWRM DMKCSB DMKCSB DMKCSB	DMKCSF DMKCSF DMKCSO	DMKCSO DMKCSO	DMKSPL DMKSPL					
ARIOUC ARPCHI ARPCHI ARSPAC ARSPPI	C 000007 K1 000002 K2 000002 C 000004	DMKCAC DMKSCH DMKSCH DMKACO	DMKCAO	DMKCPZ	DMKQVM	DMKVRS					
ARSPPE		DMKAPS DMKCSV DMKACO	DMKAPU DMKCSX DMKCKR	DMKAPV DMKCSY DMKCQH	DMKCKP DMKSFB DMKCSQ	DMKCKR DMKSPL DMKCSU	DMKCPI DMKSPS DMKCSV	DMKCQH DMKSPT DMKCSX	DMKCSQ DMKUSP DMKSFB	DMKCST DMKWRM DMKSPL	DMKCSU DMKXAB DMKSPS
ARSPRI		DMKSPT DMKCSQ	DMKUSP	DMKWRM	DMKCSQ	DMKCSU	DMKCSV	DMKDRD	DMKDRE	DMKSPK	DMKSPL
ARSPTA ASCHN ASFBAG ASLOGG ASOFF ASON ASVCL	000011	DMKVSD DMKSPS DMKCFG DMKCKF DMKCFS DMKCFS DMKCFS DMKCFI	DMKCFS DMKCPP DMKLOH DMKDEG DMKSAD	<b>DMKCKM</b>	<b>ДМКСРР</b>	DMKCQY					

ASYNELOG	000001	DMKCPJ									
ASYNFLOG ASYSIUCV	000001	DMKCPJ DMKIUA	DMKIUC	DMKIUE	DMKIUJ	DMKIUL	DMKIUN	DMKIUP			
ASYSLC	000028	DMKACO	DMKBLD	DMKCFT	DMKCFU	DMKCFV	DMKCKF	DMKLOC	DMKLOJ	DMKLOM	DMKUDR
ASYSOP	000046	DMKUDU DMKCAC	DMKUSQ DMKCKF	DMKVCT DMKCKT	DMKVCW DMKCLK	DMKCPI	DMKCPJ	DMKCPS	DMKDAD	DMKDAS	DMKDAU
ASTSOF	000040	DMKERM	DMKEXT	DMKGRF	DMKJRL	DMKLOH	DMKMIA	DMKMNT	DMKMSG	DMKMSW	DMKOPE
A (1) (1) (1)	000405	DMKQCN	DMKQVM DMKACR	DMKSAD DMKACS	DMKSPS DMKALO	DMKUSQ DMKAPI	DMKVCH DMKATS	DMKBLD	DMKCCH	DMKCFC	DMKCFF
ASYSVM	000495	DMKACO DMKCFG	DMKACR	DMKCFM	DMKCFO	DMKCFP	DMKCFQ	DMKCFR	DMKCFS	DMKCFU	DMKCKF
		DMKCKV	DMKCNS	DMKCPB	DMKCPI	DMKCPJ	DMKCPM DMKCQP	DMKCPO DMKCQS	DMKCPP DMKCQT	DMKCPS DMKCQY	DMKCPU DMKCRM
		DMKCPV DMKCSC	DMKCPW DMKCSF	DMKCPX DMKCSO	DMKCPY DMKDAD	DMKCPZ DMKDAS	DMKCQP	DMKDIB	DMKDID	DMKDIF	DMKDRD
		DMKDRE	DMKDSB	DMKDSP	DMKERM	DMKEXT	DMKFRE	DMKGRA	DMKGRC	DMKGRF	DMKGRT
		DMKHPS DMK10G	DMKHPT DMK10Q	DMKHPU DMKIOS	DMKHVC DMKIOT	DMKHVD DMKIUA	DMKHVE DMKIUJ	DMKHVF DMKIUP	DMKIDR DMKJRL	DMKI DU DMKI NK	DMKIOE DMKLOH
		DMKLOJ	DMKLOK	DMKMCC	DMKMCH	DMKMCT	DMKMHC	DMKMIA	DMKMID	DMKLNK DMKMNI	DMKMNJ
		DMKMNL DMKPGM	DMKMON DMKPGS	DMKMOO DMKPGT	DMKMSG DMKPGU	DMKNES DMKPMA	DMKNET DMKPST	DMKNLD DMKPTR	DMKNLE DMKPTS	DMKOPE DMKPTT	DMKPAG DMKQVM
		DMKRGA	DMKRGB	DMKRGC	DMKRNH	DMKRSP	DMKRST DMKSPR	DMKSCH	DMKSCN	DMKSEG	DMKSEL
		DMKSEP	DMKSNC	DMKSPK DMKSTA	DMKSPL DMKSTP	DMKSPM DMKSTR	DMKSPR DMKSVC	DMKSPS DMKSWA	DMKSPT DMKSWM	DMKSRM DMKTCS	DMKSSS DMKTCT
		DMKSST DMKTH I	DMKSSU DMKTOD	DMKTRP	DMKTRQ	DMKTRT	DMKTRU	DMKTTX	DMKUDR	DMKUDU	DMKUSQ
		DMKVAT	DMKVBM	DMKVCH	DMKVCW	DMKVCX	DMKVDA	DMKVDC	DMKVDD	DMKVDE	DMKVDR
		DMKVFC DMKVSP	DMKVFD DMKVSQ	DMKVFE DMKVSR	DMKVFR DMKVSX	DMKVFS DMKWRM	DMKVMA DMKWRN	DMKVMC DMKXAB	DMKVME	DMKVRR	DMKVSD
ATMRSN	000007	DMKCPP	DMKMOO	DMKSCH	DMKTMR						
ATTN	000089	DMKCFC DMKGRF	DMKCFM DMKHPU	DMKCKH DMKIOS	DMKCNS DMKIOT	DMKCPB DMKNLD	DMKDDR DMKNLE	DMKD I R DMKRGA	DMKDMP DMKRGB	DMKDSP DMKRGC	DMKFMT DMKRNH
		DMKRSE	DMKRSP	DMKSSP	DMKSSS	DMKVČA	DMKVCB	DMKVCN	DMKVCP	DMKVIO	DMKVMI
ATTNULT	000002	DMKVSI DMKPET									
ATTNHIT	000006	DMKDMQ									
ATTROPHI	800000	DMKBOX									
ATTRSKIP ATTR457 ATTR7	000002	DMKBOX DMKBOX									
ATTR7	000006	DMKBOX				She was a	DM/AM	DWKODE			
AUTGO	000013 000003	DMKENT DMKCKH	DMKMCC	DMKMCD	DMKMID	DMKMN I	DMKMNJ	DMKOPE			
AUTOIPL	000005	DMKMCD	DMKMIA	DMKMNJ							
AVMREAL	000160	DMKBLD DMKCFV	DMKCCH DMKCKD	DMKCCT DMKCKH	DMKCDS DMKCKM	DMKCFD DMKCKP	DMKCFF DMKCPJ	DMKCFG DMKCPO	DMKCFP DMKCPP	DMKCFQ DMKCPU	DMKCFS DMKCPY
		DMKCQP	DMKDMP	DMKDMQ	DMKDSP	DMKEXT	DMKFPS	DMKHVD	DMKHVE	DMKIOQ	DMKIOT
		DMKLOH DMKPGS	DMKLOJ DMKPMA	DMKMCC DMKPRG	DMKMCH DMKPRV	DMKMCT DMKPRW	DMKMHC DMKPTR	DMKMHV DMKPTS	DMKMN I DMKQVM	DMKMPO DMKRPA	DMKOPR DMKSAV
		DMKSCH	DMKSPM	DMKTRA	DMKUNT	DMKVIO	DMKVRR	DMKVRS	DMKVSI	DMKVSJ	DMKVSR
BADCCW	000001	DMKCCD									
BADHEDNO BALRSAVE		DMKCCD DMKAPI	DMKCFM	DMKCKD	<b>DMKCKH</b>	DMKCNT	DMKCPI	DMKCPV	DMKCSO	DMKCVT	DMKDDR
		DMKDGD	DMKDIA	DMKDIB	DMKDMP	DMKFRE	DMKFRT	DMKGRC	DMKGRH	DMKGRT	DMKIOC
		DMKIOJ DMKRET	DMKIUE DMKRGC	DMKIUS DMKRGD	DMKPGT DMKRGE	DMKPGU DMKRNH	DMKPTR DMKSCH	DMKPTT DMKSCN	DMKQCO DMKSTA	DMKQCQ DMKUNT	DMKQVM DMKVAT
B. 1 B 11/-		DMKVCA	DMKVFR	DMKVIO	DMKVMA	DMKVRR	DMKVRS	DMKVSC			
BALRSAV2 BALRO	000006 000035	DMKAP I DMKDGD	DMKCPI DMKGRT	DMKSCH DMKIDU	DMKIOS	DMKIOT	DMKPGT	DMKPRW	DMKPSA	DMKPTT	DMKSCN
		DMKSEG	DMKVMA								
BALR1	000043	DMKAPI DMKSCN	DMKCKD DMKVDS	DMKCPM	DMKCVT	DMKDIF	DMKIDU	DMKMNT	DMKPGŤ	DMKPGU	DMKPSA
		DINGUN	DRINUS								

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REFERENCES

LABEL	COUNT	REFERENCE	ES							
BALR10	000001	DMKGRC								
BALR11	000004	DMKQCO	DMKSCH							
BALR12	000001	DMKVAT								
BALR13	000001	DMKVAT								
BALR14	000010	DMKDGD	DMKTMR	DMKUNT	DMKVAT	DMKVCA				
BALR15	000017	DMKIUE	DMKPGT	DMKPSA	DMKPTR	DMKVCA	DMKVFR			
BALR2	000041	DMKCVT	DMKDMP	DMKDSP	DMKIOT	DMKPSA	DMKPTR	DMKSCN	DMKTMR	DMKVCA
URENE	000041	DMKVMA	Drintorni	DINICOU	Difference	DINKI OA	Dinki in	Drikoon	Dritteritt	DININU
BALR3	000004	DMKCNT	DMKMNT	DMKVCA						
BALR4	000010	DMKMNT	DMKQCQ	DMKVAT	DMKVAU					
BALR5	000004	DMKQCQ	DMKVAT	DIMARA	DINITIO					
BALR6	000005	DMKCNT	DMKGRT	DMKRGE						
BALR7	000002	DMKPSA	ormont	DiminoL						
BALR8	000007	DMKPGT	DMKSCN	DMKVSC						
BALR9	000006	DMKCNS	DMKDSP	DMKVCA						
BCTWAIT	000012	DMKACR	DMKACS	DMKCKD	DMKCPI	DMKCPM	DMKMNT	DMKOPR		
BFFCCPD	000013	DMKCKF	DMKTRT	DMKTRU	51		5	2		
		DMKTRT		5						
BFFENTRY	000012	DMKCKF	DMKTRT	DMKTRU						
BFFFSIZE		DMKCKF	DMKTRT	DMKTRU						
BFFPEND	000004	DMKTRT								
BFFREAL	000014	DMKCKF	DMKTRT	DMKTRU						
BFFSTAT	000014	DMKTRT								
BFFSTOP	000005	DMKTRT								
BFFVIRT	000003	DMKTRT	DMKTRU							
BIOBIRS	000007	DMKBIO	DMKCFQ							
BIOBKSZ	000005	DMKBIO								
	000003	DMKBIO								
	000002	DMKBIO								
BIOBLOK	000014	DMKBIO	DMKCFP	DMKCFQ						
BIODEVDA	000003	DMKBIO								
BIODEVDS		DMKBIO								
BIOFBA	000003	DMKBIO								
BIOFLAG2		DMKBIO								
BIONEXT	000009	DMKBIO	DMKCFP	DMKCFQ						
BIOOFFCP	000003	DMKBIO								
BIOOFFST	000004	DMKBIO								
BIOPARMA		DMKBIO								
BIOPARML		DMKBIO								
BIOPARMU BIOPATH	000001	DMKBIO DMKBIO								
BIORC	000002	DMKBIO								
BIOREAD	000002	DMKBIO								
BIORESET		DMKBIO	DMKCFP	DMKCFQ						
BIORPS	000002	DMKBIO	Drinor	Dimons						
BIOSECDS		DMKBIO								
BIOSIZE	000003	DMKBIO								
	000013	DMKBIO	DMKCFP	DMKCFQ						
BIOSVRD	000003	DMKBIO	511110111	5						
BIOVBLKS	000001	DMKBIO								
BIOVDEV	000001	DMKBIO								
BIOVDEVA		DMKBIO	DMKCFQ							
B103344K	000002	DMKBIO								
B10512BL	000005	DMKBIO								
BIRBIOBL	000002	DMKBIO								
	000004	DMKBIO								
BIRCCWS	000003	DMKBIO								
BIRCMSRD	000002	DMKBIO								

DMKVDS

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n System Logic and Decklem Determination Guide-	BIRCMSWR BIREXEND BIREXIND BIREXOFF BIREXTLI BIREXWR BIRIDAL BIRIDAW1 BIRIDAW2 BIRIDAW3 BIRLCBYT BIRLCFF BIRLCFF BIRLCCFF BIRLCCFF BIRLCWR BIRLCCLI BIRLCWR BIRLOCLI BIRLOCLI BIRLOCLI BIRPAGES BIRPAGE2	000004 00001 000003 000001 000001 000002 000002 000002 000001 000002 000001 000003 000001 000001 000001 000001 000001 000002 000002 000002 000002	DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO		
	BIRPARML BIRRWCNT BIRRWIDA BIRRWOP BIRRWRD BIRSECAR BIRSECT BIRSIZE BIRSKCYL BIRSKDA	000001 000001 000001 000001 000001 000004 000002 000003 000003 000003	DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO		
1 V20-0807-7 @ Convright IRM Corn	BIRSKDAT BIRSKOP BIRSKTRK BIRSRCYL BIRSRDAT BIRSROP BIRSRREC BIRSRTRK BIRSTCAD BIRTRGCL BIRWCNT BIRWIDA BIRWD	000001 000002 000001 000001 000001 000001 000002 000001 000002 000002 000003 000001 000002 000002	DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO DMKBIO		
IRM Corn 1982 1987	BLANK BLANKLEN BLANKS	0000228 0000228 000002 000337	DMKAPS DMKCQC DMKCSR DMKFMT DMKNEA DMKSND DMKVDA DMKACO DMKACO DMKCFM DMKCPW	DMKAPU DMKCQG DMKCSU DMKCRI DMKPEQ DMKSPL DMKVDB DMKCKF DMKACR DMKCFO DMKCQG	DMKAP DMKCQI DMKCS DMKJRI DMKQCI DMKSPI DMKVD0 DMKAPI DMKCF DMKCQI

APS	DMKAPU	DMKAPX	DMKAPY	DMKCAC	DMKCAO	DMKCKF	DMKCKM	DMKCPN	DMKCPT
CQC	DMKCQG	DMKCQH	DMKCQ I	DMKCQQ	DMKCQR	DMKCQU	DMKCSO	DMKCSP	DMKCSQ
CSR	DMKCSU	DMKCSV	DMKCSW	DMKCSX	DMKCSY	DMKDEI	DMKDGD	DMKDIR	DMKDMQ
ГМТ	DMKGR I	DMKJRL	DMKLOG	DMKLOJ	DMKLOM	DMKMCD	DMKMCH	DMKMCT	DMKMSG
<b>NEA</b>	DMKPEQ	DMKQCN	DMKQCO	DMKRGB	DMKRGD	DMKRSP	DMKRST	DMKSAD	DMKSEP
SND	DMKSPL	DMKSPR	DMKSPS	DMKSSP	DMKTOD	DMKTRM	DMKTRR	DMKURS	DMKUSQ
/DA	DMKVDB	DMKVDC	DMKVDD	DMKVDF	DMKVDT	DMKVMD			
VC0	DMKCKF								
AC0	DMKACR	DMKAPU	DMKAPX	DMKCAC	DMKCAO	DMKCCW	DMKCDS	DMKCFC	DMKCFD
CFM	DMKCFO	DMKCFS	DMKCFU	DMKCFV	DMKCFY	DMKCKF	DMKCNS	DMKCPB	DMKCPU
CPW	DMKCQG	DMKCQH	DMKCQI	DMKCQP	DMKCQQ	DMKCQR	DMKCQT	DMKCQY	DMKCSB

LABEL	COUNT	REFERENC	CES								
BLKLEN BLKMPX BLKSIZE	000008 000003 000001	DMKCSO DMKDDR DMKHVC DMKLOH DMKMOO DMKPEQ DMKSPK DMKTEE DMKUSQ DMKVDF DMKRGD DMKCPI DMKCPI DMKBIO	DMKCSP DMKDEF DMKHVF DMKLOJ DMKMSG DMKQCP DMKSPL DMKVCB DMKVCB DMKVDR DMKVSI	DMKCSQ DMKDEI DMKIDR DMKLOM DMKNEA DMKQVM DMKSPM DMKSPM DMKVCN DMKVDT	DMKCST DMKDIA DMKIOS DMKMCC DMKNES DMKREI DMKSPS DMKTRC DMKVCP DMKVMD	DMKCSU DMKDIB DMKIOT DMKMCD DMKNET DMKRGC DMKSPT DMKTRD DMKVCT DMKVSD	DMKCSV DMKDSB DMKJRL DMKNLD DMKNLD DMKRNH DMKSRM DMKTRP DMKVCU DMKVSQ	DMKCSX DMKEPS DMKLDOOE DMKMIA DMKNLE DMKRPD DMKSSS DMKTRR DMKVDA DMKVSX	DMKDAD DMKERM DMKLNK DMKMNJ DMKOPE DMKSCN DMKSST DMKUDR DMKVDC	DMKDAS DMKEXT DMKLNM DMKMNT DMKOVR DMKSEP DMKSSV DMKVDD DMKVDD	DMKDAU DMKGRG DMKLOG DMKMON DMKPEN DMKSFB DMKTCT DMKURS DMKVDE
BOXBLOK BOXINLNS BOXINWTH BOXLINES BOXLOGO BOXWIDTH BPFRESV	000007 000007 000004 000004	DMKGFM DMKGRT DMKGRT DMKGRT DMKGRT DMKGRT DMKFRE	DMKGRT DMKVCX DMKVCX DMKVCX DMKSEP DMKVCX	DMKSEP DMKVCX	DMKVCX						
BREAKSUP	000007	DMKDMQ									
BSCAUSER BSCBLOK BSCBLOK BSCCOTT BSCCCOTT BSCCCCW1 BSCECCW2 BSCETB BSCETB	000719 000004 000009 000015	DMKLACO DMKCFD DMKCFD DMKCFD DMKCFD DMKCGD DMKGRT DMKGRT DMKFRV DMKRST DMKTRC DMKVAT DMKVAT DMKVAC DMKRGA DMKRGA DMKRGA DMKRGA	DMKAPT DMKCFF DMKCQI DMKCQI DMKDGF DMKHPS DMKIOH DMKPRW DMKSEG DMKSST DMKVAU DMKVAU DMKVAU DMKVAU DMKVRD DMKRGB DMKRGC DMKRGC	DMKATS DMKCFG DMKCQS DMKDRD DMKHPT DMKIOT DMKPTR DMKSEP DMKSSV DMKTRK DMKVBM DMKVME DMKXAB DMKRGA DMKRGC	DMKBIO DMKCFH DMKCQY DMKDRE DMKHPU DMKIUA DMKNLD DMKQCN DMKQCN DMKSVC DMKXCH DMKVCH DMKXAD	DMKCCH DMKCFM DMKCSC DMKDSP DMKHVC DMKIUB DMKNLE DMKQCO DMKSNC DMKSVD DMKTRT DMKVCN DMKVSG	DMKCCW DMKCFS DMKCFO DMKERM DMKHVD DMKIUC DMKOPE DMKRGC DMKSPK DMKSWA DMKTRU DMKVCX DMKVCS I	DMKCDB DMKCFU DMKCFU DMKGIO DMKHVE DMKIUE DMKFEI DMKFPA DMKSPL DMKTCS DMKTCS DMKTRX DMKVDD DMKVSJ	DMKCDM DMKCFV DMKCSV DMKGRA DMKHVF DMKFRD DMKRPD DMKRPS DMKTCT DMKTTX DMKVDR DMKVSP	DMKCDS DMKCKR DMKCSW DMKCSW DMKGRC DMKIDU DMKLNK DMKLNK DMKRSP DMKSPT DMKSPT DMKTMR DMKVER DMKVSQ	DMKCFC DMKCKS DMKCRY DMKGRF DMKIOF DMKMCC DMKRSQ DMKSRM DMKTOD DMKUDU DMKVIO DMKVIO
BSCENQ	000008	DMKRGA									
DOULIAU	0000077	DMKRGA DMKBSC	DMKRGC DMKRGA	DMKRGB	DMKRGC						
BSCFLAG1 BSCFORCE	000053	DMKBSC DMKRGA	DMKNET	DMKRGA	DMKRGB	DMKRGC					
BSCHALT BSCIGN	000005 000005	DMKBSC DMKRGA	DMKNET DMKRGC	DMKRGA	DMKRGB						
BSCINDEX	000004	DMKRGA DMKRGA	DMKRGB DMKRGB	DMKRGC DMKRGC							
BSCLOG BSCOPIED BSCPA1 BSCPCCW1	000003	DMKRGA DMKRGA DMKRGA									
BSCPCCW1 BSCPCCW2 BSCPCCW3	000006	DMKBSC DMKBSC DMKRGC	DMKRGA DMKRGA	DMKRGB DMKRGB							

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LABEL	COUNT	REFERENC	ES								
BSCPCCW4 BSCRCVD BSCREAD	000012 000042	DMKRGA DMKBSC DMKBSC DMKRGA	DMKRGB DMKRGA DMKRGA	DMKRGC DMKRGB DMKRGB	DMKRGC						
BSCREGEN BSCRESP BSCRPTR BSCRROBN	000043 000012	DMKRGA DMKRGA DMKRGB	DMKRGA DMKRGC	DMKRGB	DMKRGC						
BSCRSTRT BSCRV1 BSCSCAN	000005 000007 000004	DMKRGB DMKBSC DMKRGA DMKRGB	DMKRGA DMKRGA DMKRGB DMKRGC	DMKRGC DMKRGC							
BSCSCCW1 BSCSCCW2 BSCSCCW3 BSCSEL	000004	DMKRGA DMKRGA DMKRGB	DMKRGB DMKRGB	DMKRGC							
BSCSEND BSCSENSE BSCSHUT BSCSIZE	000012 000039 000005 000002	DMKRGA DMKRGA DMKBSC DMKRGA	DMKRGC DMKRGC DMKNET DMKRGB	DMKRGE DMKRGB							
BSCSIZE1 BSCSIZE2 BSCSPTR		DMKRGA DMKRGB DMKRGA	DMKRGB DMKRGB	DMKRGC DMKRGC							
BSCTMRQ BSCTSTRQ BSCUCOPY	000006 000006	DMKRGA DMKRGA DMKRGA	DMKRGC DMKRGC								
BSCUP BUFAPL BUFCNT	000025 000003 000089	DMKRGA DMKGRF DMKALG	DMKRGB DMKAPX	DMKRGC DMKAPY	DMKCFM	DMKCFU	DMKCFY	DMKCPT	DMKCST	DMKERM	DMKGRG
BUFFCV	000003	DMKGRT DMKRST DMKCKF	DMKHVE DMKSND DMKCKH	DMKLOH DMKSPC	DMKM I A DMKTRP	DMKOPE DMKUDU	DMKPET DMKVCN	DMKQCO DMKVCR	DMKRE I DMKVCU	DMKRGA DMKVMD	DMKRGC
BUFFDLM BUFFER	000003 000198	DMKCKF DMKALG DMKCFV DMKCSV DMKMSG DMKSPC	DMKCKH DMKAPX DMKCFW DMKCSX DMKOPE DMKTRP	DMKAPY DMKCFY DMKEPS DMKPET DMKUDU	DMKCDM DMKCPT DMKERM DMKQCO DMKVCR	DMKCFC DMKCSB DMKGRF DMKRE1 DMKYCU	DMKCFG DMKCSO DMKGRG DMKRGA DMKVDC	DMKCFM DMKCSP DMKGRT DMKRGC DMKVMD	DMKCFO DMKCSQ DMKHVE DMKRST	DMKCFS DMKCST DMKLOH DMKSCN	DMKCFU DMKCSU DMKMIA DMKSND
BUFFOPLG BUFFSTAT BUFFVER	000003 000002 000005	DMKCKF DMKCKH DMKCKH	DMKWRM	Brittobo	Dintrolt	billitio	ыштро	DINICIPID			
BUFINLTH	000037	DMKAPX DMKAPX DMKHVE	DMKHVE DMKCFM DMKLOH	DMKLOH DMKCFY DMKREI	DMKOPE DMKEPS DMKRGC	DMKREI DMKERM DMKTTX	DMKSND DMKGRF DMKTTY	DMKVCN DMKGRG DMKVCN	DMKVFE DMKGRH DMKVCR	DMKGRT DMKVCU	DMKHVC
BUFNORM BUFNXT	000005 000099	DMKGRF DMKALG DMKCFV DMKHVE DMKSPC	DMKAPX DMKCFY DMKLOH DMKTRP	DMKAPY DMKCPT DMKMIA DMKUDU	DMKCDM DMKCSO DMKMSG DMKVCN	DMKCFC DMKCST DMKOPE DMKVDC	DMKCFG DMKCSU DMKPET DMKVFE	DMKCFM DMKCSV DMKRE I DMKVMD	DMKCFO DMKCSX DMKRST	DMKCFS DMKEPS DMKSCN	DMKCFU DMKGRT DMKSND
BUFSIZE	000086	DMKGPC DMKALG DMKGRT DMKQVM DMKVCP	DMKTRF DMKAPX DMKHPT DMKREI DMKVCR	DMK000 DMKCFM DMKHVC DMKRGA DMKVCU	DMKVCN DMKCNS DMKHVE DMKRGC DMKVCV	DMKCPJ DMKLOG DMKRST	DMKEPS DMKLOH DMKSND	DMKYMD DMKERM DMKMIA DMKTTX	DMKGRF DMKOPE DMKTTY	DMKGRG DMKPET DMKUDU	DMKGR I DMKQCO DMKVCN
BUSOUT BUSY	000011 000112	DMKDAD DMKACS DMKDSP DMKSAD	DMKDDR DMKCKH DMKEXT DMKSAV	DMKRNH DMKCNS DMKFMT DMKSSP	DMKRSE DMKCPM DMKIOQ DMKVCA	DMKCPZ DMKIOS DMKVCN	DMKCQT DMKIOT DMKVIO	DMKDDR DMKMNT DMKVMI	DMKDIR DMKOPE DMKVSI	DMKDMP DMKOPR DMKVSJ	DMKDMQ DMKRNH
BYTBL CACHBLOK CACHCPXP		DMKFRE DMKMNL DMKMNL									

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REFERENCES

**CP** Directories 523 CC 

CACHCU	U1 000003	DMKMNL									
CACHOU	Ú2 000005 T 000005	DMKMNL DMKMNL									
CACHERI	RC 000006	DMKMNL									
CACHEC	NT 000007	DMKMNL									
CACHIC	CW 000010	DMKMNL									
CACHIN	1 000004	DMKMNL									
CACHIN	2 000004 BP 000004	DMKMNL DMKMNL									
	SS 000002	DMKMNL									
CACHKX	000006	DMKMNL									
CACHNT	DF 000001	DMKMNL									
CACHNT	IM 000002 MI 000001	DMKMNL DMKMNL									
CACHPD	P 000003	DMKMNL									
CACHRC	NT 000002	DMKMNL									
CACHRD	VP 000004 T 000008										
CACHRS	D2 000007	DMKMNL									
CACHSI	D2 000007 ZE 000003	DMKMNL									
CACHST	A1 000038	DMKMNL									
CACHST	OD 000002 QP 000004	DMKMNL DMKMNL									
CACHWR	1 000003	DMKMNL			^						
CACHWR	2 000004	DMKMNL									
CACHWS	D1 000007 D2 000004	DMKMNL DMKMNL									
CACTDE	V 000002	DMKRNH									
CACTLI	N 000002	DMKNET	DMKRNH								
CACTLT	R 000002	DMKNES	DMKRNH								
CALLCV CALLER	T 000005 000013	DMKACO DMK I OQ	DMKCKF	DMKTEF							
CARDIN	000020	DMKDDR	DHKTOS	DERTLI							
CAW	000141	DMKACS	DMKCKD	DMKCKH	DMKCKN	DMKCKP	DMKCNS	DMKCPI	DMKCPM	DMKCPZ	DMKDDR
		DMKD I R DMKOPR	DMKDMP DMKSAD	DMKDMQ			DMKIOS	DMKIOT	DMKLDOOE	DMKMNT DMKVSI	DMKOPE DMKVSJ
CC	002329	DMKOPK	DMKBSC	DMKSAV DMKCCD	DMKSSP DMKCCF	DMKTRD DMKCCO	DMKVMI DMKCCS	DMKVRR DMKCCT	DMKVRS DMKCCW	DMKCFQ	DMKCKD
	002029	DMKCKH	DMKCKN	DMKCKP	DMKCNS	DMKCPM	DMKCPZ	DMKCSB	DMKCSC	DMKDAD	DMKDAS
		DMKDAU	DMKDDR	DMKDEX	DMKDGD	DMKDGF	DMKDIB	DMKDIR	DMKDMP	DMKDMQ	DMKDSB
		DMKFMT DMKMN I	DMKGRD DMKMNL	DMKGRE DMKMNT	DMKGRF DMKMON	DMKGRG DMKNLD	DMKGRH DMKNLE	DMKHPS DMKOPR	DMKIOS DMKOVR	DMKLDOOE DMKPAG	DMKMCC DMKPAH
		DMKPIA	DMKPIB	DMKRGA	DMKRGB	DMKRGC	DMKRGD	DMKRNH	DMKRSE	DMKRSF	DMKRSP
		DMKRST	DMKSAD	DMKSAV	DMKSEP	DMKSPK	DMKSPS	DMKSPT	DMKSSP	DMKSTP	DMKTAQ
		DMKTCS DMKUCS	DMKTCT	DMKTEF	DMKTPE	DMKTRK	DMKTTX	DMKTTY	DMKTTZ	DMKUCB	DMKUCC
		DMKVSI	DMKUDR DMKVSP	DMKVCA DMKVSQ	DMKVCN DMKVST	DMKVCV DMKVSX	DMKVDE DMKZTD	DMKVDR	DMKVDT	DMKVMI	DMKVSC
CCC	000092	DMKAGS	DMKBSC	DMKCCH	DMKCNS	DMKCPI	DMKCPM	DMKCPW	DMKCPZ	DMKCQT DMKGRF	DMKDAD DMKHPS
		DMKDAS DMKHPT	DMKDAU	DMKDDR	DMKDMQ	DMKDSB	DMKDSP	DMKEIG	DMKGIO	DMKGRF	DMKHPS
		DMKHPT DMKSAD	DMKHVC DMKSEV	DMKIOE DMKTAP	DMKIOS DMKTAQ	DMKIOT DMKTPE	DMKMNT DMKUNT	DMKMSW DMKVIO	DMKRSE DMKVSJ	DMKRSF	DMKRSP
CCCPUII	D 000003	DMKCCH	DHROLY	DPIKIAF	DURINIAN	UMAITE	DHKUNT	DHKYIU	DINKY 3J		
CCDESM	D 000003	DMKDIF	DMKRNH								
CCDEVT CCHADDI	YP 000001	DMKCCH									
CCHADDI	R 000002 D 000008	DMKCCH DMKCCH									
CCHCAV	000001	DMKCCH									
CCHCHCI	UA 000001	DMKCCH									
CCHCHNI	L 000012	DMKSEV	DMKSIX								

	LABEL	COUNT	REFERENC	ES						
	CCHCLOGL CCHCMDV CCHCNTB CCHCPEX	000010 000005 000002	DMKCCH DMKEIG DMKSEV DMKCCH	DMKSEV DMKSIX	DMKSIX					
	CCHCPU CCHCUA CCHDAV	000003 000002 000010	DMKSEV DMKCCH DMKEIG	DMKSIX DMKSEV	DMKSIX					
	CCHDI CCHDI CCHHIO	000003	DMKEIG	DMKSEV	DMKSIX					
	CCHINTB CCHINTFC	000001 000007	DMKCCH DMKSEV	DMKSIX						
	CCHIOH CCHLOG45 CCHLOG60		DMKCCH DMKCCH DMKSIX							
	CCHL0G80 CCHL0G70 CCHL0G80	000001	DMKSEV	DMKEIG						
	CCHLOG81 CCHRCV	000001 000003	DMKCCH DMKCCH	DMKEIG	DWYOEV	DMIKELY				
	CCHREC CCHSIOB CCHSIZE	000005 000002 000002	DMKCCH DMKCCH DMKCCH	DMKEIG	DMKSEV	DMKSIX				
	CCHSIZE1 CCHSNSB	000001 000001	DMKCCH DMKCCH							
	CCHSTG CCHTIO	000004 000002 000005	DMKSEV DMKCCH DMKEIG	DMKSTX DMKSEV	DMKSIX					
1.9.3	CCHUSV CCMASK CCPADDR	000016	DMKIUC	DMKIUE	DMKIUG	DMKIUJ	DMKIUL	DMKIUN	DMKIUP	DMKIUS
	CCPARM CCPENTRY			DMKSNC						
	CCPMAXID CCPNAME CCPPSIZE	000003	DMKNLD DMKNLD DMKNLD	DMKSNC DMKSNC						
	CCPRESID CCPROGID	000002 000003	DMKNLD DMKCCH							
	CCPRSTAT CCPRSTEP CCPRSTYP	000001	DMKNLD DMKNLD DMKNLD							
	CCPSIZE	000003 000001	DMKNLD DMKNLD	DMKSNC						
	CCPTPEP CCPTYPE CCRECTYP	000001 000002 000002	DMKNLD DMKNLD DMKCCH							
	CCSBUFCK CCSCKDON	000001 000001	DMKCCF DMKCCF							
	CCSHNSEN CCSSENSE CCSW1		DMKCCF DMKCCD DMKCCH	DMKCCO DMKCCO	DMKCCT	DMKCCW				
	CCSW4 CCS1PASS	000008 000004	DMKCCH DMKCCH							
		000005	DMKACR DMKACR DMKACR	DMKACS DMKACS DMKACS	DMKMCH DMKCCH DMKCCH	DMKMCH				
	CCTCLCC CCTCLPC	000002 000002	DMK I UN DMK I UN	DMKIUP DMKIUP	21.1.0011					
	CCTCLPQ CCTCLPR CCTCLPS	000002 000002 000002	DMKIUN DMKIUN DMKIUN	DMKIUP DMKIUP DMKIUP						
				<b></b> .						

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LABEL	COUNT	REFERENC	ES							
CCTFLAG1	000014	DMKDSP	DMKIUA	DMKIUB	DMKIUN	DMKIUP				
CCTFLAG2	000029	DMKIUA	DMKIUB	DMKIUC	DMKIUJ	DMKIUN	DMKIUP	DMKIUS		
CCTFLAG3	000003	DMKIUB DMKDSP	DMKIUN DMKIUA	DMKIUP	DMKTUJ	DMKIUN	DMKIUS			
CCTFLCNT	000005	DMKIUB	DMKIUN	DMKIUP	DARTOO	Dracton	DIANTOO			
CCTMSGCT	000009	DMKIUA	DMKIUJ	DMKIUN	DMKIUS					
CCTMXPDE CCTMXPDS	000007	DMKIUG DMKIUG	DMK I UJ DMK I UJ							
CCTMXPID	000008	DMKIUA	DMKIUJ	DMKIUP						
CCTPDSEG	000018	DMKIUA	DMKIUG	DMKIUJ	DMKIUP	DMKIUS				
CCTPDSLO	000003	DMKIUA	DMKIUG	<b>DM</b> /(1111	DMILLUD					
CCT PNDCT CCT PNDRN	000008	DMKIUB	DMKIUC	DMKIUJ	DMKIUP					
CCTPNDRP	000004	DMKIUB								
CCTPNDSN	000005	DMKIUB	DMKIUS							
CCTPNDSP CCTRCVHD	000003	DMKIUB	DMIZILIO	DMIZUUU	DMI/ FILL	DMIZILLE				
CCTRCVTL	000011	DMKIUA DMKIUA	DMK I UG DMK I UG	DMKIUJ DMKIUL	DMKIUL DMKIUS	DMKIUS				
CCTRPYHD	000015	DMKIUA	DMKIUB	DMKIUG	DMKIUJ	DMKIUL	DMKIUN			
CCTRPYN	000007	DMKDSP	DMKIUA	DMKIUB	DMKIUN	DMKIUP				
CCTRPYP CCTRPYPR	000007	DMKDSP DMKIUA	DMKIUA DMKIUB	DMKIUB DMKIUG	DMKIUN DMKIUL	DMKIUP				
CCTRPYTL	000016	DMKIUA	DMKIUB	DMKIUG	DMKIUL					
CCTSNDHD	000023	DMKIUA	DMKIUB	DMKIUE	DMKIUG	DMKIUJ	DMKIUN	DMKIUS		
CCTSNDN	000006	DMKIUA	DMKIUB	DMKIUN	DMKIUP					
CCTSNDP CCTSNDPR	000006	DMKIUA DMKIUA	DMK I UB DMK I UB	DMKIUN DMKIUE	DMKIUP DMKIUG	DMKIUN	DMKIUS			
CCTSNDTL	000019	DMKIUA	DMKIUB	DMKIUE	DMKIUG	DMKIUN	DMKIUS			
CCWBD2	000001	DMKCCD								
CCWBD4 CCWCLEAR	000002	DMKCCD DMKCCD	DMKCCO DMKCCF	DMKCCO	DMKCCS	DMKCCT				
CCWCTL	000007	DMKCCF	DMKCCS	DMKCCW	DIAKOUS	DMRCCT				
CCWCTLCM	000003	DMKCCD	DMKCCO	DMKCCS						
CCWFIRST	000003	DMKCCW	DMKOOF	DMI/OOO	DMIXOOC					
CCWFORC CCWGEN	000004 000005	DMKCCD DMKCCD	DMKCCF DMKCCF	DMKCCO DMKCCO	DMKCCS DMKCCS	DMKCCT				
CCWIDAL1	000001	DMKCCO	5111001	5111000	brinteee	Brinteer				
CCWIDASB		DMKCCO	DWYOOF	Duirooo	DM//OOO	DWYGOT				
CCWINV CCWMAN2	000005 000003	DMKCCD DMKCCD	DMKCCF DMKCCF	DMKCCO DMKCCS	DMKCCS	DMKCCT				
CCWNOOP	000005	DMKCCD	DMKCCF	DMKCCO	DMKCCS	DMKCCT				
CCWNXT	000002	DMKCCD	DMKCCO							
CCWNX1 CCWNX10	000002	DMKCCS								
CCWNX11	000001 000004	DMKCCS DMKCCD	DMKCCF	DMKCCO	DMKCCT					
CCWNX13	000002	DMKCCD	DMKCCO	Brinkooo	Brindon					
CCWNX14	000001	DMKCCO								
CCWNX18 CCWNX9	000001 000001	DMKCCS DMKCCO								
CCWSRCH3		DMKCCD								
CCWSUBR	000002	DMKCCW								
COWTO	000004	DMKCCD	DMKCCF	DMKCCO	DMKCCT					
CCWUSIDA CCO	000004	DMKCCD DMKCKD	DMKCCF DMKCKH	DMKCCO DMKCKM	DMKCCS DMKCKN	DMKCKP	DMKGRI	DMKIDR	DMKIUC	DMKIUJ
-	000071	DMKSAD	DMKSAV	DMKTOD	Shitterin	Shironi	SHION		5111100	Shiri ou
CC1	000023	DMKCKD	DMKCKH	DMKCKM	DMKCKN	DMKGR I	DMKIDR	DMKIUJ	DMKIUL	DMKIUS
CC2	000052	DMKTOD DMKCKD	DMKCKH	DMKCKM	DMKCKP	DMKGR I	DMKIDR	DMKIUA	DMKIUC	DMKIUJ
002	000072	DHILOKU	DHROKH	DENORE	DEINORF	DEMONT	DERTUR	DRIVINA	DERTUG	DRIVIOU

DMKIUS

DMKSAD

DMKIUL

LABEL	COUNT	REFERENC	ES								
CC3	000048	DMKIUP DMKCKD DMKIUL DMKVSJ	DMKIUS DMKCKH DMKIUP	DMKSAD DMKCKM DMKIUS	DMKSEG DMKCKN DMKSAD	DMKTOD DMKCKP DMKSEP	DMKDSP DMKSPT	DMKGR I DMKSRM	DMK I DR DMKTOD	DMK I UC DMKTRR	DMK I UJ DMKVS I
CD	000200	DMKVSJ DMKCCD DMKDAU DMKISM DMKRSQ DMKTTZ DMKVSX	DMKCCF DMKDDR DMKOPR DMKSAD DMKUNT	DMKCCO DMKDGD DMKPAG DMKSSP DMKVCA	DMKCCS DMKDIB DMKPAH DMKSTP DMKVCN	DMKCCT DMKDIR DMKRGA DMKTAP DMKVCV	DMKCCW DMKFMT DMKRGB DMKTAQ DMKVMI	DMKCNS DMKGRD DMKRGC DMKTPE DMKVSC	DMKCSC DMKGRF DMKRGD DMKTRK DMKVSI	DMKDAD DMKHPS DMKRGE DMKTTX DMKVSP	DMKDAS DMKHPU DMKRSP DMKTTY DMKVSQ
CDC	000078	DMKACS DMKDSP DMKRNH	DMKBSC DMKGRF DMKRSE	DMKCCH DMKHVC DMKRSF	DMKCNS DMKIOE DMKRSP	DMKCQT DMKIOF DMKSAD	DMKDAD DMKIOS DMKTAP	DMKDAS DMKIOT DMKTAQ	DMKDAU DMKMSW DMKTPE	DMKDMQ DMKNLD DMKUNT	DMKDSB DMKNLE DMKV10
CDCONF CDCTLIN CDDED CDDEF CDISPLY CDTIC	000002 000001 000003 000002 000001 000011	DMKVSJ DMKCFG DMKNET DMKLNK DMKDEF DMKNES DMKCCW	DMKPMA DMKPMA DMKPMA	DMKVDA							
CDVADD CE	000004 000128	DMKDEF DMKCFC DMKDMP DMKNLD DMKSAV DMKVSP	DMKLNK DMKCKH DMKDMQ DMKOPE DMKSSP DMKVSX	DMKVDA DMKCKN DMKFMT DMKOPR DMKVCA	DMKCKP DMKGRF DMKPAH DMKVCB	DMKCNS DMKHPS DMKRGA DMKVCN	DMKCPZ DMKHPT DMKRGE DMKV10	DMKDDR DMKHVC DMKRSE DMKVM1	DMKDIB DMKIOH DMKRSP DMKVRS	DMKDID DMKIOS DMKRST DMKVSI	DMKDIR DMKIOT DMKSAD DMKVSJ
CFSTOP CHANID CHC	000009 000006 000021	DMKCPS DMKCPN DMKBSC DMKTAP	DMKMCD DMKIOG DMKCNS DMKTAQ	DMKMIA DMKPRV DMKDAD DMKTPE	DMKMN I DMKGRF DMKUNT		DMKIOS	DMKIOT	DMKRNH	DMKRSE	DMKSAD
CHECKVF CHEKISAM CHG CHGRDV CHGREGS CHGSFB	000002 000004 000004 000004 000002 000002	DMKACO DMKCCD DMKSEL DMKCSB DMKTMR DMKACO	DMKCKF DMKCCO DMKCSO DMKAPV	DMKCCW	DMKCSQ	DMKCSV	DMKCSŴ	DMKCSY	DMKDMP	DMKRSP	DMKSPL
CHGSHQ CHNGMSG CIRCLEC CKCMASK CKPBITS CKPBKSZ CKPBLOK CKPLD CKPLOAD CKPLOAD CKPNAME	000008 000002 000001 000004	DMKVSD DMKCSQ DMKTOD DMKCCT DMKAP1	DMKXAB DMKWRN DMKCP1	DMKPMA							
CKPBITS CKPBKSZ CKPBLOK CKPLD	000003 000001 000004 000003	DMKRNH DMKRNH DMKRNH DMKCKP	DMKWRM								
CKPSIZE	000356 000003 000002 000003	DMKCKD DMKRNH DMKRNH DMKRNH	DMKCKF DMKWRM DMKWRM DMKWRM	DMKCKH	DMKCKM	DMKCKN	DMKCKP	DMKCKW	DMKERP	DMKSAV	
CKPTLIST	000098	DMKCKF DMKCKF DMKPEQ	DMKCPI DMKCKH DMKSBL	DMKRSP DMKCQU DMKTRP	DMKDAD DMKURS	DMKDAS DMKVDE	DMKDAU	DMKDIR	DMKDSB	DMKJRL	DMKPEN
CLASDASD	000331	DMKACO DMKCFR DMKCPV DMKDGD DMK10J DMKPMA	DMKACR DMKCKD DMKCPW DMKDID DMKIOQ DMKPRV	DMKALO DMKCKF DMKCQG DMKDIR DMKIOS DMKSAD	DMKBIO DMKCKM DMKCQP DMKDMP DMKIOT DMKSAV	DMKCCD DMKCKP DMKCQQ DMKDSB DMKLNK DMKSCN	DMKCCO DMKCPM DMKCQR DMKGIO DMKLOJ DMKLOJ	DMKCCS DMKCPN DMKCQT DMKHVE DMKMN I DMKSSP	DMKCCW DMKCPO DMKDDR DMKIOC DMKMNT DMKSSS	DMKCFG DMKCPS DMKDEF DMKIOE DMKMSW DMKSST	DMKCFQ DMKCPT DMKDEI DMKIOF DMKPAG DMKTRD

LABEL	COUNT	REFERENC	ES								
		DMKUDR	DMKVCH DMKVIO	DMKVDA	DMKVDC	DMKVDD	DMKVDE	DMKVDF	DMKVDR	DMKVDS	DMKVDT
CLASFBA	000343	DMKVER DMKACO DMKCFH DMKCKR DMKCQG DMKDRD	DMKV10 DMKACR DMKCFQ DMKCKS DMKCQP DMKDRE DMK10F DMKMON DMKRSP DMKVDF DMKVDF DMKVS1 DMKBLD DMKCP0 DMKC1F	DMKVMT DMKALO DMKCFR DMKCKT DMKCQQ DMKDSB	DMKVRR DMKATS DMKCFS DMKCKV DMKCQR DMKERP	DMKVDD DMKVSC DMKEFU DMKCFU DMKCPN DMKCQT DMKIOJ DMKNLE DMKNDR DMKVDS DMKVDS DMKVDS DMKCFM DMKCQG DMKEPS	DMKVST DMKCCD DMKCKD DMKCPO DMKDDR DMKHVD	DMKVDF DMKVSJ DMKCCO DMKCKF DMKCPP DMKDEF DMKHVE DMKIOS DMKPAG DMKSEG	DMKCCS DMKCKH DMKCPS DMKDID DMKHVF DMKIOT DMKPGT DMKSNC	DMKCCW DMKCKM DMKCPV DMKDIR DMKIDU DMKLNK DMKPGU DMKSPK DMKVDC DMKVMI	DMKCFG DMKCKN DMKCPW DMKDMP DMK10C
		DMKTOE DMKMNT DMKPRV DMKSSP DMKVDE DMKVSC	DMKIOF DMKMON DMKRSP DMKTCS DMKVDF DMKVSI	DMKIOG DMKMSW DMKRSQ DMKTDK DMKVDG DMKVSX	DMKIOH DMKNLD DMKSAV DMKTRD DMKVDR DMKVBM	DMKTOJ DMKNLE DMKSCN DMKUDR DMKVDS DMKVBN	DMKIOQ DMKOPE DMKSCO DMKUNT DMKVDT DMKVDT	DMKTOS DMKPAG DMKSEG DMKVCH DMKVER	DMKTOT DMKPGT DMKSNC DMKVDA DMKVTO		DMKMNI DMKPMA DMKSPS DMKVDD DMKVRR
CLASGRAF	000148	DMKACO DMKCKF DMKDID DMKIOF DMKQCO DMKQCO		DMKCCH DMKCPV DMKDIR DMKIOS DMKQVM DMKVDP	DMKVDC DMKVRR DMKCFS DMKCFS DMKCFS DMKCQR DMKCQR DMKERP DMKNLD DMKNLD DMKNLD DMKVDR DMKCPW DMKCPW DMKCPW DMKCPW DMKCSP DMKVDS DMKVDS DMKVDS DMKCQQ DMKIQJ	DMKCFM DMKCQG DMKEPS DMKLOG DMKTRD DMKTRD	DMKVDE DMKVSI DMKCCD DMKCCD DMKCPO DMKDDR DMKIOQ DMKIOQ DMKIOQ DMKVOPE DMKSCO DMKVDT DMKVDT DMKCFQ DMKCQP DMKCSQ DMKCFQ DMKCFQ DMKCFQ DMKCQT DMKCQT	DMKCFR DMKCQT DMKGRG DMKNEA DMKVCH	DMKCFT DMKDEF DMKHPS DMKOPE DMKVCN	DMKCFY DMKDIA DMKHVD DMKOPR DMKVCR	DMKCKD DMKD I B DMKHVE DMKQCN DMKVCS
CLASSPEC	000106	DMKVDA DMKBLD DMKCPM DMKDIR DMKMNT DMKTRD DMKTRD	DMKTOJ DMKQCQ DMKVDC DMKCCO DMKCCQG DMKHVE DMKNES DMKVSJ DMKCSJ DMKCFO DMKCPO DMKCFO DMKCFY DMKCFY DMKCGC DMKCCQG DMKCDR DMKCDR	DMKVDA DMKVMI DMKCFR DMKCFR DMKCFR DMKCQQ DMKDSB DMKIOG DMKRSQ DMKRSQ DMKKVDG DMKVDG DMKVDG DMKVCH DMKVCP DMKCCS DMKQVM DMKCCS DMKCQP DMKIOH DMKVCH DMKVCH DMKVCH DMKWRM	DMKCCT DMKCQQ DMKIOJ DMKNLD DMKVDA	DMKLFG DMKLOG DMKTRD DMKCCW DMKCQS DMKLOS DMKLE DMKVDC	DMKCFQ DMKCQU DMKIOT DMKQCN DMKVDR	DMKCFR DMKCQY DMKJRL DMKQCO DMKVDS	DMKCFT DMKDEF DMKLOG DMKQCP DMKVER	DMKCKD DMKDIB DMKLOH DMKRNH DMKVI0	DMKCPB DMKDID DMKLOM DMKSCN DMKVMI
CLASTAPE	000175	DMKACS DMKCPN DMKDMQ DMKMNI DMKVDS	DMKCCO DMKCPO DMKDSB DMKMNT DMKVMI	DMKCCS DMKCPS DMKGIO DMKMSW DMKMSW	DMKCCW DMKCPW DMKHVC DMKSAD DMKVSI	DMKCFQ DMKCQG DMK10E DMKSPT	DMKCFR DMKCQP DMKIOF DMKVCH	DMKCFU DMKCQR DMK10J DMKVDC	DMKCKD DMKDDR DMK10S DMKVDD	DMKCPB DMKDID DMKIOT DMKVDE	DMKCPM DMKDMP DMKMCC DMKVDR
CLASTERM	000225	DMKVER DMKACO DMKCFH DMKCKR DMKCKR DMKCQG DMKDRD DMKDRD DMKVDE DMKVDE DMKVSP DMKVSC DMKVSC DMKVSC DMKVSC DMKVSC DMKVSC DMKVDA DMKVSC DMKCPM DMKCPN DMKCPN DMKCPN DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCSS DMKCPS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DM	DMKJRL DMKQCN	DMKWRM DMKCCS DMKCPS DMKCPS DMKMSW DMKVSC DMKCCD DMKCCD DMKCCD DMKCCP DMKDEF DMKLOG DMKLOG DMKVCN DMKWRM	DMKVSI DMKBLD DMKCKF DMKCQQ DMKDIA DMKHVD DMKLOH DMKLOH DMKQCP DMKVDA	DMKCCT DMKCNS DMKCQS DMKDIB DMKHVE DMKLOJ DMKQCQ DMKVDC	DMKCCW DMKCPB DMKCQU DMKDIF DMKIDU DMKLOM DMKQVM DMKVDE	DMKCFC DMKCPJ DMKCQY DMKDIR DMKIOC DMKMNT DMKRGA DMKVDR	DMKCFM DMKCPM DMKCSP DMKDSP DMKIOF DMKNES DMKNES DMKVDS	DMKCFQ DMKCPO DMKCSQ DMKEPS DMKIOJ DMKNET DMKSSP DMKVIO	DMKCFR DMKCPV DMKCSR DMKERM DMKIOS DMKOPE DMKTRD DMKVSC
CLASURI	000121	DMKACO DMKCSB DMKDIR DMKRSP DMKVDR	DMKVCH DMKVSJ DMKCCO DMKCSO DMKDRD DMKSCN DMKVDS	DMKCCW DMKCSP DMKGIO DMKSPL	DMKCFQ DMKCSQ DMKHVE DMKSSP DMKVMI	DMKCKH DMKCSR DMKHVF DMKTRD DMKVSI	DMKCPB DMKCST DMKIOF DMKURS DMKVSJ DMKCKH DMKCSF	DMKCPO DMKCSW DMKIOS DMKVCH DMKVSP	DMKCPW DMKCSX DMKIOT DMKVDA DMKVSR DMKCKV DMKCSP	DMKCQG DMKDEF DMKQVM DMKVDC DMKVST DMKCPB DMKCSQ DMKCSP	DMKCQP DMKDID DMKRSE DMKVDD DMKVSX DMKCPO DMKCSR DMKIOJ
	000179	DMKCCS DMKCPS DMKCST DMKIOS DMKURS DMKVSP DMKKSP DMKCCH DMKCSX DMKRGA DMKTDK DMKCAD		DMKCFQ DMKCQG DMKDID DMKMNT DMKVDA	DMKCFU DMKCQP DMKDIR DMKQVM DMKVDC DMKXAD	DMKVSI DMKCKF DMKCSB DMKDMP DMKRSE DMKVDD	DMKCKH DMKCSF DMKGIO DMKRSP DMKVDR	DMKCKS DMKCSO DMKHVE DMKSAD DMKVDS	DMKCKV DMKCSP DMKIOE DMKSCN DMKVIO	DMKCPB DMKCSQ DMKIOF DMKSSP DMKVSI	DMKCPO DMKCSR DMKIOJ DMKTRD DMKVSJ
	000005 000051	DMKMTA DMKCCH DMKCSX DMKRGA	DMKVST DMKMNI DMKCPW DMKEXT DMKSPL DMKVDH	DMKXAB DMKMON DMKCQT DMKIMG DMKSSS DMKVDS	DMKCSB DMKIOG DMKVFS	DMKCSF DMK10S	DMKCSO DMKLDOOE	DMKCSP DMKMCH	DMKCSQ DMKMCT	DMKCSU DMKNEA	DMKCSV DMKPEQ
CLOCKCMP	000002 000002 000002 000011	DMKTDK DMKDMQ DMKSAD DMKMIA	DMKVDH	DMKMON							

LABEL	COUNT	REFERENC	ES								
CMDBEG	000001	DMKMHV	<b>DW</b> (000	DHILOOLI							
CMDRDC	000005	DMKCCO	DMKCCS	DMKCCW		DMI/UDC	DMKIOT	DMKRNH	DMKRSE	DMKTPE	DMKVCN
CMDREJ	000026	DMKCNS DMKVSP	DMKDAD	DMKDDR	DMKDIB	DMKHPS	DMKTUT	DHKKNN	DHIKKSE	DMKIFE	DHKYCH
	000005	DMKVSP	DMKVSX								
CNTLBTU CNTSDATA		DMKMNL									
CNTSREC	000014	DMKMNL									
CNTSSIZE		DMKMNL									
CNTSTDAT	000001	DMKMNL									
CNTSTOD	000004	DMKMNL									
CNTS2DAT	000011	DMKMNL									
CNTUNTIC	000004	DMKCCW									
CODE	000037	DMKACS	DMKCNS	DMKCPM	DMKCPZ	DMKDSP	DMKEXT	DMKFRE	DMKFRT	DMKIOS	DMKIOT
		DMKIUA	DMKLOK	DMKMCH	DMKMHC	DMKMNT	DMKPMA	DMKPRG	DMKPTS	DMKRNH	DMKSCH
		DMKSVC	DMKSVD	DMKSWA	DMKTED	DMKTEE DMKVMD	DMKVCV	DMKVCX	DMKVSJ		
COLON	000006	DMKCQC	DMKPEI	DMKPET DMKSIX	DMKSAD	DINKYMU					
COMPFES COMPSEL	000009	DMKEIG DMKEIG	DMKSEV DMKSEV	DMKSIX							
COMPSEL	000020 000006	DMKCCH	DMKEIG	DMKSEV							
CONACTV	000036	DMKCFQ	DMKCNS	DMKDSP	DMKRGA	DMKRGB	DMKRGC	DMKRNH	DMKTTX	DMKVCR	DMKVCS
CONADDR	000096	DMKCNS	DMKCNT	DMKGRA	DMKGRE	DMKGRG	DMKGR I	DMKGRT	DMKMON	DMKQCN	DMKQCO
OONADDIN	000000	DMKQCQ	DMKRGA	DMKRGB	DMKRGC	DMKRGE	DMKRNH	DMKTTX	DMKTTY	DMKVCQ	DMKVCR
		DMKQCQ DMKVCS	DMKVCU	DMKVCV							
CONADDR2	000010	DMKQCO	DMKTTY	DMKVCR							
	000002	DMKVCR								0141/01/01	
CONCCW1	000181	DMKCNS	DMKGRD	DMKGRE	DMKGRF	DMKGRG	DMKGRH	DMKGRI	DMKQCO	DMKQVM	DMKRGA
000000	000114	DMKRGB	DMKRGC	DMKRGD DMKGRE	DMKRNH DMKGRF	DMKTTX DMKGRG	DMKTTY DMKGRH	DMKVCV DMKGR I	DMKQVM	DMKRGA	DMKRGB
CONCCW2	000114	DMKCNS DMKRGC	DMKGRD DMKRGD	DMKRNH	DMKTTX	DMKTTY	DMKVCR	DIFINGINI	DHINGAH	DHILLIOA	DHKIGD
CONCCW3	000070	DMKCNS	DMKDIF	DMKGRD	DMKGRF	DMKIOE	DMKNES	DMKNET	DMKRGA	DMKRGB	DMKRGC
00110043	000070	DMKRNH	DMKTTX	DMKTTY	DMKVCR	51111102	Dimitizo	Dimite			
CONCCW4	000033	DMKCNS	DMKGRD	DMKGRE	DMKGRF	DMKGRH	DMKQCO				
CONCMD	000001	DMKRGB									
CONCMD1	000003	DMKRGA	DMKRGB								
CONCNT	000167	DMKCNS	DMKCNT	DMKGRA	DMKGRC	DMKGRD	DMKGRE	DMKGRG	DMKGRH	DMKGRI	DMKGRT
		DMKMON	DMKQCN	DMKQCO	DMKQCQ	DMKRGA	DMKRGB	DMKRGC	DMKRGD	DMKRNH	DMKTTX
CONONTI		DMKTTY	DMKVCQ	DMKVCR	DMKVCS	DMKVCU DMKRGC		DMKTTX	DMKTTY	DMKVCQ	DMKVCR
CONCNTL	000035	DMKCNS	DMKQCO	DMKRGA DMKTTY	DMKRGB DMKVCR	DMKKGC	DINKKIIN		DMKIII	DHRACA	DHAYCA
CONCNT2 CONCNT3	000015 000001	DMKQCO DMKVCR	DMKTTX	DMKIIT	DHIKYCK						
CONCOMND		DMKCNS	DMKGRE	DMKRNH	DMKTTX			•			
CONDATA	000179	DMKCNS	DMKDIF	DMKGRA	DMKGRC	DMKGRD	DMKGRE	DMKGRF	DMKGRG	DMKGRH	DMKGRT
		DMKIOE	DMKNES	DMKQCN	DMKQCO	DMKQCQ	DMKRGA	DMKRGB	DMKRGC	DMKRGD	DMKRNH
		DMKTTX	DMKTTY	DMKTTZ	DMKVCQ	DMKVCR	DMKVCS				
CONDATAL		DMKIUC									
CONDCLR	000001	DMKVCS									
CONDENCL	000004	DMKGRD	DMKQCQ	DMKRGB	DMKVCS						
CONDENT	000030 000004	DMKD I F DMKRNH	DMKIOE	DMKRGA	DMKRNH						
CONDEST	000004	DMKQCN	DMKQCQ	DMKVCR	DMKVCS						
CONDIAG	000002	DMKRGB	Durvana	DIRATION	5111400						
CONDLE1	000010	DMKRGB	DMKRGC	DMKRGD							
CONDLN	000008	DMKGRD	DMKGRH	DMKQCQ	DMKRGB	DMKVCN					
CONDRFMT	000007	DMKGRD	DMKQCQ	DMKRGB	DMKVCN	DMKVCS					
CONDWC	000042	DMKGRD	DMKGRE	DMKGRH	DMKQCN	DMKQCQ	DMKRGB	DMKVCR	DMKVCS		
CONESCP	000021	DMKCNS	DMKRNH	DMKTTX	DMKTTY	DMKVCR					
CONEWA	000007	DMKGRE	DMKRGB	DMKVCS	DMULLOC						
CONEWRT	000009	DMKGRD	DMKGRE	DMKRGB	DMKVCS						

LABEL	COUNT	REFERENC	ES								
CONEXT CONEXTR CONEXTSZ CONFGRPT	000012 000001 000002 000001	DMKIUC DMKRNH DMKIUC DMKCPJ	DMKIUE	DMKIUJ	DMKIUS						
CONFLAG CONFLAGS	000023	DMKCNS DMKCNS DMKVCV	DMKGRE DMKGRD	DMKRGD DMKQCN	DMKRNH DMKQCO	DMKTTX DMKQCQ	DMKTTY DMKRGB	DMKVCV DMKTTX	DMKTTY	DMKVCR	DMKVCS
CONFLAG2 CONFLG2	000006 000040	DMKGRF DMKGRE DMKVCR	DMKTTX DMKGRI DMKVCS	DMKTTY DMKMON	DMKQCN	DMKQCO	DMKQCQ	DMKRGA	DMKRGB	DMKRGD	DMKRGE
CONFMH5 CONFSOP CONFSS	000001 000015 000055	DMKIUC DMKCFM DMKCFM DMKRGB	DMKCFQ DMKCFQ DMKRGC	DMKGRD DMKGRD DMKRGD	DMKGRF DMKGRE DMKVCR	DMKGRG DMKGRF DMKVCS	DMKQCO DMKGRG	DMKRGB DMKQCN	DMKRGC DMKQCO	DMKVCR DMKQCQ	DMKVCS DMKRGA
CONFSYNC CONFTPN CONIDATA CONILUNM CONIMODE	000001 000001 000002 000002	DMKIŬĆ DMKIŬĊ DMKIŬĊ DMKIŬĊ DMKIŬĊ									
CONLABEL CONLED CONLENTH CONLNCNT	000008 000008	DMKRGÅ DMKGR I DMK I UC DMKGRD	DMKRGB DMKQCN DMKIUE DMKGRH	DMKRGC DMKQCQ DMK I UJ DMKQCO	DMKRGD DMKRGB DMKIUS DMKQCQ	DMKVCS DMKRGB					
CONLNRES CONMORE	000003 000024	DMKGRD DMKCNS	DMKQCQ DMKQCO	DMKRGB	DMKTTY	DMKVCS	DMKVCV				
CONMSGBK CONNCB CONNECT	000032 000003	DMKIUC DMKGRE DMKDRD	DMKIUS DMKMON DMKIUC	DMKQCN	DMKQCO	DMKRGA	DMKRGB	DMKRGD	DMKRGE	DMKVCR	DMKVCS
CONNEWL CONOUTPT	000004 000059	DMKQCQ DMKCNS DMKVCQ	DMKGRD DMKVCR	DMKGRE DMKVCS	DMKQCN DMKVCV	DMKQCO	DMKQCQ	DMKRGB	DMKRNH	DMKTTX	DMKTTY
CONPARM	000173	DMKCNS DMKQCN	DMKCNT DMKQCO	DMKGRC DMKQCQ	DMKGRD DMKRGA	DMKGRE DMKRGB	DMKGRF DMKRGC	DMKGRG DMKRNH	DMKGRH DMKTTX	DMKGRI DMKTTY	DMKGRT DMKVCR
CONPARM2 CONPFDEL CONPFWRT	000002	DMKVCS DMKGRC DMKTTX DMKTTX	DMKVCU DMKGRD DMKVCR DMKVCR	DMKGRG	DMKQCN	DMKQCQ	DMKRGB	DMKVCS			
CONPNT	000164	DMKCFM DMKQVM DMKVCW	DMKCFQ DMKRGA DMKVCX	DMKCNS DMKRGB	DMKDSP DMKRGC	DMKGRD DMKRNH	DMKGRE DMKTTX	DMKGRF DMKTTY	DMKGR I DMKVCR	DMKQCO DMKVCS	DMKQCQ DMKVCV
CONPPA1 CONP1LEN CONP2LEN	000004	DMKGRE DMKIUC DMKIUC	DMKRGB	DMKVCS							
CONRD CONRESET CONRESP	000002 000004 000034	DMKQCO DMKCFQ DMKCFQ	DMKVCR DMKRGA DMKCNS	DMKRGC DMKGR I	DMKQCO	DMKQCQ	DMKRGA	DMKRGB	DMKRGC	DMKRNH	DMKVCQ
CONRETN	000049	DMKVCR DMKCFQ DMKTTX	DMKVCS DMKCNS DMKVCQ	DMKVCV DMKGRG DMKVCR	DMKGR I DMKVCS	DMKQCO DMKVCU	DMKQCQ	DMKRGA	DMKRGB	DMKRGC	DMKRNH
CONRMOD CONRTAG CONRTRY CONSBADR CONSFCNT	000002 000003 000012 000003 000004	DMKGRF DMKRNH DMKCNS DMKRGB DMKQCQ		DHRVOR	DHRV00	DIRVOO					
CONSOLE CONSPLT	000003 000020	DMKDEF DMKCNS	DMKQCO	DMKQCQ	DMKRNH	DMKTTY	DMKVCR	DMKVCS	DMKVCV		
CONSRID CONSTAT	000014 000203	DMKRNH DMKCFQ	DMKCNS	DMKDSP	DMKGRD	DMKGRE	DMKGR I	DMKQCN	DMKQCO	DMKQCQ	DMKRGA

LABEL	COUNT	REFERENC	ES								
CONSTX CONSTX1 CONSYN	000005 000006 000001	DMKRGB DMKRGA DMKRGB DMKRGC	DMKRGC DMKRGB DMKRGC	DMKRNH	DMKTTX	DMKTTY	DMKVCQ	DMKVCR	DMKVCS	DMKVCV	
CONSYNC CONSYNXP CONSYN1	000017 000006 000005	DMKCNS DMKRGA DMKRGA	DMKGRD DMKRGB DMKRGB	DMKQCO	DMKRGB	DMKRNH	DMKTTX	DMKTTY	DMKVCS	DMKVCV	
CONSYSR	000041 000003	DMKD I F DMKNET	DMKNES DMKRNH	DMKNET	DMKRNH						
CONTASK	000238	DMKCFM DMKGRG DMKQVM DMKVCQ	DMKCFQ DMKGRH DMKRGA DMKVCR	DMKCNS DMKGR I DMKRGB DMKVCS	DMKCNT DMKGRT DMKRGC DMKVCU	DMKDSP DMKMON DMKRGD DMKVCV	DMKGRA DMKNES DMKRGE DMKVCW	DMKGRC DMKQCN DMKRNH DMKVCX	DMKGRD DMKQCO DMKTTX	DMKGRE DMKQCP DMKTTY	DMKGRF DMKQCQ DMKTTZ
CONTCMD CONTGMXB		DMKRNH DMKGRD	DMKGRE	DMKGRF	DMKGRG	DMKGR I	DMKQCN	DMKQCQ	DMKVCN		
CONTGMXD		DMKQCN DMKACS		DMKVCN DMKCPI	<b>ДМКСРТ</b> ДМКМСН	DMKDDR DMKM I D	DMKDEF DMKOPE	DMKGRC DMKOPR	DMKGRG DMKPTR	DMKGR I DMKRGC	DMKHVE DMKSAD
CONTROL	000019	DMKHVF DMKTAP DMKACO	DMKLDOOE DMKTHI DMKCCH	DMKMCD DMKTPE DMKFRE		DMKVAT DMKVAT	DMKVCB	DMKVDC DMKSEL	DMKVDF	DMKVER	DMKVSI
CONTSIZE	000087	DMKCNS DMKQCO DMKVCU	DMKGRA DMKQCQ	DMKGRC DMKRGA	DMKGRD DMKRGB	DMKGRE DMKRGC	DMKGRF DMKRNH	DMKGRG DMKTTX	DMKGRI DMKTTY	DMKGRT DMKVCQ	DMKQCN DMKVCR
CONTSKSZ	000023	DMKCNS DMKRGC	DMKGRA DMKRNH	DMKGRC DMKTTX	DMKGR I DMKTTY	DMKQCN DMKVCQ	DMKQCO DMKVCR	DMKQCQ DMKVCS	DMKQVM DMKVCV	DMKRGA	DMKRGB
CONUSER	000026	DMKCFQ DMKRNH	DMKCNS DMKTTX	DMKGRC DMKTTY	DMKGR I DMKVCQ	DMKQCN DMKVCR	DMKQCO	DMKQCQ	DMKRGA	DMKRGB	DMKRGC
CONVDEVB CONVFIXL CONVLUNM CONVMARE CONVMODE CONVTOTL CONWCC CONWORK	000001 000001 000001 000001 000001 000003 000007	DMKQCN DMKIUC DMKIUC DMKIUC DMKIUC DMKIUC DMKGRD DMKQCQ	DMKQCO DMKRGB DMKRGD	DMKQCQ							
CONWORK1 CONWORK2 CONWRT	000016 000004	DMKGRC DMKGRC DMKGRE	DMKQCN DMKGRD DMKRGA	DMKQCO DMKQCN DMKVCS	DMKQCQ DMKQCO	DMKQCQ	DMKRGB				
CONWRTRD CONWSF	000013	DMKCNS DMKRGB	DMKQCO DMKRGD	DMKTTX DMKVCS	DMKTTY						
CORBPNT CORCFLCK CORCP	000037	DMKATS DMKATS DMKCPU DMKPTR DMKVMC	DMKPTR DMKBLD DMKDMP DMKPTS DMKVRR	DMKPTS DMKCPY DMKDMQ DMKPTT DMKVSD	DMKPTT DMKHVE DMKGRE DMKSEL DMKWRM	DMKSEL DMKMCH DMKHPT DMKSPM	DMKSTR DMKPGS DMKHPU DMKSTA	DMKSWA DMKPTR DMKMCC DMKVCN	DMKSWM DMKRPA DMKMNI DMKVFC	DMKSEG DMKPMA DMKVFD	DMKVMA DMKPST DMKVFE
CORDISA	000027	DMKCDB DMKVRR	DMKCDM DMKVRS	DMKCDS	DMKCPY	DMKDMP	DMKDMQ	DMKHVD	DMKMCH	DMKPGS	DMKSTA
CORFLAG	000183	DMKATS DMKDMP DMKMNI DMKRPA DMKVFE	DMKBLD DMKDMQ DMKPGM DMKSEG DMKVFS	DMKCCW DMKGRE DMKPGS DMKSEL DMKVMA	DMKCDB DMKHPT DMKPMA DMKSPM DMKVMC	DMKCDM DMKHPU DMKPRW DMKSTA DMKVRR	DMKCDS DMKHVD DMKPSA DMKSTR DMKVRS	DMKCPP DMKHVE DMKPST DMKSWA DMKVSD	DMKCPU DMKMCC DMKPTR DMKVCN DMKVRM	DMKCPY DMKMCH DMKPTS DMKVFC	DMKDGD DMKMHV DMKPTT DMKVFD
CORFLUSH CORFPNT	000020 000084	DMKATS DMKATS DMKPMA DMKSWM	DMKCCW DMKBLD DMKPTR DMKVFD	DMKDGD DMKCPU DMKPTS DMKVFE	DMKPGM DMKCPY DMKPTT	DMKPTS DMKDMP DMKQCO	DMKPTT DMKDMQ DMKRPA	DMKSEL DMKFRT DMKSEL	DMKSWA DMKGRE DMKSTA	DMKVMA DMKHVE DMKSTR	DMKPGS DMKSWA
CORFREE CORIOLCK		DMKATS DMKATS	DMKPGS DMKBLD	DMKPTR DMKCPP	DMKPTT DMKMCH	DMKSTA DMKPGS	DMKSTR DMKPTR	DMKVMA DMKRPA	DMKSEG	DMKSEL	DMKSTA

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REFERENCES

	DMUCTO	DMKVFS							
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CORPGPNT 000066	DMKATS	DMKBLD	DMKCDS	DMKCPP		DMKFRT		DMKPGS	DMKPTR
CORPORAT DUDUOD	DMKATS		DMKRPA	DMKSEG	DMKDGD	DMKSTR	DMKMCH		
					DMKSEL		DMKSWM	DMKUNT	DMKVFS
CORSHARE 000034	DMKATS	DMKCCW	DMKCDS	DMKCPP	DMKCPY	DMKDGD	DMKMHV	DMKPGS	DMKPRW
	DMKPTR	DMKPTS	DMKPTT	DMKSEL					
CORSWAP 000010	DMKPTR	DMKPTS	DMKSEL	DMKSWA					
CORSWPNT 000060	DMKATS	DMKBLD	DMKCCW	DMKCDS	DMKCFU	DMKCPP	DMKDGD	DMKDRD	DMKDRE
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CORTABLE 000172	DMKATS	DMKBLD	DMKCCW	DMKCDB	DMKCDM	DMKCDS	DMKCFU	DMKCPP	DMKCPU
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CORVM 000062	DMKATS	DMKCPP	DMKCPY	DMKDMP	DMKDMQ	DMKHVE	DMKMCH	DMKPGS	DMKPST
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	DMKVSD	DMKWRM							
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CPAPRPND 000006	DMKDSP	DMKEXT	DMKMCT						
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CPCLOSE 000001	DMKDDR	Bringer							
CPCMD 000001	DMKSND								
CPCREG0 000099	DMKACR	DMKAPI	DMKCKD	DMKCLK	DMKCPI	DMKCPP	DMKCPU	DMKDMP	DMKDSP
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CPCREG6 000009	DMKAPI	DMKCFO	DMKCPI	DMKDSP	DHKINX	DHKVAI	DHKVIO	DURATIN	DERVIS
CPCREG8 000046	DMKCPS	DMKDSP	DMKEXT	DMKIOS	DMKIOT	DMKMCC	DMKMCD	DMKMCH	DMKMIA
CFCRE08 000040	DMKMON	DMKMOO	DMKPRG	DMKQVM	DMKSVD	DINKINGG	DINKINGD	DINKINGA	DHKHTA
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CPEX 000009	DMKDSP	DMKVCA	DHKOI 3	DHKOFI	DIACQT	DHKDGF	DHKLON		
CPEXADD 000229	DMKACO	DMKACS	DMKALG	DMKAPS	DMKAPT	DMKATS	DMKCCH	DMKCCS	DMKCCW
GPL/ADD 000229	DMKCFG	DMKCFM	DMKCFO	DMKCFR	DMKCKF	DMKCKV			
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	DMKSWA	DMKSWM	DMKTAP	DMKTHI	DMKTMR	DMKTPE	DMKTRD	DMKTRP	DMKTRT
	DMKTRX	DMKUSQ	DMKVAT	DMKVAU	DMKVCA	DMKVCB	DMKVCP	DMKVCU	DMKVCW
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ODEVDLOK AGASA	DMKVSE	DMKVSG	DMKVSJ	DMKVSP	DMKVSQ	DMKVST	DMKZTD	0.000	DMU/COL:
CPEXBLOK 000507	DMKACO	DMKACS	DMKALG	DMKAPS	DMKAPT	DMKATS	DMKCCH	DMKCCS	DMKCCW
	DMKCFG	DMKCFM	DMKCFO	DMKCFQ	DMKCFR	DMKCKF	DMKCKV	DMKCNS	DMKCPB
	DMKCPJ	DMKCPM	DMKCPN	DMKCPO	DMKCPP	DMKCPS	DMKCPT	DMKCPU	DMKCPV
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DMKPTS DMKVMA

DMKPSA

DMKIDU DMKSEG

DMKCPY DMKHVD DMKPMA

DMKSEG DMKVFC

DMKPTR DMKVRR

DMKTRA

DMKEXT DMKSTA

DMKMN I

DMKCDS DMKCPO DMKDGD DMKHPS DMKIUC DMKMCH DMKMCG DMKPTT DMKSCH DMKSVD DMKTRU

DMKVCX DMKVMC

DMKCDS DMKCPI DMKCPW DMKDIB DMKHPT DMKIUC DMKMCD

LABEL	COUNT	REFERENC	CES								
CPEXBPNT	000020	DMKMCH DMKMSG DMKPTT DMKSCH DMKSTR DMKVCB DMKVCB DMKVSG DMKVSG DMKCPP	DMKMCI DMKPAG DMKQCO DMKSEL DMKSVC DMKTRP DMKVCP DMKVFD DMKVSJ DMKDSP	DMKMCT DMKPAH DMKQCP DMKSND DMKSVD DMKTRT DMKVCU DMKVFE DMKVFE DMKFRE	DMKMHC DMKPGM DMKQVM DMKSPK DMKSWA DMKTRU DMKVCW DMKVFR DMKVFR DMKVSQ DMKPAG	DMKMIA DMKPGS DMKRGA DMKSPM DMKSWM DMKTRX DMKVCX DMKVFS DMKVST DMKPAH	DMKMID DMKPGT DMKRGB DMKSSS DMKTAP DMKUNT DMKVDA DMKVIO DMKVIO DMKWAI DMKSTK	DMKMNI DMKPRG DMKRNH DMKSSU DMKTAQ DMKUSQ DMKVDC DMKVMA DMKZTD	DMKMNL DMKPRV DMKRPA DMKSSV DMKTHI DMKVAT DMKVDE DMKVMC	DMKMON DMKPTR DMKRSP DMKSTK DMKTMR DMKVAU DMKVDR DMKVSC	DMKMPO DMKPTS DMKRST DMKSTP DMKTPE DMKVCA DMKVDS DMKVSE
CPEXDEFR CPEXFPNT	000004	DMKDSP DMKCCW DMKDAU DMKIUA DMKIUA DMKUSQ DMKCCW DMKIUA DMKPTT DMKVSP	DMKSTK DMKCDS DMKDGD DMKIUJ DMKSEL DMKVCA	DMKCFR DMKDSP DMKLOC			DMKCPP DMKGIO DMKPAH DMKSWM DMKVSP DMKCPZ DMKMNL DMKSTK	DMKCPU DMKHPS DMKPGM DMKTAP DMKVST	DMKCPZ DMKIOE DMKPTR DMKTAQ DMKWAI	DMKDAD DMKIOF DMKPTS DMKTPE DMKZTD	DMKDAS DMKIOT DMKPTT DMKUNT
CPEXMISC		DMKCCW DMKIUA DMKPTT DMKVSJ	DMKCDS DMKIUC DMKRPA	DMKVCB DMKCFG DMKIUJ DMKSCH	DMKERCT DMKMCT DMKSVC DMKVIO DMKCFR DMKIUP DMKSEL	DMKFRE DMKPAG DMKSWA DMKVSC DMKVSC DMKCPB DMKIUS DMKSSS	DMKCPZ DMKMNL DMKSTK	DMKDGD DMKPAH DMKSTR	DMKGIO DMKPGM DMKSVC	DMKHVE DMKPTR DMKSWA	DMKIOQ DMKPTS DMKSWM
CPEXPRIO CPEXPROC CPEXREGS	000005 000018 000105	DMKVDSP DMKCPP DMKACO DMKCNS DMKDID DMKMCD DMKQCP DMKQCP DMKVFC	DMKSTK DMKDSP DMKACS DMKCPM DMKDSB DMKMCH DMKQVM DMKTHI DMKVFR	DMKFRE DMKAPT DMKCPO DMKDSP DMKMCI DMKSND DMKVFS DMKVFS DMKCCW	DMKLOC DMKATS DMKCPP DMKEXT DMKMCT DMKSPK DMKTRX DMKVMA	DMKLOK DMKCCH DMKCPV DMKHVE DMKMHC DMKSPM DMKVAT DMKVSE	DMKMCT DMKCCS DMKCPW DMKIOE DMKPRG DMKSSS DMKVAU DMKVSG DMKCFR	DMKSCH DMKCFG DMKCPZ DMKIOF DMKPRV DMKSSU DMKVCP DMKVSJ	DMKSTK DMKCFM DMKDAS DMKIOQ DMKPTR DMKSVC DMKVCU	DMKSVC DMKCKF DMKDGD DMKLOC DMKPTT DMKSVD DMKVCW	DMKCKV DMKDGF DMKLOK DMKQCO DMKSWM DMKVCX
CPEXRO	000141	DMKACO DMKCPW DMKGRG DMKLOJ DMKPGM	DMKVFK DMKALG DMKCPX DMKHPS DMKMCC DMKPGS DMKSTR DMKVCA DMKVCA DMKVCA	DMKVFS DMKCCW DMKCQC DMKHPT DMKMIA DMKPTR DMKSWA DMKVCB DMKZTD DMKGIO	DMKVMA DMKCDS DMKCQT DMKHPU DMKHID DMKPTS DMKTAP DMKVCP	DMKVSE DMKCFQ DMKDAD DMKIUA DMKMNI DMKRGA DMKTPE DMKVDA	DMKVSGR DMKCFR DMKDAS DMKIUC DMKMNL DMKRPA DMKTRP DMKVDC	DMKVSS DMKCNS DMKDAU DMKIUJ DMKMON DMKSCH DMKTRT DMKVDE	DMKCPB DMKDGD DMKIUN DMKMSG DMKSEL DMKTRU DMKVDR	DMKCPN DMKFRE DMKIUP DMKPAG DMKSSS DMKTRX DMKVDS	DMKCPS DMKGIO DMKIUS DMKPAH DMKSSV DMKUSQ DMKVFD
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REFERENCES

CPEXR6 CPEXR7 CPEXR8	000008 000006 000013	DMKERP DMKCDS DMKCFM	DMKIOF DMKIOQ DMKCFQ	DMKIOQ DMKPAG DMKDSP	DMKZTD DMKPTT DMKLOJ	DMKMNL	DMKSND	DMKUSQ	DMK∨DR	DMKVDS	DMKVSP
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CPIEXLOG CPINIT CPINITD CPLOKFL CPMCHLK	000010 000006	DMKCFC DMKCPI DMKAPI DMKEXT	DMKLÖG DMKGRG DMKCFC DMKLOK	DMKLOH DMKCFM	DMKOPE DMKCPJ	DMKCPS	DMKCPT	DMKEXT	DMKIOT		
CPMCHLK CPMCHSE CPMFAWIA CPMICAVL	000002 000011 000011	DMKMCH DMKDSP DMKEXT	DMKMCH DMKLOK	DMKPRV	DMKVFR						
CPMICAVL CPMICON CPPTLBR	000013 000016 000052	DMKAPI DMKAPI DMKATS	DMKCFO DMKCFO DMKBLD DMKRPA	DMKCFV DMKCFY DMKCPP	DMKCFY DMKCPI DMKDSP	DMKCPI DMKCQU DMKFPS	DMKLOJ DMKCQY DMKMCH	DMKDSP DMKMPO	DMKLOJ DMKPGS	DMKPRV DMKPRV	DMKPRW
CPQVMCU CPRSTPND CPRUN	000004 000003	DMKPTS DMKIOT DMKDSP	DMKQVM DMKMPO	DMKSEL	DMKSPM	DMKVAT	DMKVAU	DMKVMA			
CPRUN CPSEGPRT		DMKCFP DMKSVD DMKAP1	DMKDSP DMKTMR DMKCFF	DMKEXT	DMKIOS DMKDSP	DMK I OT DMKPRW	DMKPMA DMKPSA	DMKPRG DMKVAT	DMKPRV DMKVAU	DMKSCH	DMKSTK
CPSHRLK CPSHUT CPSIMLTB	000020 000005 000003	DMKAPI DMKACS DMKPRW	DMKCCW DMKCPS DMKSTA	DMKCPM DMKCPW	DMKCPO DMK10S	DMKCPS	DMKCPU	DMKDGD	DMKDSP	DMKWA I	
CPSPMODE	000094	DMKCCT DMKCPU DMKIOS	DMKCDS DMKCPY DMKIOT	DMKCFD DMKCQY DMKLOJ	DMKCFG DMKDMP DMKMCH	DMKCFP DMKDSP DMKMHC	DMKCFQ DMKEXT DMKMPO	DMKCFV DMKFPS DMKPMA	DMKCKM DMKFRE DMKPRV	DMKCPI DMKHVD DMKPRW	DMKCPS DMKIOQ DMKPSA
CPSTAT	000003	DMKQVM DMKIOS	DMKSAD DMKMOO	DMKSPM	DMKTRA	DMKVAT	DMKVIO	DMKVRR	DMKVSI	DMKVSJ	
CPSTATUS	000061	DMKAP I DMKMPO	DMKCFP DMKPMA	DMKCLK DMKPRG	DMKCPI DMKPRV	DMKDSP DMKQVM	DMKEXT DMKSCH	DMK10S DMKSTK	DMKIOT DMKSVC	DMKMCH DMKSVD	DMKMCT DMKTMR
CPSTAT2	000193	DMKAPI DMKCFV DMKCPY DMKIQQ	DMKCCT DMKCFY DMKCQU DMKIOS	DMKCCW DMKCKM DMKCQY DMKIOT	DMKCDS DMKCPI DMKDGD DMKLOH	DMKCFD DMKCPJ DMKDMP DMKLOJ	DMKCFG DMKCPM DMKDSP DMKMCH	DMKCFO DMKCPO DMKEXT DMKMHC	DMKCFP DMKCPP DMKFPS DMKMPO	DMKCFQ DMKCPS DMKFRE DMKPMA	DMKCFS DMKCPU DMKHVD DMKPRG
CPSTAT3	000033	DMKPRV DMKVIO DMKAPI	DMKPRW DMKVRR DMKCFO	DMKPSA DMKVSI DMKCFS	DMKQVM DMKVSJ DMKCLK	DMKSAD DMKWA I DMKCP I	DMKSPM DMKCQY	DMKSVC DMKDSP	DMKTRA DMKLOH	DMKVAT DMKMOO	DMKVAU DMKMPO
0. 01/11 J			Dimoro	Dimoro	DINOLI	on nor i	or incogr	5.11001	DINLON	Sinnou	or man o

LABEL	COUNT	REFERENC	CES								
										0.44/0 EV	DUVOVN
CPSTAT4	000121	DMKACS	DMKAPI	DMKCCW	DMKCDB	DMKCDM	DMKCFC	DMKCFF	DMKCFH	DMKCFY	DMKCKM
		DMKCPI	DMKCPS	DMKCPU	DMKCPW	DMKCPY	DMKDGD	DMKDMP	DMKDSP	DMKFPS	DMKIOS
		DMKIOT	DMKLOG	DMKLOH	DMKLOJ	DMKMCH	DMKOPE	DMKPRV	DMKPRW	DMKPSA	DMKPTR
		DMKPTS	DMKQVM	DMKSEG	DMKSEL	DMKSPM	DMKSTA	DMKSTR	DMKVAT	DMKVAU	DMKVMA
		DMKVME									
CPSTAT5	080000	DMKACR	DMKACS	DMKCCH	DMKCFS	DMKCFU	DMKCKD	DMKCKF	DMKCKH	DMKCKM	DMKCKN
		DMKCKP	DMKCKV	DMKCPI	DMKCPJ	DMKCPP	DMKCPS	DMKCPZ	DMKCSO	DMKDIF	DMKDMP
		DMKDSP	DMKGRG	DMKIOG	DMKMCT	DMKMN I	DMKMNT	DMKMOO	DMKNES	DMKNET	DMKOPE
		DMKOPR	DMKPGU	DMKPRG	DMKSCN	DMKSPS	DMKTOD	DMKVCH	DMKVDA	DMKVDG	DMKVDT
		DMKVER	DMKWRM	DMKWRN							
CPSTAT6	000005	DMKIOG	DMKIOS	DMKVFC							
CPSUPER	000017	DMKAPI	DMKCLK	DMKCPI	DMKDSP	DMKEXT	DMKIOT	DMKMCT	DMKMPO	DMKPMA	DMKPRG
OT OUT LIV	000011	DMKQVM	DMKSVC		21112 21						
CPSYSLK	000004	DMKDSP	DMKEXT	DMKLOK							
CPTCH	000003	DMKIOG	DMKIOS	Difficient							
CPTERMLK		DMKMCT	5								
CPTIDLE	000008	DMKCLK	DMKDSP	DMKMOO							
CPTIONT	000003	DMKDSP	DIMOUT	brinarioo							
CPTMASK	000002	DMKAPI	DMKPMA								
CPTPAGE	000004	DMKDSP	Difference								
CPTRAPER		DMKCKF	DMKCKP								· · · ·
CPUID	000077	DMKAPI	DMKCCH	DMKCKP	DMKCKV	DMKCPI	DMKCPJ	DMKCPU	DMKCPY	DMKCQY	DMKDMP
GFUID	000077	DMKDMQ	DMKDSP	DMKEXT	DMKHVC	DMKIOE	DMKIOG	DMKIOH	DMKIOJ	DMKLOH	DMKLOK
	t and t	DMKMCH	DMKMHC	DMKMNI	DMKOPR	DMKPRV	DMKPST	DMKRSP	DMKSAD	DMKSCH	DMKSSP
		DMKSTA	DMKSTK	DMKVER	DMKVME	DMKWAI	Drinti OT	Drittitot	DIMORD	Drincoon	Dimoor
CPULOG	000003	DMKCPI	DMKDMP	DHRYLIN	DERVEL	DUNNAN					
CPUMCELL	000003	DMKCPP	DMKCPU	DMKHVD	DMKIOG	DMKPRV					
CPUMODEL	000007	DMKAPI	DMKCPJ	DMKCPS	DMKCPU	DMKIOG	DMKMCH	DMKMNT	DMKOPE	DMKPRV	DMKSEP
CPUSER	000003	DMKMNT	DMKPRV	DMKSEP	Drinor v	DHATOO	Dritteron	Drittini	DIINOIL	Dilicities	DIIIIOLI
CPUTIMER	000003	DMKDMQ	DHKLINY	DHKGLF							
	000002	DMKSAD									
CPUTIMR		DMKCPI	DMKCPM	DMKCPN	DMKCPS	DMKCPW	DMKGRD	DMKGRF	DMKHVD	DMKIOF	DMKIOG
CPUVERSN	000023		DMKMNT	DMKOPR	DMKPRV	DMKSAD	DHKOND	DHKOM	DHATYD	DHILLOI	DHILIOG
0.01/0	000002	DMKMC I DMKDMQ	DERMIN	DPIKOPK	UPIKENY	DINKOAD					
CPVR	000003	DMKAPI	DMKCLK	DMKCPI	DMKDSP	DMKEXT	DMKIOS	DMKIOT	DMKMCH	DMKMCT	DMKMOO
CPWAIT	000023	DMKSCH	DMKSTK	DMKVCA	DMRDSF	DHKENI	DFILTUS	DFILTUT	DIAMON	DITATIOT	Differioo
ODVOTOD	000033		DMKCPY	DMKDMP	DMKPTR	DMKPTS	DMKSEG	DMKSEL	DMKSTA	DMKSTR	DMKVAT
CPXSTOR	000033	DMKCKM	DMKVMA	DHKDHF	DEIKFIN	DMRFIS	DHKGEO	DHROLL	DHKSTA	DEKSTR	DHINYAT
ODVTDANC	000004	DMKVAU	DMKPXA								
CPXTRANS		DMKDSP		DMKPXB DMKCFS	DMKCPI	DMKLOH					
CP370EAV	000013	DMKAPI	DMKCFO		DMKCPI	DMKCPP	DMKCPU	DMKCQY	DMKDSP	DMKFPS	DMKLOH
CP370EON	000032	DMKAP I DMKPRG	DMKCFO DMKPRW	DMKCFS	DMKVAT	DMKVAU	DPIKCFU	DHIKOQT	UMRUSP	DHKFFS	DMRLON
00017	000001	DMKPRG	DMKPKW	DMKPSA	DMKVAI	DHRVAU					
CRBIT	000001										
CRESCND	000001	DMKRNH									
CRESDQ CRESERL	000001	DMKDIF									
CRESERL	000002	DMKRNH	DMUENT	DMUNET	DMKRNH						
CRESIMD	000004	DMKDIF	DMKEXT	DMKNET	UMKKNH						
CRLOG	000006	DMKPMA	DMKSAD								
CRTAIO	000003	DMKGRF	DMKGRG	DWYODO	DMUGDU						
CRTALRM	000010	DMKGRD	DMKGRE	DMKGRG	DMKGRH						
CRTAPL	000003	DMKGRF	DMKGRI								
CRTCARD	000005	DMKGRF	DMKGRG	DWKODO							
CRTDIAG	800000	DMKGRD	DMKGRE	DMKGRG		DMI/OD I	DMI/UDT	DMI/MI	DMI/ON MA		DMI/DOD
CRTEXT	000026	DMKCFQ	DMKCFR	DMKDID	DMKDIF	DMKGR I	DMKHPT	DMKMN I	DMKQVM	DMKRGA	DMKRGB
		DMKVCT	DMKVCV	DMKVDA	DMKVDR	DMKVDS		D41/2 22	DIMATON	D11/2/D 4	011/0/00
CRTEXTSZ	000012	DMKCFQ	DMKCFR	DMKDID	DMKGR I	DMKMN I	DMKRGA	DMKRGB	DMKVCV	DMKVDA	DMKVDR
		DMKVDS									
CRTFMT	800000	DMKGRD	DMKGRF	DMKGRG	DMKQVM						

LABEL	COUNT	REFERENC	ES								
CRTFSII CRTFSSA CRTPPA1 CRTSIO CRTUSEWA CRTWNG	000028 000029 000014 000006 000013 000006	DMKCFM DMKCFM DMKGRE DMKGRD DMKGRD DMKGRD	DMKGRD DMKGRD DMKGRF DMKGRH DMKGRE DMKGRE	DMKGRE DMKGRE DMKRGA DMKGRI DMKGRF DMKGRG	DMKGRF DMKGRF DMKRGB DMKGRG	DMKGRG DMKGRG DMKRGC	DMKRGA DMKRGA	DMKRGB DMKRGB	DMKRGC DMKRGC		
CSADDR CSETDSM	000008 000002	DMKAPI DMKDIF	DMKCP I DMKRNH	DMKCPP	DMKMCT	DMKPRV					
CSSFEAT CSW	000009 000521	DMKAPI DMKACS DMKDDR DMKIOS DMKSIX DMKSJ	DMKCPI DMKCCH DMKDGF DMKIOT DMKSSP DMKVSP	DMKCPO DMKCKD DMKDIR DMKLD00E DMKTES	DMKCPU DMKCKH DMKDMP DMKMNT DMKTRA	DMKMCT DMKCKN DMKDMQ DMKOPE DMKTRD	DMKPRV DMKCKP DMKDSP DMKOPR DMKVCN	DMKCNS DMKEIG DMKOVR DMKVIO	DMKCPI DMKEXT DMKSAD DMKVMI	DMKCPM DMKFMT DMKSAV DMKVRR	DMKCPZ DMKGIO DMKSEV DMKVRS
CSWLMEP CSWLNCP CTCADDR CTCLASS	000002 000002 000005 000004	DMKDIF DMKDIB DMKCFC DMKCFC	DMKNES DMKNES DMKCQS								
CTCADDR CTCLASS CTENTRY CTFALIAS CTFCHANG	000006 000010 000001	DMKCFC DMKCFC DMKCFC	DMKCQS DMKCMD	DMKCQS							
CTFLAG CTFLAST CTFSUBCM CTL CTL REGS	000008 000011 000022 000001 000004	DMKCFC DMKCFC DMKCFC DMKLD00E DMKDMQ	DMKCQS DMKCMD	DMKCQS							
CTNAME CTRMLTR CTSIZE CTTRUNC CTTYPE	000016 000003 000005 000002 000006	DMKCFC DMKDIF DMKCFC DMKCFC DMKCFC	DMKCQS DMKNES DMKCQS	DMKRNH							
CUE	000077	DMKACS DMKEXT DMKQVM DMKSAD	DMKCFQ DMKFMT DMKRSE	DMKCKH DMKIOS DMKSAD	DMKCPM DMKIOT DMKSAV	DMKDDR DMKMNT DMKSPM	DMKDID DMKMON DMKSSP	DMKDIR DMKNLD DMKTAP	DMKDMP DMKNLE DMKVIO	DMKDMQ DMKOPE DMKVSI	DMKDSP DMKPAH DMKVSJ
CURNTPSW CURRSAVE	000003	DMKIMG									
C0	000286	DMKACR DMKDSP DMKPRV DMKVFC	DMKAPI DMKEXT DMKPTT DMKVFR	DMKCKD DMKFPS DMKQVM DMKVRS	DMKCKP DMKFRE DMKSAD	DMKCLK DMKIOT DMKSTA	DMKCPI DMKLOK DMKSVD	DMKCPP DMKMCH DMKTMR	DMKCPU DMKMPO DMKTRC	DMKDMP DMKPMA DMKTRD	DMKDMQ DMKPRG DMKTRX
C1	000870	DMKACO DMKCDB DMKCFU DMKCFO DMKDAD DMKFPS DMKHVD DMKIUE DMKNLE DMKSCH DMKSCH DMKSRM DMKTTX DMKVER DMKVSJ	DMKAPI DMKCFV DMKCFV DMKCPP DMKDAS DMKG10 DMKHVE DMKFU DMKFT DMKSCN DMKSSS DMKTOD DMKVFR DMKVFR DMKVSP	DMKAPT DMKCPS DMKCFY DMKCPS DMKDAU DMKGRA DMKHVF DMKLNK DMKLPEI DMKQCN DMKSEG DMKSST DMKKSA DMKVSQ	DMKATS DMKCFC DMKCFU DMKCPU DMKGRC DMKGRC DMKGRC DMKKCC DMKMCC DMKSEL DMKSSV DMKTRC DMKVAT DMKVMC DMKVSX	DMKBIO DMKCFD DMKCFV DMKCPW DMKGRF DMKGRF DMKIOG DMKMCH DMKPGS DMKQVM DMKSEP DMKSTR DMKKTRD DMKVAU DMKVMD DMKWRM	DMKBLD DMKCFF DMKCFW DMKCPY DMKGRT DMKGRT DMKGRT DMKKFD DMKKPMA DMKSFB DMKSFB DMKSFB DMKSFB DMKVBM DMKVME DMKWRN	DMKCCD DMKCFG DMKCNS DMKCRS DMKHPS DMKHPS DMKISM DMKFRG DMKSNC DMKSVD DMKSVCH DMKVCH DMKXAB	DMKCCH DMKCFH DMKCPB DMKCQY DMKDSP DMKHPT DMKIUA DMKMNI DMKFRV DMKSPK DMKSPK DMKSVA DMKVCN DMKVSE	DMKCCS DMKCFM DMKCPI DMKCPI DMKERM DMKHPU DMKIUB DMKMNJ DMKMNJ DMKSPL DMKSPL DMKTCS DMKTCX DMKVCX DMKVSG	DMKCCW DMKCFS DMKCPM DMKCSO DMKEXT DMKHVC DMKNLD DMKNLD DMKSAD DMKSAD DMKSPT DMKTCT DMKTRX DMKVDR DMKVSI
C11 C13	000003 000001	DMKDSP DMKMCH									

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**CP** Directories 535

LABEL	COUNT	REFERENC	ES								
C14	000031		DMKCFH	DMKCKM	DMKCPI	DMKCPJ	DMKDMP	DMKMCH	DMKMC I	DMKPRV	DMKQVM
C15 C2	000017 000125	DMKSAD DMKCFH DMKAPI DMKEXT DMKPMA	DMKCKM DMKCFH DMKFMT DMKSAD	DMKDMP DMKCKD DMKFRE DMKSAV	DMKPMA DMKCKM DMKIOT DMKSSP	DMKPRV DMKCKP DMKLDOOE DMKVRS	DMKQVM DMKCPP DMKMCT	DMKDDR DMKMHC	DMKDMP DMKMNT	DMKDMQ DMKOPE	DMKDSP DMKOPR
C3 C5 C6	000001 000001 000070	DMKPMA DMKQVM DMKQVM DMKAPI DMKIOT DMKCPS DMKCPS DMKCPI DMKCPI DMKCPI	DMKCFO	DMKCKP	DMKCPI	DMKCPS	DMKDMP	DMKDSP	DMKEXT	DMKFPS	DMKFRE
C7 C8	000006 000055	DMKTOT DMKDSP DMKCPS DMKQVM	DMKPMA DMKMCH DMKDSP DMKSVD	DMKPRG DMKPRG DMKEXT	DMKPRV DMKPRV DMKIOT	DMKQVM DMKMCC	DMKMCD	DMKMIA	DMKMNI	DMKMON	DMKPRG
C9 DAMAGRPT DASDCL DATACHK	000002 000004 000010 000020	DMKDSP DMKCPI DMKMCC DMKCNS	DMKCPJ DMKMNI DMKDAD	DMKDMP DMKMNL DMKDAS	DMKPMA DMKMOO DMKDDR	DMKRSE	DMKRSF	DMKUNT	DMKVSP	DMKVSX	
DATAOUT DATE DDRCUA1	000001 000044 000002	DMKCFT DMKMCC DMKCNS DMKPEQ DMKACO DMKVER	DMKCKF	DMKCKM	DMKCVT	DMKMID	DMKSAD	DMKTOD	DMKVME	DIALYON	
DDRCUA2 DDRKEYN DDRREC DDRSIZE	000002 000001 000001 000001	DMKVER									
DE	000202	DMKVER DMKVER DMKACO DMKCSF DMKCSF DMKIOQ DMKIOQ DMKPAH DMKSPT DMKVMI DMKVMI	DMKCFR DMKCSO DMKDMQ DMKIOS DMKRGA DMKSSP DMKVRS	DMKCKH DMKCSR DMKDSB DMKIOT DMKRGB DMKSSU DMKVSI	DMKCKN DMKCSW DMKFMT DMKLDOOE DMKRGE DMKTAP DMKVSJ	DMKCKP DMKCSX DMKGRF DMKMNT DMKRNH DMKUNT DMKVSP	DMKCNS DMKDDR DMKHPS DMKMON DMKRSE DMKVCA DMKVSX	DMKCPB DMKDIB DMKHPT DMKNLD DMKRSP DMKVCB	DMKCPM DMKDID DMKHPU DMKNLE DMKSAD DMKVCN	DMKCPW DMKDIF DMKHVC DMKOPE DMKSAV DMKVDD	DMKCPZ DMKDIR DMKIOH DMKOPR DMKSPL DMKVIO
DEDTEST DEFER	000002 000651	DMKACO DMKCFC DMKCNS DMKCQS DMKGIO DMKHVF	DMKCKF DMKCFD DMKCFD DMKCPB DMKCQY DMKGRA DMKGRA DMKHVF DMKLNK DMKLPS DMKPBD	DMKATS DMKCFF DMKCPI DMKCSC DMKGRC DMKIDU DMKMCC DMKPMA DMKRSP	DMKBIO DMKCFG DMKCPM DMKCSO DMKGRF DMKGRF DMKKHV DMKPRG DMKRST	DMKBLD DMKCFH DMKCPO DMKDGD DMKGRT DMKIOG DMKKIA DMKPRV DMKSFG	DMKCCH DMKCFM DMKCPP DMKDGF DMKHPS DMKIOT DMKKNJ DMKKPRW DMKSFP	DMKCCW DMKCFS DMKCFS DMKDRD DMKHPT DMKIUA DMKNLD DMKSFB DMKSFB	DMKCDB DMKCFU DMKCFU DMKDRE DMKHPU DMKIUB DMKNLE DMKQCN DMKSNC	DMKCDM DMKCFV DMKCFW DMKDSP DMKHVC DMKIUC DMKOPE DMKQCO DMKSPK	DMKCDS DMKCKV DMKCPY DMKERM DMKHVD DMKIUE DMKFEI DMKRGC DMKSPL
DEFINTVL	000002	DMKTUL DMKPER DMKPER DMKSPT DMKTOD DMKVAT DMKVMD DMKWRN DMKMCD DMKBLD DMKBLD DMKCS DMKCCD DMKCCD DMKCCD DMKCCD DMKCCD	DMKKFD DMKSRM DMKTRC DMKVAU DMKVME DMKXAB DMKMNI	DMKSSS DMKTRD DMKVBM DMKVSE DMKXAD	DMKSST DMKTRK DMKVCH DMKVSG	DMKSSV DMKTRP DMKVCN DMKVSI	DMKSVC DMKTRU DMKVCX DMKVSJ	DMKSVD DMKTRX DMKVDR DMKVSP	DMKTCS DMKTTX DMKVER DMKVSQ	DMKTCT DMKUDR DMKVIO DMKVSX	DMKTMR DMKUDU DMKVMC DMKWRM
DEFRSENS DELPAGES DELSEGS DELSFB DENDCC DESTRTCC	000013 000008 000008 000005	DMKCCS DMKBLD DMKBLD DMKCKS DMKCCD	DMKCCW DMKCFP DMKDEG DMKCSQ	DMKDEG DMKUSQ DMKSPK	DMKSTR DMKTRU	DMKUSQ					
DEVCARD DEVCCH DEVICE DEVTABLE	000002 000005 000009	DMKCCD DMKCCH DMKCPB DMKCCW DMKCCD	DMKCKF DMKCPS DMKCCW	DMKLD00E	DMKOVR	DMKTEE	DMKTEF				

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DFRCC DFRCC1 DFRCC3	000002 000003 000002	DMKIOT DMKIOS DMKVIO	DMKIOT	DMKVIO							
DFRET	000059	DMKACO DMKDIB DMKSVD	DMKCFV DMKDIF DMKTHI	DMKCPS DMKDSB DMKVDD	DMKCQS DMKERM	DMKCQT DMKJRL	DMKCQU DMKPGM	DMKCQY DMKQCN	DMKDAD DMKQCP	DMKDAS DMKRNH	DMKDAU DMKSPR
DIAGCNT DIAGCNTL DIAGSNS DISCEOC DISCNCT	000001 000001 000003 000002 000001	DMKCCO DMKCCW DMKCCS DMKRNH DMKRNH									
DISCREC DISPMSG DLE DMKACOCL	000002 000002 000021 000022	DMKACO DMKACO DMKRGD DMKCPV	DMKCKF								
DMKACODS DMKACODV DMKACOFF DMKACON	000004 000010	DMKQCP DMKCPV DMKCPV DMKCPV DMKLOJ	DMKUSQ DMKD I B DMKUSQ	DMKVDR							
DMKACOQU DMKACOSA DMKACOTM	000008 000004 000006	DMKHVD DMKVCT DMKCPV	DMKJRL DMKVCX DMKCQY	DMKUSQ							
DMKACR DMKACRCO DMKACRCT	000006	DMKSYM DMKCPO DMKCCH	DMKMCT DMKMCH	DMKVSI							
DMKACRC3 DMKACR10 DMKACSCV	000001	DMKIOS DMKACS DMKACR	DMKCPO	DMKMCT							
DMKACSRF DMKALGON DMKALOCA DMKALOCP	000004 000001 000002	DMKACR DMKCFC DMKCPZ DMKMNT	DMKOPE								
DMKALODU DMKAPICK DMKAPIPR DMKAPSCN	000005	DMKIDU DMKCPI DMKCPI DMKIUB	DMKCPU DMKCPU								
DMKAPSIL DMKAPSSV DMKAPTEP	000001 000011 000002	DMKTUB DMKIUB DMKAPT DMKAPS	DMKAPU	DMKAPV	DMKAPX	DMKIUB					
DMKAPUSE DMKAPVCL DMKAPWPG	000002	DMKAPS DMKAPS DMKAPT	DMKCSV	DMKCSW	DMKCSY						
DMKAPXMG DMKAPYSD DMKAPZNO DMKATSCF	000002 000018 000012 000014	DMKAPS DMKCPT DMKAPV DMKCDS	DMKCQP DMKCSQ DMKCFD	DMKCQQ DMKCSW DMKIUE	DMKCSF DMKCSX DMKIUL	DMKCSO DMKRSP DMKTRC	DMKSND DMKSPL DMKVAT				
DMKBIOCN DMKBIOIL DMKBIORS	000001 000001 000002	DMKIUB DMKIUB DMKCFQ	31111010	5111102		2	2				
DMKBIOSV DMKBLDEC DMKBLDRL DMKBLDRT DMKBLDVM	000004 000010 000022 000016	DMKIUB DMKCFS DMKCFP DMKCFF DMKALG	DMKLOH DMKDEG DMKCFP DMKCNS	DMKPGS DMKDEG DMKD I B	DMKSTR DMKLOH DMKGRF	DMKUSQ DMKPGS DMKRGA	DMKSEG DMKRNH	DMKSTR DMKVCP	DMKVBM DMKVRR		
DMKBOXMS DMKBOXNS DMKBOXPR DMKBSCER	000003 000001	DMKGRT DMKCFM DMKSEP DMKIOS	DMKVCX DMKGRT	DMKVCX							

REFERENCES

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LABEL	COUNT	REFERENC	ES			
DMKCACOF	000002	DMKCMD				
DMKCACON	000002	DMKCMD				
DMKCACQY	000002	DMKCMD				
DMKCAOWN		DMKCMD				
DMKCCDAS		DMKCCW				
DMKCCDSK		DMKCCW				
DMKCCFBA		DMKCCW				
DMKCCFBD DMKCCHCF		DMKCCW DMKACS	DMKDID	DMKIOG		
DMKCCHEF		DMKDSP	DINKUTU	DERIOG		
DMKCCHIS		DMKIOS	DMKSYM			
DMKCCHMX		DMKIOG	51110111			
DMKCCHNT		DMKIOT	DMKSYM			
DMKCCHRF	000006	DMKDSP	DMKVIO	DMKVSJ		
DMKCCHRT		DMKIOE	DMKSYM			
DMKCCHSZ		DMKIOG	5.41/61/11			
DMKCCH60		DMKIOG	DMKSYM			
DMKCCOCH		DMKCCD DMKCCW	DMKCCS			
DMKCCODE		DMKCCW				
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DMKCCSLK		DMKCCD	DMKCCW			
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DMKCCTCN		DMKCCW				
DMKCCTDL		DMKCCW				
DMKCCTLC		DMKCCW DMKCCW				
DMKCCTRM DMKCCWB1		DMKAPI	DMKCPI			
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DMKCCWB4		DMKAPI	DMKCPI			
DMKCCWB5	000002	DMKAPI	DMKCPI			
DMKCCWB6		DMKAPI	DMKCPI			
DMKCCWB7		DMKAPI	DMKCPI			
DMKCCWB8		DMKAPI	DMKCPI	DMI/CCO	DMI/COS	
DMKCCWCN DMKCCWCW		DMKCCD	DMKCCF	DMKCCO	DMKCCS	
DMKCCWC		DMKAPI	DMKCPI			
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DMKCCWL2		DMKAPI	DMKCPI			
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DMKCCWL4		DMKAPI	DMKCPI			
DMKCCWL5		DMKAPI	DMKCPI	DUVOOO	DWY000	DUVOOT
DMKCCWRT		DMKCCD	DMKCCF	DMKCCO DMKTRK	DMKCCS	DMKCCT
DMKCCWSE		DMKSYM DMKGIO	DMKTRD DMKHVC	DMKTKK	DMKVSI	
DMKCCWO	000002	DMKAPI	DMKCPI	DIAKSTIA	DIMENSI	
DMKCCW1	000002	DMKAPI	DMKCPI			
DMKCDBDC		DMKCFC				
DMKCDBD I	000002	DMKCFC				
DMKCDMDM		DMKCFC				
DMKCDMDU		DMKCFC				
DMKCDSCF DMKCDSTC		DMKCFC DMKCFC				
DMKCFCCC		DMKUDR				
DMKCFCCC		DMKCFM				
Drinter one	000002	Danora				

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LABEL	COUNT	REFERENC	ES								
DMKCFCTB DMKCFDAD DMKCFFSB DMKCFFSB DMKCFGII DMKCFGIP DMKCFGIR DMKCFGISP DMKCFJSQ DMKCFJSQ	000002 000002 000002 000002 000002 000002 000002 000002 000002 000002	DMKCQS DMKCFC DMKCFC DMKHVC DMKLOG DMKCFC DMKCFC DMKCFC DMKCFC DMKCFC									
DMKCFJSL DMKCFMAT		DMKCFC DMKCFJ DMKVCN	DMKCNS DMKVCP	DMKGRD DMKVCU	DMKGRF	DMKGRG	DMKRGB	DMKRGC	DMKRNH	DMKSND	DMKSYM
DMKCFMBK	000073	DMKCFO DMKMCT DMKSVD	DMKCNS DMKMPO DMKSYM	DMKCPP DMKPER	DMKCPS DMKPET DMKTRD	DMKCPU DMKPRG DMKVCN	DMKDSP DMKPTR DMKVCP	DMKGRF DMKRGA DMKVCR	DMKGRG DMKRGC DMKVCU	DMKHVC DMKRNH DMKVMA	DMKMCH DMKSND
DMKCFMEN DMKCFMRU DMKCFMSD DMKCFOSA DMKCFOSA DMKCFOSF DMKCFOSP DMKCFOSP DMKCFOSP	000002 000001 000002 000002 000002 000002 000002 000002 000002	DMKGRG DMKVMA DMKSND DMKCFJ DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD	DMKSYM DMKHVC	DMKTRC DMKOPE	DMKIKU	DMKVCN DMKRGA	DMKVCP	DMKVCK	DMKVCU	UMKYMA	
DMKCFOS3 DMKCFPRR DMKCFQRD DMKCFREP	000030 000018	DMKCMD DMKCFG DMKCFP DMKCFQ	DMKCFO DMKCPB DMKVDR	DMKCFS DMKCPS	DMKCPB DMKDEF	DMKCPO DMKD I B	DMKDEG DMKLNK	DMKMCH DMKNET	DMKMCT DMKVDR	DMKUSQ	
DMKCFSAC DMKCFSAP DMKCFSAS	000002 000002	DMKCMD DMKCMD DMKDEG	DMKLOH								
DMKCFSAC DMKCFSCC DMKCFSCC DMKCFSEM DMKCFSIM DMKCFSIS DMKCFSLS DMKCFSMG DMKCFSMG	000002 000002 000002 000002 000002 000002 000002 000002 000002	DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD	DMKLON								
DMKCFSN DMKCFSPX DMKCFSSN DMKCFSSA DMKCFSSM DMKCFSSVC	000002 000002 000002 000002	DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD									
DMKCFSVS DMKCFSWG DMKCFS37 DMKCFTRM DMKCFUDU DMKCFULO	000002 000002 000002 000002 000002 000002	DMKCMD DMKCMD DMKCMD DMKCFC DMKCMD DMKCMD									
DMKCFUMI DMKCFUMO DMKCFUMW DMKCFUPA	000002 000002	DMKCMD DMKCMD DMKCMD DMKCMD									

LABEL	COUNT	REFERENC	ES	
DMKCFURE DMKCFVSE DMKCFVSE DMKCFVSE DMKCFVAC DMKCFYAC DMKCFYSA DMKCFYSA DMKCFYSA	000002 000002 000002 000003 000002 000002 000002 000002	DMKCMD DMKCMD DMKCMD DMKCFC DMKCFC DMKCMD DMKCMD DMKLOH DMKCMD	DMKDIR	
DMKCFYSN DMKCFYSN DMKCFYSF DMKCKD DMKCKDCF DMKCKDCY	1 000002 9 000006 000001 9 000002	DMKCMD DMKCMD DMKCMD DMKSAV DMKCKM DMKCKP	DMKSPC	
DMKCKDGF DMKCKDSV DMKCKDVA DMKCKF DMKCKFAL	000012	DMKCKN DMKCKH DMKCKH DMKSAV DMKCKM	DMKCKM	DMKCKP
DMKCKFBL DMKCKFCL DMKCKFCM DMKCKFCQ DMKCKFCQ	000002	DMKERP DMKERP DMKCKD DMKERP DMKCKH		
DMKCKFC2 DMKCKFD1 DMKCKFD5 DMKCKFEF DMKCKFE5	2 000004 - 000004 / 000002 > 000002	DMKCKD DMKCKH DMKCKP DMKERP DMKERP		
DMKCKFIL DMKCKFIL DMKCKFI DMKCKFIC DMKCKFLC	000002 000002 000002 000002	DMKCKP DMKCKP DMKERP DMKERP DMKCKH		
DMKCKFL DMKCKFLS DMKCKFMO DMKCKFMO DMKCKFOO	000004 000004 000002 000002	DMKCKP DMKCKP DMKERP DMKERP DMKCKM	DMKCKN	
DMKCKFOV DMKCKFRO DMKCKFRO DMKCKFSF DMKCKFTF DMKCKFTF	/ 000006 000002 000002 000004 000002 000002 000002	DMKCKM DMKCKP DMKCKH DMKCKH DMKCKD DMKCKP DMKCKN	DMKCKN	
DMKCKFTF DMKCKFVN DMKCKFVF DMKCKFVF	1 000002 2 000004 000002	DMKCKM DMKCKD DMKERP		
DMKCKFWA DMKCKFWN DMKCKFWT DMKCKH DMKCKHAC	000002 000002 000001 000001 000008	DMKCKH DMKCKH DMKCKM DMKSAV DMKCKF	DMKCKP	
DMKCKHDO DMKCKHDI DMKCKHIN	000022	DMKCKP DMKCKF DMKCKF	DMKCKP	

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LABEL	COUNT	REFERENC	ES								
DMKCKHIO DMKCKHPR DMKCKHST DMKCKHSZ	000072	DMKCKP DMKCKF DMKCKF DMKCKF	DMKCKW								
DMKCKHWM DMKCKM DMKCKMDV DMKCKMSG DMKCKMSV	000002 000001 000002 000018 000002	DMKCKF DMKSAV DMKCKP DMKCKH DMKCKP	DMKCKN	DMKCKP							
DMKCKN DMKCKNBC DMKCKNBH DMKCKNEC DMKCKNEH DMKCKNIO	000002 000002 000002	DMKSAV DMKCKM DMKCKM DMKCKM DMKCKM DMKCKF	<b>ДМКСКМ</b>	DMKCKW	DMKERP						
DMKCKNND DMKCKNPS DMKCKNPW	000002 000002	DMKCKP DMKSAV DMKCKM DMKCKM	DMKERP	DMKCKW	DMKERF						
DMKCKNPX DMKCKNPY	000002	DMKCKM DMKCKM DMKCKM	DREAL								
DMKCKNPZ DMKCKNRB DMKCKNRD DMKCKNTB	000010	DMKCKF DMKCKF DMKCKD	DMKCKH DMKCKM DMKCKF	DMKCKW DMKCKW	DMKERP						
DMKCKP DMKCKPCS DMKCKPDV	000284 000002	DMKCKD DMKCKH DMKCKH	DMKCKF	DMKCKH	DMKCKM	DMKCKN	DMKCKW	DMKERP	DMKPGU	DMKSAV	DMKSEG
DMKCKPER DMKCKPFL DMKCKPIP DMKCKPLD	000006 000014 000002 000002	DMKCKF DMKCKF DMKCKH DMKSAV	DMKCKH DMKCKH	DMKCKW							
DMKCKPLE DMKCKPLF DMKCKPLH	000001 000001 000001	DMKSAV DMKSAV DMKSAV DMKSAV									
DMKCKPLM DMKCKPLN DMKCKPLW DMKCKPMP DMKCKPNC	000001 000014	DMKSAV DMKSAV DMKCKD DMKSAV	DMKCKM								
DMKCKPND DMKCKPRM DMKCKPRS	000002 000012 000002	DMKSAV DMKSAV DMKCKD DMKSAV	DMKCKH								
DMKCKPR2 DMKCKPSN DMKCKPST DMKCKPT	000002	DMKSAV DMKCKH DMKSAV DMKSAV									
DMKCKPVR DMKCKRSY DMKCKSCV	000002 000002 000006	DMKCKM DMKCPJ DMKIDU	DMKVSE	DMKWRN							
DMKCKSPL		DMKACO DMKRSP DMKXAB DMKCKV	DMKAPV DMKSPK	DMKCSB DMKSPL	DMKCSO DMKSPS	DMKCSQ DMKTRT	DMKCSV DMKTRU	DMKCSW DMKVME	DMKCSY DMKVSD	DMKMIA DMKVST	DMKNLE DMKWRN
DMKCKTMP	000002	DMKCKV DMKWRM DMKCFU DMKSPS	DMKCKV DMKTRU	DMKCSQ DMKVME	DMKCSY DMKVSE	DMKDRD DMKVSG	DMKDRE	DMKMIA	DMKNLE	DMKRST	DMKSPK
DMKCKTSU DMKCKTUU		DMKCKS DMKACO	DMKCSQ DMKCFU	DMKCSY DMKCSW	DMKSPL DMKDRD	DMKVSG DMKDRE	DMKIDU	DMKMIA	DMKNLE	DMKSPL	DMKSPS

542	LABEL	COUNT	REFERENC	ES					
System Logic	DMKCKVWM DMKCKW DMKCKWHT DMKCKWSP DMKCLKAP DMKCLKCC	000001 000002 000002	DMKTRT DMKWRM DMKSAV DMKCKF DMKCKF DMKEXT	DMKTRU	DMKVME				
pgic and Problem Determination Guide-CP	DMKCLKCK DMKCLKSC DMKCMD DMKCMDAT DMKCMDCA DMKCMDDE DMKCMDDG DMKCMDJG	000005 000002 000002 000002 000002 000002 000002 000002	DMKEXT DMKCFI DMKEXT DMKCFC DMKCFC DMKCFC DMKCFC DMKCFC DMKCFC	DMKCPU					
1 Determin	DMKCMD I N DMKCMDNO DMKCMDNR DMKCMDQA DMKCMDQG DMKCMDQP	000002 000002 000004 000004 000004	DMKCFC DMKCFC DMKCFC DMKCFC DMKCFC DMKCFC						
ation Guid	DMKCMDQQ DMKCMDQR DMKCMDQS DMKCMDQV DMKCMDSA DMKCMDSC	000002 000002 000001 000002	DMKCFC DMKCFC DMKCFC DMKCFC DMKCFC DMKCFC						
leCP	DMKCMDSG DMKCMDSO DMKCMDSR DMKCNS DMKCNSEN	000002 000002 000002 000001 000004	DMKCFC DMKCFC DMKCFC DMKSYM DMKCPV	DMKOPE	DMKQVM				
LY20-0897-7	DMKCNSIC DMKCNSIN DMKCNTED DMKCPBEX DMKCPBNR DMKCPBRS	000002 000022 000002 000002 000002	DMKQCO DMKACS DMKCNS DMKCFC DMKCFC DMKCFC	DMK10T DMKGRG	DMKGRT	DMKRGC	DMKRNH	DMKVCR	DMKVCU
	DMKCPBRW DMKCPBRY DMKCPBSR DMKCPE DMKCPE I D	000002 000002 000005	DMKCFC DMKCFC DMKCFC DMKFRE DMKCQY	DMKFRT DMKDMP	DMKLDOOE DMKHVD	DMKPSA DMKMN I	DMKOPE	DMKSEP	DMKSYM
yright II	DMKCPEML DMKCPEND DMKCPEPP DMKCPIBD DMKCPICA	000004 000010 000004 000003	DMKAPI DMKCPI DMKHVD DMKOPE DMKCPJ	DMKCPI DMKCPJ DMKMNI DMKSTA	DMKCPJ DMKDMQ DMKSYM DMKVRR	DMKSYM DMKMN I	DMKSTA	DMKSYM	
© Copyright IBM Corp. 1982, 1987	DMKCPICD DMKCPIFL DMKCPIFT DMKCPIF1	000003 000001 000001 000003	DMKCQY DMKSTA DMKALO DMKCPJ	DMKOPE	DMKSAV				
. 1982, ]	DMKCPILF DMKCPIMS DMKCPINT DMKCPINU	000006 000002 000001	DMKALO DMKALO DMKSAV DMKALO	DMKCPJ DMKSSP	DMKCPZ	DMKMNT			
1987	DMKCPIOL	000003 000002	DMKCPJ DMKALO	DMKOPE DMKMNT	DMKWRM				

DMKVME

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LABEL	COUNT	REFERENCI	ES
DMKCPISH DMKCPISW	000006 000001	DMKALO DMKSTA	DMKMNT
DMKCPITR	000002	DMKOPE	DMKSTA
DMKCPIVP	000002	DMKALO	DMKMNT
DMKCPIWC	000008	DMKCPJ	DMKOPE
DMKCPIWT	000005	DMKMNT	
DMKCPJNT DMKCPJST	000002 000002	DMKCPI DMKCPI	DMKSTA
DMKCPJTA	000001	DMKSTA	DHKJIA
DMKCPMIO	000002	DMKCPU	
DMKCPNVY	000002	DMKCPT	
DMKCPOFF	000002	DMKCPU	
DMKCPPUP	000005	DMKCPO	DMKMCT
DMKCPSH	000002	DMKCFC	
DMKCPSSH	000002 000002	DMKCFC DMKCFC	
DMKCPUVY	000002	DMKCPT	
DMKCPVAA	000002	DMKHVD	
DMKCPVAC	000004	DMKCFC	DMKUDU
DMKCPVAE	000004	DMKCPJ	DMKRNH
DMKCPVDS DMKCPVEN	000002	DMKCFC DMKCFC	
DMKCPWFB	000002 000002	DMKCPN	
DMKCPWUN	000002	DMKCPS	
DMKCPWVF	000002	DMKCPT	
DMKCPX	000001	DMKIOT	5.44 <i>4</i> (5.4
DMKCPXCK DMKCPXZT	000004		DMKVDH DMKZTD
DMKCPYLK	000002 000002	DMKIOT DMKCFC	DMKZTU
DMKCPYUL	000002	DMKCFC	
DMKCPZFD	000002	DMKMNT	
DMKCPZMG	000002	DMKMNT	
DMKCPZPG	000002	DMKCPT	
DMKCPZVR DMKCQCPT	000002 000002	DMKMNT DMKCMD	
DMKCQGID	000002	DMKCMD	
DMKCQGQA	000002	DMKCMD	
DMKCQGQC	000002	DMKCMD	
DMKCQGQD	000002	DMKCMD	
DMKCQGQG DMKCQGQH	000002 000002	DMKCMD DMKCMD	
DMKCQGQL	000002	DMKCMD	
DMKCQGQS	000002	DMKCMD	
DMKCQGQT	000002	DMKCMD	
DMKCQGQU	000002	DMKCMD	
DMKCQHFG DMKCQHF1	000002 000004	DMKCMD DMKCPJ	DMKLOM
DMKCQHFS	000002	DMKCMD	DHKLOH
DMKCQHNG	000004	DMKCMD	
DMKCQHNS	000004	DMKCMD	
DMKCQHRG	000004	DMKCMD	
DMKCQHRS DMKCQHTG	000004 000004	DMKCMD DMKCMD	
DMKCQHTS	000004	DMKCMD	
DMKCQIFR	000002	DMKCQH	
DMKCQPQA	000002	DMKCMD	
DMKCQPQD DMKCQPQG	000002	DMKCMD DMKCMD	
DHROQPQG	000002	UNICOND	

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LABEL DMKCQPQL DMKCQPQS DMKCQPQS DMKCQPQU DMKCQPQU DMKCQQQI DMKCQQQI DMKCQQQQT DMKCQQQQT DMKCQQQQT DMKCQQQQT DMKCQQQQT DMKCQQSMA DMKCQQSMA DMKCQQSMA DMKCQSSNA DMKCQSSNA DMKCQSSNA DMKCQSSNA DMKCQSSNA DMKCQSSNA DMKCQSSNA DMKCQSSCA DMKCQYSA DMKCQYSA DMKCQYSA DMKCQYSA DMKCQYSA DMKCQYSA DMKCQYSA DMKCQYSA DMKCQYSA DMKCQYSA DMKCQYSA DMKCQYSA DMKCQYSA DMKCQYSA DMKCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA DMKCCQYSA	COUNT 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 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000002 000002 000002 000002 000002 000002 000002 000002 000002 000002	REFERENCE DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD 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DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMKCCMD DMK	DMKSPC
DMKCSFBS DMKCSFFL	000002 000002	DMKCFC DMKCFC	

LABEL	COUNT	REFERENC	ES								
DMKCSFRP DMKCSODR DMKCSOST DMKCSOST DMKCSQST DMKCSQCL DMKCSQFR DMKCSQFL DMKCSQFL DMKCSQFL DMKCSQFL DMKCSQFL DMKCSVCG DMKCSVCG DMKCSVOG DMKCSVOS DMKCSVPS DMKCSVPS DMKCSVFS DMKCSYTR DMKCSYTR DMKCVTAB	000002 000002 000002 000014 000002 000002 000002 000002	DMKCFC DMKCFC DMKCFC DMKAPV DMKCFC DMKCFC DMKCFC DMKCFC	DMKCPJ	DMKCSQ	DMKCSW	DMKCSX	DMKRSP	DMKSPL			
DMKCSGT DMKCSRGT DMKCSRFO DMKCSTAG DMKCSUCG DMKCSVOG DMKCSVOS DMKCSVOS DMKCSVPG	000002 000002 000002 000002 000002 000002 000002 000002 000002	DMKCFC DMKCSP DMKCFC DMKCFC DMKCFC DMKCFC DMKCFC DMKCFC DMKCFC									
DMKCSÝPS DMKCSWCH DMKCSXTG DMKCSXTS DMKCSYTR DMKCVT	000002 000002 000002 000002 000002 000002 000002 000001	DMKCFC DMKCSU DMKCFC DMKCFC DMKCSX DMKSYM									
DMKCVTAB	000112	DMKACO DMKDIF DMKJRL DMKPGM DMKSTP	DMKACS DMKDRD DMKLOK DMKPGS DMKTMR	DMKBLD DMKDRE DMKLOM DMKPTS DMKTRQ	DMKCFJ DMKENT DMKMCH DMKQCP DMKVCV	DMKCFQ DMKEXT DMKMN1 DMKRE1 DMKVDA	DMKCFR DMKGRF DMKMNJ DMKRGA DMKVDR	DMKCFU DMKIOE DMKMNL DMKRGB DMKVDS	DMKCPO DMKIOH DMKMON DMKSCH DMKVDT	DMKCQT DMKIUJ DMKMOO DMKSEL DMKVMC DMKCPN	DMKDID DMKIUS DMKNEA DMKSPS DMKVRR
DMKCVTBD	000388	DMKCAC DMKCPT DMKCSU DMKIOE DMKPEN DMKSPR DMKVCU	DMKCDB DMKCQG DMKCSV DMKJRL DMKPEQ DMKSPS DMKVDE	DMKCDM DMKCQH DMKCSX DMKLNM DMKPER DMKSRM DMKVDF	DMKCFG DMKCQI DMKDEF DMKLOM DMKPST DMKTCS DMKVFC	DMKCFV DMKCQP DMKDEG DMKMIA DMKQCP DMKTCT DMKWRM	DMKCKT DMKCQQ DMKDIB DMKMNJ DMKRGC DMKTHI DMKXST	DMKCKV DMKCQR DMKDIF DMKMSG DMKRSE DMKTRT	DMKCPJ DMKCQS DMKERM DMKMSW DMKSAV DMKTTX	DMKCPN DMKCQU DMKGRG DMKOPE DMKSEP DMKURS	DMKCPO DMKCQY DMKHVF DMKPEI DMKSPL DMKUSQ
DMKCVTBH	000984	DMKACO DMKCFD DMKCFY DMKCPU DMKCQS DMKDIB	DMKACR DMKCFG DMKCKF DMKCPV DMKCQT DMKDID	DMKALO DMKCFH DMKCNS DMKCPW DMKCQY DMKDRD	DMKBLD DMKCFO DMKCPB DMKCPY DMKCST DMKDRE	DMKCAC DMKCFP DMKCPI DMKCPZ DMKDAD DMKDSB	DMKCAO DMKCFQ DMKCPN DMKCQC DMKDAS DMKDSP	DMKCCH DMKCFR DMKCPO DMKCQG DMKDAU DMKIOH	DMKCDB DMKCFS DMKCPP DMKCQP DMKDEF DMKJRL	DMKCDM DMKCFT DMKCPS DMKCQQ DMKDE1 DMKLNK	DMKCDS DMKCFU DMKCPT DMKCQR DMKDIA DMKLNM
		DMKLOG DMKNES DMKRSE DMKTCS DMKVDA DMKVFD	DMKLOH DMKNET DMKRSP DMKTCT DMKVDB DMKVFE	DMKLOJ DMKNLD DMKRST DMKTHI DMKVDD DMKVMA	DMKLOM DMKNLE DMKSAV DMKTRC DMKVDE DMKVMD	DMKMCD DMKOPE DMKSEP DMKTRD DMKVDF DMKVSE	DMKMCH DMKPAH DMKSSP DMKTRX DMKVDR DMKVSG	DMKMCT DMKPEQ DMKSSS DMKURS DMKVDS DMKVSP	DMKMNT DMKQCP DMKSSU DMKUSQ DMKVDT DMKVST	DMKMSW DMKRGA DMKSSV DMKVCB DMKVER	DMKNEA DMKRNH DMKSVD DMKVCH DMKVFC
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DMKCVTDT	000056	DMKACO DMKMSG	DMKCFH DMKNLE DMKVME	DMKCFU DMKQCN DMKVSX	DMKCQY DMKRNH	DMKHVC DMKRSP	DMKJRL DMKSEP	DMKLOM DMKSPL	DMKMIA DMKTOD	DMKMID DMKTRT	DMKMN I DMKTRU
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LABEL	COUNT	REFERENC	ES				1				
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LABEL	COUNT	REFERENC	ES								
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LABEL	COUNT	REFERENC	ES						
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System Logic and Problem Determination Guide-CP LY

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LABEL COUNT REFERENCES

DMKFREDB DMKFREE	000001 000741	DMKDSP DMKACO DMKACX DMKCCSP DMKCFP DMKCQT DMKCQT DMKCQT DMKCST DMKDSB DMKGRT DMKDSB DMKGRT DMKLOJ DMKLOJ DMKLOJ DMKLOJ DMKRGC DMKSND DMKSTD DMKSTD DMKSTD DMKKTCD DMKVCB DMKVCX DMKVER	DMKACR DMKCFQ DMKCCW DMKCFQ DMKCPZ DMKCQU DMKCSU DMKDEI DMKDSP DMKIOF DMKIOF DMKIOF DMKFPEI DMKSPK DMKSVD DMKSVD DMKVCH DMKVCA DMKVSD	DMKACS DMKAPZ DMKCPB DMKCPB DMKCQC DMKCQC DMKCQV DMKCSV DMKDEX DMKHPT DMKHPT DMKMCD DMKMSG DMKFPL DMKSPL DMKSPL DMKSPL DMKSPL DMKSVA DMKVCN DMKVCN DMKVCC DMKVFD DMKVSG	DMKALG DMKATS DMKCDM DMKCFS DMKCPI DMKCQG DMKCRM DMKCSW DMKCGD DMKERM DMKHPU DMKIOH DMKIOH DMKKOCH DMKMCH DMKKQCN DMKKSPM DMKSPM DMKSPM DMKSVCP DMKVCP DMKVCP DMKVFE DMKVSI	DMKALO DMKCFC DMKCFT DMKCFJ DMKCQH DMKCSB DMKCSB DMKCSS DMKFRT DMKHVC DMKHVCS DMKJRL DMKNEA DMKSPR DMKSPR DMKSPR DMKSPP DMKVCQ DMKVCQ DMKVCSP	DMKAPI DMKBLD DMKCFD DMKCFU DMKCQI DMKCQI DMKCSC DMKCSC DMKCSY DMKDIA DMKHVD DMKHVD DMKNES DMKNES DMKKPET DMKRSPS DMKSPS DMKKSPS DMKKSPS DMKVCR DMKVCR DMKVCR DMKVDQ DMKVAQ	DMKAPS DMKCFF DMKCFF DMKCFO DMKCQP DMKCSF DMKDAD DMKDAD DMKGRC DMKHVE DMKHVE DMKKIIA DMKNET DMKNET DMKRSQ DMKRSQ DMKRSQ DMKCAT DMKVAT DMKVCT DMKVST	DMKAPT DMKCFH DMKCFY DMKCPS DMKCQQ DMKCSO DMKDAS DMKDID DMKGRF DMKHVF DMKHVF DMKNLD DMKNLD DMKNLD DMKREI DMKRST DMKSRM DMKTCS DMKVAU DMKVCU DMKVDR DMKVSW	DMKAPU DMKCFJ DMKCFJ DMKCFT DMKCQR DMKCQR DMKDAU DMKDAU DMKDAU DMKJCR DMKIDR DMKIDR DMKIDR DMKIDC DMKNLE DMKKGA DMKSCH DMKSSS DMKTRI DMKVBM DMKVCV DMKVDS DMKVSX	DMKAPW DMKCCH DMKCCFM DMKCCPU DMKCQS DMKCQS DMKDEF DMKDRE DMKJRC DMKIDU DMKIDU DMKIDU DMKIDH DMKPSA DMKSEL DMKSSU DMKSEL DMKSSU DMKTTX DMKVCA DMKVCA DMKVCT DMKVDT DMKVMG DMKWRM
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LABEL	COUNT	REFERENC	ES								
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COUNT REFERENCES

DMKGRDSR 000003 DMKGRECL 000006 DMKGREFD 000001 DMKGRESM 000002	DMKGRG DMKGRD DMKGRF DMKGRD	DMKGRF	DMKGRG			
DMKGRF 000001 DMKGRFBR 000024 DMKGRFCF 000013 DMKGRFEN 000002	DMKSYM DMKGRD DMKGRD DMKCPV	DMKGRE DMKGRE DMKQVM	DMKGRG DMKGRG			
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DMKGRGBR 000014 DMKGRGCL 000003 DMKGRGCR 000001 DMKGRGER 000002 DMKGRGPF 000002 DMKGRGTI 000003 DMKGRGTI 000001	DMKGRD DMKGRF DMKGRF DMKGRF DMKGRF DMKGRF DMKGRF	DMKGRE	DMKGRF			
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DMKIDRSV 000001	DMKIUB						
DMKIDUMP 000002	DMKCPJ						
DMKIDUSF 000002	DMKCPJ						
DMKIOCVT 000009	DMKCKF	DMKCPI	DMKIOE	DMKIOF	DMKRSP		
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DMK10QFC 000001	DMKIOS						
DMK10QFP 000004	DMKIOS						

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LABEL	COUNT	REFERENC	ES								
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	000002										
DMKLNKSS	000001	DMKSSS									
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DMKMCDIN		DMKMCC	or marries								
DMKMCDLI		DMKMCC									
DERECULI	000002	DITKINGG									

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LAE	BEL	COUNT	REFERENCE	ES								
DM	KMCDSE	000002	DMKMCC									
DM	KMCDST	000002	DMKMCC									
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	KMONMI		DMKMCC									
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	KMPORS		DMKPSA									
DM		000002	DPINE SA									

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	LABEL	COUNT	REFERENCI	ES								
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			STILL LI	Junci Liv								

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DMKPELSD ( DMKPEND (	00002	DMKPEI									
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DMKPGMUS (		DMKSWM									
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DMKPGSPP (	00006	DMKCFF	DMKCFP	DMKUSQ							
DMKPGSPR (	000006	DMKCFF	DMKCPY	DMKRPA							
DMKPGSPS (		DMKCFF	DMKCFG								
DMKPGSSS (	000002	DMKHVC									
DMKPGTAE (	000001	DMKTDK									
DMKPGTAN (	000002	DMKTDK	DMKVDG								
DMKPGTAT (		DMKTDK									
DMKPGTAW (		DMKTDK	DMKVDG								
DMKPGTAO (		DMKTDK	DMKVDG								
DMKPGTA4		DMKTDK	DMKVDG								
DMKPGTA5		DMKTDK	DMKVDG								
DMKPGTCG (	100002	DMKNLE	51111100								
DMKPGTCP (	100000	DMKCQS	DMKPST	DMKVDH							
DMKPGTDC (		DMKPGM	DMKPGU	DINCON							
DMKPGTDF (		DMKSRM	DMKSTP	DMKSYM							
DMKPGTDG (		DMKSPS	DIAKOTT	DIANO TA							
DMKPGTDK (		DMKSRM	DMKSYM	DMKVDG							
DMKPGTDL (		DMKPST	DMKSRM	DMKVDG	DMKXST						
DMKPGTDM (		DMKMOO	DMKPST	DMKSRM	DMKSYM	DMKVDG	DMKXST				
DMKPGTDP (	200000	DMKPGM	DMKSRM	DHKSNH	DHKSTH	DHKYDO	DPIKAGI				
DMKPGTDS (		DMKCFU	DMKDRD	DMKDRE	DMKIDU						
DMKPGTDS (	000000	DMKCFU	DMKDRD	DMKDRE	DMRIDU						
DMKPGTGC (	000000	DMKVDH	DHIKDKD	DHRDRE							
DMKPGTMS (		DMKPGU									
DMKPGTM5 (	000001	DMKPGM									
DMKPGTMV (	000003	DMKDIB	DMKDIF	DMKLOH	DMKPGM						
DMKPGTMX (	000009	DMKPGM	DMKSTP	DMKSTR	DHKFGH						
DMKPGTPB (	000010	DMKCQS	DMKMOO	DMKPAG	DMKPGM	DMKPST	DMKPTR	DMKSEL	DMKXST		
DMKPGTPC (	000009	DMKPGM	DMKPGU		DrikeGri	DMKFSI	DMAFIN	DHROLL	DINKASI		
				DMKSYM	DMI/CCI	DMICOT	DMIZOVM				
DMKPGTPG (		DMKCDS	DMKCFG	DMKSEG	DMKSEL	DMKSST	DMKSYM				
DMKPGTPI (		DMKCQS		DMKPST	DMKXST						
DMKPGTPL (	100004	DMKSRM	DMKSYM	DMKVDG							
DMKPGTPM		DMKPGM	DMKSWM	0.00	DIMINATO	D.1.1/03/04	014/01/00	DIMINIOT			
DMKPGTPN (		DMKPGM	DMKPST	DMKSRM	DMKSTP	DMKSYM	DMKVDG	DMKXST			
DMKPGTPT (		DMKMOO	DMKPST	DMKSYM	DMKXST						
DMKPGTPU (	000005	DMKMOO	DMKPGU	DMKPST	DMKXST						
DMKPGTQT (		DMKSRM	DMKSTP	DWINDOT	0144070	<b>61</b> 1/01/14	DUNNOT				
DMKPGTSB (	200003	DMKCQS	DMKMOO	DMKPST	DMKPTR	DMKSWA	DMKXST				
DMKPGTSF (	000005	DMKMOO	DMKPGM	DMKSWM	DMKSYM						
DMKPGTSG	00048	DMKACO	DMKCSV	DMKCSW	DMKCSY	DMKMIA	DMKRST	DMKSPL	DMKSPS	DMKTRT	DMKTRU
		DMKVME	DMKVSP	DMKVSQ	DMKXAB	DMKXAD					
DMKPGTSI (		DMKCQS	DMKMN I	DMKPST	DMKXST						
DMKPGTSL (		DMKPST	DMKSRM	DMKXST	B						
DMKPGTST (		DMKMOO	DMKPST	DMKSRM	DMKXST						
DMKPGTSU (	000006	DMKMOO	DMKPGM	DMKPGU	DMKPST	DMKXST					

LABEL	COUNT	REFERENC	ES								
DMKPGTSW DMKPGTTM	000006	DMKS₩A DMKSYM	DMKVDG								
DMKPGTTU	000006	DMKPGU	DMKSYM	DMKVDG							
DMKPGTTU DMKPGTVR	000002	DMKCFU	DMKIDU								
DMKPGT4A	000002	DMKTDK	DMKVDG								
DMKPGT4A DMKPGT5A DMKPGT7A	000002	DMKTDK	DMKVDG								
DMKPGI/A	000002	DMKTDK DMKTDK	DMKVDG DMKVDG								
DMKPGTOA	000002	DMKPGU	DMKVDG								
DMKPGUAL	000031	DMKPGU DMKATS	DMKPAG	DMKPGM	DMKPGS	DMKPTR	DMKSEL	DMKSTR	DMKSWA	DMKSWM	
DMKPGUBN	000003	DMKCKV DMKCFU	DMKSEG	DMKSYM							
DMKPGT8A DMKPGT90 DMKPGUAL DMKPGUBN DMKPGUDU DMKPGUDU	000008	DMKCFU	DMKDRD	DMKDRE	DMKIDU						
DPIKEGUEE	000002	DMKSWA DMKATS	DMKCEC			DMIZECS		DMISEI			
DMKPGUPR DMKPGUSD	000010	DMKDRD	DMKCFG DMKDRE	DMKHVD DMKNLE	DMKPGM DMKRST	DMKPGS DMKSPK	DMKRPA DMKSPS	DMKSEL DMKTRU	DMKVDD	DMKVSP	DMKVSQ
		DMKXAB	DIMONE	DIMALE	Diminor	of into the	5111(01 0	Britting	51111100	britter	Dimitoq
DMKPGUSP DMKPGUSR	800000	DMKXAB DMKCPP	DMKPGS	DMKRPA	DMKSSV						
DMKPGUSR	000002	DMKSPK DMKPTR									
DMKPGUSW		DMKPTR	DMKSWA	DMKSWM DMKCFS	DMILOUP	DMKCKS	DMKCKV	DMKCQI	DMKCQY	DMKCST	DMKCSV
DMKPGUVG	000162	DMKACO DMKCSW	DMKCFH DMKCSY	DMKDRD	DMKCKR DMKDRE	DMKGRC	DMKHVF	DMKIOF	DMKIOG	DMKIOH	DMKMCC
		DMKMIA	DMKNLD	DMKNLE	DMKOPE	DMKPMA	DMKPTT	DMKRSP	DMKRSQ	DMKRST	DMKSEP
		DMKSPK	DMKSPL	DMKSPT	DMKSSS	DMKSST	DMKTCS	DMKTCT	DMKTRU	DMKUDR	DMKUDU
		DMKVDD	DMKVME	DMKVSQ	DMKVSR	DMKVSW	DMKWRM	DMKWRN	DMKXAB	DMKXAD	
DMKPGUVR	000198	DMKCFH	DMKCFS DMKCSY	DMKCKR DMKDRD	DMKCKS DMKDRE	DMKCKV DMKGRC	DMKCQI DMKHVF	DMKCQY DMKIOF	DMKCST DMKIOG	DMKCSU DMKIOH	DMKCSV DMKMIA
		DMKCSW DMKMN I	DMKNLD	DMKNLE	DMKOPE	DMKRSP	DMKRSQ	DMKRST	DMKSEP	DMKSPK	DMKSPS
		DMKSSS	DMKSST	DMKTCS	DMKTCT	DMKTRU	DMKUDR	DMKUDU	DMKVDD	DMKVME	DMKVSP
		DMKVSQ DMKCSC	DMKVST	DMKVSW	DMKWRM	DMKWRN	DMKXAB	DMKXAD			
DMKPIALD DMKPIBLD	000001	DMKCSC									
DMKPIBLD	000001 000002	DMKCSC DMKCFG	DMKSYM								
DMKPMACD	000002	DMKCFG	DMKDEF	DMKLNK	DMKSYM	DMKVDA					
DMKPMACR	000003	DMKCFG DMKVSI DMKLNK	UNINDEI	DITIL	brinterin	01111071					
DMKPMADT	000002	DMKLNK									
DMKPMAEW DMKPMAEX DMKPMAIN	000003	DMKEXT DMKCFP	DMKIOT	DMKSYM							
	000004	DMKCFP	DMKSYM DMKSYM								
DMKPMAIO	000003	DMKDSP	DMKIOT	DMKMID	DMKPTR	DMKVRR					
DMKPMA I 1	000005	DMKCPI	DMKSYM	DIAMITE	britter itte	<b>Brinten</b>					
DMKPMA12	000004	DMKCP I DMKCP I	DMKSEG	DMKSYM							
DMKPMAMC	000004	DMKMCH	DMKSYM	014/0/000							
DMKPMARW DMKPMASS DMKPMASW DMKPMASW DMKPMATM DMKPMAWD	000006	DMKDSP DMKEXT	DMKQVM	DMKVRS							
DMKPMASS	000003	DMKDSP	DMKEXT	DMKMCH	DMKMCT	DMKSYM					
DMKPMATM	000006	DMKQVM	DMKVRR	DMKVRS	51111101						
DMKPMAWD	000008	DMKCCW	DMKPRV	DMKPRW							
DMKPMAWT DMKPMAXF	000004	DMKDSP	DMKSYM								
	000008	DMKPRV DMKPRV	DMKSYM DMKPRW	DMKSYM							
DMKPMAXS DMKPRGCT	000007	DMKMOO	DMKPMA	DMKSYM							
DMKPRGC8	000018	DMKMCC	DMKMCD	DMKMIA	DMKMN I	DMKMNL	DMKMON	DMKSYM			
DMKPRGIN	000005	DMKAPI	DMKCPI	DMKSTA	DMKSYM	DMKTOD	B.444	<b>B1</b> (1):			
DMKPRGMC	000035	DMKCPS DMKMON	DMKDMP DMKMOO	DMKENT	DMKMCC	DMKMCD	DMKMIA	DMKMID	DMKMN I	DMKMNJ	DMKMNL
DMKPRGMI	000003	DMKMON	DMKMOU	DMKSYM							
DMKPRGM I DMKPRGMO	000005	DMKMNI	DMKMNJ	DMKMOO							
DMKPRGRF	000003	DMKSVD	DMKSYM								

LABEL	COUNT	REFERENCE	s								
DMKPRGSM	000031	DMKDSP	DMKHVC	DMKIUA	DMKMPO	DMKPRV	DMKPRW	DMKRPD	DMKSYM	DMKTMR	DMKVAT
DUNDOOT		DMKVAU	DMKVFR	5.00/000 I		D.11/1/00					
DMKPRGTI		DMKMCD	DMKMN I	DMKMNJ	DMKMNL	DMKMOO					
DMKPRVCA DMKPRVCD	000001	DMKAPI DMKMOO	DMI/CVM								
DMKPRVCE	000002	DMKMOO	DMKSYM								
DMKPRVCH	000001	DMKMOO	DMKSYM								
DMKPRVCP	000002	DMKMOO	DMKSYM								
DMKPRVCS	000002	DMKMOO	DMKSYM								
DMKPRVCT	000002	DMKMOO	DMKSYM								
DMKPRVDI	000002	DMKMOO	DMKSYM								
DMKPRVCS DMKPRVCT DMKPRVDI DMKPRVEK	000002	DMKMOO	DMKSYM								
DMKPRVFP	000002	DMKMOO	DMKSYM								
DMKPRVIK	000002	DMKMOO	DMKSYM								
DMKPRVIP	000002	DMKMOO	DMKSYM								
DMKPRVLC	000002	DMKMOO	DMKSYM	DM//01/44							
DMKPRVLG	000005	DMKPMA	DMKPRG	DMKSYM							
DMKPRVLP	000002	DMKMOO DMKMOO	DMKSYM DMKSYM								
DMKPRVLR DMKPRVMA	000002	DMKPSA	DHKSTH								
DMKPRVMN	000001	DMKMOO	DMKSYM								
DMKPRVMO	000002	DMKMOO	DMKSYM								
DMKPRVMS	000002	DMKMOO	DMKSYM								
DMKPRVNC	000002	DMKMOO	DMKSYM								
DMKPRVPB	000002	DMKMOO	DMKSYM								
DMKPRVPE	000002	DMKMOO	DMKSYM								
DMKPRVPT DMKPRVRR	000001	DMKMOO DMKMOO	DMKSYM								
DMKPRVTC	000002	DMKMOO	DMKSYM								
DMKPRVTF	000002	DMKMOO	DMKSYM								
DMKPRVTP DMKPRVX1	000001	DMKMOO	Dinto in								
DMKPRVXI	000002	DMKMOO	DMKPRW								
DMKPRVXR	000001	DMKPRW									
DMKPRVXS	000002	DMKMOO	DMKPRW								
DMKPRWIP	000003	DMKPRV	DMKSYM								
DMKPRWSK	000002	DMKAPI	DMKCPI								
DMKPRWTB DMKPRWTP	000002	DMKPRV DMKPRV	DMKSYM								
DMKPRWXK	000002	DMKPRV	DMKSTM								
DMKPSA	000002	DMKLDOOE									
DMKPSACC	000026	DMKCDS	DMKDGD	DMKDGF	DMKIUE	DMKIUL	DMKVCN	DMKVSR	DMKVSX		
DMKPSADU	000004	DMKAPI	DMKCLK	DMKCPI	DMKSYM						
DMKPSAER	000002	DMKEXT									
DMKPSAFC	000008	DMKHPU	DMKIUE	DMKVMC					<b>B1111111111111</b>		
DMKPSAFP	000072	DMKBIO DMKSFB	DMKFPS	DMKHVC	DMKHVD	DMKHVE		DMKIUC DMKXAB	DMKIUL	DMKPRV	DMKPRW
DMKPSANX	000004	DMKEXT	DMKTMR DMKMOO	DMKVER DMKSYM	DMKVMC	DMKVMD	DMKVME	DINKAAD			
DMKPSAPO		DMKCCH	DMKCCW	DMKCDB	DMKCDM	DMKCDS	DMKCPB	DMKDSP	DMKGIO	DMKHVD	DMKMPO
Britte OFE O	000000	DMKPMA	DMKPRG	DMKPRV	DMKPRW	DMKQVM	DMKTRK	DMKVCN	DMKVIO	DMKVME	DMKVSI
		DMKVSJ	DMKVSP						····· • •		
DMKPSARR		DMKTRC									
DMKPSARS		DMKTRC									
DMKPSARX		DMKTRC					~~~~		<b></b>	D1410 040	
DMKPSASC	000040	DMKCDS	DMKCFD	DMKDGD	DMKHPU	DMKIUE	DMKIUL	DMKTRC	DMKVCN	DMKVMC	DMKVSR
	000000	DMKVSX DMKAPT	DMKRIO			DMKFPS	DMKHVC	DMKHVD	DMKHVE	DMKHVF	DMKIUA
DMKPSASP	000090	DMKIUE	DMKBIO	DMKDRD DMKPRV	DMKDRE DMKPRW	DMKSFB	DMKHVC DMKTMR	DMKTRC		DMKXAB	UNKIUA
DMKPSAST	000002	DMKMPO	DMKSTA			DIROLD					

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| DMKPSTIN 00002<br>DMKPTRAN 000328<br>DMKACO<br>DMKCRS<br>DMKCGS<br>DMKCGS<br>DMKCRS<br>DMKCRC<br>DMKPGS<br>DMKRPG<br>DMKPGS<br>DMKPGS<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVTRE<br>DMKPTRES 00003<br>DMKFRE<br>DMKPTREF 00002<br>DMKSSM<br>DMKPTREF 00002<br>DMKSEL<br>DMKPTREF 00003<br>DMKSEL<br>DMKPTREF 00003<br>DMKSEL<br>DMKPTRFF 00003<br>DMKCPI<br>DMKPTRFF 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| DMKPTRAN 000022<br>DMKPTRAN 000328<br>DMKACO DMKACT<br>DMKCFC DMKCFD<br>DMKCRS DMKCPB<br>DMKCQS DMKCQY<br>DMKGRC DMKGRF<br>DMKIDU DMKIOF<br>DMKLNK DMKMCCQ<br>DMKPGS DMKPMA<br>DMKPGS DMKRMA<br>DMKPGS DMKRMA<br>DMKPTSP<br>DMKUDU DMKVAT<br>DMKVMC DMKVMD<br>DMKVMC DMKVMD<br>DMKVTRE DMKSSS<br>DMKTMR DMKTOD<br>DMKVMC DMKVMD<br>DMKVTRE DMKSSS<br>DMKTMR DMKSSS<br>DMKTMR DMKSSS<br>DMKTMR DMKSSS<br>DMKTMR DMKSSS<br>DMKTMR DMKSSS<br>DMKTMR DMKSSS<br>DMKSTM<br>DMKPTRE 000003 DMKATS DMKSFL<br>DMKSTRE DMKSTM<br>DMKPTREF 000002 DMKSEL DMKSTR<br>DMKPTREF 000003 DMKSEL DMKSTR<br>DMKPTREF 000003 DMKSEL DMKSTR<br>DMKPTREF 000003 DMKSEL DMKSTR<br>DMKPTRFF 000003 DMKSPM<br>DMKPTRFF 000003 DMKSPM<br>DMKPTRFF 000003 DMKSPF DMKSTR<br>DMKPTRFF 000004 DMKSPT<br>DMKPTRFF 000005 DMKNO0 DMKSEL<br>DMKPTRFF 000004 DMKSPT<br>DMKPTRFF 000006 DMKSPM<br>DMKPTRFF 000007 DMKM00 DMKSEL<br>DMKPTRFF 000007 DMKM00 DMKSYM<br>DMKPTRFF 000007 DMKM00 DMKSFT<br>DMKPTRFP 000002 DMKNO0 DMKSFT<br>DMKPTRFP 000002 DMKNO0 DMKSYM<br>DMKPTRFP 000002 DMKNO0 DMKSYM<br>DMKPTRFP 000002 DMKNO0 DMKSYM<br>DMKPTRFP 000002 DMKACPI DMKSFT<br>DMKPTRFP 000002 DMKACPI DMKSFT<br>DMKPTRFP 000002 DMKACO DMKSYM<br>DMKPTRFP 000002 DMKACO DMKSYM<br>DMKPTRFP 000002 DMKACO DMKSYM<br>DMKPTRFP 000002 DMKACO DMKSYM<br>DMKPTRFP 000002 DMKACPI DMKSYM<br>DMKPTRPF 000002 DMKSYM<br>DMKPTRPF 0000002 DMKSYM<br>DMKPTRPF 000002 DMKSYM<br>DMKPTRPF 0000002 DMKSYM<br>DMKPTRPF 0000002 DMKSYM<br>DMKPTRPF 000002 DMKSYM<br>DMKPTRPF 000002 DMKSYM<br>DMKPTRPF 000002 DMKSYM<br>DMKPTRPF 000002 DMKSYM<br>DMKPTRFF 000002 DMKSYM<br>DMKPTRPF 0000002 DMKSYM<br>D | DMKPTRAN 000022<br>DMKPTRAN
000328<br>DMKACO<br>DMKCPTC<br>DMKCPTC<br>DMKCPTC<br>DMKCPTC<br>DMKCPTC<br>DMKCQY<br>DMKCQY<br>DMKCQY<br>DMKCQY<br>DMKCQC<br>DMKGRC<br>DMKGRC<br>DMKGRF<br>DMKGRT<br>DMKIDU<br>DMKIDU<br>DMKIGT<br>DMKTRC<br>DMKTRD<br>DMKTRC<br>DMKTRC<br>DMKTRC<br>DMKVMC<br>DMKVAU<br>DMKVAU<br>DMKVAU<br>DMKVAU<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVMC<br>DMKVTREF<br>000003<br>DMKSEL<br>DMKSYM<br>DMKPTRF0<br>00002<br>DMKMO0<br>DMKSEL<br>DMKSYM<br>DMKPTRF0<br>00001<br>DMKVMC<br>DMKVFT<br>DMKSEL<br>DMKSYM<br>DMKPTRF0<br>00002<br>DMKMO0<br>DMKSCH<br>DMKSYM<br>DMKPTRF0<br>00001<br>DMKVCN<br>DMKVFC<br>DMKVFT<br>DMKSEL<br>DMKSYM<br>DMKPTRF1<br>000001<br>DMKVCN<br>DMKVFC<br>DMKVFT<br>DMKSEL<br>DMKSYM<br>DMKPTRF2<br>000002<br>DMKMO0<br>DMKSCH<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKVCN<br>DMKVFT<br>DMKSEL<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKVT<br>DMKPTRF2<br>000001<br>DMKVT<br>DMKPTRF2<br>000001<br>DMKVT<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM<br>DMKPTRF2<br>000001<br>DMKSYM | DMKPTRAN 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| DMKPTTRAN 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LABEL	COUNT	REFERENCI	ES
DMKPTRRM DMKPTRRQ DMKPTRRW DMKPTRSC	000002 000004 000002 000018	DMKSTA DMKPAG DMKMOO DMKATS DMKVMA	DI DI DI DI
DMKPTRSS DMKPTRSW DMKPTRS2 DMKPTRUL	000004 000007 000006 000481	DMKVMA DMKMOO DMKSEL DMKACO DMKCFP DMKCPX DMKCST	DI DI DI DI DI DI
DMKPTRUX	000003	DMKHPU DMKIUL DMKQCP DMKSPT DMKVCH DMKVSE DMKAPI	
DMKPTRU1 DMKPTRWQ DMKPTSAD	000005 000006 000067	DMKPTS DMKCDS DMKATS DMKSEL	DN DN DN DN
DMKPTSAE DMKPTSMW DMKPTSPW DMKPTSRS	000034 000005 000018 000004	DMKDAD DMKVFS DMKCFU DMKATS DMKPGS	DN DN DN DN
DMKPTTAL DMKPTTCL DMKPTTC1 DMKPTTC2 DMKPTTC3 DMKPTTDM DMKPTTFT	000004 000008 000006 000006 000006 000006 000016 000078	DMKCPY DMKPTR DMKDAD DMKDAD DMKDAD DMKPRV DMKATS	DN DN DN DN DN DN DN
		DMKPST	D

		DMKVMA			
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DMKPTRS2	000006	DMKSEL	DMKSWA	DMKSWM	DMKSYM
DMKPTRUL	000481	DMKACO	DMKAPS	DMKAPT	DMKAPY
DERFINCE	000401				
		DMKCFP	DMKCFQ	DMKCFR	DMKCFS
		DMKCPX	DMKCPY	DMKCQH	DMKCQI
		DMKCST	DMKCSV	DMKCSW	DMKCSY
		DMKHPU	DMKHVC	DMKHVE	DMKHVF
		DMKIUL	DMKLNK	DMKLOH	DMKMHC
		DMKQCP	DMKREI	DMKRPA	DMKRPD
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DMKSYM DMKPGM DMKSYM DMKCPP

DMKSTR

DMKDSP

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DMKSYM

DMKMOO

DMKPGS

DMKSYM

DMKATS DMKCKR DMKCQS DMKDGD

DMK I DR DMKM I A

DMKRST DMKTRC

DMKVIO

DMKVST

DMKSEL DMKCPI

DMKVFD

DMKPTT

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DMKCPY DMKSEL DMKVFS

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DMKVAT

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DMKVCN

LABEL	COUNT	REFERENC	ES								
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DMKRGBPL DMKRGBRE		DMKRGA DMKRGA	DMKRGC								

LABEL	COUNT	REFERENC	ES								
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LABEL	COUNT	REFERENC	ES								
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DMKSCHMS		DMKMOO	DMKPGM	DMKSTR							
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LABEL	COUNT	REFERENC	ES								
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LABEL	COUNT	REFERENCI	ES								
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DMKSCNVD	000056	DMKACO DMKDEF	DMKALG DMKDIA	DMKCCW DMKD I B	DMKCFG DMKG10	DMKCFP DMKHVE	DMKCFR DMKLOJ	DMKCFT DMKPMA	DMKCPS DMKQVM	DMKCQQ DMKSSU	DMKCST DMKUNT
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		DMKCPS DMKDIA	DMKCQG DMKD I B	DMKCQP DMKD I F	DMKCQQ DMKDRD	DMKCSB DMKDSP	DMKCSQ DMKFPS	DMKCSR DMKG10	DMKCST DMKHVC	DMKDEF DMKHVE	DMKDGD DMKHVF
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		DMKVDC DMKXAB	DMKVDD	DINKYDK	DHKYDS	DINKYDI	DMRYEN	DMKVIO	DERVIN	DIANUST	DPRVOF
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DMKSPLOR	000002	DMKRST	-								

LABEL	COUNT	REFERENCE	S								
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LABEL	COUNT	REFERENC	ES								
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DMKSYSHE	000007	DMKCKF	DMKCPI	DMKRSP	DMKVSD	DMKVSE	DMKWRM				

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	DMKTCSET		DMKRSP								
:	DMKTCSML		DMKTCT								
	DMKTCSSP		DMKSEP								
•	DMKTCSTR DMKTCTET	000000	DMKRSP DMKTCS								
)	DMKTDKGT		DMKVDS								
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	DMKTEESF		DMKTEM								
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•	DMKTEMEP		DMKTRR								
	DMKTESUB	000002	DMKTEE	DMKTEF							
	DMKTHIDP	000002	DMKCMD								
•											

DMKVSG

570 System Logic and Problem Determination Guide-CP

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LABEL	COUNT	REFERENC	ES								
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DMKTHIIO		DMKCMD									
DMKTHILO	000002	DMKCMD									
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**Restricted Materials of IBM** 

LABEL	. 1	COUNT	REFERENC	ES								
		000001	DMKCSC DMKCSC									
		000001	DMKCSC									
		000002	DMKCPI									
		000002	DMKHVD									
		000004	DMKLNK									
DMKUD	RFU	000060	DMKCAO	DMKCFT	DMKCFY	DMKCSP	DMKCSQ	DMKCSU	DMKCSV	DMKCSX	DMKDEG	DMKHVD
			DMKIUP	DMKLNK	DMKLOG	DMKLOH	DMKLOM	DMKMNJ	DMKOPE	DMKREI	DMKRPD	DMKRST
DMI/UD		00000	DMKSPL	DMKTRP DMKIUC	DMKVCT	DMKVMD						
		000008 000022	DMK I DR DMKCFT	DMKCFY	DMKDEG	DMKHVD	DMKIUP	DMKLOG	DMKLOH	DMKOPE	DMKREI	DMKSPL
DIIKUD	INPLD 1	000022	DMKTRP	DIAKOT	DINDLO	DIMANUU	Dritter	DIIILEOO	DIIILEOII	DIRICITE	Diffice	5 more
DMKUD	ROV	000002	DMKCPI									
		000002	DMKLOJ									
DMKUD	RRV	000042	DMKCFT	DMKCFY	DMKDEG	DMKHVD	DMKIUC	DMKIUP	DMKLNK	DMKLOG	DMKLOH	DMKLOJ
			DMKOPE	DMKREI	DMKSPL	DMKTRP						
		000004	DMKLOH DMKHVD	DMKREI								
		000002 000001	DMKSYM									
		000021	DMKAPI	DMKCCW	DMKCFR	DMKCPI	DMKGIO	DMKHVC	DMKSYM	DMKVIO	DMKVSI	
		000001	DMKCPI		2							
DMKUN	TIS	000003	DMKISM	DMKSYM								
		000009	DMKAPI	DMKCPI	DMKGIO	DMKSYM	DMKTRK	DMKVIO				
		000003	DMKCCS	DMKSYM	DMI/DCD	DMKRST						
		000022 000019	DMKCQP DMKCDB	DMKCSO DMKCDM	DMKRSP DMKCDS	DMKDSP	DMKPRG	DMKPRV	DMKPRW	DMKSYM		
		000003	DMKCFV	DMKDSP	DMKPRV	DHILDOT	Diantino	Diffici ice	Dritti tti	Drinterin		
		000033	DMKCDS	DMKCFP	DMKCFV	DMKCPB	DMKDSP	DMKMPO	DMKQVM	DMKSPM	DMKSYM	DMKUSQ
			DMKVAU			-						
		000007	DMKDSP	DMKPRV	DMKSYM	DMKTMR	DMUDOD	DMI/CDM	DHI/OVA	DMI/VALL		
		000022 000001	DMKCDS DMKVRR	DMKCFG	DMKCFV	DMKCPB	DMKDSP	DMKSPM	DMKSYM	DMKVAU		
		000002	DMKPRG									
		000003	DMKPRG	DMKSYM								
		000018	DMKBLD	DMKMCH	DMKPGS	DMKPTS	DMKPTT	DMKRPA	DMKSEL			
		000003	DMKPRG	DMKSYM								
		000002	DMKAPI	DMKCPI								
		000002	DMKAPI DMKPRG	DMKCPI DMKPRV	DMKPRW	DMKSVD	DMKSYM					
		000009 000019	DMKPEI	DMKPER	DMKPRV	DMKSVD	DMKSYM	DMKTMR	DMKTRC	DMKTRD	DMKVER	
		000002	DMKAPI	DMKCPI	Britting	5111075	Dimoini	Diment	Diminio	5111110	Dimit	
		000004	DMKHPS	DMKHVF								
		000002	DMKHVF									
DMKVB	MVR	000002	DMKHVF									
		000002	DMKVSI DMKCFQ									
		000002	DMKDEF	DMKDIB	DMKVDR							
		000002	DMKVSJ	5111010								
DMKVC	BTS	000002	DMKVSI									
		000004	DMKVDA	DMKVDD								
		000003 000002	DMKSYM DMKFRT	DMKVSI DMKSYM								
		000002	DMKIUB	DMKSYM								
		000006	DMKVCP	DMKVCR	DMKVCS							
		000002	DMKVCS									
		000011	DMKVCR	DMKVCS	DMKVCT	DMKVCX						
		000001	DMKSYM	DMI/L/OD	DMI/UOU	DMILLION						
DMKVC	KNK (	000010	DMKDSP	DMKVCP	DMKVCU	DMKVCX						

 $\sim$ 

LABEL

COUNT REFERENCES

DMKVCRRD 0000 DMKVCSWT 0000	05 DMKQCO	) DMK		DMKVCQ				
DMKVCTCH 0000 DMKVCTDA 0000 DMKVCTEN 0000 DMKVCTER 0000	003 DMKCPV 003 DMKCPV	( (	SYM					
DMKVCTLO 0000 DMKVCTTM 0000 DMKVCUIL 0000	010 DMKDIE 001 DMKVC	B DMK	QCP	DMKUSQ				
DMKVCVCE 0000 DMKVCVEB 0000 DMKVCVER 0000	082 DMKVCI 046 DMKVCI	P DMK P DMK	VCR	DMKVCR DMKVCS DMKVCS	DMKVCS DMKVCT DMKVCU	DMKVCT DMKVCU	DMKVCW DMKVCX	DMKVCX
DMKVCVIN 0000 DMKVCVIX 0000	010 DMKVC0 024 DMKVC0	DMK	VCR	DMKVCT DMKVCS	DMKVCT	DMKVCX		
DMKVCVKS 0000 DMKVCVLD 0000 DMKVCVLY 0000	20 DMKVCI	P DMK	VCR	DMKVCS	DMKVCT	DMKVCU	DMKVCX	
DMKVCVND 0000 DMKVCVND 0000 DMKVCVUT 0000	DMKVC	) DMK	VCR	DMKVCW DMKVCT				
DMKVCWCN 0000 DMKVCWQS 0000	01 DMKIU	3						
DMKVCWRM 0000 DMKVCWSV 0000 DMKVCXD2 0000	01 DMKIU	3	VCW					
DMKVCXFU 0000 DMKVCXGF 0000	034 DMKVCI 004 DMKVCI	P DMK P DMK	VCQ VCR	DMKVCR	DMKVCS	DMKVCT	DMKVCV	DMKVCW
DMKVCXGO 0000 DMKVCXIO 0000 DMKVCXOR 0000	07 DMKSYN	1 DMK	VCP	DMKVCQ	DMKVCR			
DMKVCXOX 0000 DMKVCXSA 0000	06 DMKVC 12 DMKVCI	DMK DMK	VCW VCQ	DMKVCR	DMKVCS	DMKVCU		
DMKVDAAA 0000 DMKVDAAC 0000 DMKVDAS1 0000	02 DMKCMI	)						
DMKVDAS2 0000 DMKVDBMD 0000	01 DMKSSS 02 DMKVD/	5						
DMKVDCPS 0000 DMKVDCSC 0000 DMKVDDDC 0000	04 DMKVD	DMK	VDD					
DMKVDDDG 0000 DMKVDDDR 0000	DO2 DMKCMI	)						
DMKVDDDS 0000 DMKVDEDC 0000 DMKVDERR 0000	02 DMKVD	ί.						
DMKVDFER 0000 DMKVDGAL 0000	02 DMKVDI	)	VDA					
DMKVDHBB 0000 DMKVDHFR 0000	02 DMKVDO	;						
DMKVDHOR 0000 DMKVDHPG 0000 DMKVDREL 0000	04 DMKVDO	;	NEA	DMKNLD	DMKUSQ	DMKVCH	DMKVDD	
DMKVDRES 0000 DMKVDSAT 0000	06 DMKNLI 011 DMKLO	) DMK I DMK	VIO LOM	DMKVCH	DMKVDA	DMKVRR		
DMKVDSDF 0000 DMKVDSLK 0000 DMKVDTPG 0000	04 DMKLNI	(	LOJ VCH	DMKNEA DMKVDA	DMKVRR			
DMKVERD 0000 DMKVERO 0000	01 DMKSVI	)	+ UII		Grintendi			

	COUNT	REFERENC									
LABEL	COUNT	REFERENC	25								
DMKVFCQV		DMKCMD									
DMKVFCVV		DMKCPT					·				
DMKVFDDP	000002	DMKCDB									
DMKVFEST DMKVFRCH	000002	DMKCDS DMKSYM	DMKVFC	DMKVFS							
DMKVFRIA	000004	DMKVFC	DMKYFC	DMKYFS							
DMKVFRIM		DMKMCH									
DMKVFRIN		DMKPRG									
DMKVFRIP		DMKAPI	DMKMOO	DMKVFC							
DMKVFRNA		DMKVFC									
DMKVFRNC		DMKMOO									
DMKVFRNM		DMKMCH									
DMKVFRNO		DMKMOO	DMULTO								
DMKVFRNP DMKVFRNR		DMKAPI DMKMOO	DMKVFC								
DMKVFRNS		DMKMOO									
DMKVFRSA		DMKVFS									
DMKVFRSS	000003	DMKVFD	DMKVFE	DMKVFS							
DMKVFRSV	000018	DMKACO	DMKCKF	DMKCPO	DMKDMP	DMKEXT	DMKMOO	DMKPRV	DMKTHI	DMKVFC	DMKVFD
		DMKVFE	DMKVFS								
DMKVFRVL		DMKVFS		D.440/5D							
DMKVFSOS	000006	DMKVFD	DMKVFE	DMKVFR							
DMKVFSRS		DMKPGS	DMKVSJ								
DMKVIOCL DMKVIOC1		DMKDSP DMKVSI	DINKYSJ								
DMKVIOIN		DMKACO	DMKCFR	DMKCSR	DMKCSW	DMKCSX	DMKDIF	DMKHPT	DMKHPU	DMKIOQ	DMKIOS
Britty Forth	000020	DMKIOT	DMKRGA	DMKRGB	DMKRGC	DMKRGE	DMKSPL	DMKSPM	DMKSYM	DMKUNT	DMKVCA
		DMKVCB	DMKVCN	DMKVCP	DMKVSI	DMKVSJ					
DMKVIOMK		DMKCFM	DMKCFQ	DMKCPB	DMKDSP	DMKSSS	DMKVCN	DMKVDC	DMKVSI	DMKVSJ	DMKVSP
DMKVIOXK		DMKVSJ	DANKATO	DUIVOOLI	0.000	DMI/ODO	DM//OD I	DMUDOD	DWUDOC	DMI/DOD	DMI/DOC
DMKVMASH	000039	DMKAPI	DMKATS	DMKCCW		DMKCDS	DMKCPI	DMKDGD	DMKDGF	DMKDSP	DMKPGS
DMKVMASW	000010	DMKSYM DMKCPP	DMKUSQ DMKLOK	DMKVCN DMKWA I	DMKVSR	DMKVSX					
DMKVMAS1		DMKCFF	DMKCFH	DMKCPP	DMKCPU						
DMKVMAS2		DMKCFF	DHIKOTH	Difficult	Difficitio						
DMKVMCEX		DMKDSP									
DMKVMCFC	000004	DMKHVC	DMKMSG								
DMKVMCUA	000002	DMKCFP									
DMKVMDEP		DMKCFC									
DMKVMDIA	000002	DMKHVE									
DMKVMEDP DMKVMGCN		DMKVMD DMKIUB									
DMKVMGIL		DMKIUB									
DMKVMGSV	000001	DMKIUB									
DMKVMI	000004	DMKCFG	DMKRPA	DMKSEG							
DMKVRR	000011	DMKBLD	DMKCKF	DMKCPI	DMKDMP	DMKDMQ	DMKPMA	DMKRSP	DMKSYM		
DMKVRRDD		DMKCPI	DMKMNT	DMKSYM							
DMKVRRIS	000006	DMKDMP	DMKDMQ	DMKOPR	DMKSYM						
DMKVRROP DMKVRRRC	000006	DMKDMP DMKCP I	DMKDMQ DMKSYM	DMKLOJ	DMKVRS						
DMKVRRRS		DMKCPJ	DMKSYM								
DMKVRSSV		DMKDMP	DMKDMQ	DMKSTA	DMKSYM						
DMKVSC	000001	DMKSYM									
DMKVSCVR	000002	DMKVSI									
DMKVSDAD	000030	DMKACO	DMKCKR	DMKCSV	DMKCSY	DMKDRD	DMKMIA	DMKNLE	DMKSPL	DMKSPS	DMKTRT
DMIAVODDA	000010	DMKVME	DMKVSW		DMIZEDE	DMIAVES	DMI/I./DM				
DMKVSDDL DMKVSDFH		DMKCSV DMKLOH	DMKCSY DMKVSF	DMKDRD DMKVSG	DMKSPS	DMKVSW	DMKWRN				
DERVOUEN	000012	Difficult	01111401	DIRAGO							

574 System Logic and Problem Determination Guide-CP

LABEL	COUNT	REFERENCE	ES							
DMKVSEER		DMKVSG								
DMKVSELK		DMKVSG								
DMKVSETR		DMKCKR	DMKCSY	DMKSPS	DMKVSD	DMKVSF	DMKVSG			
DMKVSFID	000014	DMKCQH	DMKCST	DMKDRD	DMKDRE	DMKSFB	DMKXAB			
DMKVSFNS	000016	DMKCQH	DMKCQI	DMKCSV	DMKCSW	DMKCSY	DMKSFB	DMKSPT		
DMKVSFNU	000040	DMKCQH	DMKCQI	DMKCSR	DMKCSV	DMKCSW	DMKCSY	DMKDRD	DMKDRE	DMKSFB
		DMKVSW	-							
DMKVSGAI	000012	DMKCKT	DMKCSY	DMKRST	DMKSPL	DMKWRN				
DMKVSGRI	000028	DMKCFU	DMKCSY	DMKDRD	DMKDRE	DMKMIA	DMKNLE	DMKRST	DMKSPK	DMKSPL
		DMKTRU	DMKVME	DMKWRN						
DMKVSGUM	800000	DMKCKR	DMKVSE							
DMKVSICH	000001	DMKVSJ								
DMKVSICI		DMKMOO	DMKSYM							
DMKVSICT	000001	DMKMOO								
DMKVSICW		DMKMOO	DMKSYM							
DMKVSIEX		DMKHVC	DMKHVE	DMKPRV	DMKSYM	DMKVSJ				
DMKVSIFT		DMKVSJ								
DMKVSISF		DMKMOO	DMKSYM							
DMKVSISI	000002	DMKMOO	DMKSYM							
DMKVSITC		DMKMOO	DMKSYM							
DMKVSITI	000002	DMKMOO	DMKSYM							
DMKVSIVS		DMKPRV	Drinoitti							
DMKVSJEX	000001	DMKVSI								
DMKVSJHD	000002	DMKMOO	DMKSYM							
DMKVSJHI	000002	DMKMOO	DMKSYM							
DMKVSP	000001	DMKVSV	DIAKOTA							
DMKVSPEX		DMKSYM	DMKVSI							
DMKVSPPE	0000003	DMKVSU	DHIKTOT							
DMKVSPST		DMKVSU								
DMKVSPUS		DMKVSU								
DMKVSPWA		DMKSYM	DMKUSP	DMKVST						
DMKVSQPD		DMKSYM	DMKVSP	DMKVST						
DMKVSRGC	000011	DMKSYM	DMKVSP	DMKVSX						
DMKVSRMD		DMKSYM	DMKVSP	DMKVSX						
DMKVSTOP	000009	DMKSYM	DMKVSP	DIFIKY SA						
DMKVSTPT		DMKCDM	DMKPET	DMKRNH	DMKTRC	DMKTRD				
DMKVSTVP		DMKQCO	DMKTTX	DMKTTY	DMKVCU	DRATIND				
DMKVSUCO	000000	DMKCFQ	DMKCPS	DMKCSQ	DMKCSR	DMKVDR				
DMKVSUCR	000011	DMKCFQ	DMKCSQ	DMKDRD	DMKVDR	DHKYDK				
DMKVSVLD	000000	DMKVSP	DHRUSQ	DIAKDIND	DHKYDK					
DMKVSWDC	000002	DMKHVF	DMKSYM	DMKVSP	DMKVSU	DMKVSX				
DMKVSWFC	000011	DMKSYM	DMKVSP	DMKVSU	DMKVSX	DHK¥3A				
DMKVSWOC	000011	DMKVSX	DMRAJE	DHKYJU	DHIKY SA					
DMKVSWOR		DMKHVF	DMKVSP							
DMKVSWOT	000004	DMKVSX	DPIKYSP							
DMKVSWTO		DMKSYM	DMKVSI							
DMKVSXCL		DMKVSP	UPIK V S I							
DMKVSXRD		DMKVSW								
DMKVSXSE		DMKVSP								
DMKVSXSI		DMKVSP								
DMKVSXSR		DMKVSP								
DMKVSXSR	000002	DMKVSP								
			DMIZEVM							
DMKWAIST DMKWAITA	000002	DMKDSP	DMKSYM	DMIZEN						
DMKWATTA			DMKDSP	DMKSYM						
	000001	DMKLDOOE								
DMKWRMST		DMKCPJ								
DMKWRMSY DMKWRNSB	000002	DMKCPJ								
DHWWWW3D	000006	DMKCKV	DMKWRM							

DMKSPT

DMKSPS

LABEL	COUNT	REFERENCE	S				
DMKWRNWM	000004	DMKWRM					
DMKXABDG	000004	DMKAPT	DMKHVE				
DMKXADVS	000002	DMKVSP					
DMKXSTOR	000002	DMKPST					
DMKZTDDF	000002	DMKVDS					
DMKZTDST	000002	DMKCPX					
DMPABEND	000002	DMKDMP	DMKSAD				
DMPAP	000003	DMKDMQ					
DMPCKCOM	200000	DMKSAD	DMKVME				
DMPCPUID	000004	DMKDMP	DMKSAD	DMKVME			
DMPCPUTM	000001	DMKSAD					
DMPCRS	000003	DMKSAD	DMKVME				
DMPDMPID	000003	DMKVME					
DMPFLAG	000001	DMKDMP					
DMPFPRS	000003	DMKDMP	DMKSAD	DMKVME			
DMPGPRS	000005	DMKDMP	DMKSAD	DMKVME			
DMPINREC		DMKDMP	DMKSAD	DMKVME			
DMPIPCS	000021	DMKDMP	DMKDMQ	DMKSAD	DMKVMD	DMKVME	
DMPKEY	000002	DMKDMP					
DMPKYREC	000001	DMKDMP					
DMPLCORE		DMKDMP	DMKSAD				
DMPMP	000009	DMKDMQ	DHI/CAD	DMIAME			
DMPPGMAP		DMKDMP	DMKSAD DMKSAD	DMKVME			
DMPPGMP2		DMKDMP DMKDMP	DMKSAD				
DMPPROCA		DMKDMP	DHKSAD				
DMPPSW	000001	DMKVME					
DMPROC	000003	DMKDMQ					
DMPSMSG	000004	DMKDMQ					
DMPSMSGL		DMKDMQ					
DMPSTAT	000021	DMKDMQ					
DMPSYSRM		DMKDMP	DMKSAD	DMKVME			
DMPSYSRV		DMKDMP	DMKSAD	DMKVME			
DMPTODCK		DMKDMP	DMKSAD	DMKVME			
DMPUNI	000010	DMKDMQ	0111(0710	51111112			
DMPVMTYP		DMKSAD	DMKVME				
DOLOCAL	000002	DMKACO	DMKCKF				
DORDEV	000006	DMKACO	DMKCKF				
DOTERMID	000002	DMKACO	DMKCKF				
DOUBLESP		DMKDMQ					
DOVMTERM	000002	DMKACO	DMKCKF				
DPAPUOP	000006	DMKAPI	DMKCPP	DMKEXT	DMKPSA		
DPLEND	000004	DMKUDR	DMKUDU				
DPLENDFG	000004	DMKUDR	DMKUDU				
DPLHIGH	000005	DMKUDR					
DPLIST	000003	DMKUDR	DMKUDU				
DPLLOW	000005	DMKUDR					
DPLNGTH	000002	DMKUDR					
DPLSIZE	000005	DMKUDR	DMKUDU				
DPLS1Z2	000001	DMKUDR	DMI/UDU				
DPLVADD	000005	DMKUDR	DMKUDU	DMIZODI	DMKPSA		
DPMICON	000007		DMKCFO	DMKCPI DMKDSP	DMKPSA		
DPOKTLB	000005	DMKAPI DMKAPI	DMKCPI	DMKDSP	DHALDH		
DPSEGPRT DPSTAT	000019	DMKAPI	DMKCFO	DMKCPI	DMKCPP	DMKDSP	DMKEXT
DRHA	000001	DMKCCD	DEROFU	DEROFT	DIINOTI	UNINDOT	DUILLAI
DSPA	000004	DMKDSP	DMKPXA	DMKPXB			
DSPB	000006	DMKDSP	DMKPXA	DMKPXB			

LABEL	COUNT	REFERENCE	ES								
DSPCH	000004	DMKDSP	DMKPXA	DMKPXB							
DSPRQ DSPRU	000011	DMKAPI	DMKCPP	DMKDSP	DMKFRE	DMKPXA	DMKPXB	DMKWAI			
DSPRU	000004	DMKDSP	DMKPXA	DMKPXB							
DSPWA	000004	DMKPXA	DMKPXB	DMKWAI				DUVODT	D1110 010	014/1014	
DUMP	000019	DMKCFC	DMKCKT	DMKCMD	DMKDDR	DMKDMQ	DMKRND	DMKSPT		DMKWRM	
DUMPSAVE	000036	DMKCCH	DMKCKH	DMKCKN	DMKDMP	DMKMCT	DMKMPO	DMKPRG	DMKPSA	DMKSVC	
DWHA D1	000002	DMKCCD DMKCKH	DMKCCO	DMI/EMT							
D2	000005 000010	DMKDDR	DMKDDR DMKFMT	DMKFMT DMKIOE	DMKSAV						
D28	000001	DMKFMT	DERTET	DHILIOL	DHKOAV						
D3	000003	DMKDDR	DMKFMT	DMKPAG							
D4	000022	DMKCKH	DMKCKN	DMKDMP	DMKFMT	DMKFRT	DMKMNT	DMKPAG	DMKSAV	DMKSSP	DMKUNT
D5	000002	DMKDDR	DMKFMT								
D66LNCNT		DMKGRD	DMKQCQ	•							
D66LNLEN		DMKQCQ									
D8	000005	DMKCKN	DMKFMT	DMKLOH	DMKZTD						
ECBLOK	000155	DMKHVC	DWYOVD								
ECKD ECPSOP	000004 000005	DMKCKH DMKFRE	DMKCKP								
ECPSUBTB	000005	DMKFRE									
ECSWBYT3	000001	DMKCCH									
ECSWLOG	000010	DMKCCH	DMKIOG								
EDIT	000041	DMKCFH	DMKCFM	DMKCFU	DMKCNS	DMKCNT	DMKCPJ	DMKEPS	DMKGRG	DMKGRT	DMKMSW
		DMKNLD	DMKNLE	DMKOPE	DMKRGC	DMKRNH	DMKTOD	DMKTRR	DMKVCN	DMKVCR	DMKVCU
EGPRO	000002	DMKIMG									
EGPR15	000007	DMKIMG									
EGPR8	000001	DMKING	DMICAD								
EIGHT EMSINQSC	000006 000003	DMKLDOOE DMKDSP	DMKSAD								
FMSMASK	000003	DMKAPI	DMKDSP	DMKIOT	DMKLOK	DMKPMA					
EMSMASK EMSPCLKC EMSPEND	000003	DMKCLK	DMKEXT	DHILLOT	DAINEON	DEIXTEA					
EMSPEND	000024	DMKCLK	DMKDSP	DMKEXT	DMKMCT						
EMSPEXI	000005	DMKDSP	DMKEXT								
EMSPQUI	000007	DMKDSP	DMKEXT	DMKMCT							
EMSPSHD	000003	DMKEXT	DIMENT								
EMSPSYNC	000004	DMKCLK	DMKEXT								
EMSPXEX EMSRCLKC	000003	DMKEXT DMKCLK									
EMSREC	000008	DMKCLK	DMKDSP	DMKEXT							
EMSREXT	000001	DMKDSP	DIIRDOI	DHILEAT							
EMSRQUI	000001	DMKDSP									
EMSRSHD	000001	DMKEXT									
EMSRSYNC		DMKCLK									
EMSRXEX	000001	DMKEXT									
ENDSIZES ENDWORK	000002	DMKFRE									
ENDWORK	000002	DMKCCW									
ENQ EQCHK	000011 000020	DMKBSC DMKCQT	DMKDAD	DMKDDR	DMKFMT	DMKIOE	DMKRSE				
ERASWRT	000003	DMKVCN	DINKDAD	DIAKODIN	DUKTIN	DHKICL	DHRIGE				
ERRBI OK	000017	DMKERP	DMKIOE	DMKIOF	DMKIOJ						
ERRCCNT	000002	DMKIOE	DMKIOJ								
ERRCCW	000005	DMKIOE	DMKIOJ								
ERRCODE	000003	DMKCQG	DMKVMD								
ERRCONT	000003	DMKIOJ	DMILLOI								
ERRCORR	000003	DMKIOE	DMKIOJ								
ERRCPID	000006 000011	DMKIOE DMKIOE	DMKIOJ DMKIOF								
ERRHEADR		DMKIOE	DENTOF								
		5									

LABEL	COUNT	REFERENC	ES								
ERRIOB ERRIOER ERRKEEP ERRKEY ERRMIOB ERRMIOER ERRMSG	000013 000003 000005 000005 000006 000002 000027	DMKIOE DMKIOE DMKIOE DMKIOE DMKIOE DMKIOE DMKIOE DMKACR	DMKIOF DMKIOF DMKIOJ DMKIOJ DMKIOJ DMKIOJ DMKIOJ	DMKIOJ DMKIOJ DMKCFM	DMKCQI	DMKCQP	DMKDDR	DMKDIR	DMKERM	DMKNLD	DMKNLE
ERRMSIZE	000001	DMKOVR DMKIOE	DMKPRG	DMKRNH	DMKTRR	DMKURS					
ERROBR ERROR ERRPARM ERRPATH	000008 000118 000004 000005	DMKERP DMKMCD DMKIOE DMKIOF	DMKIOE DMKMNI DMKIOF	DMKIOF DMKMON			·*				
ERRSDR	000009	DMKIOE	DMKIOF	DMKIOJ							
ERRSRDEV ERRSW2	000001	DMKERP DMK10E	DMKIOE								
ERRVOLID ETB ETX EWA	000006 000006 000020 000003	DMKIOE DMKRGD DMKRGD DMKGRG	DMKIOJ								
EXCLOCK EXCOLOR EXDCCF	000004 000009 000001	DMKFRE DMKGRE DMKMCH	DMKFRT								
EXDCNO EXDINSTO EXDINTTO EXDRESVD EXHAUST	000001 000001 000001 000001	DMKMCH DMKMCH DMKMCH DMKMCH									
FXHIII(GH	000009	DMKMIA DMKGRE									
EXNPSW EXOPSW EXPSS	000016 000029 000003	DMKAPI DMKAPI DMKGRE	DMKCPI DMKDSP	DMKDSP DMKEXT	DMKEXT DMKMPO	DMKMCT DMKPMA	DMKMPO DMKSAD	DMKPMA	DMKSAD	DMKSAV	
EXSUBTOP EXTMASK EXTMODE	000003 000017 000097	DMKFRE DMKACR DMKAPI DMKDMQ DMKPRG DMKVAT	DMKFRT DMKAPI DMKCDS DMKDSP DMKPRV DMKVER	DMKCFH DMKCFF DMKFPS DMKPRW DMKVM1	DMKCPI DMKCFG DMKMHC DMKPSA	DMKCPU DMKCFV DMKPE1 DMKSAD	DMKEXT DMKCLK DMKPEL DMKSTA	DMKSVD DMKCPB DMKPEN DMKSVD	DMKTMR DMKCPI DMKPER DMKTOD	DMKTRC DMKCPJ DMKPET DMKTRC	DMKDMP DMKPMA DMKTRD
EXTRACTL EXTVPORL FAILADD FAILCCW FAILCSW FAILFCSW	000010	DMKVAT DMKPRV DMKHVC DMKCCH DMKCCH DMKCCH DMKCCH	DMRVER	UNKANI							
FAILECSW FAILSTAD FAKEATT	000003	DMKPMA DMKGR I									
FASTCPU	000002 000089	DMKAPI DMKAPI DMKPRG DMKACR	DMKCPI DMKCFO DMKPTR	DMKCFV DMKPTS DMKAP1	DMKCLK DMKPTT	DMKCPI DMKSEL	DMKCPO DMKSTA	DMKCPP	DMKCPU DMKVRR	DMKMNT DMKWRM	DMKOPE
FFS	000413	DMKACR DMKCCS DMKCFR DMKCPI DMKCRM DMKDAU	DMKACS DMKCCW DMKCKD DMKCPJ DMKCSQ DMKDEF	DMKCDB DMKCKF DMKCPM DMKCSR DMKDGD	DMKAPS DMKCDM DMKCKH DMKCPN DMKCST DMKDGF	DMKAPT DMKCFC DMKCKP DMKCPO DMKCSU DMKCSU	DMKAPU DMKCFF DMKCKR DMKCPT DMKCSV DMKDIB	DMKATS DMKCFG DMKCKS DMKCPV DMKCSW DMKDMQ	DMKBLD DMKCFM DMKCKT DMKCPY DMKCSX DMKDRD	DMKCCF DMKCFP DMKCKV DMKCPZ DMKDAD DMKDSP	DMKCCH DMKCFQ DMKCPB DMKCQP DMKDAS DMKEIG
		DMKEPS DMKIDR	DMKERP	DMKFRE DMK10G	DMKGRC DMK I OQ	DMKGRD DMK10S	DMKGRF DMK10T	DMKGRT DMK I US	DMKHPS DMKJRL	DMKHVC DMKLNM	DMKHVE DMKLOH

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FIRSTRI FLAG FLAGS FLAG2 FNAME FORMBLI
FORMFL FORMNA FORMNT FORMOP FORMSE FORMUS FPREGS FPRLOG FRADDR

COUNT

REFERENCES

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LADEL	COONT	NEFENER	10							
		DMI/LO I	DMI/MOO	DMI/MOU	DMI/MOT			DMKMN I	DMKMNJ	DMKMNT
		DMKLOJ	DMKMCC	DMKMCH	DMKMCT	DMKMIA	DMKMID			
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		DMKVCH	DMKVCN	DMKVCV	DMKVDB	DMKVDC	DMKVDD	DMKVDE	DMKVDF	DMKVDH
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		DMKVSI	DMKVSP	DMKVSQ	DMKVST	DMKVSV	DMKWRM	DMKXAB	DMKXAD	DMKZTD
FIRSTRCW	00001/	DMKCCD	DMKCCO	DMKCCS	DMKCCW	Drinteot	Drittmini	DITIONED	DITIONED	DINCEID
			DMKCCU			DMI/DCO	DMI/DID	DMI/DMD		DMKFMT
FLAG	000068	DMKCPZ	DMKCSW	DMKDAD	DMKDAS	DMKDEG	DMKDIR	DMKDMP	DMKERP	DMKENT
FLAGS	000040	DMKCQP	DMKCQQ	DMKCQT	DMKPEI	DMKPST	DMKSPT			
FLAG2	000005	DMKTRA	DMKVMI							
FNAME	000001	DMKACO								
FORMBLOK	000011	DMKAPU	DMKAPV	DMKCKF	DMKCKR	DMKCSR	DMKCSW	DMKMNT	DMKSEP	DMKSPL
		DMKVSQ								
FORMFLAG	000002	DMKSEP	DMKVSQ							
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FORMOPER	000008			DMKCKF					DHKOFL	
FORMSEND	000007	DMKAPU	DMKAPV	DMKCKF	DMKCSW	DMKSEP	DMKSPL	DMKVSQ	DUVODI	DMUM
FORMUSER		DMKAPU	DMKAPV	DMKCKF	DMKCKR	DMKCSR	DMKCSW	DMKSEP	DMKSPL	DMKVDS
FPREGS	000006	DMKDMQ								
FPRLOG	000007	DMKDMP	DMKPMA	DMKSAD						
FRADDR	000004	DMKFRE	DMKFRT							
FRBYTES	000002	DMKFRT								
FRECALLR		DMKFRE								
FREDPACA		DMKFRE								
FREE	000024	DMKFRE	DMKMOO	DMKPXA	DMKPXB					
FREEH		DMKFRE	DHKHUU	DHILTMA	DERLIND					
	000007									
FREELOWE		DMKNMT								
FREENUM	000001	DMKMOO								
FREEP	000008	DMKFRE								
FREERO	000009	DMKFRE	DMKFRT							
FREER1	800000	DMKFRE								
FREER12	000001	DMKFRE								
FREER13	000001	DMKFRT								
FREER14	000003	DMKFRE	DMKFRT							
FREER15	000011	DMKFRE	DMKFRT							
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FREEXT										
	000014	DMKFRE	DMKFRT	DMKSVC						
FRELSNSS	000002	DMKFRE								
FRELSTTS		DMKFRE								
FRENAME	000001	DMKFRE								
FRLKPROC	000006	DMKDSP	DMKFRE							
FROSIZE	000007	DMKFRE	DMKFRT							
FRPALLOC		DMKFRE	DMKFRT	DMKSVC						
FRPCACHL		DMKFRT	21.000	5111010						
FRPMCALR	000003	DMKFRE	DMKSVC							
FRPMEXT	000010	DMKFRE	DMKFRT	DMKSVC						
FRPMNAME				DINKSYC						
		DMKFRE	DMKSVC	DMICONC						
FRPMSIZE		DMKFRE	DMKFRT	DMKSVC						
FRPMTAG	000008	DMKFRE	DMKFRT	DMKSVC						
FRPRETRN		DMKFRT	DMKSVC							
FRPSIZE	000003	DMKFRE	DMKFRT	DMKSVC						
FRSIZE	000005	DMKFRE	DMKFRT							
FRTAG	000006	DMKFRE	DMKFRT	DMKSVC						

DMKMON DMKPGS DMKRSP DMKSSS DMKTRK DMKVCB DMKVDR DMKVSG

DMKVDS

DMKVSQ

	LABEL	COUNT	REFERENCES									
	FSCBANIT FSCBBUFF FSCBDIF FSCBNOIT FSCBSIZE FSIZE FSREAD FSTD FSTFMODE FSTFNAME FSTFNAME FSTFNAME FSTFNAME FSTFNAME FSTRECCT FSTRECCT FSTRECCT FSTRECCT FTRAWSF FTRDIAL FTREXTSN FTRFH FTRNODRP FTROPRDR FTRRPS FTRRSRL FTRTYP1 FTRUCS	000005 000006 000011 000003 000002 000010 000004 000001 000001		MKVCP MKRGA	DMKRGB	DMKRGC	DMKVCN					
	FTREXTSN FTRFH FTRNODRP	000007 000007 000007 000002	UPINGNU UP	MKDAS	DMKDSB DMKPAG	DMKRGC DMKIOE DMKVDE	DMKVCN DMKIOJ	DMKRSF	DMKSSP			
	FTRRPS FTRRSRL FTRTYP1 FTRUCS	000009 000010 000003 000002	DMKBIO DM DMKCCS DM DMKNLD DM DMKCSB DM	MKDAS MKDAD MKNLE MKSSP	DMKDSB DMKDAS	DMKIOS DMKDAU	DMKMNT DMKDSB	DMKVDE DMKMNT	DMKTAP	DMKVDS		
	FTRUCS FTRVIRT FTR2311B FTR2311T FTR3088 FTR3270 FTR3270E FTR35MB	000004 000005 000005 000006 000003	DMKDEF DM	MKDIR MKVDS	DMKVDE DMKSCO DMKSCO DMKSCN	DMKVER DMKVER DMKVDS						
	FTR3270E	000004	DMKDTD DM DMKALO DM DMKVDE DM	MKBIO MKVER	DMKHVE DMKCCD DMKVSC	DMKDAS	DMKDSB	DMKHVE	DMKLNK	DMKMNT	DMKPMA	DMKUNT
	FTR4WCGM FTR70MB	000017	DMKDEF DM DMKDAS DM DMKZTD	MKDEG MKDSB	DMKD I R DMKLNK	DMKTCT DMKMNT	DMKVDS DMKPAG	DMKPMA	DMKSPK	DMKVDE	DMKVDG	DMKVSC
•	FWDTIC FXDLOG F0	000003 000022 000475	DMKACO DM DMKCDS DM DMKCFV DM DMKCPO DM DMKCSC DM DMKFRE DM DMKFRE DM DMKIUB DM DMKIUB DM DMKNLE DM DMKNLE DM DMKSCN DM DMKSCN DM DMKSCN DM DMKTCT DM DMKTCT DM DMKVCP DM DMKVCP DM DMKVSI DM DMKVSI DM	MKUPE MKPTR MKSEG MKTMR MKUDR MKVCQ MKVCQ MKVDR MKVSJ	DMKDMP DMKAPT DMKCFD DMKCFD DMKCPS DMKCSW DMKGRA DMKHVD DMKHVD DMKNCD DMKNCD DMKSEL DMKSST DMKVDD DMKVCR DMKVDS DMKVSP	DMK10G DMKATS DMKCFF DMKCFF DMKCPU DMKCSY DMKGRC DMKHVE DMKIUG DMKHVE DMKPEI DMKSEP DMKSSV DMKSSV DMKTRC DMKVCS DMKVSQ	DMKPMA DMKBIO DMKCFG DMKCFW DMKCPW DMKCPW DMKCAU DMKGRF DMKIUL DMKFER DMKFER DMKSFB DMKSFB DMKSFB DMKSFD DMKVCT DMKVCT DMKVSR	DMKQVM DMKBLD DMKCFH DMKCFY DMKCPY DMKDRD DMKGRG DMKIUN DMKNJ DMKFGM DMKSTR DMKSTR DMKSTR DMKVAT DMKVAT DMKVAT DMKVSX	DMKCCH DMKCFM DMKCRS DMKCQC DMKDRE DMKGRI DMKIUS DMKMNL DMKPGS DMKSPC DMKSPC DMKSVC DMKVCV DMKVCV DMKVCV DMKVRM	DMKCCW DMKCFQ DMKCPB DMKCQS DMKERM DMKGRT DMKJRL DMKJRL DMKJRL DMKSVD DMKSVD DMKSVD DMKTRT DMKVBM DMKVCW DMKVRN	DMKCDB DMKCFS DMKCPI DMKCQU DMKEXT DMKHPS DMKIOT DMKKSRG DMKPRG DMKSPS DMKTAP DMKTAP DMKTAP DMKVCK DMKVCK DMKVCSE DMKXAB	DMKCDM DMKCFU DMKCFU DMKFMT DMKHPT DMKIUA DMKLOH DMKNLD DMKPRV DMKRST DMKTCS DMKTCS DMKTCS DMKTCS DMKVCC DMKVSG DMKXAD
	F1	000626	DMKACO DM	MKACR	DMKACS	DMKALG	DMKALO	DMKAPI	DMKATS	DMKBLD	DMKBSC	DMKCCF

System Logic and Problem Determination Guide-CP

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COUNT REFERENCES

		DMKCCH	DMKCCW	DMKCDB	DMKCDM	DMKCDS	DMKCFD	DMKCFF	DMKCFG	DMKCFH	DMKCFJ
		DMKCFP	DMKCFQ	DMKCFR	DMKCFT	DMKCDS DMKCFU	DMKCFV	DMKCFY	DMKCKF	DMKCKM	DMKCKR
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		DMKCPY	DMKCQG	DMKCQH	DMI/COL	DMKCQP	DMICCOO	DMKCQR	DMKCQS	DMKCQT	DMKCOV
		DMKOPT	DMKCQG	DMKCQT	DMKCQ I DMKCSP	DMKCQP	DMKCQQ	DMKCST	DMKCSU	DMKCSV	DMICCOU
		DMKCSB	DMKCSC	DMKCSF	DMKCSP	DMKCSQ	DMKCSR	DMKCST	DMKCSU	DMKC3V	DMICOW
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		DMI//MD	DMKIGC	DMI/VAR	DHKIUC	DHKHJM	DHILLAO	DHRQUQ	DRIKSTIK	DHINGTA	DARTDO
F15	000107	DMIATS	DMKIOH DMKVSC DMKBLD DMKCFR	DMKXAB DMKBSC	DMI/COD	DMKCCF	DMKCCO	DMKCCT	DMKCCW	DMKCDB	DMKCDS
F 15	000107	DMKAIS		DMKCNS	DMKCCD DMKCPP	DMKCOF	DMKDAS	DMKDEC	DMKCCW		DMKCDS DMKFPS DMKPGS
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		DMIXIN	DEILEGA	UNIKIKU	DHILODO	DEIKON	DHKYAT	DHILYUA	DHKYUD	DULLADO	DUILADI
F255	000090	DMKACO			DMKCAC		DMKCFF	DMKCEM	DMKCEO	DMKCFT	DMKCEV
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		DMKCSP	DMKCKF	DMKDEF				DMKDSP	DMKERM	DMKCQU DMKEXT	DMKCOA
		DINKUSP	DEIKCOU	UPIKUEF	DMKDIA	DMKDIB	DMKDRD	DUNINUSP	UPIKEKPI	DPIKEAT	DMKGRA

LABEL	COUNT	REFERENC	CES								
		DMKHVD DMKNEA DMKRSQ DMKVDS	DMKHVE DMKNES DMKSCN DMKVSQ	DMKIDU DMKNET DMKSTA DMKVSR	DMKIOE DMKQCN DMKTDK	DMKIOF DMKQCO DMKTRD	DMKIOG DMKRGB DMKUNT	DMKLOG DMKRGC DMKVAT	DMKLOH DMKRGD DMKVAU	DMKMCH DMKRNH DMKVCN	DMKMCT DMKRSP DMKVDR
F256	000070	DMKATS DMKDRD DMKNLD DMKVBM	DMKCFC DMKDRE DMKNLE DMKVCN	DMKVSR DMKCFF DMKEXT DMKQCO DMKVMA	DMKCFH DMKGRI DMKQCQ DMKVSP DMKCAO DMKCFO DMKCFO	DMKCFO DMKHVC DMKRGB DMKVSQ	DMKCKT DMKHVD DMKRNH DMKVSB	DMKCKV DMKHVF DMKSCH DMKVSV	DMKCNT DMKIOE DMKSNC DMKWRM	DMKCPP DMKIOS DMKTCS	DMKDAS DMKMNT DMKTDK
F3	000244	DMKRSQ DMKVDS DMKVDS DMKDRD DMKVBM DMKVBM DMKCFG DMKCFG DMKCFG DMKCFF DMKHVC DMKRCT DMKVCN DMKVCN DMKVCN DMKVCSQ DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKDC DMKDC DMKDC DMKDC DMKCCS DMKDC DMKDC DMKDC DMKCCS DMKDC DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCS DMKCS DMKCS DMKCS DMKCCS DMKCCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS D	DMKAPX DMKCFH DMKCPT DMKCSO DMKDEG DMKHVD	DMKCAC DMKCFJ DMKCPV DMKCSP DMKDGD DMKHVE	DMKCAO DMKCFO DMKCPY DMKCSQ DMKDIA DMKLNM	DMKCFO DMKHVC DMKRGB DMKCCD DMKCCD DMKCCFT DMKCQC DMKCSR DMKDIB DMKLOH DMKMSG DMKPEQ DMKSPM DMKTRD	DMKCKT DMKHVD DMKRNH DMKCCW DMKCCW DMKCGG DMKCGG DMKCGJ DMKLOJ DMKNEA DMKNEA DMKNEA DMKSPT DMKTRK	DMKVSV DMKCDB DMKCCFW DMKCQH DMKCSU DMKDSP DMKLOM DMKNES	DMKWKM DMKCCM DMKCCY DMKCQP DMKCSV DMKEXT DMKMCC DMKNET	DMKCFD DMKCKT DMKCQQ DMKCSX DMKGRG DMKMCD DMKNLD DMKRGC DMKRAD	DMKCFF DMKCPB DMKCSB DMKDAD DMKGRT DMKMCH DMKNLE
c.		DMKMCT DMKPAG DMKRSP DMKTCT DMKVCN DMKVCQ	DMKMTA DMKPEI DMKSAV DMKTHI DMKVCU DMKVST	DMKMON DMKPEL DMKSPK DMKTOD DMKVDC DMKVSV	DMKCSQ DMKDIA DMKLNM DMKNOO DMKPEN DMKSPL DMKSPL DMKVDE DMKVSW DMKCFF DMKCSU DMKCSU DMKCSU DMKCSU DMKCSU DMKCSU DMKLOE DMKLOE DMKLOE DMKLOE DMKNEA DMKNEA		DMKVFD	DMKQCQ DMKSRM DMKTRP DMKVFE	DMKRGB DMKSTP DMKTTX DMKV10		DMKRSE DMKTCS DMKVCH DMKVSP
F4	000286	DMKACS DMKCDS DMKCFU DMKCPT DMKCSO DMKDID	DMKAPU DMKCFC DMKCKM DMKCPU DMKCSP DMKEPS	DMKATS DMKCFD DMKCKT DMKCPV DMKCST DMKERM	DMKBLD DMKCFF DMKCNS DMKCPY DMKCSU DMKEXT	DMKVDX DMKCCD DMKCCG DMKCFG DMKCPB DMKCQH DMKCSV DMKFPS	DMKCCO DMKCFH DMKCPI DMKCQQ DMKCSW DMKGRF	DMKCCT DMKCFO DMKCPJ DMKCQS DMKCSX DMKGR1	DMKCCW DMKCFQ DMKCPN DMKCVU DMKCVU DMKHPS DMKIUA DMKMCD DMKRPA DMKRPA	DMKCDB DMKCFS DMKCPP DMKCSB DMKDEF DMKHPT	DMKCDM DMKCFT DMKCPS DMKCSF DMKDGD DMKHVC
. ,		DMKHVD DMKLNK DMKMOO DMKPEN DMKSAV DMKSVD DMKVCN DMKVSP	DMKHVE DMKLNM DMKMSG DMKPEQ DMKSEG DMKTAQ DMKVCQ DMKVCQ	DMKHVF DMKLOG DMKMSW DMKPRV DMKSEP DMKTHI DMKVCU DMKVCU	DMKTMR DMKVDC	DMKIOF DMKLOJ DMKNES DMKPST DMKSPM DMKTOD DMKVDE DMKVSP	DMKIOJ DMKLOM DMKNET DMKRGB DMKSPT DMKTRD DMKVDH	DMKISM DMKNCC DMKNLE DMKRNH DMKSRM DMKTRP DMKVER		DMKI UN DMKMIA DMKPAG DMKRSE DMKSSV DMKUSQ DMKVRR	DMKHVC DMKJRL DMKMON DMKPEL DMKRSP DMKSTA DMKVCH DMKVSC
F4095	000097	DMKBLD DMKCSC DMKIOE DMKPRV DMKTMR DMKVSI	DMKCCD DMKDAD DMKIOF DMKPRW DMKTRT DMKVSQ	DMKCCO DMKDAS DMKIUA DMKPTR DMKTRX DMKVSR	DMKCCW DMKDAU DMKIUE DMKQCN DMKVCN DMKVSV	DMKCDB DMKDGD DMKIUL DMKRGC DMKVER DMKVSX	DMKCDM DMKFPS DMKLNK DMKRSP DMKVFR	DMKCDS DMKGRG DMKMN I DMKSCN DMKV I O	DMKCFG DMKHVC DMKMON DMKSTA DMKVMA	DMKCPJ DMKHVD DMKPER DMKSVC DMKVME	DMKCPY DMKHVF DMKPMA DMKTCS DMKVSF
F4096	000361	DMKHVD DMKLNK DMKLNK DMKSAV DMKSAV DMKSAV DMKSAV DMKVSP DMKVSP DMKVSP DMKCSC DMKCKS DMKCKS DMKCKS DMKCKS DMKCKS DMKCKS DMKCKS DMKKNLE DMKKNLE DMKKNLE DMKKNCD DMKKNCD DMKKDS DMKCDS DMKCDS DMKCDS	DMKVSQ DMKVSQ DMKVSQ DMKVFC DMKVCR DMKVCR DMKVCR DMKVCR DMKCFH DMKCFH DMKCFH DMKCFH DMKCFH DMKCFU DMKCFU DMKVST DMKVST DMKVST DMKVST DMKVST DMKVST DMKVST DMKVST DMKVST DMKVST DMKVSQ DMKKSEG DMKKSEG DMKKSEG DMKKSEG DMKKSEG DMKKVCQ DMKKSEG DMKKVCQ DMKKSEG DMKKVCQ DMKKVCQ DMKKCCD DMKKCCD DMKKCCD DMKKCCD DMKKCCD DMKKCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD DMKCCCD	DMKEXT DMKCAC DMKCGA DMKCFJ DMKCAC DMKCFJ DMKCSP DMKCSD DMKCSD DMKKOSD DMKKOSD DMKKOSV DMKCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCS	DMKVST DMKCCW DMKCCW DMKIUE DMKQCN DMKVCN DMKVSV DMKBIO DMKCFP DMKCFP DMKCFP DMKCRE DMKCRE DMKCRE DMKCRE DMKSEG DMKTRT	DMKVSV DMKCDB DMKDGD DMKIUL DMKRGC DMKVER DMKVER DMKCCH DMKCFS DMKCCP DMKCCP DMKCCP DMKCRF DMKLOH DMKPRW DMKSEL DMKTRU	DMKCCS DMKCKF DMKCPS DMKDGD DMKGRI DMKRCH DMKPSA DMKSNC DMKSNC	DMKCCW DMKCKH DMKCPU DMKDRD DMKHPS DMKMNT DMKPST DMKSPL DMKUDR	DMKCDB DMKCKM DMKCPY DMKDRE DMKHPT DMKMOO DMKPTR DMKSPM DMKSPM	DMKCDM DMKCKP DMKCQY DMKDSP DMKHPU DMKMPO DMKPTT DMKSST DMKVBM	DMKCDS DMKCKR DMKCSC DMKFPS DMKHVC DMKNLD DMKQVM DMKSTA DMKVCN
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F6	000107	DMKVDE DMKAPS DMKCQP	DMKVDF DMKCDB DMKCSO	DMKVFC DMKCDM DMKCSU	DMKVMD DMKCDS DMKCSV	DMKVME DMKCFD DMKCSX	DMKVSP DMKCFH DMKDGD	DMKVST DMKCFJ DMKEXT	DMKCFU DMKFPS	DMKCKT DMKHVC	DMKCPB DMKHVE

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									DMKPEL
						DMKPCT	DMKSCH		DMKSWM
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<u>(</u> )	0000112								DMKHVC
60	000043								
							DMKPRW	DMKPSA	DMKRNH
-								<b>B1</b> 110 <b>FF</b>	<b>N</b> 11/0 F0
7	000084								DMKCFG
									DMKDIB
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									DMKRSE
		DMKSIX		DMKTMR	DMKTOD			DMKUNT	DMKVCA
		DMKVDC	DMKVDT	DMKVER	DMKVMC	DMKVSP	DMKVST		
8	000362	DMKACS	DMKATS	DMKBLD	DMKBSC	DMKCAC	DMKCCD	DMKCCF	DMKCCT
		DMKCFC	DMKCFF	DMKCFH	DMKCFO	DMKCFQ	DMKCFS	DMKCFT	DMKCFU
						DMKCPP	DMKCPT	DMKCPV	DMKCQH
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		DMKCSV							DMKDEG
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		DMKNLD							
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		DMKVCN							DMKVDF
									DMKXST
9	000023	DMKACS	DMKCAC	DMKCCD	DMKCCO				DMKEXT
		DMKMCD	DMKMSG	DMKQCN	DMKQCQ	DMKSEP	DMKUNT	DMKVDC	DMKVDG
RACDAPL	000003	DMKGR I							
RACDTXT		DMKGR I							
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	000015	DMKGRD	DMKGRG						
RCVMRD	000006	DMKGRD							
		DMKGRD	DMKGRE	DMKGRF	DMKGRG				
REBI	000004								
	000004			DMKGRF	DMKGRG				
REBT	000004	DMKGRD	DMKGRE	DMKGRF DMKGRF					
REBT RECR	000004 000004	DMKGRD DMKGRD	DMKGRE DMKGRE	DMKGRF	DMKGRG				
REBT RECR REC1	000004 000004 000004	DMKGRD DMKGRD DMKGRD	DMKGRE DMKGRE DMKGRE						
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	60 7 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8	7         000084           8         000362           8         000023           RACDAPL         000003           RACDTXT         000003           RACDTXT         000003           RACDTXT         000003           RACDTXT         000003           RACDTXT         000003           RACDEV78         000005           RAFDEV         000007           RCALRM         000009           RCCLRIN         000006           RCEHLT         000006           RCHUN         000007           RCMXDLN         000006           RCINH         000007           RCMXDLN         000006           RCINALEN         000006           RCNAC         000006           RCNAC         000007           RCMXDLN         000007           RCMXDLN         000007           RCMXDLN         000007           RCMXDLN         000007           RCMXDLN         000006           RCNAC         000006           RCNAC         000006           RCSFSTA         000018	7         000084         DMK1UA DMKTMR           7         000084         DMKCPI DMKKCPI           0MKVDC         DMKVDC           8         000362         DMKCFC           0MKVDC         DMKCCR           0MKCQR         DMKCQR           0MKNLD         DMKKCSV           0MKRSP         DMKKCSV           0MKNLD         DMKVDC           9         000023           RACDAPL         000003           RACDAPL         000003           RACDAPL         000003           RACDAPL         000005           DMKGRI         RAFESC           RAFESC         000007           DMKGRI         RAFESC           RCCLRIN         000007           RCCLRIN         000006           RCCLRIN         000007           RCCLRIN         000007           RCCLRIN         000006           RCKRD         DMKGRD           RCMXDLN         000007           DMKGRD         DMKGRD           RCCLRIN         000006           DMKGRD         DMKGRD           RCCLRIN         000007           RCKRGR         DMKGRD	DMKNEA DMKNES DMKPTS DMKQVM DMKTRK DMKUNT 60 000043 DMKACO DMKCFJ DMK1UA DMKMCD DMKTMR DMKTOD 7 000084 DMKBLD DMKBSC DMKCPI DMKCPP DMKHVF DMK10E DMKKVC DMKCPF DMKVVC DMKVDT 8 000362 DMKACS DMKATS DMKCFC DMKCFF DMKCFY DMKCNS DMKCGR DMKCQT DMKCQR DMKCQT DMKCQR DMKCQT DMKCQR DMKCQT DMKCSV DMKCSW DMKDGF DMKD1D DMKNLD DMKNLE DMKNDD DMKNLE DMKNDD DMKNLE DMKNDD DMKNLE DMKSSS DMKSST DMKSSS DMKSST DMKCQR DMKCQC DMKVCN DMKVCQ DMKVCD DMKNLC DMKNCD DMKNLC DMKNDD DMKNLC DMKNDD DMKNLC DMKNCD DMKKC1 DMKNCD DMKNC1 DMKNCD DMKNC1 DMKNCD DMKNC2 MKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ C Q C Q DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ C Q DMKQQ DMKQQ DMKQQ DMKQQ DMKQQ C Q DMKQQ DMKQQ DMKQQ DMKQQ C DMKQQ DMKQQ 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DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKCFY DMKCFY DMKCFY DMKCRC DMKCRC DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCC DMKCCR DMKCCR DMKCCC DMKCCR DMKCCD DMKCCR DMKCCC DMKCCC DMKCCR CCLRIN 000007 DMKGRI RCALRM 000007 DMKGRD RCCLRIN 000006 DMKGRD RCCLNA 000006 DMKGRD RCCLNA 000007 DMKGRD RCCLNA 000006 DMKGRD RCMNLEN 000007 DMKGRD RCMNLEN 000007 DMKGRD RCMNLEN 000006 DMKGRD RCMNLEN 000007 DMKGRD RCMNLEN 000007 DMKGRD RCMNLEN 000006 DMKGRD RCMNLEN 000007 DMKGRD RCMNLEN 000006 DMKGRG RCMNLEN 000007 DMKGRD RCMNLEN 000006 DMKGRG RCMNLEN 000007 DMKGRD RCMNLEN 000006 DMKGRG RCMNLEN 000006 DMKGRG RCMNLEN 000007 DMKGRD DMKGRG RCMNLEN 000006 DMKGRG RCMNLEN 000007 DMKGRD DMKGRG RCMNLEN 000006<br< td=""><td>DMKNEA         DMKNET         DMKNET         DMKNEGA         DMKRGC         DMKNET         DMKNECT         DMKNECT         DMKNECT         DMKNECT         DMKNECT         DMKNET         DMKNEGT         DMKNET         DMKNEGT         DMKNET         DMKNECT         DMKNECT</td><td>DMKNEA         DMKNES         DMKNET         DMKNLD         DMKNLE         DMKACL           60         000043         DMKTRK         DMKUNT         DMKVDC         DMKVDE         DMKVDF         DMKVDF           60         000043         DMKACD         DMKUNT         DMKVDC         DMKVDE         DMKVDF         DMKVDF           7         000084         DMKCPT         DMKCFD         DMKCFF         DMKCCF         DMKCCH         DMKCCF           7         000084         DMKNDC         DMKKCF         DMKCFT         DMKCFD         DMKCCH         DMKCCF           7         000084         DMKNDC         DMKKCF         DMKCFC         DMKCCF         DMKCCH         DMKCGC         DMKCCF         DMKCFD         DMKCFD         DMKCFD         DMKNDD         DMKKRS         DMKNSC         DMKCFC         DMKCFCF</td><td>OMKNEA         DMKNEA         DMKNET         DMKNLE         DMKNLE         DMKRSE         DMKRSE&lt;</td></br<></td>	DMKNEA DMKPTS DMKQVMDMKNET DMKRGA DMKRGC DMKRGC DMKCC DMKCC DMKCC DMKCC DMKCC DMKCC DMKCC DMKCC DMKCC DMKCC DMKCC DMKTR DMKTR DMKTR DMKTR DMKTR DMKTR DMKTR DMKTR DMKTR DMKTR DMKCP1 DMKCP1 DMKCP1 DMKCP1 DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKNOC DMKCFY DMKCFY DMKCFY DMKCRC DMKCRC DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCR DMKCCC DMKCCR DMKCCR DMKCCC DMKCCR DMKCCD DMKCCR DMKCCC DMKCCC DMKCCR CCLRIN 000007 DMKGRI RCALRM 000007 DMKGRD RCCLRIN 000006 DMKGRD RCCLNA 000006 DMKGRD RCCLNA 000007 DMKGRD RCCLNA 000006 DMKGRD RCMNLEN 000007 DMKGRD RCMNLEN 000007 DMKGRD RCMNLEN 000006 DMKGRD RCMNLEN 000007 DMKGRD RCMNLEN 000007 DMKGRD RCMNLEN 000006 DMKGRD RCMNLEN 000007 DMKGRD RCMNLEN 000006 DMKGRG RCMNLEN 000007 DMKGRD RCMNLEN 000006 DMKGRG RCMNLEN 000007 DMKGRD RCMNLEN 000006 DMKGRG RCMNLEN 000006 DMKGRG RCMNLEN 000007 DMKGRD DMKGRG RCMNLEN 000006 DMKGRG RCMNLEN 000007 DMKGRD DMKGRG RCMNLEN 000006 <br< td=""><td>DMKNEA         DMKNET         DMKNET         DMKNEGA         DMKRGC         DMKNET         DMKNECT         DMKNECT         DMKNECT         DMKNECT         DMKNECT         DMKNET         DMKNEGT         DMKNET         DMKNEGT         DMKNET         DMKNECT         DMKNECT</td><td>DMKNEA         DMKNES         DMKNET         DMKNLD         DMKNLE         DMKACL           60         000043         DMKTRK         DMKUNT         DMKVDC         DMKVDE         DMKVDF         DMKVDF           60         000043         DMKACD         DMKUNT         DMKVDC         DMKVDE         DMKVDF         DMKVDF           7         000084         DMKCPT         DMKCFD         DMKCFF         DMKCCF         DMKCCH         DMKCCF           7         000084         DMKNDC         DMKKCF         DMKCFT         DMKCFD         DMKCCH         DMKCCF           7         000084         DMKNDC         DMKKCF         DMKCFC         DMKCCF         DMKCCH         DMKCGC         DMKCCF         DMKCFD         DMKCFD         DMKCFD         DMKNDD         DMKKRS         DMKNSC         DMKCFC         DMKCFCF</td><td>OMKNEA         DMKNEA         DMKNET         DMKNLE         DMKNLE         DMKRSE         DMKRSE&lt;</td></br<>	DMKNEA         DMKNET         DMKNET         DMKNEGA         DMKRGC         DMKNET         DMKNECT         DMKNECT         DMKNECT         DMKNECT         DMKNECT         DMKNET         DMKNEGT         DMKNET         DMKNEGT         DMKNET         DMKNECT         DMKNECT	DMKNEA         DMKNES         DMKNET         DMKNLD         DMKNLE         DMKACL           60         000043         DMKTRK         DMKUNT         DMKVDC         DMKVDE         DMKVDF         DMKVDF           60         000043         DMKACD         DMKUNT         DMKVDC         DMKVDE         DMKVDF         DMKVDF           7         000084         DMKCPT         DMKCFD         DMKCFF         DMKCCF         DMKCCH         DMKCCF           7         000084         DMKNDC         DMKKCF         DMKCFT         DMKCFD         DMKCCH         DMKCCF           7         000084         DMKNDC         DMKKCF         DMKCFC         DMKCCF         DMKCCH         DMKCGC         DMKCCF         DMKCFD         DMKCFD         DMKCFD         DMKNDD         DMKKRS         DMKNSC         DMKCFC         DMKCFCF	OMKNEA         DMKNEA         DMKNET         DMKNLE         DMKNLE         DMKRSE         DMKRSE<

REFERENCES

COUNT

DMKMSG DMKPEQ DMKTAP DMKVST DMKHVE DMKSVD

DMKCFU DMKHPT DMKMCD DMKSCN DMKVCN

DMKCCW DMKCFV DMKCQP DMKCST DMKDEI DMKHVC DMKHVC

DMKLOJ DMKNES DMKPMA DMKRSE DMKSPT DMKTAQ DMKUDR DMKVDS DMKZTD DMKGRD

DMKMSW DMKPRV DMKTPE

DMKIOE DMKTHI

DMKCFY DMKHPU DMKMCH DMKSEV DMKVDA

DMKCDS DMKCFW DMKCQQ DMKCSU DMKDGD DMKHVD

DMKHVD DMKMCC DMKNET DMKPRV DMKRSF DMKSRM DMKTHI DMKVAT DMKVER

DMKHVE

LABEL	COUNT	REFERENCE	ES		
GREDBCCW GREDCIOS GREDCK1D GREDCVMP GREDFIOB	000003 000003 000003 000003 000003 000001	DMKGRE DMKGRE DMKGRE DMKGRE DMKGRF	DMKGRF DMKGRF DMKGRF ØMKGRF	DMKGRG DMKGRG DMKGRG DMKGRG	
GREDIIOB GREDNCTL GREDNSEL GREDRD77	000002 000003 000003 000001	DMKGRF DMKGRE DMKGRE DMKGRG	DMKGR I DMKGRF DMKGRF	DMKGRG DMKGRG	
GREDRIOB GREDRSCP GREDWBOK GREFC	000004 000003 000003 000002	DMKGRE DMKGRE DMKGRE DMKGRE	DMKGRF DMKGRF DMKGRF DMKGRG	DMKGRG DMKGRG DMKGRG	DMKGR I
GREFCCLR GREFI GREFL GREFLTST	000001 000004 000003 000001	DMKGRG DMKGRD DMKGRD DMKGRG	DMKGRE DMKGRE	DMKGRF DMKGRF	DMKGRG
GREFR GREFS GREFSINT GREFSMOR	000003 000002 000001 000002	DMKGRE DMKGRE DMKGRG DMKGRD	DMKGRF DMKGRF DMKGRG	DMKGRG	
GREFSM77 GREFSREJ GREFSR77 GREFSWNG	000001 000001 000001 000001	DMKGRG DMKGRG DMKGRG DMKGRG			
GREFSW77 GREFT GREFTFMT GREFUTCP	000001 000003 000001 000001	DMKGRG DMKGRE DMKGRD DMKGRE	DMKGRF	DMKGRG	
GREGB77C GREGC GREGCMOR GREGGCLR GREGGCLT	000001 000004 000001 000001 000001	DMKGRE DMKGRD DMKGRD DMKGRD DMKGRF	DMKGRE	DMKGRF	DMKGRG
GREGGRUN GREGL GREGN GREGS GREGSBUF	000001 000003 000003 000003 000001	DMKGRD DMKGRD DMKGRD DMKDMQ DMKGRF	DMKGRE DMKGRE	DMKGRF DMKGRF	
GREGTHLD GREIC GREIS GRERT GRESR GREST	000001 000004 000004 000004 000004 000003	DMKGRD DMKGRD DMKGRD DMKGRD DMKGRD DMKGRD	DMKGRE DMKGRE DMKGRE DMKGRE DMKGRE	DMKGRF DMKGRF DMKGRF DMKGRF DMKGRF	DMKGRG DMKGRG DMKGRG DMKGRG
GRESV GREWS GRLOG GRTABLEN GRTBLOK GRTHILEN GRTSALEN	000004 000003 000005 000002 000007 000001 000001	DMKGRD DMKGRD DMKDMP DMKGRG DMKGRD DMKQCQ DMKQCN	DMKGRE DMKGRE DMKPMA	DMKGRF DMKGRG DMKSAD	DMKGRG
GT16MEG G16CHAN HADAPOLL HADISAM HADRCGEN HADUTIC	000005 000003 000003 000007 000005 000004	DMKCCW DMKIOG DMKCCT DMKCCD DMKCCD DMKCCW	DMKVER DMKCCW DMKCCO DMKCCF	DMKCCW DMKCCW	

LABEL

CP Directories 585

COUNT	REFERENCES

HALF1EN1 HALF1EN2	000006 000011 000009	DMKDMP DMKTRP DMKCQC DMKCQC DMKTRP DMKCQC	DMKTRR DMKTRP DMKTRP DMKTRR DMKTRP	DMKTRT DMKTRR DMKTRR DMKTRR	DMKTRT		
HALF1SZE HALF1VAL HALF2 HARDSTOP	000018 000019 000002 000003	DMKCQC DMKCQC DMKTRP DMKCPI	DMKTRP DMKTRP DMKTRR DMKCPJ	DMKTRR DMKTRR DMKDMP	DMKTRT DMKTRT	DMKTRX	
HBOUND HCBLOK HCBPNT HCCVU HCDVU HCFLAG HCFPNT HCIRTN HCMSFACT	000004 000027 000005 000001 000001 000009 000013 000001 000001	DMKFRE DMKCPU DMKMHC DMKVFC DMKVFC DMKMHC DMKMHC DMKMHC DMKMHC	<b>ДМКМНС</b>	<b>ОМКМН∨</b>	DMKPST	DMKVFC	DMKXST
HCMSFCW HCMSFDB HCMSFDBV	000008 000015 000003 000003 000008 000008	DMKCPU DMKCPU DMKMHC DMKMHC DMKMHC DMKMHC DMKPST	DMKMHC DMKMHC DMKMHV DMKXST	ОМКМН∨ ОМКМН∨	DMKPST DMKPST	DMKVFC DMKVFC	DMKXST DMKXST
HCRSCP HCSIZE HCVMBLK HCVMREQ HCVRX	000002 000012 000006 000004 000002	DMKMHC DMKMHC DMKMHC DMKMHC DMKMHC	DMKPST DMKMHV	DMKPST	DMKVFC	DMKXST	
HEADER HILIGHT HIOCCH HLDADDR1 HLDAREA HLDCCUU HLDCCW1 HLDCCW3 HLDCCW4 HLDCCW5 HLDCCW5 HLDCCW5 HLDCCW7 HLDCHARS HLDCHAR3 HLDCHAR3 HLDCHAR3 HLDCHAR3 HLDCFA HLDFLAG HLDFLAG HLDFLAG HLDFLAG HLDFLAG HLDFLSHC HLDHLPU HLDMCHR HLDMCHR HLDMSIZE	000012 000006 000004 000085 000005 000015 000015 000005 000005 000002 000023 000012 000012 000012 000012 000012 000012 000012 000005 000004 000005 000004 000005 000004 000005 000005 000005 000005	DMKCQY DMKCCH DMKCCH DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS	DMKDDR DMKGRG DMKSEV DMKTCT DMKTCT DMKTCT DMKTCT DMKTCT DMKTCT DMKTCT DMKTCT DMKTCT DMKTCT DMKTCT DMKTCT	DMKMCC DMKMSG DMKSIX	DMKSAD DMKRGC	DMKSRM	DMKVMD

LABEL	COUNT	REFERENCE	ES								
HMAXSIZE HWORD H7 IC IDA	000005 000011 000001 000034 000104	DMKFRE DMKFRE DMKDIR DMKBOX DMKCCD DMKDGD DMKRGD	DMKGRE DMKCCF DMKDIB DMKTAQ	DMKGRG DMKCCO DMKDMP DMKTCS	DMKGR I DMKCCS DMKGRE DMKTCT	DMKCCT DMKGRF DMKTRD	DMKCCW DMKHPS DMKUNT	DMKCKN DMKHPU DMKVCA	DMKDAD DMK I SM DMKVCB	DMKDAS DMKPAG DMKVCN	DMKDAU DMKQCO DMKVSC
I DAENTRY I DAEWRD1 I DAEWRD2 I DAHCNT	000011 000005 000005	DMKVSP DMKCFQ DMKCFQ DMKVCN DMKVCN	DMKVSX DMKRGD DMKRGD DMKVCR	DMKVCN DMKVCN DMKVCV			DMKVDR DMKVDR	DIRYUA	DIRVOD	DHAVON	DHRVSG
I DAHCURR I DAHEAD	000008 000037	DMKRGD DMKCFQ DMKVDR	DMKVCN DMKGRE	DMKQCN	DMKRGA	DMKRGB	DMKRGD	DMKRGE	DMKVCN	DMKVCR	DMKVCV
I DAHPAGE	000005	DMKCFQ	DMKVCN DMKVCN	DMKVCV DMKVDR	DMKVDR						
I DAHSIZD I DAHSTRT I DAHWRK1	000020	DMKCFQ DMKCFQ	DMKGRE DMKRGA	DMKQCN DMKRGE	DMKRGA DMKVCN	DMKRGB DMKVCR	DMKRGD DMKVDR	DMKVCN	DMKVCR	DMKVCV	DMKVDR
IDAHWRK2 IDLEWAIT IETYPMNP IETYPMP IETYPRNP	000019 000001 000001	DMKRGA DMKAPI DMKIUN DMKIUN DMKIUA	DMKRGE DMKCLK	DMKVCN DMKCP I	DMKVCR DMKDSP	DMKMON	DMKMOO	DMKSTP			
I ETYPRP I FCC	000001 000097	DMKIUA DMKBSC DMKDAU DMKIOE DMKSIX	DMKCCH DMKDDR DMKIOS DMKTAP	DMKCNS DMKDID DMKIOT DMKTAQ	DMKCPI DMKDMQ DMKMNT DMKTPE	DMKCPM DMKDSB DMKMSW DMKUNT	DMKCPW DMKDSP DMKRSE DMKV10	DMKCPZ DMKEIG DMKRSF DMKVSJ	DMKCQT DMKG10 DMKRSP	DMKDAD DMKGRF DMKSAD	DMKDAS DMKHVC DMKSEV
IGBLAME IGPRGFLG IGTERMSQ IGVALIDB IL	000048	DMKEIG DMKCCH DMKCCH DMKCCH DMKBIO	DMKSEV DMKSEV DMKEIG DMKEIG DMKCNS	DMKSIX DMKSIX DMKSEV DMKSEV DMKDAD	DMKSIX DMKSIX DMKDAS	DMKDIB	DMKGIO	DMKHPS	DMKHPU	рмкнус	DMKLOS
IMGBUFLN IMGCCWDS IMGCCWLN IMGFCBF1 IMGFCBF1 IMGFCB71	000001 000001 000002 000003 000002	DMKIOT DMKVCA DMKCSB DMKCSB DMKCSB DMKCSB DMKCSB DMKCSB	DMKNLD DMKVCN DMKCSC	DMKNLE DMKVIO	DMKPAH DMKVM1	DMKRNH DMKVSP	DMKRST DMKVSX	DMKSPS	DMKTAQ	DMKTPE	DMKTRK
IMGF3211 IMGHDR IMGHDRSD IMGHDRSZ IMGNAME IMGNAME IMGNEXT INBUFF INCRBY1	000003	DMKCSB DMKCSB DMKCSB DMKCSC DMKCSC DMKCSC DMKCVR DMKFRE	DMKCSC DMKCSC DMKCSC								
INGISTI INFILE INHIBIT INMSFBLK INPUT INTBLK INTCODE INTCTRL	000005 000038	DMKFRE DMKACO DMKACO DMKRGC DMKDSP DMKDRR DMKTRR DMKTRR DMKTRR	DMKCNS DMKRNH DMKEXT DMKRND DMKTRT DMKTRT	DMKEPS DMKTTX DMKMHC DMKSSP	DMKGRD DMKTTY	DMKGRG DMKVCN	DMKLOG DMKVCR	DMKQCN	DMKQCQ	DMKRGA	DMKRGB

LABEL	COUNT	REFERENC	ES								
INTERCCH INTEX INTEXF INTKFLIN INTLTH INTMASK INTMC	000019 000009	DMKCCH DMKDSP DMKDSP DMKCKH DMKTRR DMKCPI DMKCPI	DMKEIG DMKEXT DMKEXT DMKCKP DMKTRT DMKPMA	DMKSEV DMKMPO DMKMPO	DMK PMA DMK PMA						
INTMTYPE	000003	DMKMCH DMKTRR DMKTRR	DMKPMA								
INTPR	000002 000049		DMKCPI	DMKCPY	DMKPGS	DMKPMA	DMKPRG	DMKPRV	DMKPRW	DMKSAD	DMKSTA
INTPRL INTRC	000023 000007	DMKDMP	DMKDSP DMKPMA	DMKFPS	DMKPMA	DMKPRG	DMKPRV				
INTREQ	000067	DMKCNS DMKFMT DMKNLF	DMKCPI DMKGRF DMKRNH	DMKCPM DMKGRG DMKRSE	DMKCPS DMKHPS DMKSAV	DMKCQT DMKHPT DMKVCA	DMKDAD DMKIOS DMKVCN	DMKDDR DMKIOT DMKVIO	DMKD I B DMKMNT DMKVS I	DMKD I D DMKMSW DMKVSP	DMKDMP DMKNLD DMKVSX
INTSVC INTSVCL	000008 000021	DMKSVC DMKCKP	DMKSVD DMKSVC	DMKSVD	DMKTRC						
INTTO	000005 000047	DMKTRR DMKCCH DMKOPR	DMKTRX DMKCKH DMKSAD	DMKCKN DMKSSU	DMKCKP DMKVM I	DMKDMP DMKVRR	DMKDMQ DMKVRS	DMKDSP	DMKIOT	DMKMNT	DMKOPE
INTTMACH INTTVG INTTVT INTTYPE	000001 000005 000005 000012	DMKOPR DMKTRR DMKTRR DMKTRR DMKTRR	DMKTRX DMKTRX DMKTRX DMKTRT	DMKSSU	DMKVMI	DMKYRR	DMRAKS				
INVCCW INVCCW1 INVLD	000005 000004 000002	DMKTKK DMKCCD DMKCCD DMKCDB	DMKCCO DMKCCF DMKCDM	DMKCCS DMKCCO	DMKCCS						
IOBALTSK	000008	DMKCCD DMKCPN	DMKDAS	DMKTRK	DMKUNT	DMKVIO					
I OBASN I OBASNCT I OBBADCH	000009	DMKCPN DMKCPN	DMKCPW DMKCPT	DMKTPE							
IOBBPNT	000037	DMKACS DMKSSU	DMKCFQ DMKVRR	DMKCPP	DMKDSP	DMKIOQ	DMKIOS	DMKIOT	DMKNLD	DMKPAG	DMKQVM
IOBCAW	000310	DMKACS DMKCPN DMKDGD DMKGRI DMKMNL DMKRNH	DMKBIO DMKCPS DMKDGF DMKHPS DMKMON DMKRSE	DMKBSC DMKCPW DMKDIB DMKHPT DMKNLD DMKRSF	DMKCAC DMKCPZ DMKDID DMKHVC DMKNLE DMKRSP	DMKCCH DMKCQT DMKDSB DMKIOH DMKPAG DMKRST	DMKCCW DMKCSB DMKGIO DMKIOS DMKPAH DMKSEP	DMKCFQ DMKDAD DMKGRD DMKIOT DMKQVM DMKSPK	DMKCFR DMKDAS DMKGRF DMKISM DMKRGA DMKSPT	DMKCNS DMKDAU DMKGRG DMKMCC DMKRGB DMKTAP	DMKCPB DMKDEX DMKGRH DMKMNI DMKRGC DMKTAQ
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I OBCCH I OBCC1	000006 000025 000004	DMKCCH DMKCNS DMKRSP	DMKDGF DMKRST DMKIOS	DMKDIB DMKVCA DMKVI0	DMK I OQ DMKVDE	DMKIOS	DMKIOT	DMKNLD	DMKNLE	DMKRNH	DMKRSE
IOBCC2	000062	DMKIOH DMKACS DMKDSB DMKRSE DMKVSI	DMKTOS DMKCAC DMKGIO DMKRSF DMKZTD	DMKVTO DMKCFQ DMKIOQ DMKSSU	DMKCNS DMKIOS DMKTAP	DMKCPN DMKIOT DMKTRK	DMKCPW DMKNLD DMKUNT	DMKCPZ DMKNLE DMKVCA	DMKCQT DMKPAG DMKVDC	DMKDGF DMKRGA DMKVDE	DMKD I B DMKRNH DMKV I O
IOBCLN IOBCLRIO IOBCOPY	000006	DMKCCW DMKDID DMKGRF	DMKG10 DMK10S DMKGRG	DMKUNT DMKIOT	DMKVSI						
IOBCP	000096	DMKACR DMKDIA DMKNLD	DMKACS DMKDID DMKNLE	DMKCCH DMKHPS DMKPAG	DMKCPS DMKHPT DMKRGB	DMKCPZ DMKHPU DMKRNH	DMKCSF DMKIOE DMKRSP	DMKCSO DMKIOQ DMKRST	DMKDAD DMKIOS DMKSPK	DMKDAS DMKIOT DMKTAP	DMKDAU DMKMNL DMKTAQ
IOBCSW	000575	DMKTPE DMKACO	DMKTRK DMKACS	DMKUDR DMKB10	DMKVDC DMKBSC	DMKVDE DMKCCH	DMKVDT DMKCCS	DMKZTD DMKCFR	DMKCNS	DMKCPM	DMKCPW

LABEL	COUNT	REFERENC	ES								
		DMKCPZ DMKDAU DMKGRF DMKMON DMKRNH DMKTAP DMKVDC	DMKCQT DMKDGF DMKGRG DMKNLD DMKRSE DMKTAQ DMKVDD	DMKCSB DMKDIB DMKHPS DMKNLE DMKRSP DMKTRC DMKVDE	DMKCSF DMKDID DMKHPT DMKPAG DMKRST DMKTRD DMKVI0	DMKCSO DMKDIF DMKHPU DMKPAH DMKSEP DMKTRK DMKVRR	DMKCSR DMKDSB DMKHVC DMKQVM DMKSPL DMKUNT DMKVRS	DMKCSW DMKDSP DMKIOH DMKRGA DMKSPM DMKVCA DMKVSI	DMKCSX DMKGIO DMKIOQ DMKRGB DMKSPS DMKVCB DMKVSJ	DMKDAD DMKGRC DMKIOS DMKRGC DMKSPT DMKVCN DMKZTD	DMKDAS DMKGRE DMKIOT DMKRGE DMKSSU DMKVCP
I OBCTRQ I OBCUBSY	000008 000030	DMK I OQ DMKCFQ	DMKSSU DMK10Q	DMKIOS	DMKIOT	DMKSPM	DMKVIO	DMKVSJ			
I OBCUE I OBCYL	000004 000040	DMKSPM DMKBIO DMKSPK	DMKVIO DMKCCD DMKSSU DMKVIO	DMKCCF DMKZTD	DMKCCO	DMKCCW	DMKDGD	DMKIOQ	DMKIOS	DMKMON	DMKPAG
I OBD I AG I OBDYNP	000003 000003	DMKSPK DMKCCW DMKCPN	DMKVIO DMKCPW	DMKTPE							
I OBERCNT I OBERP	000006 000066	DMKIOS DMKBSC DMKBSF	DMKCNS DMKRSP	DMKDAD DMKTAP	DMKDAS DMKTAQ DMKCAC	DMKDAU DMKTPE	DMKGR I	DMKIOQ	DMKIOS	DMKIOT	DMKRSE
IOBFATAL	000161	DMKRSF DMKACS DMKCQT DMKCRF DMKPAG DMKSPS DMKY10	DMKBIO DMKCSB DMKIOE DMKPAH DMKSSU	DMKBSC DMKCSO DMKIOH DMKRGA DMKTAP	DMKCAC DMKDAD DMKIOJ DMKRGB DMKTAQ	DMKCCH DMKDAS DMKIOQ DMKRSE DMKTCS	DMKCFQ DMKDAU DMKIOS DMKRSF DMKTCT	DMKCNS DMKDGF DMKIOT DMKRSP DMKTPE	DMKCPN DMKDID DMKMNI DMKRST DMKTRK	DMKCPS DMKDSB DMKMNL DMKSEP DMKUDR	DMKCPW DMKGIO DMKMON DMKSPK DMKVDE
I OBFCNS I OBFH I OBFLAG	000005 000007 000338	DMKD I D DMK I OQ DMKACR DMKCSB DMKD I D DMK I OS DMKRSE DMKTAQ DMKVDR	DMKZTD DMKGRF DMKIOS DMKCSF DMKCSF DMKDSP DMKIOT DMKRSF DMKTCS DMKVDT DMKSSU	DMKMON DMKBSC DMKCSO DMKGIO DMKMNI DMKRSP DMKTCT DMKVIO	DMKPAG DMKCCH DMKCRI DMKGRI DMKMNL DMKRST DMKTPE DMKVSC	DMKCCO DMKDAS DMKHPS DMKMON DMKSEP DMKTRK DMKVSI	DMKCCW DMKDAU DMKHPT DMKNLD DMKSPK DMKUDR DMKZTD	DMKCFR DMKDGD DMKHPU DMKNLE DMKSPS DMKUNT	DMKCNS DMKDGF DMKIOE DMKPAG DMKSPT DMKVCA	DMKCPS DMKDIA DMKIOH DMKRGB DMKSSU DMKSSU	DMKCPZ DMKD1B DMK10Q DMKRNH DMKTAP DMKVDE
I OBFLT I OBFPNT	000006 000097		DMKACS	DMKCFQ DMKNLD DMKWAI	DMKCPP DMKPAG	DMKVST DMKDID DMKQVM	DMKDSP DMKSSU	DMKHPS DMKSTK	DMKHPT DMKV10	DMKHPU DMKVRR	DMK I OQ DMKVRS
I OBFUNCT I OBH I O I OBHVC	000002 000041 000038	DMKIOS DMKVSC DMKCPN DMKCCH DMKACS DMKIQQ	DMKVSJ DMKCPW DMKCFR DMKCCH DMKIOS	DMKDID DMKCCW DMKIOT	DMKIOS DMKCFR DMKTAP	DMKIOT DMKCPS DMKTRK	DMKV I O DMKDGD	DMKVSJ DMKDGF	DMKDID	DMKGIO	DMKIOE
IOBINSTK IOBIOER		DMKACS DMKIOQ DMKCPS DMKACS DMKCPZ DMKCIO DMKMON DMKSPS DMKIOS	DMKIOS DMKBIO DMKCQT DMKGRF DMKNLD DMKTAP	DMKBSC DMKDAD DMKGRI DMKNLE DMKTAQ	DMKCCH DMKDAS DMKHPS DMKRGA DMKTPE	DMKCFQ DMKDAU DMKHPT DMKRGB DMKTRK	DMKCFR DMKDGF DMKIOE DMKRGE DMKVCA	DMKCNS DMKDIA DMKIOS DMKRNH DMKVCN	DMKCPN DMKDIB DMKIOT DMKRSE DMKVDC	DMKCPS DMKDID DMKMNI DMKRSF DMKVDE	DMKCPW DMKDSB DMKMNL DMKRSP DMKVIO
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IOBLDCCW IOBLDRUN IOBLDSZ IOBLDTXT	000004	DMKVDR DMKIOS DMKIOS DMKVDR	DMKVDR DMKVDR	2				,			

<u> </u>							
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			DMKCPZ	DMKCSB	DMKCSF	DMKCSO	DMKCSR
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1 ·			DMKIQQ	DMKIOS	DMKIOT	DMKMNL	DMKNLD
<b>L</b>							
9			DMKRGC	DMKRGE	DMKRNH	DMKRSP	DMKRST
9			DMKTCS	DMKTCT	DMKUNT	DMKVCA	DMKVCB
)			DMKVRR	DMKVSI	DMKZTD		
	IOBLOK	000685	DMKACO	DMKACR	DMKACS	DMKBIO	DMKBSC
			DMKCCS	DMKCCW	DMKCFQ	DMKCFR	DMKCKD
			DMKCPP	DMKCPS	DMKCPT	DMKCPW	DMKCPZ
2				DMKCSW			
+			DMKCSR		DMKCSX	DMKDAD	DMKDAS
-			DMKDIB	DMKDID	DMKDIF	DMKDSB	DMKDSP
3			DMKGRG	DMKGRH	DMKGR I	DMKHPS	DMKHPT
			DMKIOS	DMKIOT	DMKISM	DMKLOH	DMKMCC
-			DMKNLE	DMKOPE	DMKPAG	DMKPAH	DMKQVM
1			DMKRNH	DMKRSE	DMKRSF	DMKRSP	DMKRSQ
			DMKSPS	DMKSPT	DMKSSU	DMKSTK	DMKTAP
5			DMKTRD	DMKTRK	DMKTTX	DMKTTY	DMKUDR
-			DMKVCP	DMKVDA	DMKVDC	DMKVDD	DMKVDE
2							
C 00 1			DMKVRS	DMKVSC	DMKVSI	DMKVSJ	DMKWA I
	IOBMID	000006	DMKCFR	DMKDID	DMKIOS		
1007	IOBMINI	000024	DMKACS	DMKDID	DMKIOQ	DMKIOS	DMKPAG
õ	IOBMISC	000196	DMKCCW	DMKCFQ	DMKCFR	DMKCNS	DMKCPB
1			DMKCSB	DMKCSF	DMKCSO	DMKDAD	DMKDAS
			DMKGRF	DMKGRG	DMKGRH	DMKGR I	DMKHVC
			DMKMON	DMKNLD	DMKNLE	DMKPAG	DMKPAH
			DMKRST	DMKSEP	DMKSPS	DMKSPT	DMKTAP
	10041000	000105	DMKVDA	DMKVDE	DMKVDG	DMKVIO	DMKVSI
	IOBMISC2	000185	DMKBIO	DMKBSC	DMKCCW	DMKCFQ	DMKCFR
			DMKDGD	DMKDID	DMKGIO	DMKGRD	DMKGRF
			DMKNLE	DMKPAG	DMKPAH	DMKRGA	DMKRGB
			DMKSPK	DMKTCS	DMKTCT	DMKTRK	DMKUDR
	IOBMQDTO	800000	DMKACS	DMKIOQ			
	IOBMSG	000004	DMKCFR				
	IOBMSIZE	000007	DMKMNT				
	IOBOERR	000003	DMKACS	DMKCCH			
	IOBOFF	000006	DMKIOQ	DMKIOS	DMKPAG		
						DMU(1.0.0	DM1/100
	IOBPAG	000012	DMKDAD	DMKDAS	DMKDSP	DMKIOQ	DMKIOS
	IOBPATH	000004	DMKIOS				
	IOBPATHF	000021	DMKACS	DMKCPN	DMKCPW	DMKIOQ	DMKIOS
	IOBPGID	000004	DMKCPN	DMKCPW			
	IOBPMINT	000003	DMKIOT	DMKVRR	DMKVRS		
	IOBPROC	000019	DMKCPN	DMKCPW	DMKCPZ	DMKDID	DMKIOQ
	IOBPST	000003	DMKIOS	DMKVIO	DINOL	DINDID	
	IOBPVM				DMKVSI		
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	OBQDTO	000015	DMKACS	DMKIOQ	DMKIOS	DMKIOT	
<b>`</b>	IOBQSTAT	000006	DMKCQT	DMKIOE	DMKIOS	DMKIOT	
G	IOBRADD	000145	DMKACR	DMKACS	DMKCAC	DMKCCH	DMKCKD
			DMKCPZ	DMKCSB	DMKCSF	DMKCSO	DMKDAD
2			DMKDSP	DMKGRF	DMKHPS	DMKHPT	DMKHPU
•			DIVI/I OT	<b>B1</b>	D. H. M. COL	014141	

DMKIOT

DMKRSE

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DMKDAU

DMKRSP

DMKDID

DMKRST

DMKMNL

DMKRSP

DMKVSJ

DMKCCH

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REFERENCES

DMKCPS DMKCPW DMKDIA DMKDID DMKHPU DMKIOH DMKRGB DMKRGA DMKTAP DMKSSU DMKVDT DMKVIO DMKCCH DMKCCO DMKCPM DMKCPN DMKCSF DMKCSO DMKDGF DMKDIA DMKGRE DMKGRF DMKIOH DMKIOQ DMKNLD DMKMSW DMKRGD DMKRGE DMKSPM DMKSPL DMKTPE DMKTRC DMKVCB DMKVCN DMKVIO DMKVRR

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DMKCSB

DMKNLD

DMKSEP

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DMKHVC

DMKRNH

DMKVDE

DMKMON

DMKTPE

DMKMNL

DMKCPM

DMKDAU

DMKIOE

DMKPAH

DMKTRD

DMKDIF

DMKTAQ

DMKHPS

DMKTAQ

LABEL

COUNT

**IOBRCAW** 

IOBRCNT

000141

000158

LABEL	COUNT	REFERENC	ES								
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I OBRES I OBRESGN I OBRESRV I OBRETRY I OBRREL	000008	DMKCPW DMKCCS DMKACS DMKCFR DMKBSC	DMKVIO DMKIOS DMKIOS	DMKIOT							
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I OBRTCT I OBRTCT2 I OBRUNLD I OBRUN1 I OBSENS	000003	DMKIOS DMKCPS DMKCQT DMKGRF	DMKGRG								
IOBSENS IOBSENSE IOBSETP IOBSIMS IOBSIOEX	000003	DMKCCS DMKCPN DMKIOS DMKIOS	DMKCCW	DMKDEX	DMKVIO	DMKVSI					
IOBSIOF IOBSIZE	000008 000339	DMK I OQ DMKACO DMKCPN DMKCSR DMKD I F	DMKIOS DMKACS DMKCPO DMKCSW DMKDMQ	DMKIOT DMKBIO DMKCPS DMKCSX DMKDSB	DMKTRK DMKCAC DMKCPT DMKDAD DMKDSP	DMKVIO DMKCCS DMKCPW DMKDAS DMKGIO	DMKVSI DMKCFQ DMKCPZ DMKDAU DMKGRD	DMKCFR DMKCQT DMKDGD DMKGRF	DMKCNS DMKCSB DMKDGF DMKGR I	DMKCPB DMKCSF DMKDIA DMKHPS	DMKCPM DMKCSO DMKDID DMKHPT
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I OBSNSE I OBSNS I O	000003	DMKVSJ DMKCPS DMKIOQ	DMKZTD	DMKIOT							
IOBSPEC	000165	DMKACS DMKDAU DMKIOS DMKRSP DMKVDE	DMKBSC DMKDID DMKIOT DMKSPL DMKVIO	DMKCCH DMKDSB DMKNLD DMKSPM DMKVSI	DMKCFR DMKGRD DMKNLE DMKSSU DMKVSJ	DMKCNS DMKGRF DMKPAG DMKTAP	DMKCPM DMKGRG DMKRGA DMKTRK	DMKCPS DMKHPS DMKRGB DMKVCA	DMKCPW DMKHPT DMKRGC DMKVCP	DMKDAD DMKHPU DMKRGE DMKVDC	DMKDAS DMKIOQ DMKRNH DMKVDD
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I OBVADD	000055	DMKVSI DMKACO DMKHPU DMKTRC	DMKVSJ DMKCFQ DMKIOQ DMKUNT DMKIOS	DMKWAI DMKCFR DMKIOS DMKVCA DMKIOT	DMKZTD DMKCPS DMKIOT DMKVCB DMKVSI	DMKCSR DMKRGA DMKVCN DMKVSJ	DMKCSW DMKRGB DMKVCP	DMKCSX DMKRGC DMKVIO	DMKD I F DMKRGE DMKVS I	DMKHPS DMKSPL DMKVSJ	DMKHPT DMKSSU
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I OERCCRL	000003	DMKBSC DMKTAP DMKACS	DMKCCH DMKTAQ DMKCCH	DMKDAD DMKTPE DMKDID	DMKDAS	DMKDAU	DMKDSB	DMKIOE	DMKRSE	DMKRSF	DMKRSP
I OERCLN I OERCLOG	000012 000006 000002	DMKCNS DMKDAD DMKACS DMKTAP DMKACS	DMKDIB DMKDAS DMKCCH DMKTAQ DMKCCH	DMKIOS DMKDAU DMKDID	DMKVCA DMKIOE	DMKVCB DMKRSE	DMKTAP	DMKTRK			
IOERCNCL IOERCPEX IOERCPID IOERCSW	000022	DMKMSW DMKDAD DMKACS DMKACS DMKGIO DMKRGE DMKVCN	DMKDAS DMKCCH DMKBSC DMKGRF DMKRSE DMKV10	DMKDAU DMKDID DMKCCH DMKHPS DMKRSF DMKVSI	DMKTAP DMKDSB DMKCQT DMKHPT DMKRSP	DMKTAQ DMKIOE DMKDAD DMKIOE DMKTAP	DMKTPE DMKIOF DMKDAS DMKIOF DMKTAQ	DMKIOS DMKDAU DMKIOJ DMKTPE	DMKRSF DMKDIB DMKIOS DMKTRK	DMKU I D DMK I OT DMKVCA	DMKDSB DMKMSW DMKVCB
I OERCYLR I OERDASD I OERDATA	000012	DMKUNT DMKDAD DMKBSC	DMKDAS DMKCCS	DMKDAU DMKCNS	DMKMSW DMKCPW	DMKCQT	DMKDAD	DMKDAS	DMKDAU	DMKDGF	DMKDIB

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LABEL	COUNT	REFERENC	ES								
	000006	DMKDID DMKIOS DMKTAP DMKVIO DMKVAD	DMKDSB DMKIOT DMKTAQ DMKVRR DMKVAS	DMKDSP DMKMSW DMKTPE DMKVS1 DMKVS1	DMKGIO DMKNLD DMKTRK DMKMSW	DMKGRF DMKNLE DMKUNT	DMKHPS DMKRGE DMKVCA	DMKHPT DMKRNH DMKVCB	DMKIOE DMKRSE DMKVCN	DMKIOF DMKRSF DMKVDC	DMKIOJ DMKRSP DMKVDE
I OERDEC I OERDEF I OERDEPD	000006	DMKDAD DMKRSE	DMKRSP	DINKDAU	DHNHOW						
IOERDERD IOERDW IOERECF	000005 000039 000037	DMKRSE DMKBSC DMKDAD	DMKRSP DMKDAD DMKDAS	DMKDAS DMKDAU	DMKDAU	DMKDSB	DMKTAP	DMKTAQ	DMKTPE	DMKTRK	
IOERECSW	000009 000012	DMKACS DMKTAP	DMKCCH DMKTAQ	DMKDID DMKTPE	DMKRSE	DMKVIO					
IOERERP	000004 000122	DMKRSE DMKAPT DMKDRD	DMKRSP DMKCFG DMKDRE	DMKCFH DMKERM	DMKCFS DMKGRC	DMKCFU DMKHPS	DMKCPI DMKHVC	DMKCPS DMKHVD	DMKCPU DMKHVE		DMKCSO DMK I DU
		DMKMCC DMKSNC DMKVSE	DMKMIA DMKSPK DMKVSG	DMKNLD DMKSSS DMKVSP	DMKNLE DMKSST DMKVSQ	DMKPTR DMKSVC DMKVSX	DMKRPA DMKTCS DMKXAB	DMKRSP DMKUDR	DMKRST DMKUDU	DMKSEG DMKVMD	DMKSEP DMKVME
I OERETRY I OEREXT	000006 000081	DMKDAU DMKACS	DMKMSW DMKBSC	DMKRSE DMKCCH	DMKCCW	DMKCFQ	DMKCFR	DMKCNS	DMKCPN	DMKCPS	DMKCPW
		DMKCQT DMKGR1 DMKRGA	DMKDAD DMKHPT DMKRGB	DMKDAS DMK10E DMKRNH	DMKDAU DMK10J DMKRSE	DMKDGD DMKIOS DMKRSF	DMKDGF DMKIOT DMKRSP	DMKDIA DMKMNL DMKTAP	DMKDID DMKNLD DMKTAQ	DMKDSB DMKNLE DMKTPE	DMKGIO DMKQVM DMKTRK
I OERFIXP I OERFLG1		DMKVDC DMKTPE DMKDAD	DMKVDE DMKDAS	DMKV10 DMKDAU	DMKVSC DMKMSW	DMKRSE	DMKRSF	DMKRSP	DMKTAP	DMKTAQ	DMKTPE
IOERFLG2	000156	DMKBSC DMKUNT	DMKDAD	DMKDAS	DMKDAU	DMKIOE	DMKRSE	DMKTAP	DMKTAQ	DMKTPE	DMKTRK
IOERFLG3		DMKBSC DMKTAP DMKDAS	DMKCNS DMKTAQ DMKIOE	DMKDAD DMKTPE	DMKDAS DMKTRK	DMKDAU	DMKGRF	DMKIOE	DMKIOF	DMKIOS	DMKRSE
IOERFLM IOERFSR IOERHA	000005 000019 000008	DMKIOE DMKTAP DMKDAD	DMKTAP DMKTAQ DMKDAS	DMKTPE							
IOERIGN IOERIGNR	000004 000008	DMKMSW DMKDAD	DMKRSE DMKDAS	DMKDAU	DMKMSW	DMKTAP	DMKTAQ				
IOERIND3 IOERIND4 IOERINFO	000021	DMKBSC DMKDAD DMKBSC	DMKDAD DMKDAS DMKDAD	DMKDAS DMKDAU DMKDAS	DMKDAU DMKMSW DMKDAU	DMKMSW DMKTAP DMKMSW	DMKRSE DMKTAQ DMKRSE	DMKTAP DMKTAP	DMKTAQ DMKTAQ	DMKTPE DMKTPE	
IOERLEN	000026	DMKCCS DMKMSW	DMKCQT DMKRGE	DMKDID DMKRSF	DMKDSB	DMKHPS DMKVCA	DMKHPT DMKVCB	DMKIOE DMKVCN	DMKIOJ DMKVSI	DMKIOS	DMKIOT
I OERLG45 I OERLOC I OERLOGL	000087	DMKCCH DMKBSC DMKACS	DMKDAD DMKCCH	DMKDAS DMKDID	DMKDAU	DMKDSB	DMKTAP	DMKTAQ	DMKTPE	DMKTRK	
I OERMSG I OERMSW I OERNOLG	000005	DMKDAD DMKBSC DMKIOS	DMKDAS DMKDAD DMKTPE	DMKDAU DMKDAS	DMKTAP DMKDAU	DMKTAP	DMKTAQ	DMKTPE			
IOERNUM	000126	DMKBSC DMKTPE	DMKCNS	DMKDAD	DMKDAS	DMKDAU	DMKGRF	DMKMSW	DMKRSE	DMKTAP	DMKTAQ
I OERORA I OERPEND I OERPNT	000035	DMKTAP DMKDAD DMKCFQ DMKRSF	DMKTAQ DMKDAS DMKCQT DMKTAP	DMKDAU DMKDAD DMKTRK	DMKMSW DMKDAS	DMKRSE DMKDAU	DMKTAP DMKDGF	DMKTAQ DMKDID	DMKTPE DMKGIO	DMKIOE	DMKRSE
I OERQUE I OERRBK I OERRDRO I OERREAD	000020	DMKDAS DMKTAP DMKDAS DMKBSC	DMKIOE DMKTAQ DMKTRK DMKCNS	DMKDAD	DMKDAS	DMKDAU	DMKGRF	DMKIOF	DMKRSE	DMKTAP	DMKTAQ
IOERREW	000003	DMKTAP	DMKTAQ								

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1002 1007

LABEL	COUNT	REFERENC	ES								
IOERSIZE		DMKACS DMKCPW DMKDID DMKIOT DMKRNH DMKVCB	DMKBIO DMKCPZ DMKDSB DMKMNI DMKRSE DMKVCN	DMKBSC DMKCQT DMKGIO DMKMNL DMKRSF DMKVDC	DMKCCH DMKDAD DMKGRF DMKMON DMKRSP DMKVDE	DMKCCW DMKDAS DMKGRI DMKNLD DMKSPS DMKVIO	DMKCFQ DMKDAU DMKHPS DMKNLE DMKTAP DMKVRR	DMKCFR DMKDGD DMKHPT DMKQVM DMKTAQ DMKTAQ	DMKCNS DMKDGF DMKIOE DMKRGA DMKTPE DMKVSI	DMKCPN DMKDIA DMKIOJ DMKRGB DMKTRK	DMKCPS DMKDIB DMKIOS DMKRGE DMKVCA
IOERSNSZ IOERSPID	000019	DMKDAD DMKTPE	DMKDAS	DMKDAU	DMKDSB	DMKIOS	DMKVDE				
IOERSTAT	000035 000008 000006	DMKDAD DMKDAD DMKTAQ DMKCCH	DMKDAS DMKDAS	DMKDAU DMKDAU	DMKTAP DMKMSW	DMKTAP	DMKTAQ				
IOERS80 IOERTEMP IOERVLD	000003	DMKIOE DMKTAQ	DMKTPE								
IOERVOL1 IOERVSER IOERWRK IOERXERP IOER2860	000009 000022 000014 000003 000003	DMKDAD DMKDAD DMKTAQ DMKRSE DMKCCH	DMKDAS DMKDAS DMKTRK DMKRSF DMKDID	DMKDAU DMKDAU	DMKDSB	DMKIOE	DMKIOJ	DMKTPE			
IOER2870 IOKEEP	000072	DMKCCH DMKAPT DMKDRD DMKSPT	DMKDID DMKCFG DMKDRE DMKSRM	DMKCFH DMKERM DMKTCS	DMKCFS DMKHVC DMKTRP	DMKCFU DMKH∨D DMKTRU	DMKCKV DMKHVE DMKUDU	DMKCPY DMKHVF DMKVBM	DMKCQY DMKPTR DMKVMD	DMKCSC DMKRSP DMKVME	DMKCSO DMKSEP DMKWRM
IOLOG IOMASK	000001 000018	DMKCPJ DMKAPI	DMKCFM	DMKDMP	DMKDMQ	DMKDSP	DMKEXT	DMKLDOOE	DMKSAD	DMKSVD	DMKTRC
IONPSW	000036	DMKVRR DMKCKH DMKSAD	DMKVRS DMKCKN DMKSAV	DMKCKP DMKSSP	DMKDMP DMKVRS	DMKDMQ	DMKDSP	DMKFMT	DMKMNT	DMKOPE	DMKOPR
IONTWAIT IOOPSW IPLADDR	000011 000049 000002	DMKAPI DMKCCH	DMKCPI DMKCKH	DMKDSP DMKDSP	DMKMON DMKFMT	DMKSTP DMKIOT	DMKMNT	DMKSAD	DMKSAV	DMKSSP	DMKVRS
I PLCCW1 I PLPSW	000013 000035	DMKVMI DMKCPI DMKCKP	DMKDMP DMKCPI	DMKDSP DMKDMP	DMKMPO DMKLDOOE	DMKVM I DMKM I A	DMKMON	DMKMOO	DMKSAD	DMKVMI	
I PLREQ I PUADDR	000005 000070	DMKNLD DMKAPI DMKCPU DMKMCH	DMKNLE DMKCCH DMKCQP DMKMCI	DMKRNH DMKCFO DMKCQQ DMKMCT	DMKCFP DMKCQY DMKMNT	DMKCFY DMKDMP DMKMPO	DMKCKD DMKDSP DMKPMA	DMKCPI DMKFPS DMKPRV	DMKCPO DMKHVD DMKTHI	DMKCPP DMKIOG DMKURS	DMKCPS DMKIOS DMKVDC
I PUADDRX	000083	DMKVDT DMKACO DMKCPS DMKMCH DMKTHI	DMKVFC DMKACS DMKCPU DMKMCI DMKURS	DMKAPI DMKCQP DMKMCT DMKVDC	DMKCCH DMKCQQ DMKMNT DMKVFC	DMKCFO DMKCQY DMKMOO	DMKCKD DMKDMP DMKPTR	DMKCLK DMKDSP DMKPTT	DMKCPI DMKEXT DMKSCH	DMKCPO DMKFRE DMKSEL	DMKCPP DMKIOS DMKSTK
IREALVF IRMAND IRMBIT1 IRMBIT2 IRMBLOK	000002 000003 000002 000002 000004	DMKVFC DMKCFU DMKCFU DMKCFU DMKCFU	DMKIOE DMKIOE DMKIOE DMKIOE	Shirtbo	Shirty C						
IRMBYT1 IRMBYT2 IRMFLG IRMLMT IRMLMTCT	000002 000002 000006 000003 000003	DMKCFU DMKCFU DMKCFU DMKCFU DMKCFU	DMKIOE DMKIOE DMKIOE DMKIOE								
IRMMAXCT IRMOR IRMRLADD	000005 000003 000003 000005 000012	DMKIOE DMKCFU DMKCFU DMKCFU DMKIUA DMKIUP	DMKIOE DMKIOE DMKIOE DMKIUB	DMKIUC	DMKIUP						

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LABEL	COUNT	REFERENC	CES								
IUCBFLN1	000002	DMKIUP									
IUCCBFA1	000002	DMKIUB	DMKIUC	DMKIUP							
IUCDWRD	000002	DMKIUP	DIMITOO	Dintiol							
IUCLINK1		DMKIUA									
IUCMSG	000005	DMKIUN	DMKIUP	DMKIUS							
IUCMXCN	000005	DMKIUC	DMKIUG	DMKIUP	0000000						
IUCPNDHD	000012	DMKIUB DMKIUB	DMKIUC	DMKIUJ DMKIUJ	DMKIUP DMKIUP						
IUCSIZE	000002	DMKIUP	DMKIUC	DMKTUJ	DMKTUP						
IUCTOTCN		DMKIUC	DMKIUJ						-		
IUCVBLOK		DMKDSP	DMKIUA	DMKIUB	DMKIUC	DMKIUE	DMKIUG	DMKIUJ	DMKIUL	DMKIUN	DMKIUP
		DMKIUS	DMKPGS								
IUCVCNT	000004	DMKCPP	DMKIUN								
I UCVCODE I UCVCPEX	000001	DMKDSP DMKIUA	DMKIUC	DMKIUJ	DMKIUP	DMKIUS	DMKPGS				
IUCVMASK	000010	DMKDSP	DMKTUC	DMKT0J	DMKIOF	DMKTUS	DMKF05				
LUCVMB	000013	DMKIUA	DMKIUC	DMKIUE	DMKIUJ	DMKIUL	DMKIUN	DMKIUP	DMKIUS		
IUSAVE	000065	DMKIUA	DMKIUC	DMKIUE	DMKIUG	DMKIUJ	DMKIUL	DMKIUN	DMKIUP	DMKIUS	
IUSBUFF	000002	DMKIUP									
IUSCC	000001	DMKIUN		o	0.000	<b></b>	<b>D1</b>	<b>D1</b>	<b>DM</b> ////D		
IUSCCODE	800000	DMKIUA	DMKIUC	DMKIUE	DMKIUG	DMKIUJ DMKIUJ	DMKIUL DMKIUL	DMKIUN DMKIUN	DMKIUP DMKIUP	DMKIUS	
IUSCPENT IUSFCODE	000020	DMKIUA DMKIUA	DMKIUC	DMKIUE	DMKIUG	DINKIUJ	DMKIUL	DHKION	DINKIOP	DMKIUS	
LUSFLAGS	000002	DMKIUA									
IUSFLAG2	000020	DMKIUA	DMKIUC	DMKIUE	DMKIUG	DMKIUJ	DMKIUL	DMKIUN	DMKIUP	DMKIUS	
IUSINSTR	000002	DMKIUA									
IUSIPSIZ		DMKIUP	DMILLIO	DMULLIE	DMICLUO	DUKLILI	DMIZER	DMI/ LUN	DMICLUD		
IUSIUCV	000009	DMKIUA	DMKIUC	DMKIUE DMKIUP	DMKIUG	DMKIUJ	DMKIUL	DMKIUN	DMKIUP		
IUSIUCV2	000004	DMKIUA DMKIUA	DMKTUN	DMKTUP							
IUSLEN2	000002	DMKIUA									
IUSMASK	000002	DMKIUN									
IUSMSGBK	000010	DMKIUE	DMKIUG	DMKIUL	DMKIUN	DMKIUS					
IUSMXCN	000001	DMKIUP		<b></b>							
IUSPAGE1	000006	DMKIUA	DMKIUC	DMKIUN							
IUSPAGE2 IUSPARMS	000002	DMKIUA DMKIUA									
IUSPATH	000013	DMKIUC	DMKIUE	DMKIUG	DMKIUJ	DMKIUL	DMKIUN	DMKIUP	DMKIUS		
IUSRCODE		DMKIUA	0111102	5	51111 00	5			2		
IUSSIZE	000005	DMKIUA									
IUSVMBK	000002	DMKIUA			000005	<b>DM</b> /(1)114	DMULLIO	<b>D11</b> (1)111	DMU/LOU		D.11/2 (O.D.
IXBLOK	000052	DMKAPY DMKVCU		DMK I DR DMKVCX	DMKIOF	DMKIUA	DMKIUC	DMKIUJ	DMKLOH	DMKMSG	DMKVCP
IXEXBLOK	000028	DMKIDR	DMKIUA	DMKIUC	DMKIUJ	DMKVCQ	DMKVCR	DMKVCS	DMKVCT	DMKVCV	
IXIRA	000014	DMKAPY	DMKCRM	DMKIDR	DMKIOF	DMKIUA	DMKIUC	DMKIUJ	DMKMSG	DMKVCV	DMKVCX
IXNEXT	000017	DMKIDR	DMKIUC	DMKIUJ							
IXREGS	800000	DMKAPY	DMKIDR	DMKIOF	DMKIUA	DMKMSG	DMKVCV	DMKVCX			
IXRO	000003	DMKCRM									
IXR1	000001	DMKCRM									
IXR11 IXR12	000004 000004	DMKCRM DMK I DR	DMKIDR DMKVCV	DMKIUA DMKVCX	DMKLOH						
IXR12	0000004	DMKVCV	DEIXYOY	DINKYON							
IXSIŽE	000026	DMKAPY	DMKCRM	DMKIDR	DMKIOF	DMKIUJ	DMKMSG	DMKVCP	DMKVCQ	DMKVCT	DMKVCU
		DMKVCV	DMKVCX			-		-	• •		
JPSCBLOK	000015	DMKLNK	DMKLNM	DMKLOG							
JPSFLAGS		DMKLNK DMKLNK		DMKLOG							
JPSLNKDS	000002	DRIVENK	DMKLNM								

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JPSLOGDS	000002	DMKLOG									
KEEPSEGS	000014	DMKBLD	DMKCFF	DMKCFP	DMKPGS						
KEYCHG KEYMASK	000009 000001	DMKPTR DMKCP I	DMKPTS	DMKSEL							
KEYREF	000009	DMKPTS	DMKSEL								
LANGADDR	000006	DMKCPI	DMKERM	DMKHVF	DAIVING	DMI/LOU					
LANGBLOK LANGDBCS	000014	DMKCP I DMKERM	DMKCQT	DMKERM	DMKHVF	DMKLOH					
LANGFLAG	000003	DMKCPI	DMKERM								
LANGHIGH	000002	DMKCP I DMKCQT	DMKERM DMKHVF	DMILLOU							
LANGLANG LANGLOCK	000003	DMKHVF	DIMENT	DMKLOH							
LANGLOW	000002	DMKCPI	DMKERM								
LANGNEXT LANGNTRY		DMKCP I DMKCP I	DMKHVF DMKERM	DMKLOH DMKHVF							
LANGNTSZ	000006	DMKCPI	DMKERM	DMKHVF							
LANGPAGE	000007	DMKCPI	DMKERM	DMKHVF							
LANGSIZE LANGVMB	000006 000002	DMKCP I DMKCP I	DMKERM DMKERM	DMKHVF							
LANGVMBK	000004	DMKCPI	DMKERM	DMKHVF							
LAP370E	000026	DMKDMP	DMKDSP	DMKFPS	DMKPRG	DMKSVD	DMKVAT				
LASTALOC LASTMSG	000001	DMKTDK DMKLD00E									
LASTUSER		DMKAPI	DMKATS	DMKBLD	DMKCDM	DMKCPI	DMKCPP	DMKDIB	DMKDIF	DMKDSP	DMKFRE
LINE	000031	DMKIOS DMKDEF	DMKIOT DMKDMQ	DMKLOK DMKFCB	DMKPAG DMKSAD	DMKPGS	DMKSEL	DMKSWA	DMKUSQ		
LINECT	000004	DMKDDR	DHEDHQ	DHKLCD	DHKJAD						
LINESUP	000005	DMKDMQ									
LINKJRL LINKJRLI	000005 000006	DMKLNK DMKLNK	DMKLNM								
LISTSIZE	000001	DMKCKF	Drittenin								
LLTTY	000001	DMKBLD									
LL2741 LL3066	000001 000001 000001	DMKBLD DMKBLD									
LMSG	000001	DMKSRM									
LOADELC1	000003	DMKCMD DMKCSB	DMKSPT DMKCSC								
LOADFLG1 LOADHCPY LOADHEAD	000004	DMKCSB	DMKCSC								
LOADHEAD	000008	DMKCSB	DMKCSC								
LOADIMAG LOADIML1	000011	DMKCSB DMKCSB	DMKCSC DMKCSC								
LOADIML2	000003	DMKCSB	DMKCSC								
LOADNAME LOADPAG1	000004	DMKCSB DMKCSB	DMKCSC DMXCSC								
LOADPAG2	000013	DMKCSB	DMKCSC								
LOADPARM	000011	DMKCSB	DMKCSC								
LOADSIZE	000002	DMKCSB DMKCSB	DMKCSC								
LOADSZDW	000003	DMKCSB	51110000								
LOADUCS LOADVPAG	000007	DMKCSB DMKCSC	DMKCSC								
LOAD2CCW	000005	DMKCSC	DMKCSC								
LOCCNT	000001	DMKIMG									
LOCGRAF LOCK	000002 000657	DMKACO DMKACO	DMKACR	DMKACS	DMKALG	DMKALO	DMKAPI	DMKAPS	DMKAPT	DMKAPU	DMKAPV
2001	0000077	DMKAPW	DMKAPX	DMKAPY	DMKAPZ	DMKATS	DMKBIO	DMKBLD	DMKBSC	DMKCAC	DMKCAO
		DMKCCD DMKCFC	DMKCCF DMKCFF	DMKCCH DMKCFG	DMKCCO DMKCFH	DMKCCS DMKCFJ	DMKCCT DMKCFM	DMKCCW DMKCFO	DMKCDB DMKCFP	DMKCDM DMKCFQ	DMKCDS DMKCFR
		DHINGEO	DRINGFF	DHINGEG	UPINGER	DRIKUFJ	DRINGTIN	DHINGFU	DRINGFF		DRINGEN

DMKCFS DMKCFT DMKCFU DMKCFV DMKCFY DMKCFY DMKCKD DMKCKF DMKCKH DMKCKN DMKCKP DMKCKR DMKCKS DMKCKT DMKCKV DMKCKW DMKCLK DMKCNS DMKCPB DMKCPI DMKCPJ DMKCPM DMKCPN DMKCPO DMKCPP DMKCPS DMKCPT DMKCPV DMKCPW DMKCPX DMKCPY DMKCPZ DMKCQC DMKCQS DMKCQY DMKCRM	DMKCKM DMKCNT DMKCPU DMKCSB DMKCSW DMKDE I
DMKCRS DMKCK1 DMKCK1 DMKCK2 DMKCK1 DMKCK1 DMKCK4 DMKCK4 DMKCR4 DMKCR4 DMKCR4 DMKCR4 DMKCR4 DMKCR5 DMKCF1 DMKCF2 DMKCF2 DMKCF2 DMKCF2 DMKCF2 DMKCF2 DMKCF2 DMKCF4 DMKCF4 DMKCF4 DMKCF4 DMKCF5 DMKCF5 DMKCF5 DMKCF5 DMKCF4 DMKCF4 DMKCF4 DMKCF4 DMKCF4 DMKCF4 DMKCF4 DMKCF5 DMKCF5 DMKCF4 DMKCF4 DMKCF4 DMKCF4 DMKCF5 DMKCF4 DMKCF5 DMKF7 DMK104 DMK104 DMK105 DMKMC5 DMKN55 DMKS55 DMK	DMKDET DMKFRE DMKFRE DMKFRE DMKIOE DMKIUC DMKLNM DMKMCI DMKMON DMKOPE DMKPGM DMKPGM DMKPGM DMKSCO DMKSPR DMKSCO DMKSTP DMKTCT DMKTCT DMKVCT DMKVCT DMKVCT DMKVFR DMKVST DMKVST
DMKDGF DMKDTA DMKDTB DMKDTF DMKDSB DMKDSP DMKEAT DMKFRE DMKFR DMKIOQ DMKIOS DMKLOH DMKLOJ DMKMCH DMKMNT DMKMOO DMKNLD DMKPGM DMKPTR DMKPTS DMKPTT DMKRPA DMKSCH DMKSEL DMKSSS DMKSST DMKSSU DMKSTR DMKSVC DMKSWA DMKSWM DMKTRQ DMKTRX DMKUSP DMKVAT DMKVAU DMKVDA DMKVDD DMKVDF DMKVDS DMKVDS DMKVDT DMKVFD DMKVFE DMKVFS	DMKDGD DMKGRI DMKPRV DMKSSV DMKVCH DMKVMA
DMKVSJ LOCR 00003 DMKCCD DMKCCO DMKCCW LOCRCCHH 00006 DMKCCD DMKCCO LOCSCCHH 00002 DMKCCD LOCSRR 00001 DMKCCD LOCSRR 00002 DMKCCW	
LOGDROP 000027 DMKCNS DMKGRD DMKGRF DMKGRG DMKQCN DMKRGA DMKRGB DMKRNH DMKUSQ Loghold 000024 DMKCNS DMKDIB DMKGRD DMKGRF DMKGRG DMKQCN DMKRGA DMKRGB DMKRNH DMKVCS	DMKVCS DMKUSQ
LOCWORK 000002 DMKCCW LOGDROP 000027 DMKCNS DMKGRD DMKGRF DMKGRG DMKQCN DMKRGA DMKRGB DMKRNH DMKUSQ LOGHOLD 000024 DMKCNS DMKDIB DMKGRD DMKGRF DMKGRG DMKQCN DMKRGA DMKRGB DMKROB DMKVCS LOGMBLOK 000014 DMKCFU DMKCQY DMKLOM LOGMLEN 000005 DMKCFU DMKCQY DMKLOM LOGMSTZB 000002 DMKCFU LOGMSIZB 000002 DMKCFU LOGMSIZD 000004 DMKCFU LOGMSIZD 000004 DMKCFU LOGMSIZD 000005 DMKCFU LOGMSIZD 000005 DMKCFU LOGMSIZD 000005 DMKCFU	
LOKREQ 000012 DMKDSP DMKFRE DMKSTK LOKSAVE 000021 DMKCKW DMKLOK DMKVAT	

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LABEL	COUNT	REFERENC	ES								
LOKSAV2 LOKSYS LOWCORE LPRAN38D LPRES8B0 LPRCHGE LPRCLASS LPRCNVD LPRCPXAD LPRCPXAD LPRCPXAD LPRCFLT LPRDEFLT LPRFLG3 LPRFLG3 LPRFLG3 LPRFLG3 LPRFLG3 LPRFLG3 LPRFLG3 LPRFLG3 LPRFLG3 LPRFLG4 LPRSELD LPRLSPLC LPRNSAD LPRNSAD LPRNSAD LPRNSAD LPRNSESID LPRSEVER LPRSELDA LPRSEVER LPRSELDA LPRSESID LPRSEVER LPRTBLOK LPRVSIZE LPRVSIZE LPRVSIZE LPRVSIZE LPRVSIZE LPRVSIZE	000010 000003 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002	DMKLOK DMKAPU DMKAPU DMKAPU DMKAPU DMKAPU DMKAPU DMKAPU DMKAPU DMKAPU DMKAPU DMKAPU DMKAPU DMKAPU	DMKAPT								
LPRFLASH LPRFLG1 LPRFLG1D	000004 000007 000004	DPIKALU									
LPRFLG2 LPRFLG3 LPRFORM	000010 000007 000004	DMKAPU DMKAPS DMKAPU	DMKAPT DMKAPZ	DMKAPU	DMKAPZ						
LPRLSELD LPRLSPLC LPRNAME	000002 000007 000003	DMKAPU DMKAPS DMKAPS	DMKAPZ DMKAPT DMKAPY	DMKAPU	DMKAPV	DMKAPW	DMKCKH				
LPRNCNVD LPRNEXT LPRN038D	000002 000011 000002	DMKAPU DMKAPS DMKAPU	DMKAPU	DMKAPW	DMKAPY	DMKAPZ	DMKCKH				
LPRN5AD LPRPATH LPRREADB LPRRECBF LPRSELDA LPRSESID	000002 000006 000001 000003 000001	DMKAPU DMKAPS DMKAPU DMKAPU DMKAPU DMKAPS DMKAPS DMKAPS DMKAPU DMKAPU DMKAPU DMKAPT DMKAPU DMKAPU DMKAPU	DMKAPW	DMKAPY	DMKAPZ						
LPRSEVER LPRTBLOK LPRTSIZE LPRUSRID LPRXAB	000004 000018 000003 000001 000003	DMKAPU DMKAPU DMKAPS DMKAPS DMKAPS DMKAPS DMKAPT DMKAPU DMKACR DMKCFG DMKCFG	DMKAPT DMKAPT	DMKAPU	DMKAPV	DMKAPW	DMKAPY	DMKAPZ	<b>DM</b> КСКН		
LPR5AD LPUADDR	000002 000561	DMKAPU DMKACR DMKCFG DMKCPJ DMKCPJ DMKDIF DMKIOQ DMKMHC DMKPGM DMKRSP DMKSVC DMKVAT DMKVFE	DMKACS DMKCFP DMKCPM DMKCQQ DMKDSB DMKIOS DMKNI DMKPMA DMKSCH DMKSVD DMKVAU	DMKAPI DMKCFQ DMKCPN DMKCQU DMKDSP DMKIOT DMKPRG DMKSEL DMKSWA	DMKATS DMKCFR DMKCPO DMKCSO DMKENT DMKLOC DMKMOO DMKPRV DMKSPM DMKSWM	DMKBLD DMKCFS DMKCPP DMKDEI DMKEXT DMKLOH DMKPO DMKPTR DMKSSS DMKTMR	DMKCCH DMKCFY DMKCPS DMKDGD DMKFRE DMKLOJ DMKNES DMKPTS DMKSST DMKTOD	DMKCCS DMKCKV DMKCPT DMKDGF DMKFRT DMKLOK DMKNLD DMKPTT DMKSSU DMKTRQ	DMKCCW DMKCLK DMKCPU DMKDIA DMKGRI DMKMCC DMKRGA DMKSSV DMKTRX	DMKCDS DMKCPW DMKCPW DMKDIB DMKIOG DMKMCH DMKPAG DMKRPA DMKSTK DMKURS	DMKCFF DMKCPI DMKCPZ DMKDID DMKIOJ DMKMCT DMKPAH DMKRSE DMKSTR DMKUSP
LPUADDRX	000102	DMKACS DMKCPS DMKMNI	DMKVAU DMKVFR DMKAP1 DMKCPU DMKMNT DMKVFR	DMKVCH DMKVFS DMKCFS DMKDSB DMKMOO	DMKVDA DMKVMA DMKCFY DMKDSP DMKOPE	DMKVDD DMKVMC DMKCLK DMKENT DMKPMA	DMKVDE DMKVRR DMKCNS DMKFRE DMKPTR	DMKTRQ DMKVDR DMKVSJ DMKCPI DMKIOG DMKSCH	DMKVDS DMKWAI DMKCPJ DMKIOQ DMKSTK	DMKVDT DMKCPO DMKLOH DMKSVC	DMKVFD DMKCPP DMKMCT DMKTOD
LSPCONVT LSPID LSPIOPND LSPLCTL	000004 000009	DMKVCH DMKAPU DMKAPU DMKAPS DMKAPS	DMKVFR DMKAPV DMKAPW DMKAPT DMKAPT	DMKAPV DMKAPU	DMKAPW DMKAPV	DMKAPW	<b>DM</b> KCKH				

LABEL	COUNT	REFERENC	ES								
LSPLFLG1 LSPLNEXT LSPLPRTB	000015	DMKAPS DMKAPS DMKAPU	DMKAPT DMKAPT	DMKAPU DMKAPU	DMKAPV DMKAPV	DMKAPW DMKAPW	DMKCKH DMKCKH				
LSPLSIZE	000005	DMKAPS	DMKAPU	DMKAPV	DMKAPW						
LSPMSK	000003 000004	DMKAPS	DMKAPU	DMKAPV	DMKCKH						
LSPPURGE LSPSFBLK LSPSIZE LSPSIZE\$ LSPSPLNK	000006 000006 000002	DMKAPT DMKAPS DMKFRE DMKFRE DMKAPT	DMKAPW DMKAPT DMKFRT DMKFRT DMKAPU	DMKAPU	DMKAPV	<b>DMKCKH</b>					
L1	000048	DMKDAD DMKLNK DMKDDR	DMKDAS DMKPAG	DMKDDR DMKPMA	DMKDGD DMKRGE	DMKD I R DMKTDK	DMKDMP DMKTRK	DMKDRD	DMKDRE	DMKFMT	DMKHVE
L1024 L15	000001	DMKFMT DMKCPX									
L16 L2	000047 000059	DMKCPX DMKCAC DMKFPS	DMKDDR DMKCFG DMKHVE	DMKDGD DMKCFT DMKPRV	DMKDGF DMKCPI DMKRGE	DMKD I R DMKDAD DMKSSS	DMKDMP DMKDAS DMKTRK	DMKFMT DMKDDR DMKVSC	DMKPAG DMKDGD DMKVS I	DMKZTD DMKD I D	DMKDMP
L20	000002	DMKDGD	DMKFMT			DINGUU	DHKINK	DHR¥50	DAK¥SI		
L2048 L24	000010 000001	DMKCFG DMKDAS	DMKCPU	DMKPTR	DMKSTA						
L3	000032	DMKCFT DMKMCH	DMKCKH DMKMNT	DMKCKN DMKPAG	DMKDAS DMKSAV	DMKDEF DMKSSP	DMKDE I DMKTRK	DMKDMP	DMKDRD	DMKDRE	DMKFMT
L4	000072	DMKCCD DMKDEI DMKSAV	DMKCFG DMKDGD DMKSRM	DMKCFT DMKDGF DMKSSS	DMKCKH DMKDIR DMKTRK	DMKCPI DMKDMP DMKVDD	DMKCPS DMKFMT	DMKCQG DMKHVE	DMKDAD DMKPAG	DMKDAS DMKPRV	DMKDDR DMKPTR
L400 L4096	000001	DMKFMT DMKD I R	DMKFMT								
L4096	000008 000040	DMKCKH	DMKCKN	DMKDDR	DMKDGD DMKFMT	DMKDMP	DMKFMT	DMKMNT	DMKSAV	DMKSSP	
L5 L6 L7	000009 000009	DMKCP I DMKCAO	DMKDGD DMKDDR	DMKDGF DMKDEF	DMKDEI	DMKDIR	DMKFMT	DMKSAV			
Ē8	000142	DMKAPI DMKDMP DMKZTD	DMKAPS DMKFMT	DMKCAO DMKLOH	DMKCKH DMKMCH	DMKCKN DMKPAG	DMKCLK DMKPGU	DMKCQP DMKPMA	DMKDAD DMKSAV	DMKDDR DMKTOD	DMKDEI DMKTRK
L80 L9 MACRO MAINHIGH MAINPSA	000001 000002 000001 000004 000006	DMKFMT DMKDDR DMKDMP DMKIMG DMKDMQ	DMKDEI								
MASKLINK MASKLOG MATCH	000001 000002 000002	DMKLNK DMKLOG DMKFPS	DMKHVF								
MAXCSIZE MAXHSIZ MAXLEN MAXSIZE	000001 000002 000021 000012	DMKFRE DMKFRE DMKFMT DMKFRE	DMKFRT DMKSSP								
MAXSPSIZ MCCPUID MCFXDLOG	000002 000001	DMKFRE DMKMCH DMKMCH									
MCHAREA	000021	DMKACR DMKMCH	DMKCCH DMKMC I		DMKCPO DMKMHC	DMKCPP DMKMHV	DMKCPU DMKPRV	DMKDID DMKVAT	DMKHVC DMKVAU	DMKIOG	DMKIOH
MCHCPEX MCHDAMLN		DMKCPP DMK10G			DMKMCH DMKDMP			DMKPMA	DMKSAD	DMKSTA	
MCHEK	000035	DMKAP I DMKTOD	DMKCLK	DMKCPI		DMKDMQ	DMKMCH	UMKPMA	UMKSAU	UMKSIA	DMKSVD
MCHFIX MCHFLAG0	000006 000007	DMKCPP DMKMCH	DMKCPU	DMKIOG	DMKMCH						

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LABEL	COUNT	REFERENCI	ES								
MCHFLAG3 MCHFLAG4 MCHFLAG5	000024 000002 000002	DMKACR DMKMCH DMKMCH DMKMCH	<b>ОМКМСН</b> ОМКМСТ	<b>DMKMCT</b>							
MCHFLAG7 MCHFSAR	000037 000007	DMKMCH DMKMCH	DMKMCT								
MCHLEN1	000004	DMKCPP DMKCCH	DMKCPU DMKCFU	DMK10G DMKCP0	DMKMCH DMKD I D	DMKHVC	DMKIOG	DMKIOH	DMKMCH	DMKMC	DMKMHC
MCHPDAR6 MCHPDAR7 MCHPROCA MCHP1IDE MCHP1IKE MCHP1SDE MCHP1SDE MCHP1STD	000003 000013 000001 000001 000002 000001 000002 000002 000004	DMKMHV DMKMCH DMKMCH DMKMCH DMKMCH DMKMCH DMKMCH DMKMCH DMKMCH	υμκρκά	<b>ΜΚΥΑΙ</b>	DMKVAU						
MCHP6CBA MCHREC MCHRESEV MCH0HDWR MCH0QUIT MCH00SFTR MCH0TERM MCH0USAD	000003 000008 000002 000002 000001 000001 000001 000001	DMKMCH DMKCPP DMKMCH DMKMCH DMKMCH DMKMCH DMKMCH DMKMCH	DMKCPU	DMKIOG	<b>ДМКМСН</b>						
MCH1COST MCH1GERR MCH1IOTO	000002 000002 000001	DMKMCH DMKACR DMKMCH	<b>DMKMCH</b>								
MCH1PROC MCH1TODC MCH3BCST MCH3DATA MCH3DGRD MCH3HARD MCH3INTE MCH3PASS	000002 000002 000003 000002 000004 000004 000002 000002	DMKMCH DMKMCH DMKMCH DMKMCH DMKMCH DMKMCH DMKMCH DMKMCH	<b>ДМКМСТ</b> ДМКМСТ								
MCH3PROT MCH3SOLD MCH4BURE MCH4BURE MCH4REPA MCH4CF5IFSA MCH7CHTM MCH7CHTM MCH7CSTR MCH7OFSW MCH7PURG MCH7SWCR MCH7SWP MCH7SVST	000001 000003 000004 000001 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000002 000003 000003 000003 000008	DMKMCH DMKMCH DMKMCH DMKMCH DMKMCH DMKMCH DMKMCH DMKMCH DMKMCH DMKMCH DMKMCH DMKMCH DMKMCH DMKMCH	DMKMCT								
	MCHFLAG1 MCHFLAG5 MCHFLAG64 MCHFLAG65 MCHFLAG66 MCHFLAG7 MCHFSAR MCHLEN1 MCHDDAR1 MCHDDAR1 MCHPDAR1 MCHPDAR6 MCHPDAR7 MCHPDAR7 MCHPDAR7 MCHPDAR7 MCHP1SE MCHP1SE MCHP1SE MCHP1SE MCHP1SE MCHP1SE MCHP1SE MCHP1SE MCHP1SE MCHP1SE MCHP1SE MCHP1SE MCHP1SE MCHP1SE MCHP1SE MCH0SFT MCH00SFT MCH00SE MCH100TERM MCH0USAD MCH1BUFF MCH10TERM MCH10TERM MCH120T MCH3BCST MCH3BCST MCH3BCST MCH3SOFT MCH3SOFT MCH3SOFT MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM 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MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHTM MCH7CHT	MCHFLAG1         000012           MCHFLAG3         000024           MCHFLAG4         000002           MCHFLAG5         000002           MCHFLAG5         000001           MCHFLAG6         000001           MCHFLAG7         000037           MCHFLAG6         000001           MCHFLAN         000009           MCHFDAR1         00009           MCHPDAR1         000001           MCHPDAR7         000013           MCHPDAR6         000002           MCHP11DE         000001           MCHP11KE         000002           MCHP11KE         000002           MCHP11KE         000002           MCHP11KE         000001           MCHP2SE         000002           MCHP1SKE         000002           MCH0001         00001           MCH0001         00001           MCH0001         00001           MCH0001         00001           MCH0001         00001           MCH0001         000001           MCH00001         MCH000002           MCH000002         MCH000002           MCH000002         MCH10000002           MCH10000002	MCHFLAG1         000012         DMKACR           MCHFLAG3         000024         DMKMCH           MCHFLAG4         000002         DMKMCH           MCHFLAG5         000001         DMKMCH           MCHFLAG7         000037         DMKMCH           MCHFLAN         000001         DMKMCH           MCHFLAN         000001         DMKMCH           MCHLEN         000003         DMKMCH           MCHPDAR1         000009         DMKMCH           MCHPDAR6         000013         DMKMCH           MCHPDAR7         000010         DMKMCH           MCHP1DAR6         000002         DMKMCH           MCHP1SE         000001         DMKMCH           MCHP1SE         000002         DMKMCH           MCHP1SE         000002         DMKMCH           MCHP1SE         000001         DMKMCH           MCHP1SE         000002         DMKMCH           MCHP1SE         000001         DMKMCH	MCHFLAG1         000012         DMKACR         DMKMCH           MCHFLAG3         000024         DMKMCH         DMKMCH           MCHFLAG5         000002         DMKMCH         DMKMCT           MCHFLAG6         000001         DMKMCH         DMKMCT           MCHFLAG7         000037         DMKMCH         DMKMCT           MCHFLAG7         000001         DMKMCH         DMKCPU           MCHFLAG1         000004         DMKCPP         DMKCPU           MCHPDAR1         000009         DMKMCH         MKCPU           MCHPDAR1         000009         DMKMCH         MKCPU           MCHPDAR7         000013         DMKMCH         MKCPU           MCHPDAR6         00002         DMKMCH         MKCPU           MCHP11DE         000002         DMKMCH         MKCPU           MCHP11KE         000002         DMKMCH         MKCPU           MCHP1SE         000001         DMKMCH         MKCPU           MCHP1SE         000002         DMKMCH         MKCPU           MCHP100001         DMKMCH         MCHP1         MKCPU           MCHP100001         DMKMCH         MCHP1         MKCPU           MCHP1000001         DMKMCH	MCHFLAG1         000012         DMKACR         DMKMCH         DMKMCT           MCHFLAG3         00002         DMKMCH         DMKMCT         DMKMCT           MCHFLAG6         000002         DMKMCH         DMKMCT         DMKMCT           MCHFLAG6         000001         DMKMCH         DMKMCT         DMKMCT           MCHFLAG1         000001         DMKMCH         DMKMCT         DMKMCT           MCHPLAN         000001         DMKMCH         DMKCPU         DMKVCT           MCHPDAR1         000009         DMKMCH         DMKVCT         DMKVAT           MCHPDAR1         000001         DMKMCH         DMKVAT         DMKVAT           MCHPDAR1         000002         DMKMCH         DMKVAT         DMKVAT           MCHPDAR5         000001         DMKMCH         MKVAT         DMKVAT           MCHPDAR6         000001         DMKMCH         MKVAT         DMKVAT           MCHP1SDE         000002         DMKMCH         MKVAT         MKVAT           MCHP1ABE         000002         DMKMCH         MKCPU         DMKIOG           MCHP1SDE         000001         DMKMCH         MKCPU         DMKIOG           MCHP1GER         000002         DMKMCH<	MCHFLAG1         000012         DMKACR         DMKMCH         DMKMCT           MCHFLAG3         000001         DMKMCH         DMKMCH         DMKMCT           MCHFLAG5         000001         DMKMCH         DMKMCH           MCHFLAG7         000001         DMKMCH         DMKMCH           MCHFLAG7         000007         DMKMCH         DMKMCH           MCHFLAG7         000004         DMKCPU         DMKCPU           MCHLD1         0000050         DMKCCH         DMKCFU           MCHPDAR1         0000050         DMKMCH         DMKVAT           MCHPDAR3         000001         DMKMCH         DMKVAT           MCHPDAR4         000001         DMKMCH         DMKVAT           MCHPDAR5         000001         DMKMCH         DMKVAT           MCHP1DE         000001         DMKMCH         DMKVAT           MCHP1DAR5         000001         DMKMCH         DMKVAT           MCHP1DAR5         000001         DMKMCH         DMKCPU           MCHP1DAR5         000001         DMKMCH         DMKCPU           MCHP1SDE         000001         DMKMCH         MCHP1SDE           MCHP1SDE         000001         DMKMCH         MCHP1SDE	MCHFLAG1         000012         DMKACR         DMKMCH         DMKMCT           MCHFLAG3         000024         DMKMCH         DMKMCT         DMKMCT           MCHFLAG5         000002         DMKMCH         DMKMCH         DMKMCT           MCHFLAG5         000001         DMKMCH         DMKMCT         MCMFSAR           MCHFLAG7         000017         DMKMCH         DMKCPU         DMKCPU         DMKCPU           MCHEN         000001         DMKCH         DMKCPU         DMKCPU         DMKCPU         DMKHVCH           MCHPDAR1         000009         DMKMCH         DMKCPU         DMKVAU         DMKHVCH           MCHPDAR5         000003         DMKMCH         DMKCPU         DMKVAT         DMKVAU           MCHPDAR6         000003         DMKMCH         MKVAT         DMKVAU         DMKHVC           MCHPDAR5         000001         DMKMCH         MKVAT         DMKVAU         DMKHVCH           MCHPDAR6         000002         DMKMCH         MKVAT         DMKVAU         DMKHVCH           MCHPDAR5         000001         DMKMCH         MKMCH         MKHCH         MKHCH           MCHPLAG5         000002         DMKMCH         MKMCH         MKMCH	MCHFLAG1         000012         DMKACR         DMKMCH         DMKMCT           MCHFLAG3         000002         DMKMCH         DMKMCH         DMKMCH           MCHFLAG5         000002         DMKMCH         DMKMCH           MCHFLAG7         000037         DMKMCH         DMKNCH           MCHFLAG7         000001         DMKMCH         DMKNCH           MCHFLAG7         0000050         DMKCP         DMKCPU           MCHEDAT         000009         DMKMCH         DMKCPU           MCHPDAR1         000009         DMKMCH         DMKCPU           MCHPDAR1         000002         DMKMCH         DMKCPU           MCHPDAR5         000001         DMKMCH         DMKCPU           MCHPDAR5         000002         DMKMCH         DMKCPU           MCHPCBAR5         000001         DMKMCH         DMKCPU           MCHRESEV         000002         DMKMCH         DMKCPU           MCHRESEV         000002         DMKMCH         DMKCPU           MCHRESEV         000001         DMKMCH         DMKCPU           MCHRESEV         000001         DMKMCH         DMKMCH           MCHRESEV         000001         DMKMCH         DMKMCH <tr< td=""><td>MCHFLAGI MCHFLAGI MCHFLAGI MCHFLAGI 000002         DMKACR DMKMCH DMKMCH         DMKMCH DMKMCT           MCHFLAGI MCHFLAGI 000001         DMKMCH DMKMCH         DMKMCH         DMKMCH           MCHFLAGI MCHFLAGI MCHFLAGI MCHFLAGI MCHFLAGI MCHFLAGI MCHFLAGI MCHFLAGI MCHFLAGI MCHFLAGI MCHFLAGI MCHFLAGI MCHPDARI 000003         DMKMCH DMKCPU DMKCPU DMKCPU DMKVAT         DMKMCH DMKVAT         DMKMCH DMKVAT           MCHPDARI MCHPDARI MCHPDARI 000003         DMKMCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKCH DMKCH DMKCH DMKCH DMKCH DMKCH DMKRCH DMKRCH DMKRCH DMKCH DMKCH DMKCH DMKCH DMKCH DMKRCH DMKCH DMKRCH DMKCH DMKCH DMKRCH DMKCH DMKCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH DMKRCH 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LABEL	COUNT	REFERENC	CES								
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MDRSENS	000003	DMKVER									
MDRSIZE	000001	DMKVER									
MDRSIZE1 MDRSWS3	000001 000002	DMKVER DMKVER									
MDRVOL	000001	DMKVER			5.44/6.04 h						
MEMO1 MEMO2	000034 000044	DMKCCD DMKCCD	DMKCCF DMKCCF	DMKCCO DMKCCO	DMKCCW DMKCCS	DMKCCT	DMKCCW				
MEMO2	000014	DMKCCD	DMKCCS	DMKCCT	DMKCCW	Dimoor	brincoon				
MESSAGE MFAMASK	000004 000012	DMKCFC DMKACR	DMKCKM DMKAPI	DMKMC I DMKCP I	DMKCPU	DMKDSP	DMKEXT	DMKIOT	DMKLOK	DMKPMA	
MFASAVE	0000012	DMKMCT	DHKAFT	DHKOFT	DIAKOTO						
MICBLOK	000080	DMKAPI	DMKBLD	DMKCDS	DMKCFF DMKDSP	DMKCFG DMKFPS	DMKCFO DMKIOT	DMKCFP DMKIUN	DMKCFS DMKLOJ	DMKCFY DMKMCH	DMKCPB DMKPGS
		DMKCPI DMKPMA	DMKCPP DMKPRV	DMKCPU DMKPTR	DMKQVM	DMKRPA	DMKSAD	DMKSCH	DMKSEL	DMKSPM	DMKSTR
		DMKSWA	DMKTMR	DMKTRA	DMKTRQ	DMKVAT	DMKVRR				
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MICDASA	000010	DMKAPI	DMKCFG	DMKCFO	DMKCFS	DMKCPI	DMKLOJ	DMKVAT	DMKVRR		
MICDIAG MICEVMA	000001 000004	DMKCPI DMKCFS	DMKCFY	DMKLOJ							
MICEVMA		DMKCFP	DMKCFS	DMKCPB	DMKDSP	DMKFPS	DMKLOJ	DMKQVM	DMKSCH	DMKSPM	DMKTMR
		DMKTRQ	DMKVAT	DMKVRR	DMKOES	DMICOEV	DMKODI	DMUDED			DMKSPM
MICEVMA	000033	DMKAPÍ DMKVAT	DMKCFG DMKVRR	DMKCFO	DMKCFS	DMKCFY	DMKCPI	DMKDSP	DMKLOJ	DMKPMA	DMKSPM
MICEVMA	000005	DMKCFS	DMKPMA				5.444 (B 5				
MICFSSE	000011 000003	DMKCFG DMKPMA	DMKCFO	DMKCFS	DMKLOJ	DMKVAT	DMKVRR				
MICIPTP2	000003	DMKDSP	DMKVAT								
MICISKE MICLCTL2	000005	DMKAPI DMKVAT	DMKCFS	DMKCFY	DMKCPI	DMKDSP					
MICLPSW	000001	DMKCPI									
MICLRA2	000003	DMKDSP	DMKVAT	DMIZIUN							
MICPEND	000005	DMKDSP DMKCFG	DMKFPS DMKSPM	DMK I UN DMKVRR							
MICPMAV	000005	DMKCFG	DMKPMA	DMKVRR							
MICPMMŠK MICPMPSA		DMKCFG DMKCFG	DMKIOT DMKCPP	DMKVRR DMKCPU	DMKSAD	DMKVRR					
MICPTLB	000001	DMKLOJ		SPIROT 0	UNICAD	DEIXYIXX					
MICPTLB2		DMKDSP	DMKVAT								
MICRRBE	000004 000004	DMKCFS DMKBLD	DMKCFY DMKCFS	DMKDSP DMKCFY	DMKLOJ						
MICSCSP	000001	DMKCPI									

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LABEL	COUNT	REFERENCE	ES						
MICSIO	000003	DMKCPI	DMKLOJ						
MICSIZE	000024	DMKBLD	DMKCFS	DMKCFY	DMKLOJ	DMKPMA	DMKUSQ		
MICSKYMD		DMKAPI	DMKCFY	DMKCPI	DMKLOJ		DIMETING	0.44/TD0	
MICSPT	000009	DMKCFP	DMKCPB	DMKFPS	DMKQVM	DMKSCH	DMKTMR	DMKTRQ	
MICSPT4	000002	DMKCFS	DMUGEN	DHUDOD					
MICSSKE	000003	DMKCFS	DMKCFY	DMKDSP	DMUCON	DMULAT	DMI/MDD		
MICSTBVR		DMKCFS	DMKDSP	DMKLOJ	DMKSPM	DMKVAT	DMKVRR		
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MICSTSM2		DMKLOJ	DMKSPM	DMKVAT					
MICSVC4	000002	DMKCFS	DHKSTH	DRINKA					
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MICVIP	000004	DMKDSP	DMKFPS						
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MIHGRAF	000001	DMKDID							
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MIHKEYN	000002	DMKIOE	DMKVER						
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MNCHSAM1	000006	DMKENT	DMKMOO						
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MNCHSIZE		DMKENT	DMKMNI	DMKMOO					
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MNCHZMP	000001	DMKMNI							
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MNCLINST	000005	DMKPRV	DMKPRW						

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LABEL	COUNT	REFERENCE	S			
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MNCLRESP	000007	DMKGRG	DMKQCN	DMKQCO	DMKRGC	
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MNCLSEEK		DMKIOS				
MNCLSWAP	000009	DMKPTR	DMKPTS	DMKSWA		
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MNCOCYL	000001	DMKIOS				
MNCODA	000001	DMKMON				
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MNCOSUS	000001	DMKMON				
MNCOSYS	000001	DMKMOO				
MNCOTH	000001	DMKMNI				
MNCOTT	000001		DMI/MOO			
MNCOUSER MNCOWR I T	000002 000001	DMKMN I DMKQCN	DMKMOO			
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MNDVLEN MNDVMORE	000006 000010	DMKENT	DMKMCC	DMKMNI	DMKMOO	
MNDVSIZE	000004	DMKMCC	DMKMNI	DEINERI	DHKHOO	
MNHCLASS	000001	DMKMON	of that here			
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MN000PAP	000001	DMKMOO				
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MOOUFDE	000001	DHINHUU				

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LABEL	COUNT	REFERENC	ES
MN000PBS MN000PFP MN000PFS MN000PPC MN000PPC MN000PPC MN000PTS MN000Q1E MN000Q2E MN000SSC MN000SSC MN000SSC MN000VFC MN000VFC MN000VFC MN000VFC MN000VSC MN000VSC MN000VFC MN000VSC MN000VSC MN000VSC MN000VSC MN000VSC MN000VSC MN000VSC MN000VFC MN000VSC MN000VSC MN000VSC MN000VSC MN000VSC MN002IBM MN002IBM MN002Q1D MN002Q1D MN002Q1D MN002Q1D MN002Q1D MN002Q1D MN002Q1D MN002Q2D MN002Q2D MN002Q2D MN002Q2D MN002Q2D MN002Q2D MN002Q2D MN002Q2D MN002Q2D MN002Q2D MN002Q2D MN002Q2D MN002Q2D MN002CSE MN003CSE MN003CCFP MN003SCFP MN003SCFP MN003SCFP MN003SCFP MN003PNS MN003SWP MN004HL MN004HL MN004HL MN004HL MN004NP	000001 000001 000001 000001 000001 000001 000002 000001 000002 000001 000002 000001 000002 000002 000001 000002 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000000	DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DMKM00 DM	Омкмоо

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MN004N2 MN005	000001	DMKMOO DMKMOO
MN005ADR	000003	DMKMOO
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MN097FSS	000001	
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MN097LSN	000003	DMKMNI
MN097MOD MN097NUC	000003	DMKMN I DMKMN I
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MN097PCS	000001	DMKMNI
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MN097TTS	000001	DMKMNI
MN097UID	000001	DMKMNI
MN097VR MN098	000001 000001	DMKMN I DMKMN I
MN098LEN	000001	DMKMN I
MN098UID	000001	DMKMNI
MN099	000001	DMKMON

CP Directories 605

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LABEL	COUNT	REFERENC	ES
MN500VAD	000002	DMKMON	
MN600ADD	000006	DMKMNI	DMKMOO
MN600CNT	000002	DMKMN I	DMKMOO
MN600DEV MN600DLN	000002 000005	DMKMN I DMKMN I	DMKMOO DMKMOO
MN600HDR	000003	DMKMNI	DMKMOO
MN600HLN	000006	DMKMN	DMKMOO
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MN700	000001	DMKMON	
MN700ADD	000001	DMKMON	
MN700CCY	000002	DMKMON	
MN700CHR	000003	DMKMON	

	LABEL	COUNT	REFERENCE	Es				
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	MN700RS2 MN700UID MN802CLN		DMKMON DMKMON DMKMON					
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	MN802DEV MN802DLN	000001	DMKMON					
	MN802NAU MN802NPP	000001	DMKMON DMKMON					
	MN802NUM MN802PGR	000001	DMKMON DMKMON					
	MN802PGW MN802PRB	000001	DMKMON DMKMON					
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	MODEL145 MODEL148	000004	DMKCCH DMK I OG	DMKIOG	DMKMCH			
	MODEL155 MODEL158	000004 000001	DMKCCH DMK I OG	DMKIOG	DMKMCH			
:	MODEL165 MODEL168	000005 000001	DMKCCH DMK I OG	DMKIOG	DMKMCH			
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•	MOD4321 MOD4331 MOD4341	000001 000004 000001	DMKCCH DMKLOG	DMKIOG	DMKMCH			
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	MONATRB MONBUFAC MONBUFAV	000006 000010 000006	DMKMCC DMKMCC DMKMCC	DMKMNI DMKMIA DMKMON	DMKMNJ DMKMON			
	MONBUFIO MONBUFI		DMKM1A DMKMCC	DMKMON DMKMIA	DMKMN I	DMKMNJ	DMKMON	
•		-						

DMKVAT

DMKMHV

DMKVAU

DMKPRV

LABEL

COUNT

REFERENCES

MONBUE1V	000001	DMKMN I									
MONBUF1V MONCAD	000002	DMKDMQ DMKENT									
MONCHPTR MONCLASS MONCLASS MONCLOCK MONCODE MONCOM MONCORSLT MONDASS MONDASS MONDASS MONDASS MONDASS MONDASS MONDVNUM MONEX MONFLAG1 MONFLAG3 MONFLAG3 MONFLAG3 MONFLAG3 MONFLAG3 MONFLAG3 MONFLAG3 MONFLAG3 MONFLAG4 MONFLAG4 MONFLAG5 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFLAG4 MONFL	000012	DMKENT	DMKMCC	DMKMCD DMKMON	DMKMN I DMKMOO	DMKMOO					
MONCLASS	000016	DMKMNI	DMKMNL	DMKMON	DMKMOO	DMKPRG					
MONCLKSA	000002	DMKMON									
MONCL OCK	000002										
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MONCRSLT	0000025	DMKMCC	DMKMNL DMKDMQ DMKMON	DINCENT	Britanoo	Dimator	0	<b>D</b> in a in t	51114110	5.114.001	
MONCURBE	000000	DMKMON DMKCPS DMKMCC DMKMCC DMKMIA DMKMIA	DMKMIA	DMKMNI	DMKMON						
MONDAS	000010	DMKMIA	QUINTIN	Dimana	Difference						
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MONELAGZ	000013			DMKMCC	DMKMCD		DMKMN I	DMKMON			
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MONISTRI	000004	DMKMOU	DIMENTION								
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MONPDLY	000003	DMKMON	044/000								
MONREGS	000008	DMKMON DMKMOO	DMKPRG								
MONSACT	0000015 000003 000008 000004 000004	DMKMOO									
MONSAVET	000001	DMKMON									
MONREGS MONSAVE1 MONSAVE2 MONSFB MONSIZE MONSUSLMT MONSUSCT MONSUSCT MONSUSCT MONSUSCT MONSUSCT MONSUSCT MONSUSCT MONSUSCT MONSUSCT MONSUSCT MONSUSCT MONSUSCT MONSUSCT MONSUSCT MONSUSCT MONUSCR	000001	DMKMON DMKMIA	DAUGANI								
MONSEB	000006	DMKMIA	DMKMNJ								
MUNSIZE	000003	DMKMCC	DMKMN I								
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MONUSER	800000	DMKCPS	DMKMCC DMKMCC	DMKMCD	DMKMIA	DMKMNI	DMKMON				
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	000008 000362	DMKMCC	DMKMIA	DMKMON							
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		DMKCKD	DMKCKF	DMKCKH	DMKCKM	DMKCKN	DMKCKP DMKCPJ	DMKCKR	DMKCKS DMKCPN	DMKCKT DMKCPO	DMKCKV DMKCPP
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		DMKIOG	DMKIOH	DMKIOJ	DMKIOQ	DMKIOS	DMKIOT	DMKINM	DMKLOC	DMKLOG	DMKLOH
		DMKLOJ	DMKLOK	DMKLOM	DMKMCC	DMKMCD	DMKMCH	DMKMCI	DMKMNT	DMKMON	DMKMOO
		DMKCPS DMKDEX DMKIOG DMKLOJ DMKPO DMKPSA DMKRGC DMKRSQ DMKSVA DMKSVA DMKSVA	DMKLOK DMKMSG DMKPST DMKRGD DMKSAV DMKSRM DMKTAQ	DMKTOJ DMKLOM DMKNET DMKPTR DMKRNH	DMKNLD DMKPTS DMKRPA	DMKOPE DMKPTT	DMKPET DMKQCN	DMKPGM	DMKPGS DMKQCP DMKRSE	DMKPGT	DMKMOO DMKPRW DMKRGB
		DMKPSA	DMKPST	DMKPTR	DMKPTS	DMKPTT	DMKQCN	DMKQCO DMKRPW	DMKQCP	DMKRGA	DMKRGB
		DMKRGC	DMKRGD	DMKRNH	DMKRPA	DMKRPD	DMKRPI	DMKRPW	DMKRSE	DMKRSF	DMKKSP
		DMKRSQ	DMKSAV	DMKSBL	DMKSCH	DMKSCN	DMKSND	DMKSPK	DMKSPL	DMKSPM	DMKSPR
		DMKSPS	DMKSRM	DMKSSS	DMKSST	DMKSSU	DMKSSV	DMKSTA	DMKSTK	DMKSTP	DMKSVD
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		DMKVCQ	DMKVCR	DMKVCS	DMKVCT	DMKVCU	DMKVCV	DMKVCW	DMKVER	DMKVFC	DMKVFD
			2	2	2			2			

DPK/FEAT         D00012         DPK/FEAT         D00012         DPK/FEAT         DPK/FEAT <th< th=""><th></th><th>LABEL</th><th>COUNT</th><th>REFERENC</th><th>ES</th><th></th><th></th><th></th><th></th><th></th><th></th><th></th><th></th><th></th></th<>		LABEL	COUNT	REFERENC	ES									
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MSGAALEN 000001 DMKIUL MSGAANXX 000001 DMKIUL MSGAANYX 000001 DMKIUL MSGAANYX 000001 DMKIUL MSGABLEN 000001 DMKIUL MSGABTOT 000001 DMKIUE MSGADDR 000001 DMKIUE MSGAITRN 000001 DMKIUE MSGAITRN 000001 DMKIUE MSGAISL 000010 DMKIUE MSGARSL 000001 DMKIUE MSGARSL 000001 DMKIUE MSGARSL 000001 DMKIUE MSGARSL 000002 DMKIUE MSGARCX 000001 DMKIUE MSGARCX 000002 DMKIUE MSGARCX 000002 DMKIUE MSGASVX 000001 DMKIUE MSGASVX 000001 DMKIUE MSGASVX 00001 DMKIUE MSGASVX 00001 DMKIUE MSGASVX 00001 DMKIUE MSGASVX 00001 DMKIUE MSGAUDT 000008 DMKIUE MSGAUDT 000009 DMKIUL MSGAUDT 000009 DMKIUE MSGAUDT 000009 DMKIUE MSGAUDT 000001 DMKIUE MSGAUDT 000001 DMKIUE MSGAUDT 000001 DMKIUE MSGCPI0 00001 DMKIUE MSGCPI0 000012 DMKIUB DMKIUC MSGCTLS 000012 DMKIUB DMKIUE DMKIUB DMKIUC MSGCTLS 000012 DMKIUB DMKIUE DMKIUB DMKIUB DMKIUB MSGCTLS 000012 DMKIUB DMKIUE DMKIUB MSGCTLS 000012 DMKIUB DMKIUE DMKIUB DMKIUB DMKIUB DMKIUB MSGCTLS 000012 DMKIUB DMKIUE DMKIUB DMKIUB DMKIUB DMKIUB MSGCTLS 000011 DMKIUB DMKIUB DMKIUB DMKIUB DMKIUB MSGCTLS 000012 DMKIUB DMKIUB DMKIUB DMKIUB DMKIUB DMKIUB MSGCTLS 000012 DMKIUB DMKIUB DMKIUB DMKIUB DMKIUB DMKIUB MSGCTLS 000012 DMKIUB DMKIUB DMKIUB DMKIUB DMKIUB MSGCTLS 000001 DMKIUB DMKIUB DMKIUB DMKIUB DMKIUB DMKIUB MSGCTLS 000002 DMKIUB DMKIUB DMKIUB DMKIUB DMKIUB DMKIUB DMKIUB MSGCTLS 000001 DMKIUB DMKIUB DMKIUB DMKIUB DMKIUB DMKIUB DMKIUB DMKIUB DMKIUB MSGCTLS 000002 DMKIUB DMKIUB MSGCTLS 000011 DMKI		MSFDB3 MSFINFO MSFLNG MSFRSP MSFR01 MSFR41	000001 000001 000004 000010 000002 000001	DMKMHV DMKMHV DMKCPU DMKCPU DMKMHC DMKMHV	DMKMHC	DMKMHV								
MSGADDR 000001 DMKIUE MSGAITRN 000001 DMKIUE MSGAISLN 000001 DMKIUL DMKIUL DMKIU MSGANSAD 000009 DMKIUL DMKIUD DMKIUE DMKIUL DMKIU MSGAPSEN 000019 DMKIUL DMKIUB DMKIUE DMKIUE DMKIUJ DMKIUJ DMKIUS MSGAPPEN 000010 DMKIUL MSGARCAX 00002 DMKIUE MSGARCAX 000002 DMKIUE MSGARPEN 000002 DMKIUE MSGARPEN 000002 DMKIUE MSGARPEN 000002 DMKIUE MSGASNPX 000002 DMKIUL MSGASNPX 000001 DMKIUE MSGASNPX 000001 DMKIUE MSGASNPX 000001 DMKIUE MSGASTINV 00001 DMKIUE MSGATINV 00001 DMKIUE MSGAUDIT 00008 DMKIUE DMKIUE MSGAUDIT 00008 DMKIUE DMKIU MSGAUDIT 00008 DMKIUE DMKIU MSGAUDIT 00009 DMKIUA MSGAUDIT 00009 DMKIUA MSGAUDIT 00009 DMKIUE DMKIU MSGAUDIT 00001 DMKIUE MSGAUDIT 00008 DMKIUE DMKIU MSGAUDIT 000014 DMKIUE DMKIU MSGAUDIT 00008 DMKIUE DMKIU MSGAUDIT 000025 DMKIUA DMKIUE DMKIU MSGUDIT 000020 DMKIUA DMKIUE DMKIU MSGUDIT 000020 DMKIUA DMKIUE DMKIU MSGUDIT 00002 DMKIUA DMKIUE DMKIU MSGUDIT 000004 DMKIUE DMKIU MSGUDIT 000025 DMKIUA DMKIUE DMKIU MSGUDIT 00001 DMKUA DMKIUE DMKIU MSGUDIT 000014 DMKIUE DMKIU MSGUDIT 000025 DMKUA DMKIUE DMKIU MSGUDIT 000014 DMKIUE DMKIU MSGUDIT 000014 DMKIUB DMKIUE DMKIU MSGUDIT 000014 DMKIUB DMKIUE DMKIUS MSGUDIT 000012 DMKIUA DMKIUB DMKIUE DMKIUS MSGUDIT 000014 DMKIUB DMKIUE DMKIUS MSGUDIT 000014 DMKIUB DMKIUE DMKIUS MSGUDIT 000012 DMKIUA DMKIUB DMKIUE DMKIUS MSGUDIT 000014 DMKIUB DMKIUE DMKIUS MSGUDIT 000014 DMKIUB DMKIUB DMKIUE DMKIUS MSGUDIT 000014 DMKIUB DMKIUB DMKIUB DMKIUS MSGUDIT DMO025 DMKIUA DMKIUB DMKIUB DMKIUB DMKIUS MSGUDIT 000002 DMKUA MSGUDIT DMO025 DMKUA DMKIUB DMKIUB DM		MSGAALEN MSGAANAX MSGAANPX MSGAATOT MSGABLEN	000001 000001 000001 000001 000001 000001	DMKIUL DMKIUL DMKIUL DMKIUL DMKIUE										
MSGANSLN 000019 DMKIUA DMKIUB DMKIUE DMKIUE DMKIUU DMKIUS DMKIUS MSGAPRO 00001 DMKIUE DMKIUE DMKIUG DMKIUG DMKIUU DMKIUS MSGARGAX 000002 DMKIUE MSGARCAX 000002 DMKIUE MSGARJCT 000001 DMKIUE DMKIUE DMKIUE MSGARJCT 000001 DMKIUE DMKIUL DMKIUL MSGARJCT 000002 DMKIUL DMKIUL DMKIUL MSGARPAX 000002 DMKIUL DMKIUL DMKIUS MSGARPAX 000002 DMKIUL DMKIUL MSGARPAX 000002 DMKIUL MSGASNAX 000001 DMKIUE MSGASNAX 000001 DMKIUE MSGASNAX 000001 DMKIUE MSGASNAX 000001 DMKIUL MSIUS MSGAUDT3 000008 DMKIUE DMKIUG DMKIUS MSGELIST 000004 DMKIUE DMKIUN DMKIUC DMKIUS MSGELIST 0000012 DMKIUB DMKIUC DMKIUC DMKIUS MSGELIST 0000012 DMKIUB DMKIUG DMKIUC DMKIUS MSGELIST 000012 DMKIUB DMKIUE DMKIUG DMKIUS MSGELIST 000012 DMKIUB DMKIUE DMKIUG DMKIUS MSGESC 000011 DMKQCN MSGESC 000011 DMKUB DMKIUE DMKIUG DMKIUS MSGESC 000011 DMKIUB DMKIUE DMKIUS MSGESC 000011 DMKIUB DMKIUE DMKIUS DMKIUE DMKIUS MSGESC 000011 DMKIUB DMKIUE DMKIUS DMKIUS MSGESC 000012 DMKIUB DMKIUS DMKIUS DMKIUS MSGESC 000022 DMKUA DMKIUS DMKIUS DMKIUS DMKIUS DMKIUS DMKIUS DMKIUS DMKIUS DMKIUS MSGESC 000022 DMKUA DMKIUS DMKIU		MSGADDR MSGAIINV MSGAITRN MSGALIST	000002 000001 000001 000004	DMKCSF DMKIUE DMKIUE										
MSGARJGT 000001 DMKIUE DMKIUL MSGARPAX 000002 DMKIUL DMKIUL MSGARPAX 000002 DMKIUL MSGARPAX 000002 DMKIUL MSGASNAX 000001 DMKIUE MSGASNAX 000001 DMKIUE MSGASVRD 000002 DMKIUL MSGASVRD 000001 DMKIUL MSGATTRN 000001 DMKIUL MSGAUDT 000009 DMKIUL DMKIUG DMKIUL DMKIUS MSGAUDT 000008 DMKIUE DMKIUG DMKIUL DMKIUS MSGAUDT 000008 DMKIUE DMKIUL DMKIU MSGAUDT 000008 DMKIUE DMKIUL DMKIU MSGAUDT 000008 DMKIUE DMKIUL DMKIUS MSGAUDT 000004 DMKIUE DMKIUL DMKIU MSGBLST 000004 DMKIUE DMKIUL MSGBLST 000001 DMKIUE DMKIUB DMKIUC DMKIUE DMKIUG DMKIUJ DMKIUL DMKIUN DMKIUP DMKIUS MSGEPIO 000011 DMKUB DMKIUE DMKIUC DMKIUE DMKIUS MSGEPIO 000011 DMKUB DMKIUE DMKIUC DMKIUS MSGEPIO 000011 DMKUB DMKIUE DMKIUD MSGEDK 000055 DMKIUB DMKIUB DMKIUC DMKIUS MSGEPIO 000011 DMKUB DMKIUB DMKIUC DMKIUS MSGETLS 000012 DMKIUB DMKIUE DMKIUB DMKIUS MSGETLS 000012 DMKIUB DMKIUE DMKIUG DMKIUD DMKIUS MSGETLS 000012 DMKIUB DMKIUE DMKIUG DMKIUN DMKIUS MSGETS 000012 DMKIUB DMKIUE DMKIUG DMKIUN DMKIUS MSGETS 000012 DMKIUB DMKIUE DMKIUG DMKIUN DMKIUS MSGETS 000012 DMKIUB DMKIUE DMKIUG DMKIUN DMKIUS MSGERROR 000026 DMKUA DMKIUB DMKIUJ DMKIUL DMKIUS	•	MSGANSLN MSGAPPC MSGAPRMD MSGARCAX	000019 000018 000001 000002	DMKIUA DMKIUB DMKIUL DMKIUE	DMKIUB	DMKIUE				DMKIUN	DMKIUS			
MSGASNAX 000001 DMKIUE MSGASNPX 000001 DMKIUE MSGASVPX 000002 DMKIUJ MSGATINV 000001 DMKIUL MSGAUDT 00009 DMKIUA DMKIUB DMKIUG DMKIUL DMKIUS MSGAUDT1 00008 DMKIUE DMKIUG DMKIUL DMKIUS MSGAUDT2 000014 DMKIUE DMKIUL MSGBLIST 000004 DMKIUE DMKIUU MSGBLOK 000055 DMKIUA DMKIUB DMKIUC DMKIUE DMKIUG DMKIUJ DMKIUL DMKIUN DMKIUP DMKIUS MSGCTLS 000012 DMKIUB DMKIUG DMKIUL DMKIUN DMKIUS MSGCTLT 000012 DMKIUB DMKIUE DMKIUG DMKIUN DMKIUS MSGDESC 000011 DMKIUB DMKIUE DMKIUG DMKIUN DMKIUS MSGENSG 000002 DMKUB DMKIUE DMKIUG DMKIUN DMKIUS MSGERSG 000002 DMKUB DMKIUB DMKIUB DMKIUC DMKIUN DMKIUS MSGERSG 00002 DMKUB DMKIUB DMKIUE DMKIUN DMKIUS MSGERSG 00002 DMKUA DMKIUB DMKIUB DMKIUL DMKIUN DMKIUS MSGERSG 00002 DMKUA DMKIUB DMKIUB DMKIUL DMKIUN DMKIUS MSGERSG 000026 DMKUA DMKIUB DMKIUB DMKIUL DMKIUL DMKIUS		MSGARJCT MSGARLST MSGARPAX MSGARPLE	000001 000006 000002 000002	DMKIUG DMKIUE DMKIUL DMKIUL										
MSGAUDIT 000009 DMKIUA DMKIUB DMKIUG DMKIUL DMKIUS MSGAUDT1 000008 DMKIUE DMKIUG DMKIUL DMKIUS MSGAUDT3 000014 DMKIUE DMKIUL MSGBLIST 000004 DMKIUE DMKIUL MSGBLIST 000004 DMKIUE DMKIUB DMKIUC DMKIUE DMKIUG DMKIUJ DMKIUL DMKIUN DMKIUP DMKIUS MSGCPLO 00001 DMKQCN MSGCTLS 000012 DMKIUB DMKIUG DMKIUL DMKIUN DMKIUS MSGCTLT 000012 DMKIUB DMKIUE DMKIUG DMKIUN DMKIUS MSGCPLS 000012 DMKIUB DMKIUE DMKIUG DMKIUN DMKIUS MSGCTLT 000012 DMKIUB DMKIUE DMKIUG DMKIUN DMKIUS MSGCPLS 000012 DMKIUB DMKIUE DMKIUG DMKIUN DMKIUS MSGCTLT 000012 DMKIUB DMKIUE DMKIUG DMKIUN DMKIUS MSGCPLS 000012 DMKIUB DMKIUE DMKIUG DMKIUN DMKIUS MSGCPLS 000012 DMKIUB DMKIUE DMKIUG DMKIUN DMKIUS MSGERROR 0000026 DMKQCN MSGERROR 000026 DMKIUA DMKIUB DMKIUJ DMKIUL DMKIUS		MSGASNAX MSGASNPX MSGASVRD MSGATINV	000001 000001 000002 000001	DMKIUE DMKIUE DMKIUJ DMKIUL										
MSGBLOK 000055 DMKIUA DMKIUB DMKIUC DMKIUE DMKIUG DMKIUJ DMKIUL DMKIUN DMKIUP DMKIUS MSGCPIO 000001 DMKQCN MSGCTLS 000012 DMKIUB DMKIUG DMKIUL DMKIUN DMKIUS MSGCTLT 000012 DMKIUB DMKIUE DMKIUG DMKIUN DMKIUS MSGDESC 000011 DMKIUB DMKIUE DMKIUG DMKIUN DMKIUS MSGEMSG 000002 DMKQCN MSGERROR 000026 DMKIUA DMKIUB DMKIUJ DMKIUL DMKIUS		MSGAUDIT MSGAUDT1 MSGAUDT2	000009 000008 000014 000008	DMKIUA DMKIUE DMKIUE DMKIUE	DMKIUG DMKIUJ DMKIUL	DMKIUL		DMKIUS						
MSGERROR 000026 DMKIUA DMKIUB DMKIUJ DMKIUL DMKIUS		MSGBLOK MSGCPIO MSGCTLS MSGCTLT MSGDESC	000055 000001 000012 000012 000012 000011	DMKIUA DMKQCN DMKIUB DMKIUB DMKIUB	DMKIUB DMKIUG DMKIUE	DMK I UL DMK I UG	DMKIUN DMKIUN	DMKIUS DMKIUS	DMKIUJ	DMKIUL	DMKIUN	DMKIUP	DMKIUS	
		MSGERROR	000026	DMKIUA		DMKIUJ DMKIUC	DMKIUL DMKIUE		DMKIUJ	DMKIUL	DMKIUN	DMKIUS		

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LABEL	COUNT	REFERENC	ES								
MSGFLAG2 MSGFLAG3 MSGFPNT MSGID MSGIMSG	000028 000032 000070 000025 000002	DMKIUB DMKIUA DMKIUA DMKIUB DMKQCN	DMKIUC DMKIUB DMKIUB DMKIUE	DMKIUE DMKIUE DMKIUE DMKIUG	DMKIUG DMKIUG DMKIUG DMKIUJ	DMKIUJ DMKIUL DMKIUJ DMKIUL	DMKIUL DMKIUN DMKIUL DMKIUN	DMKIUN DMKIUP DMKIUN	DMKIUS DMKIUS DMKIUS		
MSGKEY MSGMASK1 MSGMSG	000006 000008 000001	DMKIUE DMKIUB DMKMSG	DMKIUL DMKIUE	DMKIUN DMKIUG	DMKIUS DMKIUL	DMKIUN					
MSGNOFL MSGNORPY MSGPARTL	000003 000010 000006	DMKIUA DMKIUB DMKIUE	DMKIUN DMKIUC DMKIUS	DMKIUS DMKIUE	DMKIUG	DMKIUN	DMKIUS				
MSGPRM MSGPRMD MSGPRTY	000007 000015 000010	DMKTUB DMKTUB DMKTUA	DMKIUE DMKIUE DMKIUB	DMKIUL DMKIUG DMKIUL	DMKIUN DMKIUL DMKIUN	DMKIUN DMKIUS	DMKIUS	DMIZIUN	DMI/111C		
MSGPURGE MSGSCCLS MSGSCIF	000034 000015 000001	DMKIUA DMKIUB DMKQCN	DMK I UB DMK I UG	DMKIUE DMKIUJ	DMKIUG DMKIUL	DMK I UJ DMK I UN	DMKIUL	DMKIUN	DMKIUS		
MSGSCPID MSGSIZE MSGSMSG	000023 000054 000001	DMKIUA DMKIUA DMKMSG	DMKIUB DMKIUN	DMKIUC DMKIUP	DMKIUE DMKIUS	DMKIUG	DMKIUJ	DMKIUL	DMKIUN	DMKIUS	
MSGSNDAD MSGSNDLN MSGSNDOP	000010 000021 000028	DMKIUC DMKIUB DMKIUA	DMKIUE DMKIUC DMKIUB	DMKIUN DMKIUE DMKIUC	DMKIUS DMKIUJ DMKIUE	DMKIUN DMKIUJ	DMKIUS DMKIUL	DMKIUN	DMKIUS		
MSGTAG MSGTGCLS MSGTGPID MSGUSED	000006 000014 000031 000008	DMKIUA DMKIUB DMKIUA DMKIUA	DMKIUB DMKIUE DMKIUB DMKIUN	DMKIUG DMKIUG DMKIUC DMKIUP	DMKIUL DMKIUJ DMKIUE DMKIUS	DMKIUN DMKIUL DMKIUG	DMKIUN DMKIUJ	DMKIUL	DMKIUN	DMKIUS	
MSGVMIO MSGWHTRC MSGWNG	000001 000017 000001	DMKQCN DMKIUA DMKMSG	DMKIUB	DMKIUE	DMKTUJ	DMKIUL	DMKIUS				
MSSFINTR MSSFMASK MSSPRES	000002	DMKDSP DMKAPI DMKCFG	DMKEXT DMKCP1 DMKCFQ	DMKPMA DMKCPI	DMKCPW	DMKDEI	DMKDGD	DMKHVE	DMKLNK	DMKLOJ	DMKSST
MVSA370E M3880CUU M3880FLG	000002	DMKSSV DMKCFG DMKMNL DMKMNL	DMKUSQ DMKDSP	DMKVDA DMKPRG	DMKVDR DMKQVM	DMKVSJ DMKVRR					
M3880M13 M3880NAC M3880RCU	000001 000001 000002	DMKMNL DMKMNL DMKMNL									
M3880RLN M3880SD2	000003	DMKMNL DMKMNL DMKMNL									
M3880SG M3880SSC M3880SSS M3880TOD M3880TOD	000003	DMKMNL DMKMNL DMKMNL									
M3880TOT NAME NAMEENV NAMEMIN	000001	DMKMNL DMKTRP DMKTRP DMKTRP	DMKTRR DMKTRR DMKTRR								
NAME NAMEENV NAMEMIN NAMENTRY NAMEROUT NAMESIZE NAMNOPRO NAMPROC	000002 000002 000002	DMKTRP DMKTRP DMKTRP	DMKTRR DMKTRR DMKTRR			5					
NAMNOPRO NAMPROC NAM2NUM	000006 000006 000001	DMKTRP DMKTRP DMKTRR									
NAM2WORD NCPNAME		DMKTRP DMKNLD	DMKTRR DMKSNC								

LABEL	COUNT	REFERENC	ES								
NCPTBL NCPVOL	000006 000002 000003 000004	DMKNLD DMKNLD DMKNLD DMKNLD DMKNLD	DMKSNC DMKSNC DMKSNC DMKSNC DMKSNC								
NDISK NEEDSEEK NEWPAGES NEWSEGS NICADFF	000001 000016 000019 000011 000007	DMKSAV DMKCCD DMKBLD DMKBLD DMKBLD DMKGRC	DMKCCF DMKCFF DMKCFP	DMKCCW DMKDEG DMKDEG DMKRGB	DMKLOH DMKLOH	DMKSEG DMKSEG	DMKSTR DMKVBM	DMKVBM			
NICADVF	000046 000004	DMKGRC DMKRGB	DMKGRT DMKHVE DMKRGC	DMKNET	DMKRGA	DMKRGB	DMKRGC	DMKVDR			
NICALRM NICAPL NICATOF	000010 000013	DMKRGA DMKCFT DMKCFT	DMKRGB DMKCQU DMKCQU	DMKRGC DMKRGA DMKRNH	DMKRGB	DMKRGC	DMKVCN				
NICATRB	000006 000041 000008	DMKCFM DMKRNH	DMKCFQ	DMKDIF	DMKLOH	DMKRGA	DMKRGB	DMKRGC			
NICAWSF NICBLOK	000007 000087	DMKGRC DMKACO DMKCQT DMKHVD DMKRGA DMKWRM	DMKRGB DMKBLD DMKCQU DMKHVE DMKRGB	DMKRGC DMKCFM DMKDEF DMKLOG DMKRGC	DMKRGD DMKCFQ DMKDIA DMKLOH DMKRGD	DMKVCN DMKCFT DMKDIB DMKNEA DMKRGE	DMKCFY DMKDIF DMKNES DMKRNH	DMKCKD DMKERM DMKNET DMKSCN	DMKCKF DMKEXT DMKNLD DMKVCN	DMKCPJ DMKGRC DMKQCN DMKVDR	DMKCQG DMKGRT DMKQCO DMKVDS
NICCARD NICCDCNT NICCIBM NICCORD NICCPNA NICDED	000005 000004 000006 000007 000005 000005	DMKWRM DMKRGC DMKRGB DMKBLD DMKQCN DMKRGA DMKRNH	DMKD I A DMKRGA DMKRGC	DMKNES DMKRGB	DMKNET DMKRGC	DMKNLD	DMKRNH				
NICDIAG	000010 000036	DMKRGA DMKCKD DMKWRM	DMKRGB DMKCPJ	DMKRGC DMKCQT	DMKDIB	DMKDIF	DMKNEA	DMKNES	DMKNET	DMKRGC	DMKRNH
NICDISB NICDMSG	000016 000004	DMKCKD DMKD I B	DMKNET DMKRGA	DMKRGA DMKRGC	DMKRGB	DMKRGC	DMKRNH	DMKVDR			
NICDTYPE		DMKACO DMKRGC	DMKCFM DMKVCN	DMKCFT DMKVDS	DMKCFY	DMKCKF DMKHVE	DMKDIA DMKNET	DMKHVE DMKRGA	DMKNEA DMKRGC	DMKNET	DMKRGB
NICDXSC NICD3275 NICD3276	000018 000010 000002	DMKCQG DMKDIA DMKHVE	DMKCQT DMKHVE DMKRGB	DMKD I B DMKNEA	DMKD I F DMKRGB	DMKRGC	DMKNET	DMKKGA	DMKKGC		
	000017 000008 000002	DMKCFM	DMKCFT DMKCFY	DMKCFY DMKDIA	DMKD I A DMKRGB	DMKRGB DMKVCN	DMKRGC DMKVDS	DMKVCN	DMKVDS		
NICD3284 NICECOL NICEHLT			DMKNET DMKRGB DMKRGB	DMKRGB DMKRGC DMKRGC	DMKRGC	DMKVCN					
NICENAB	000035		DMKCQT	DMKDIB	DMKNEA	DMKNES	DMKNET	DMKRGA	DMKRGC	DMKRNH	DMKVDR
NICEPAD NICEPMD NICERLK	000010 000015 000004	DMKD I A DMKD I B DMKRNH	DMKDIF DMKDIF	DMKNEA DMKNES	DMKNES DMKNET	DMKNET DMKNLD	DMKNLD DMKRNH				
NICEWO NICFLAG	000004	DMKGRC DMKCFT DMKNLD DMKNET	DMKRGB DMKCKD DMKRGA DMKRGA	DMKCQT DMKRGB DMKRGB	DMKCQU DMKRGC DMKRGC	DMKD I B DMKRNH DMKVDR	DMKD I F DMKVDR	DMKLOG DMKWRM	DMKNEA	DMKNES	DMKNET
NICFMT NICGRAF NICHOLD NICHT	000011 000002 000008 000013	DMKBLD DMKRGA DMKGRC	DMKHVD DMKRGB DMKGRT	DMKRGC DMKQCN	DMKRGB	DMKRGC	DMKVCN				
NICLBSC	000002	DMKNES	DMKNET								

LABEL	COUNT	REFERENC	ES								
NICLGDRP NICLGRP NICLINE NICLLEN NICLLOGDT	000008 000013 000006	DMKRGA DMKCKD DMKCKD DMKBLD DMKRGC	DMKNET DMKDIA DMKCFT	DMKRGA DMKNES DMKCQU	DMKVCN DMKNET DMKHVE	DMKWRM DMKRNH					
NICITRC	000013	DMKDIF	DMKNES	DMKRNH							
NICMDL	000002	DMKACO DMKRGA	DMKCKF DMKRGB	DMKRGC							
NICMTA	000002 000019	DMKRNH DMKBLD	DMKCPJ	DMKCQT	DMKDIF	DMKEXT	DMKNET	DMKNLD	DMKRGA	DMKRGC	DMKRNH
NICNTRL	000042	DMKRGA	DMKRGB	DMKRGC	DMKRNH	DHKLAI	DPIKING	DHKILD	DHKIGA	DHKIGO	DURINI
NICNTRL NICOPRDR NICPOLL NICPSS	000001 000004 000001	DMKACO DMKRGA DMKHVE	DMKRGB								
NICPSUP	000012	DMKCFT	DMKLOG DMKHVE	DMKNEA	DMKNES	DMKNET	DMKNLD	DMKRNH			
NICPT	000002	DMKRGB	DMKRGC	044/450	214/222			D.44/D.0.0			
NICQPNT NICQREP	000091 000002	DMKCFQ DMKGRC	DMKD I F DMKHVE	DMKNES	DMKQCO	DMKRGA	DMKRGB	DMKRGC	DMKRNH		
NICORY	000021	DMKNET DMKNET	DMKRGA DMKRGA	DMKRGB DMKRGB	DMKRGC DMKVDR	DMKVDR					
NICRATTN	000009	DMKNET	DMKRGA	DMKRGB	DMKRGC	DMKVCN	DMKVDR				
NICRCNT	000012 000021	DMKRNH DMKCQT	DMKDIB	DMKD I F	DMKHVE	DMKNEA	DMKNET	DMKRGA	DMKRGB	DMKRGC	DMKVCN
NICREAD	000010	DMKVDR DMKRGA	DMKRGB	DMKRGC							
NICRFLG	000064	DMKCQG DMKRGC	DMKCQT DMKRGD	DMKD I B DMKVCN	DMKD I F DMKVDR	DMKGRC	DMKHVE	DMKNEA	DMKNET	DMKRGA	DMKRGB
NICRFLG1 NICROPER NICRSPL NICRUNN NICSELT	000007	DMKRGA DMKNEA	DMKRGC DMKVDR								
NICRSPL	000003	DMKNEA DMKNEA	DMKNEA	DMKNET	DMKRGA	DMKRGC	DMKSCN				
NICRUNN	000023 000003	DMKRGA	DMKNET DMKRGB	DMKRGA	DMKRGB	DMKRGC					
NICSESN	000012 000005	DMKD I B DMKRGA	DMKD I F DMKRGB	DMKNES	DMKNET	DMKRNH					
NICSIZE	000083	DMKACO DMKCQG	DMKACR DMKCQT	DMKCFM DMKCQU	DMKCFQ DMKDEF	DMKCFT DMKDIA	DMKCFY DMKDIB	DMKCKD DMKD I F	DMKCKF DMKERM	DMKCPJ DMKEXT	DMKCPW DMKHVD
		DMKHVE	DMKLOG	DMKLOH	DMKNEA	DMKNES	DMKNET	DMKNLD	DMKQCN	DMKQCO	DMKRGA
NICSTAT	000147	DMKRGB DMKCKD	DMKRGC DMKCPJ	DMKRNH DMKCQT	DMKSCN DMKDIA	DMKVCN DMKD I B	DMKVDR DMKD I F	DMKVDS DMKLOH	DMKWRM DMKNEA	DMKNES	DMKNET
NICSWEP	000007	DMKNLD DMKDIA	DMKRGA DMKD I B	DMKRGB DMKNES	DMKRGC DMKNLD	DMKRNH	DMKWRM				
NICTELE	000010	DMKDIF	DMKNET	DMKRNH		000000	DMUDOA	DMUDAUL	D14/4 /D14		
NICTERM	000019 000014	DMKBLD DMKCFT	DMKCKD DMKCQU	DMKNEA DMKRGA	DMKNET DMKRGB	DMKNLD	DMKRGA DMKVCN	DMKRNH	DMKWRM		
NICTMAT	000006 000041	DMKACO DMKCFT	DMKCKF DMKCQU	DMKD I B DMKHVE	DMKD I F DMKRGA	DMKNEA DMKRGB	DMKVDR DMKRGC	DMKVCN			
NICTRQ	000015	DMKDIF	DMKLÓH	DMKRGA	DMKRGB	DMKRGC			DMUUND		DMI/NEO
NICTYPE	000076	DMKACO DMKNET	DMKBLD DMKNLD	DMKCKD DMKRGA	DMKCKF DMKRGB	DMKCQG DMKRGC	DMKD I A DMKRNH	DMKD I F DMKSCN	DMKHVD DMKVCN	DMKNEA DMKWRM	DMKNES
NICUSER	000060	DMKBLD DMKRGA	DMKCQT DMKRGB	DMKD I B DMKRGC	DMKD I F DMKRNH	DMKEXT DMKVDR	DMKLOH	DMKNEA	DMKNES	DMKNET	DMKNLD
NICUSEWA NICVDEVB	000014	DMKRGA DMKDEF	DMKRGB DMKD I B	DMKRGC DMKD I F	DMKNEA	DMKNET	DMKRGA	DMKRGB	DMKRGC	DMKRGE	DMKVCN
		DMKVDR			DPINICA	DANKING	DPIKNOA	DUINNOD	DINKINGO	DINKINGE	DURACIA
NICWSF NICWTH	000010 000016	DMKRGA DMKBLD	DMKRGB DMKERM	DMKRGC DMKGRC	DMKGRT	DMKHVE	DMKQCN	DMKRGB	DMKRGC	DMKVCN	
NIC14AD	000001	DMKHVE					•				

LABEL	COUNT	REFERENC	ES								
NIC14B NIC3274 NIC3275 NLC3276 NLSNAME NLSNEXT NLSPGCT NLSSTRT NLSTBL NLSVOL	000003 000002 000003 000001 000001 000006 000003 000003 000005 000001	DMKRGB DMKRGB DMKRGB DMKRVF DMKHVF DMKHVF DMKHVF DMKHVF DMKHVF DMKHVF	DMKVCN DMKRGC								
NOADD NOAT	000006 000002	DMKCKM DMKACO	DMKCPO DMKCKF	DMKCPT	DMKPMA	DMKVDT					
NOALCARY NOAUTO	000035 000029	DMKSCH DMKCFM DMKTTY	DMKSTP DMKCFU DMKVCN	DMKTRQ DMKCNS DMKVCS	DMKCPJ	DMKNLD	DMKNLE	DMKOPE	DMKOPR	DMKRNH	DMKTOD
NOCARRY1 NOCODE NOCOPY NOINVPTE NOMC NOMODEL NOP	000005 000002	DMKACO DMKEXT DMKSPL DMKPTT DMKGRG DMK10G DMK10G DMKCCO DMKFMT	DMKCKF DMKVSD DMKVFD DMKQCN	DMKVFE DMKRGC							
NOPRINT NOPTLB NORESTR NORET	000012 000002 000002 000601	DMKFPS DMKPTR DMKACO DMKCFC DMKCPI	DMKVAT DMKPTT DMKACR DMKCFD DMKCPJ	DMKALO DMKCFF DMKCPN	DMKBLD DMKCFH DMKCPO	DMKCAC DMKCFM DMKCPP	DMKCAO DMKCFS DMKCPS	DMKCCH DMKCFU DMKCPT	DMKCDB DMKCFV DMKCPU	DMKCDM DMKCFW DMKCPV	DMKCDS DMKCPB DMKCPW
		DMKCPY DMKCQT DMKDAU DMKLNM DMKMNJ DMKPEI DMKRGC DMKTRA DMKVCN DMKVME	DMKCPZ DMKCQU DMKDEI DMKLOM DMKMNT DMKPEN DMKRNH DMKTRC DMKVRR	DMKCQC DMKCQY DMKDIB DMKMCC DMKMSG DMKPGM DMKSPL DMKSPL DMKTRD DMKVDB	DMKCQG DMKCSB DMKDIF DMKMCD DMKMSW DMKPGT DMKSPM DMKTRP DMKVDD	DMKCQH DMKCSF DMKDSB DMKMCH DMKNEA DMKPRG DMKSPR DMKSPR DMKTRU DMKVDR	DMKCQI DMKCSO DMKERM DMKMCI DMKNES DMKPTR DMKSPS DMKUDR DMKVDT	DMKCQP DMKCSV DMKEXT DMKMCT DMKNET DMKQCN DMKSRM DMKURS DMKVER	DMKCQQ DMKCSX DMKGRG DMKMIA DMKNLD DMKQCP DMKSVD DMKUSQ DMKVFC	DMKCQR DMKDAD DMKIDU DMKNID DMKNLE DMKQCQ DMKTHI DMKVCB DMKVFD	DMKCQS DMKDAS DMKJRL DMKMNI DMKOPE DMKQVM DMKTOD DMKVCH DMKVFE
NORLSE NOSIGP NOSLBORO	000009 000004 000015	DMKCFG DMKPTR DMKSCH	DMKRPA DMKPTT DMKSTP	DMKSEL							
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NOTIPL NOTMID NOTRESP	000002 000003 000024	DMKCKM DMKACO DMKACO	DMKCPI DMKCKF DMKCFH	DMKCFM	DMKCSV	DMKCSX	DMKEPS	DMKGRD	DMKGRG	DMKMID	DMKMSG
NOTUSED	000011	DMKNLD	DMKNLE DMKCKT	DMKQCN DMKDAD	DMKQCO DMKDAS	DMKQCQ DMKDAU	DMKRGC DMKD I R	DMKSPL DMKDSB	DMKRSF	DMKSAD	DMKTAP
N PRCNT N PRNAME	000013	DMKTAQ DMKCKV DMKCKV	DMKCPS DMKCPS	DMKCSO DMKCSO	DMKHVD DMKHVD	DMKRSP DMKRSP	DMKWRM DMKTCS	DMKWRM			
NPRPAGCT NPRPNT	000021		DMKCPS	DMKCSO	DMKHVD	DMKRSP	DMKTCS	DMKWRM			
NPRSTART NPRTBL NPRVOL NUCON	000002 000009 000002 000002	DMKHVD DMKCKV DMKHVD DMKIMG	DMKTCS DMKCPS DMKTCS DMKNMT	DMKCSO	DMKHVD	DMKRSP	DMKTCS	DMKWRM			

COUNT

LABEL

REFERENCES

NUMCAPLS 000002 NUMPOOLS 000005 DMKFRE DMKMOO DMKPXA DMKPXB 
 NUMSTEAL
 000001

 OBRBLKLN
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 OBRCCHS
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 OBRSSNSN
 000020

 OBRSUSN
 000020

 OBRSNST
 000010

 OBRSSNSN
 000002

 OBRVOLN
 0 DMKFRE DMKIOJ DMKIOJ DMKVER DMKIOJ DMKIOJ DMKVER DMKIOJ DMKVER DMKVER DMKVER DMKIOJ DMKIOJ DMKIOJ DMKVER DMKIOC DMKIOE DMKIOC DMKVER DMKTOJ DMKVER DMKIOJ DMKIOJ DMKVER DMKVER DMKIOJ DMKIOJ DMKVER DMKVER DMKIOJ DMKVER DMKIOJ DMKVER DMKIOE DMKIOJ DMKIOC DMKIOJ DMKVER DMKIOJ DMKIOJ DMKVER DMKIOC DMKIOE DMKIOJ DMKIOJ DMKIOJ DMKVER DMKIOC DMKIOE DMKIOJ DMKVER DMKIOJ DMKIOJ DMKVER DMKVER DMKVER 000019 DMKVER DMKCFU DMKSRM DMKVCH DMKIOJ DMKCAC DMKSPT DMKCPO DMKDSP DMKMCH OFF DMKCMD. DMKCQC DMKCSV DMKCSX DMKLOK DMKVAT OFFLPROC 000006 OLDKEYOP 000007 DMKCKP DMKFPS DMKPRV DMKSTA DMKCFF DMKCMD DMKRGD DMKCAC DMKCAC DMKCFP DMKCPT OLDVMSEG 000014 DMKBLD DMKPGS ON 000015 ONEENT 000005 OPERATOR 000134 DMKCAC DMKCPU DMKCQC DMKCQY DMKRND DMKVAT DMKVCN DMKCCH DMKRGA DMKVCV DMKCFP DMKCLK DMKERM DMKACR DMKCPJ DMKCPP DMKCPY DMKDAD DMKDAU DMKDIF DMKGRD DMKLOM DMKMCH DMKMC I DMKDSB DMKDTB DMKMSW DMKUDR DMKVRR DMKVRR DMKCKR DMKSEL DMKTRR DMKMIA DMKNEA DMKOPE DMKQCN DMKQCP DMKNLD DMKNLE DMKPGT DMKVDR DMKSPR DMKURS DMKVCH DMKVDA DMKVDB DMKVDD DMKUSQ DMKVER DMKWRM OPNSFB 000020 OPPRSTRT 000015 ORIGSEL 000006 OVERLAPT 000003 DMKACO DMKPTT DMKTRT DMKTRU DMKVST DMKCKV DMKMIA DMKTRP DMKPEL DMKCKF OWNDLIST 000042 DMKACR DMKALO DMKCFS DMKCFU DMKCKM DMKCKN DMKCKV DMKCPI DMKCPO DMKCQY DMKDRD DMKDRE DMKIDU DMKMNT DMKMON DMKPAG DMKPAH Restricted Materials of IBM Licensed Materials – Property of IBM

DMKMCT

DMKDAS

DMKMCT

DMKRNH

DMKVDT

DMKCPJ

DMKPGU

LABEL	COUNT	REFERENC	ES								
OWNDRDEV	000035	DMKSCN DMKACR DMKCPO DMKUDR	DMKSPK DMKALO DMKCQY DMKVDA	DMKSPS DMKCFS DMKDRD DMKVDG	DMKUDR DMKCFU CMKDRE DMKVSE	DMKVDA DMKCKF DMKIDU DMKVSG	DMKVDG DMKCKM DMKPAG DMKWRM	DMKVSE DMKCKN DMKPAH	DMKVSG DMKCKV DMKPGU	DMKWRM DMKCP I DMKSCN	DMKCPJ DMKSPS
OWNDVSER	000013	DMKCFS	DMKCKM	DMKCKN	DMKCKV	DMKCPJ	DMKCQY	DMKMNT	DMKMON	DMKSCN	DMKUDR
OWNID O2ENTRY O2LENGTH O2MAXLEN		DMKCAO DMKCQC DMKTRP DMKCQC	DMKCSV DMKTRP DMKTRR	DMKCSX DMKTRR							
02NAME 02SIZE 02TABEND 02TABLE	000002 000003	DMKTRP DMKCQC DMKTRP DMKTRP	DMKTRR DMKTRP DMKTRR DMKTRR	DMKTRR							
O3DISP O3ENTRY O3HEXLTH O3MCHFLD	000002 000004 000004 000003	DMKTRP DMKTRP DMKTRP DMKTRP	DMKTRR DMKTRR DMKTRR DMKTRR								
O3NUMSZ O3NUMOO	000003 000006	DMKTRP DMKTRP	DMKTRR DMKTRR	DMI/TOD							
O3SIZE O3TABEND O3TABLE O3TRPEND	000006	DMKCQC DMKTRR DMKTRP DMKTRP	DMKTRP DMKTRR	DMKTRR							
04ENTRY 04FMTADR 04FORMAT	000002 000002	DMKTRR DMKTRR DMKTRR									
04MINLEN 04NAME 04SIZE	000001 000002	DMKTRR DMKTRR DMKTRR									
O4TABEND O4TABLE O4VALUE	000002 000005 000002	DMKTRR DMKTRP DMKTRR	DMKTRR								
PAGACT PAGBMP PAGCORE	000031 000040 000209	DMKATS DMKATS DMKATS DMKDAS DMKRPA	DMKCFF DMKBLD DMKBLD DMKDAU DMKSEG	DMKPGS DMKCFF DMKCCW DMKHVD DMKSEL	DMKPTR DMKCPP DMKCDS DMKMCH DMKSTR	DMKPTS DMKCPU DMKCFF DMKPGS DMKSWA	DMKVMA DMKFRT DMKCFG DMKPMA DMKSWM	DMKPGS DMKCKM DMKPSA DMKVFR	DMKPTR DMKCKW DMKPTR DMKVMA	DMKCPP DMKPTS	DMKDAD DMKPTT
PAGECCWS PAGECUR PAGECYL PAGEFBAT PAGEFSNS	000004 000031 000004	DMKPAG DMKCPP DMKMON DMKPAG DMKPAG	DMKPAH DMKMCC DMKPAG DMKPAH	DMKMNI							
PAGEFTIC PAGEHEAD PAGEIDA1 PAGEIDA2	000002 000022 000004	DMKPAG DMKPAG DMKPAG DMKPAG									
PAGEIOB PAGELOAD PAGELOCA PAGELOCB	000024 000004 000005 000001	DMKPAG DMKCPP DMKPAG DMKPAG	DMKPAH DMKSTP DMKPAH	DMKTHI							
PAGELOCD PAGELOCN PAGELOCO PAGELOCW	000004 000003	DMKPAG DMKPAG DMKPAG DMKPAG	DMKPAH DMKPAH								
PAGEND PAGENUME PAGENXT	000008	DMKMCC DMKPAG DMKMNI	DMKMIA	DMKMN I	DMKMON						

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LABEL	-	COUNT	REFERENC	ES								
PAGEP	ARM	800000	DMKPAG									
PAGEP	RI	000006	DMKPAG									
PAGER	RATE	000006 000004	DMKCPP	DMKSTP	DMKTHI							
PAGER	RCD	000010	DMKPAG									
PAGER	RM .	000003	DMKPAG	DMKPAH								
PAGES	SECT	000011	DMKPAG									
PAGES	SEEK	000005	DMKPAG	DMKPAH								
PAGES	SIZE	000024	DMKPAG	DMKSTA	DMKUSP							
PAGES	SK OT	000029 000011		DMKPAH								
PAGES	SNS	000022	DMKPAG DMKPAG	DMKPAH								
PAGES	SRCD	000029	DMKPAG	DINKI AN								
PAGES	SRCH	000009	DMKPAG									
PAGES	SS	000020	DMKPAG	DMKPAH								
PAGET	YPE	000010	DMKPAG	DMKPAH								
PAGEW	AIT	000011	DMKAPI	DMKCPI	DMKDSP	DMKMON	DMKSTP					
PAGEX	DED	000002	DMKPAG									
PAGEX	ECC.	000001 000001	DMKPAG									
PAGEA		000001	DMKPAG DMKPAG	DMKPAH								
PAGEX		000006	DMKPAG	DMKPAH								
PAGEX		000001	DMKPAG	Dritti All								
PAGEX	(OP	000002	DMKPAG									
PAGEX	(RW	000003	DMKPAG	DMKPAH								
PAGEX	(SHC	000001	DMKPAG									
PAGEX	SHH	000001	DMKPAG									
PAGEX	SIZ	000002	DMKPAG	DMKUSP								
PAGEX		000002 000001	DMKMON DMKPAG	DMKPAG								
PAGEA	SIT	000001	DMKPAG									
PAGEX	(SI 1	000006 000002	DMKPAG									
PAGEX	SNS	000004	DMKPAG	DMKPAH								
PAGEX	STR	000004 000003	DMKPAG	2								
PAGEX	(ST2)	000002	DMKPAG									
PAGEX	TIC	000005 000006	DMKPAG	DMKPAH								
PAGE2	2K	000006	DMKCFV	DMKPRV	DMKPTT	DMKVAT						
PAGE4	ik N/AI	000008	DMKCPI	DMKFPS	DMKPMA	DMI/CDD		DMI/DAC		DMI/MOU		DMKPMA
PAGIN	IVAL	000044	DMKATS DMKPTR	DMKCDS DMKPTS	DMKCKM DMKPTT	DMKCPP DMKRPA	DMKDAD DMKSEL	DMKDAS DMKSTR	DMKDAU DMKSWA	DMKMCH DMKSWM	DMKPGM DMKVMA	DHKFMA
PAGPG	SWP	000006	DMKBLD	DMKCFS	DMKCFY	DMKLOJ	DMKPMA	DMKSTR	DRIKSWA	DHKJAH	DUINTING	
PAGRE	SITS	000016	DMKATS	DMKPGM	DMKPGS	DMKPTR	DMKSEL	DMKSTR				
PAGRE	EF	000019	DMKMCH	DMKPGS	DMKPTR	DMKPTS	DMKRPA	DMKSEL	DMKSTR	DMKSWA	DMKSWM	
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PAGSW		000001	DMKPAG	DMU/DAU								
PAGSW	ICC7	000002 000003		DMKPAH								
PAGSW		000002	DMKPAG DMKPAG									
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PACSW	JFXT	000002	DMKPAG	DMKPAH								
PAGSW	/HH_	000001	DMKPAG									
PAGSW	NOP	000001	DMKPAG	D14/01 0	Dutters	D11/0 50	0111/070	D14/2				
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PAGSW	irk JRW	000001	DMKPAG DMKPAG									
		000003	DMKPAG									
PAGSW	TIC	000001	DMKPAG									

LABEL	COUNT	REFERENC	ES								
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PAGTSWP	000027	DMKATS	DMKCFF	DMKCPP	DMKPGS	DMKPTR	DMKSEL				
PAGXIDA1 PAGXIDA2	000002	DMKPAG DMKPAG									
PARMSIZE	000001	DMKHVC									
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PCI	000039	DMKDAD DMKVCA	DMKVCB	DMKVCN	DMKVIO	DMKVSI	DMKVSP	DMKVSX		DHKKSE	DHAINA
PCIF	000013	DMKCCW	DMKDGD	DMKPAG	DMKPAH	DMKVCA	DMKVCB	DMKVCN	DMKVIO	DMKVSP	DMKVSX
PDALLOCD PDAPLOCK	000019	DMKIUC DMKIUJ	DMKIUE DMKIUS	DMKIUJ	DMKIUS						
PDAPMSGP	000007	DMKIUB	DMKIUN	DMKIUS							
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PDAPPCFL PDAPPEND	000005	DMKIUA DMKIUA	DMKIUB DMKIUJ	DMKIUC	DMIKTUE	DMKTUG	DMKTUJ	DMKIUL	DMKTUN	DMKTUP	DMKIUS
PDAPRECQ	000004	DMKIUS									
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PDAPRECQ PDAPREQS PDAPSNDQ PDAPSYCF	000003	DMKIUC	DMKIUS								
PDAVAIL	000003	DMKIUG	DMKIUJ	DMKIUP	DMIZIU	DMKIUN	DMKIUP	DMKIUS			
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PDENTMAX PDENTMIN	000002	DMKIUG DMKIUG									
PDFCNCD	000008	DMKIUC	DMKIUJ	DMKIUN	DMKIUP						
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PDFLAGSZ	000003	DMKIUA	DHKTUJ	DHKIUL	DMKTUS						
PDLRCINV	000013	DMKIUE	DMKIUJ	DMKIUL	DMKIUS						
PDLRECL PDMSGCT	000024 000010	DMKIUE DMKIUA	DMKIUJ DMKIUJ	DMKIUL	DMKIUS DMKIUS						
PDMSGLIM PDPEND1	000004	DMKIUC	DMKIUN		Dinicioo						
PDPEND1 PDPEND2	000006 000006	DMKIUC	DMKIUJ DMKIUJ	DMKIUS DMKIUS							
PDPRMD	000005	DMKIUC	DMKIUL	DMKIUN							
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PDSEND PDSEVERD	000011 000024	DMKIUC DMKIUA	DMKIUJ DMKIUC	DMKIUN DMKIUE	DMKIUP DMKIUG	DMKIUJ	DMKIUL	DMKIUP	DMKIUS		
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PDSIZE PDSTALLC	000006	DMKIUG DMKIUC	DMKIUJ DMKIUJ	DMKIUS							
PDSTATE	000041	DMKIUB	DMKIUC	DMKIUE	DMKTUJ	DMKIUN	DMKIUS				
PDSTCONF	000008	DMKIUJ	DMKIUS	DMIZELLE	DMIZING						
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PDSTSEVR PDTGIUCV	000006	DMKIUJ DMKIUA	DMKIUS DMKIUC	DMKIUE	DMKIUG	DMKIUJ	DMKIUL	DMKIUN	DMKIUP	DMKIUS	
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PDVALID PDZERO	000005 000003	DMKIUA DMKIUJ	DMKIUC	DMKIUG							
PENDCONN	000008	DMKIUC	DMKIUJ								
PERADD	000004	DMKDSP	DMKPMA	DMKPRG							

LABEL	COUNT	REFERENC	ES								
PERADDR PERANYTH PERAPPND	000006	DMKPER DMKPEI DMKPEN	DMKPET DMKPEN	DMKPRG	DMKPRV	DMKPRW	DMKTMR				
PERBLIP PERBLOK	000003 000020	DMKPEI	DMKPER DMKPEI	DMKPEL	DMKPEN	DMKPEQ	DMKPER	DMKPET	DMKPRG	DMKPRV	DMKPRW
PERBUF PERCDE	000050 000007	DMKSVD DMKPEI DMKPER	DMKTMR DMKPEN	DMKPEQ	DMKPER	DMKPET					
PERCHAIN	000031	DMKPEI DMKPEN	DMKPEL	DMKPEN	DMKPEQ	DMKPER	DMKPET				
PERCLEAR	000002	DMKPEN DMKPEI	DMKPEL								
PERCNTCH PERCODE PERCOUNT	000016	DMKPEL DMKDSP DMKPEL	DMKPEN DMKPMA DMKPEN	DMKPRG							
PERCR10 PERCR11	000001	DMKPEL DMKPEL	DINCIEN								
PERCR9 PERCTACT	000017 000010	DMKDSP DMKPE1	DMKPEI DMKPEN	DMKPEL DMKPEQ	DMKPER DMKPER	DMKPRV	DMKPRW	DMKTMR			
PERCTEND PERDATA PERDATON	000002	DMKPEN DMKPEI DMKPER	DMKPET	DMKPRG	DMKPRV	DMKSVD					
PERDATA PERDATON PERENDIT PERERROR PEREX	000002 000006	DMKPEN DMKPEI	•	2	2						
PEREXADD	1111111115	DMKPER DMKPER DMKPER	DMKPET DMKPET DMKPET								
PEREXMOD PERFCL PERFLAG	000003 000008 000021	DMKPER DMKMCC DMKPEI	DMKMIA DMKPEN	DMKMN I DMKPEQ	DMKMOO DMKPER	DMKPET	DMKPRG	DMKPRV	DMKSVD		
PERCALT	000003	DMKPER DMKPEI	DMKPEN	DMKPER							
PERGPRP PERGPRS PERHITS PERINST	000009 000009 000043	DMKMPO DMKPEI DMKPER	DMKPRV DMKPEN DMKPET	DMKPRW DMKPEQ	DMKPER						
PERINTO	000004 000003	DMKPEI	DHKFLI								
PERMODE	000008 000005	DMKDSP DMKPEL	DMKPEL	DMKPEN	DMKPMA	DMKSVD	DMKTRC				
PERON PEROPNOT PEROPQU	000007 000006 000008	DMKPEL DMKPER DMKPER	DMKPET								
PEROPTN PEROP1	000001 000032	DMKPEI DMKPER	DMKPET								
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PERPUSED PERREGSV PERRNGTB	000016 000002	DMKPEI DMKPEI DMKPEI	DMKPEL								
PERRNGTB PERSALT PERSAVED	000006 000011	DMKPEI DMKPRV	DMKPEL DMKPRW	DMKTMR							
PERSONN	000001	DMKPEN DMKPEI DMKPEI	DMKPEQ DMKPEN	DMKPET							
PERSCAN PERSCDLN PERSEQP PERSEQT	000006 000001	DMKPEN DMKPET									
PERSEQT PERSIZE PERSTLEN	000004	DMKPET DMKPEI DMKPER	DMKPEN								
LUSICU	000002	DINKELN									

LABEL	COUNT	REFERENC	ES					
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PERWORK PERWRKCT PESBLOK PESCHAIN PESCOUNT PESELIM PESFLAG	000015 000009 000010 000003 000003	DMKPEI DMKPEI DMKPEN DMKPEN DMKPEN DMKPEN DMKPEN	DMKPEL DMKPEQ DMKPEQ	DMKPEN				
PESNAME PESNEXT PESSIZE	000005 000015 000005	DMKPEN DMKPEN DMKPEN	DMKPEQ DMKPEQ					
PEXBLOK PEXBR PEXCHCMP	000053 000009 000002	DMKPEI DMKPEI DMKPEL	DMKPEL DMKPEL	DMKPEN DMKPEN	DMKPEQ DMKPEQ	DMKPER DMKPER	DMKPET DMKPET	
PEXCMND PEXDATA	000012 000020	DMKPEL DMKPEI	DMKPEN DMKPEL	DMKPEQ DMKPEQ	DMKPET DMKPER	DMKPET		
PEXDINV PEXDLEN PEXELIM	000006 000018 000007	DMKPEI DMKPEI DMKPEI	DMKPER DMKPEL DMKPEL	DMKPEQ DMKPEN	DMKPER	DMKPET		
PEXFLAGO PEXFLAGT PEXFROM	000033 000025 000013	DMKPEI DMKPEI DMKPEL	DMKPEL DMKPEL DMKPEQ	DMKPEN DMKPEN DMKPER	DMKPEQ DMKPEQ	DMKPER DMKPER	DMKPET DMKPET	
PEXFROM2 PEXGPR PEXGREG PEXGSUC	000002 000012 000019 000002	DMKPEI DMKPEI DMKPEI DMKPER	DMKPEL DMKPEL DMKPET	DMKPEN DMKPEQ	DMKPEQ DMKPER	DMKPER	DMKPET	
PEXINST PEXINTO PEXINTO1	000011 000015 000004	DMKPEI DMKPEI DMKPEI	DMKPEL DMKPEL DMKPER	DMKPEN DMKPEQ	DMKPER DMKPER	DMKPET		
PEXLEN PEXMASK PEXMECMP	000007 000007 000002	DMKPEI DMKPEI DMKPEL	DMKPEL DMKPEL	DMKPEN DMKPEN	DMKPER	DMKPET		
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PEXPRINT	000002	DMKPEI	DMKPEL	DMKPET	DMINDET			
PEXRUN PEXSECND PEXSIZE	000003	DMKPEI DMKPEI DMKPEI	DMKPEL DMKPEL	DMKPEQ DMKPEQ	DMKPET DMKPER			
PEXSTEP PEXSTEPN PEXSTORE		DMKPEL DMKPET DMKPEI	DMKPEQ DMKPEL	DMKPET DMKPEN		DMKPER	DMKPET	
PEXSTORE PEXSUCC PEXTERM PEXTHIRD	000005 000012	DMKPET DMKPER DMKPET DMKPET	DMKPEL DMKPEL DMKPEL		DMKPEQ DMKPET DMKPER	טויותר בת	JUNK E I	
PEATHTRD PFDATA PFDCMD PFDCPAD4 PFDCPYAD	000023 000011 000006	DMKCFY DMKCFY DMKCFY DMKGRG DMKVCU	DMKCQY DMKCQY DMKCQY DMKRGC	DMKPEQ DMKGRG DMKGRG	DMKPER DMKGRT DMKRGC	DMKRGC DMKVCU	<b>DMKTTX</b>	DMKVCU

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REFERENCES

PFDCPYSP	000003	DMKCFY	DMKGRG	DMKRGC							
PFDTEXT	000002	DMKTTX									
PFDVAL	000002 000006 000013 000005 000003	DMKCFY	DMKCQY	DMKGRT	DMKVCU				<b>D1</b> 1/1/0/1		
PEKADDR	000013	DMKCFY	DMKCQY DMKCFY	DMKGRG	DMKGRT	DMKRGC	DMKTTX	DMKUSQ	DMKVCU		
PEKCOUNT	000005	DMKCFC	DMKCFY	DMKCQY	DMKUSQ						
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PEKELAG	000014	DMKCFY	DMKCQY	DMKGRG DMKGRG	DMKRGC	DMKTTX	DMKVCU				
PEKIMM	000007		DMKCQY	DMKGRG	DMKRGC DMKGRT	DMKTTX DMKRGC	DMKVCU DMKTTX	DMIAVOU			
PFDVAL PFKADDR PFKCOUNT PFKDWDS PFKFLAG PFKIMM PFKLNG PFKRET PFKSIZE PFKTABLE PFKTABLSZ	000007 000015 000007	DMKCFY		DMKGRG DMKGRG	DMKGRT		DMKVCU	DMKVCU			
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	000003		DMKCOV	DMKGRG	DMKGRT	DMKRGC	DMKTTX	DMKUSQ	DMKVCU		
PEKTRISZ	000003 000015 000004		DMKUSO	DHKONO	DHKGKI	DHKNGG	DHKITA	DINKUSQ	DINKYCO		
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DETADO	^^^^	DMKCOV	DMKGRT	DMKVCU	Dillity OU						
PGADDR	000002	DMKDSP	DMKVAT	brittee							
PGBLOK	000003 000002 000003 000003	DMKCQY DMKDSP DMKCFP DMKCFP	DMKDSP	DMKVAT							
PGBSIZE	000003	DMKCFP	DMKDSP	DMKVAT							
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PG I DMOD	000001	DMKMNT									
PGADDR PGBLOK PGBSIZE PGIDADDR PGIDCPU PGIDTOD PGIDTOD	000001 000001 000002	DMKMNT									
PGPNT PGREAD PGSRATIO PGWAITIM PGWRITE	000003	DMKCFP	DMKDSP	DMKVAT							
PGREAD	000015	DMKAPI DMKCPP	DMKCPI	DMKMON	DMKMOO	DMKPGM	DMKPTR	DMKSTP	DMKSTR		
PGSRATIO	000004	DMKCPP	DMKSTP	DMKTHI							
	000007	DMKCPU DMKMON	DMKDSP DMKMOO	DMKSTP DMKPGM	DMKSEL	DMKSTP					
	000009	DMKPIB	DIMENTOO	DMKPGM	DHKSEL	DMKSTP					
PGWAITTM PGWRITE PIB PLALARM PLDFRET PLDIAG PLEDIT PLERRMSG PLEACI	000001	DMKQCN	DMKQCQ								
PLDFRFT	000007	DMILOON	DMKQCQ	DMKVDD							
PLDIAG	000004	DMKQCN	DMKQCQ	5111(166							
PLEDIT	000001	DMKQCO									
PLERRMSG	000004	DMKQCN									
PLFLAG1	000038	DMKQCN DMKQCO DMKQCO DMKQCN DMKCSU DMKTRT	DMKCSV DMKVCN DMKCSV	DMKCSX	DMKDEF	DMKDEG	DMKERM	DMKQCN	DMKQCO	DMKQCQ	DMKSPL
		DMKTRT	DMKVCN	DMKVDD							
PLFREI	000017	DMKCSU	DMKCSV	DMKCSX	DMKDEF	DMKERM	DMKQCN	DMKQCO	DMKSPL	DMKTRT	DMKVDD
PLFRET PLHILITE PLIMSG	000008	DMKCSU DMKQCN DMKCSU	DMKQCQ DMKCSV	DMILOOV	DMUDEE	DHIVDEO	DMI/OON	DMICOL	DMI/TOT	DMI/UDD	
PLIMSG	000019	DMKCSU	DMKCSV	DMKCSX	DMKDEF	DMKDEG	DMKQCN	DMKSPL	DMKTRT	DMKVDD	
PLIST	000072	DMKQCN	DMKCSV	DMKCSX	DMKDEF	DMKDEG	DMKERM	DMKQCN	DMKQCO	DMKQCQ	DMKSPL
		DMKQCN DMKCSU DMKTRT DMKERM	DMKQCO DMKCSV DMKVCN	DMKVDD	DHKDLI	DHKDLO	DHALINH	DHROOM	DITINGOO	DHKQOQ	
PLLED	000006	DMKFRM	DMKQCN	DMKQCQ DMKQCQ	DMKVCN						
PLLOGDRP	000007	DMKQCN	DMKQCO	DMKQCQ	2						
PLLOGHLD	000007	DMKQCN	DMKQCO	DMKQCQ							
PLLED PLLOGDRP PLLOGHLD PLNCB PLNCAUTO	000007	DMKQCN DMKQCN DMKQCN	DMKQCN DMKQCO DMKQCO DMKQCO								
PLNOAUTO	000002	UMIKUUN	DMKQCQ								
PLNORESP	000003	DMKQCN DMKCSU	DMKQCO	DMKQCQ							
PLNOREI	000012	DMKCSU	DMKDEF	DMKDEG	DMKQCN	DMKQCQ	DMKTRT	DMKVDD			
PLNCB PLNOAUTO PLNORESP PLNORET PLNOTIME PLOPERTR	000009	DMKQCN DMKQCN DMKQCN DMKCSU	DMI/OCO	DMI/OCO							
PLPRIOR	000005	DMKQCN	DMKQCO DMKQCO	DMKQCQ DMKQCQ							
PLR2	000004 000037	DMKQCN	DMKCSV	DMKCSX	DMKDEF	DMKDEG	DMKERM	DMKQCN	DMKQCO	DMKSPL	DMKTRT
	000037	DMKVCN	DMKVDD	DINOON	DRADET	DRINDLO	DRIVENIA	DUILGON	Dimagoo	DENOFL	DUNTIN
PLSECUSR	000006	DMKQCN	5								
PLSIZE	000039	DMKČŠU	DMKCSV	DMKCSX	DMKDEF	DMKDEG	DMKERM	DMKQCN	DMKQCO	DMKSPL	DMKTRT
		DMKVCN DMKQCN DMKCSU DMKVDD									
PLSVR2	000017 000005	DMKQCN DMKQCN	DMKQCO	DMKQCQ							
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LABEL	COUNT	REFERENC	ES								
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PLVIRDVD PLVMGNIC PLWRTRD	000011	DMKQCN DMKQCN DMKQCO	DMKQCO DMKQCO	DMKQCQ DMKQCQ							
PMÄAVATL	000026	DMKAPI DMKQVM	DMKCFG DMKSAD	DMKCPP DMKSEG DMKPRG	DMKCPS DMKSPM	DMKCPU DMKVRR	DMKDMP	DMKDSP	DMKFRE	DMKHVD	DMKPMA
PMAGUEST PMAMODE	000041	DMKEXT DMKCCH DMKIOT	DMKPMA DMKCFG DMKMCH	DMKCFQ DMKMCT	<b>ДМКСКР</b> <b>ДМКМНС</b>	<b>ДМКСРО</b> <b>ДМКРМА</b>	DMKCPP DMKQVM	DMKCPU DMKSPM	DMKD I D DMKVRR	DMKDMP	DMKEXT
PMAON PMASTAT	000012 000073	DMKCFG DMKAPI DMKDMP DMKPRG	DMKMCH DMKCCH DMKDSP DMKQVM	DMKMCT DMKCFG DMKEXT DMKSAD	DMKPMA DMKCFQ DMKFRE DMKSEG	DMKQVM DMKCKP DMKHVD DMKSPM	DMKSAD DMKCPO DMKIOT DMKVRR	DMKVRR DMKCPP DMKMCH	DMKCPS DMKMCT	<b>ДМКСРИ</b> ДМКМНС	DMKD I D DMKPMA
PMSGLIM PNTR POFFLINE	000004 000023 000027	DMKCRM DMKFRE DMKACR	DMK I DR DMKAP I	<b>ДМКСРО</b>	DMKCPP	DMKCPU	DMKENT	DMKIOG	DMKMCT	DMKVCH	
POWEROFF PPMAP PPMBSEPF PPMHP01 PPMHP02 PPMHP03 PPMHP03 PPMHP034 PPMHP034 PPMHP034 PPMHP042 PPMHP042 PPMHP042 PPMHP042 PPMHP05 PPMHP05 PPMHP2 PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH PPMSCH	000009 000002 000001 000008 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001 000001	DMKCKH DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD DMKHVD	DMKCCO DMKCCO DMKCCO DMKCCO	DMKCPJ DMKCCS DMKATS	DMKCPS	DMKOPE	DMKTOD	DMKCDM	DMKCDS	DMKCFF	DMKCFG
	000204	DMKCFS DMKCFS DMKCPS DMKENT DMKIOT DMKMCH DMKPMA DMKSEL DMKTRT DMKVRR	DMKCFY DMKCPU DMKEXT DMKIUA DMKMCT DMKPRG DMKSPM DMKTRU DMKVSJ	DMKCKD DMKCPW DMKFRE DMKIUC DMKMHC DMKPSA DMKSTA DMKTRX DMKTRX	DMKCKF DMKCPY DMKFRT DMKIUJ DMKMIA DMKPTR DMKSTP DMKVBM	DMKCNS DMKCPZ DMKGRF DMKIUN DMKMID DMKPTS DMKSVC DMKVCT	DMKCPI DMKCQC DMKHVD DMKIUP DMKMNI DMKPTT DMKSVD DMKVCV	DMKCPJ DMKCQS DMKHVF DMKLOH DMKRGC DMKRGC DMKSWA DMKVCW	DMKCPM DMKCQT DMKIOF DMKLOJ DMKMON DMKRNH DMKTHI DMKVCX	DMKCPN DMKCQY DMKIOQ DMKLOK DMKOPE DMKSAD DMKTOD DMKVIO	DMKCPP DMKDSP DMKIOS DMKMCC DMKPAG DMKSCH DMKTRP DMKVMG
PREFIXB	000212	DMKACO DMKCFS	DMKACR DMKCFV	DMKACS DMKCFY	DMKAPI DMKCKD	DMKBLD DMKCKF	DMKCCH DMKCP I	DMKCDB DMKCPJ	DMKCDM DMKCPM	DMKCDS DMKCPO	DMKCFG DMKCPP

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REFERENCES

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		DMKCPS DMKFRT DMKMCC DMKMPO	DMKCPU DMKHVD DMKMCD DMKPAG	DMKCPY DMKIOG DMKMCH DMKPGS	DMKCQU DMKIOJ DMKMCT DMKPMA	DMKCQY DMKIOQ DMKMIA DMKPRG	DMKDSP DMKIOS DMKMID DMKPRV	DMKENT DMKIUP DMKMNI DMKPRW	DMKEXT DMKLOH DMKMNT DMKPSA	DMKFPS DMKLOJ DMKMON DMKPST
		DMKRPA DMKTOD DMKVFC	DMKSAD DMKTRP DMKVFD	DMKSCH DMKTRQ DMKVFE	DMKSEL DMKTRT DMKVFR	DMKSPM DMKTRU DMKVFS	DMKSSV DMKVAT DMKVME	DMKSTK DMKVAU DMKVRR	DMKSTP DMKVCH DMKVSJ	DMKSVC DMKVDC
PREFIXVL PREFLAG PREFRG PREPRD	000003 000001 000011 000002	DMKSAD DMKCCD DMKDMQ DMKCCO	DMKCCW	DMRVFE	DMKYFN	DMKYFS	DMRVME	DHRYNN	DHKV35	
PREVRCW PRGC	000002 000032	DMKCCS DMKBSC	DMKCCW DMKCNS	DMKDAD	DMKDAS	DMKDAU	DMKDIB	DMKGRF	DMKHPS	DMKHVC
	00000	DMKIOT	DMKRNH DMKVSX	DMKRSE	DMKSAD	DMKTAP DMKMOO	DMKTAQ	DMKTPE DMKSVC	DMKUNT	DMKVCA
PRIMEHDR PRIMEHI PRIMELO	000024 000010 000009	DMKAPI DMKAPI DMKAPI	DMKCPP DMKCPP DMKCPP	DMKFRE DMKFRE DMKFRE	DMKFRT DMKFRT DMKFRT	DMKSTA DMKSTA	DMKSTA DMKSVC DMKSVC	DMKSVC		
PRIORITY		DMKACO DMKMSG	DMKCNS	DMKCPO	DMKCPS DMKTTX	DMKCQR DMKUSQ	DMKDAS DMKVCN	DMKD I B DMKVCS	DMKGRD	DMKGR I
PRNPSW	000140	DMKAP I DMKPRW	DMKCKD DMKSAD	DMKCKP DMKSAV	DMKCP I DMKSSP	DMKDMP DMKSTA	DMKDSP DMKTOD	DMKPGS DMKVRR	DMKPMA DMKVRS	DMKPRG
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		DMKDMQ DMKMNI DMKSTP	DMKD3B	DMKMON DMKTHI	DMKMOO	DMKMPO DMKVER	DMKPGS	DMKPMA DMKVFR	DMKPRV DMKVMA	DMKSCN DMKVRR
PROCSCHK PROPSW	000003 000080	DMKCLK DMKCKP	DMKDMP	DMKDSP	DMKFMT	DMKFPS	DMKIUA	DMKMON	DMKMPO	DMKPMA
PROTATRQ PROTBEG	000004 000003	DMKPRV DMKCFY DMKREI	DMKSAD DMKREI DMKVMA	DMKSTA						
PROTBLOK PROTBUFF PROTCAW	000018	DMKCFG DMKCFG DMKCCW	DMKCFY DMKHVE	DMKDSP DMKREI	DMKHVE	DMKLOH	DMKPRG	DMKPTR	DMKREI	DMKVMA
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PROTPGAD PROTPRGL PROTPSW	000001 000001 000003	DMKVMA DMKPRG DMKDSP	DMKPRG							
PROTRCNT PROTRE I	000008 000008	DMKDSP DMKDSP	DMKPRG DMKPRG	DMKPTR DMKPTR	DMKREI DMKREI	DMKVMA DMKVMA				
PROTREIL PROTSIZE PROTSYSN	000005	DMKDSP DMKCFY DMKVMA	DMKPRG DMKLOH	DMKPTR	DMKVMA					

DMKFRE DMKLOK DMKMOO DMKPTS DMKTHI DMKVER

DMKIOS DMKVCN

DMKMCT

DMKPRV

DMKTEE

DMKCPP DMKIOT DMKTHI

DMKCKM DMKDID DMKMCH DMKSEL

DMKPRG

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PROTTRAN PRTC PRTCHN PRVCOMND	000027 000010	DMKHVE DMKBSC DMKRNH DMKCKR DMKCCW	DMKPRG DMKCNS DMKRSE DMKCKS	DMKDAD DMKSAD DMKCKV	DMKDAS DMKTAP DMKCSY	DMKDAU DMKTAQ DMKSPL	DMKD I B DMKTPE DMKSPS	DMKGRF DMKUNT DMKVST	DMKHVC DMKVCA DMKWRM	DMKIOS DMKVCN	DMKIOT DMKVSP
PRVFLAG PSA	000011 001180	DMKCCD DMKAPW DMKCCD DMKCFC DMKCFC DMKCFC DMKCFC DMKCFC DMKCSPU DMKCSPU DMKCSP DMKCSF DMKCSF DMKCSF DMKCSF DMKDSB DMKFRE DMKFRE DMKFRE DMKFRE DMKNET DMKNET DMKNET DMKNET DMKRPD DMKSPK DMKSPK DMKSPK DMKSCN DMKVCA DMKVCA DMKVCA DMKVCA DMKVCA DMKVCA DMKVCA DMKVCA DMKVCA DMKVCA DMKVCA DMKVCA DMKVCA DMKVCA DMKVCA DMKVSD DMKVSU DMKVSU DMKVSU DMKCPI	DMKCCF DMKACR DMKACR DMKCFD DMKCFD DMKCFD DMKCFD DMKCPV DMKCPV DMKCQQ DMKCVT DMKCQQ DMKCST DMKCST DMKAGF DMKST DMKJRL DMKJRL DMKJRL DMKJRL DMKJRL DMKSE DMKSCO DMKSSV DMKSSV DMKTAQ DMKVDR DMKVCB DMKVCB DMKVSE DMKVSV	DMKCCS DMKACS DMKAPY DMKCCH DMKCFF DMKCFF DMKCFF DMKCPW DMKCPW DMKCPW DMKCPW DMKCPW DMKCVU DMKDIA DMKCPW DMKCVU DMKDIA DMKFIG DMKIOE DMKIOE DMKIOE DMKIOE DMKIOE DMKIOE DMKLNK DMKNCH DMKNCH DMKSTA DMKSFA DMKVCH DMKVCK DMKVSF DMKVSW	DMKCCW DMKALG DMKAPZ DMKCCO DMKCFG DMKCFG DMKCFU DMKCPJ DMKCPJ DMKCPS DMKCQS DMKDAD DMKDAD DMKDIB DMKCQ DMKDAD DMKDIB DMKCQ DMKCQ DMKCQ DMKCQ DMKCQ DMKCPT DMKIOF DMKIOF DMKIOF DMKNON DMKOPE DMKSPK DMKSEL DMKSFK DMKSTK DMKTCT DMKVCN DMKVDA DMKVDA DMKVCN DMKVSG DMKVSX	DMKALO DMKATS DMKCCS DMKCFH DMKCFY DMKCFY DMKCPY DMKCPY DMKCAR DMKDID DMKDS DMKDID DMKFPS DMKHPU DMKIOG DMKHPU DMKIOG DMKPGS DMKPGS DMKSEP DMKSFP DMKSFP DMKSFP DMKSTP DMKVCP DMKVCP DMKVCP DMKVCP DMKVCP	DMKAPI DMKCFJ DMKCFJ DMKCFJ DMKCFW DMKCFV DMKCPZ DMKCQU DMKCST DMKCAU DMKDIF DMKERM DMKCRD DMKHVC DMKIOH DMKIOH DMKIOH DMKIOH DMKIOH DMKFQT DMKPGT DMKRGC DMKRST DMKSFT DMKSFT DMKSFT DMKSTR DMKVCQ DMKVFC DMKVFC DMKVFC DMKVFC DMKVSJ DMKWRM	DMKAPS DMKBLD DMKCFM DMKCFM DMKCFY DMKCKV DMKCQC DMKCQC DMKCQC DMKCQC DMKCQC DMKCQC DMKCQC DMKCQC DMKCQC DMKCQC DMKCQC DMKCQC DMKCQC DMKCQC DMKCQC DMKCQC DMKCQC DMKCQC DMKCQC DMKSFB DMKSFB DMKSFB DMKSFC DMKVCC DMKVCD DMKVFD DMKVSP DMKVSP DMKVSP DMKVSP	DMKAPT DMKBSC DMKCFO DMKCKDB DMKCKD DMKCKW DMKCKW DMKCQG DMKCQG DMKCQG DMKCQG DMKCQG DMKCQG DMKLOQ DMKLOQ DMKHVQQ DMKLOQ DMKLOQ DMKLOQ DMKPEI DMKLOQ DMKPEI DMKVCS DMKSVD DMKSVD DMKSVD DMKVCS DMKVCS DMKVCS DMKVCS DMKVCS DMKVCS DMKVCS DMKVAB	DMKAPU DMKCAC DMKCCFP DMKCCKF DMKCCKF DMKCCKF DMKCQH DMKCQH DMKCSB DMKCSB DMKCSB DMKCSB DMKLOS DMKLOS DMKLOS DMKLOS DMKLOS DMKLOS DMKLOS DMKLOS DMKNEA DMKPEL DMKVEBL DMKSBL DMKSBL DMKSSA DMKVCT DMKVCT DMKVCT DMKVSR DMKVSR DMKVSR DMKVSR DMKXSAD	DMKAPV DMKCAOS DMKCCFQ DMKCCFQ DMKCCFQ DMKCCSC DMKCCST DMKCQFI DMKCQSC DMKCQSC DMKCQSC DMKCQC DMKCQSC DMKKGPA DMKKCCD DMKKQCA DMKSSVM DMKSSVM DMKVCDG DMKVSC DMKVSST DMKVSST DMKXST
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PSAEXTX PSAIOSW PSALANG	000020 000005 000013	DMKSEL DMKAPI DMKIOS DMKBLD	DMKSTK DMKCPP DMKIOT DMKCPI	DMKSWA DMKDSP DMKCPP	DMKSWM DMKFRE DMKHVF	DMKTRQ DMKSCH DMKLOH	DMKWA I DMKSTK DMKOPE	DMKTRQ DMKVBM			
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PSAPGID PSASVCCT PSASYSID PSBCLR2	000015 000004 000009 000002	DMKACS DMKMOO DMKCNS DMKCPI	DMKCKP DMKSVD DMKCP I	DMKCP I DMKCQY	DMKCPM DMKOPE	DMKCPN	DMKCPP	<b>DMKCPW</b>	DMKMNT	DMKTPE	

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COUNT	REFERENCES

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PSENDCLR		DMKAPI	DMKCPI								
PSTARTSV		DMKSAV	DIAKOT								
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PSTPOUSW		DMKPXA	DMKPXB	DMKSWA	01111022						
PSTRCBYT	000008	DMKPST	DMKXST	0							
PSTRCPAG	000004	DMKPST	DMKXST								
PSTRCPG\$	000006	DMKPGT	DMKPGU	DMKPST	DMKVDH	•					
PSW	000002	DMKLDOOE									
PSWCC1	000001	DMKCFG									
PSWCC2	000003	DMKCFF	DMKCFG	DMKHVF							
PSWCC3	000001	DMKHVF									
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PTRDVCT	000005	DMKMNL									
PTRENLN	000009	DMKMNL									
PTRLIST	000004	DMKMNL									
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PTRRDEV	000004	DMKMNL									
PTRSIZE	000002	DMKMNL									
PURGESTO		DMKFPS	DMKVAT								
PWDALOG	000001	DMKJRL									
PWDCHAIN		DMKGRT	DMKJRL								
PWDDATE	000002	DMKJRL									
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PWDITRQ	000007	DMKGRT	DMKJRL								
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PWDUSRID	000002										
PWDVSMNM	000002	DMKJRL DMKJRL									
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PXAEND	000002	DMKPXA	DMKPXB	DHKSWA	DHKSMM	Diak i Ng	DUINMAI				
QDISK	000001	DMKSAV	DIANIAD								
QUANTUM	000017	DMKDSP	DMKEXT	DMKFPS	DMKPRG	DMKQVM	DMKSVD				
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Q1DR0P	000006	DMKMON	DMKSCH	bring i o	DINCION	brindholf	Dimano	DUINTING	Dilititite	Dimogra	5111070
RANGE	000121	DMKCAC	DMKCAO	DMKCDB	DMKCDM	DMKCPN	DMKCPT	DMKCQG	DMKCQP	DMKCQQ	DMKCQT
	000121	DMKEMT	DMKMCD	DMKPEI	DMKPEL	DMKPEQ	DMKVDA	DMKVDC	DMKVDD	DMKVDE	DMKVDF
RAPF	000002	DMKPRG	2	57777 21	2	5	5111110/1		5.000		
RB	000003	DMKGRD	DMKGRE	DMKVCR							
RCADDRCK		DMKIUE	DMKIUL								
RCANSBND		DMKIUL	DMKIUN	DMKIUS							
RCAPPC	000007	DMKIUE	DMKIUG	DMKIUL	DMKIUN	DMKIUP					
RCBADDIR		DMKIUC									
RCBADFCN		DMKIUC									
RCBADLIM	000002	DMKIUC									
RCBUFBND		DMKIUE	DMKIUN	DMKIUS							
RCCOMSRV		DMKIUC	DMKTUJ	DMKIUS							
RCEXTLEN		DMKIUC									
RCHADD	000041	DMKACR	DMKACS	DMKCCH	DMKCFP	DMKCKD	DMKCKH	DMKCKN	DMKCPN	DMKDID	DMKDMQ

LABEL	COUNT	REFERENC	CES								
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RCHQCNT RCHRSTQ RCHSCED RCHSEL	000012 000007 000002 000003	DMKENT DMKIOQ DMKIOS DMKIOS	DMK I OQ DMK I OS DMKQVM	DMKIOS	DMKMON						
RCHSIZE RCHSTAT	000002 000045	DMKQVM DMKACR DMK10Q	DMKSSP DMKACS DMK10S	DMKCCH DMKIOT	DMKCFP DMKPRV		DMKC PM DMKVCH	DMKCPN DMKVDA	DMKCPO	DMKCPW	DMKIOG
RCHSTIDC RCHTYPE RCH370 RCIINVLN	000014 000003 000001	DMKCCH DMKIOG DMKIOG DMKIUL	DMKCPN DMK10Q DMK10S	DMKDID DMKIOS	DMK10G DMKQVM	DMKPRV					
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RCINVMOD RCINVREC RCINVSCD RCINVSCV	000002	DMKIUC DMKIUJ DMKIUJ DMKIUJ	DMKIUS DMKIUS								
RCINVSND RCINVSRV RCMSGCT RCMSGLEN	000001 000001 000005	DMKIUS DMKIUC DMKIUJ DMKIUE	DMK I UN DMK I UL	DMK I US DMK I UN	DMKVCV DMK1US	DMKVCX					
RCNEGLEN RCNLOG RCNODATA	000002 000004 000005	DMKIUE DMKIUC DMKIUE	DMKIUL DMKIUG	DMKTUL	DMRTUS						
RCNONAPP RCNOPATH RCNOPRMD RCNOPRTY	000012 000002	DMKIUJ DMKIUC DMKIUL DMKIUN	DMKIUS DMKIUE DMKIUN	DMKIUG	DMKIUJ	DMKIUL	DMKIUN	DMKIUP	DMKIUS		
RCNOSEND RCNTRGIU RCPRMLST	000002 000003 000002	DMKIUN DMKIUC DMKIUL	DMKRPI DMKIUN								
RCPROTCK RCPTHSVD RCPURGED RCRECVSH	000002 000004	DMKIUE DMKIUC DMKIUE DMKIUE	DMKIUL DMKIUL DMKIUL								
RCSNDLST RCSNDOP RCSYCLVL	000004 000002	DMKIUE DMKIUJ DMKIUS	DMKIUL DMKIUS								

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LABEL COUNT REFERENCES

RCTINVLN	000001	DMKIUE									
RCTOTLEN	000002	DMKIUE	DMKIUL								
RCTRUNC	000003 000001	DMKIUJ DMKIUE	DMKIUL	DMKIUS							
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RCUCHB	000005	DMKCPN	DMKMNL	DMKPRV	DMKSSP	DMKURS	Dimool	brinteee	5111101110	5	
RCUCHC	000004	DMKMNL	DMKPRV	DMKSCO	DMKSSP	DUULIOU					
RCUCHCNT RCUCHD	000012 000007	DMKCQQ DMKACR	DMK I OQ DMKC PM	DMKMNT DMKCPO	DMKSCN DMKMNL	DMKVCH DMKPRV	DMKSCN	DMKSSP			
RCUCUBSY		DMKCFQ	DMKIOQ	DMKIOS	DMKIOT	DMKQVM	DMKVSJ	DARGOT			
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RCURSTQ	000007	DMKIOQ	DMKIOS	<b>·</b>							
RCUSCED	000017	DMKACS DMK I OQ	DMKCNS DMKIOS	DMK I OQ DMK I OT	DMKIOS	DMKIOT	DMKQVM				
RCUSENSE		DMKIOQ	DMKIOS	DMKTOT							
RCUSHRD	000007	DMKIOQ	DMKIOS	DMKVDS							
RCUSIZE	000012 000080	DMKCAC DMKACR	DMKCAO DMKACS	DMKCPZ DMKCFU	DMKMNT DMKCNS	DMKQVM DMKCPM	DMKSSP	DMKVDT DMKCPW	DMKVRS DMKCPZ	DMKCQQ	DMKDID
REUSTAT	000080	DMKENT	DMKIOQ	DMKIOS	DMKIOT	DMKMNT	DMKNES	DMKNLD	DMKQVM	DMKSCN	DMKVCH
	_	DMKVRR	•						• •		
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	000070	5111000	5111001	Shirooo	2111000	Shiloot	DINCOM	DINDEN	2111000	5111201	0111010

LAI	BEL	COUNT	REFERENC	CES								
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	Neen	000.01	DMKDGF	DMKDIB	DMKHVC	DMKIOS	DMKISM	DMKTRD	DMKTRK	DMKUNT	DMKVCA	DMKVCB
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RC	WIO	000012	DMKCCW	DMKDGD	DMKISM	DMKUNT	DIANYOD					
RCI	WISAM	000002	DMKCCD	DMKCCO	DIANION	DIMONI						
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RC	40	000007	DMKCQP	DMKCQQ	DMKDEF	DMKVDC	DMKVDS					
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ND.	OBLOR	000040	DMKIOC	DMKIOJ	DMKLNK	DMKMNT	DMKPAG	DMKPAH	DMKPGU	DMKPMA	DMKRSP	DMKRSQ
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	CCKD	000003	DMKCPW	DMKMNT								
		000001	DMKUNT									
	CDIAGN		DMKUNT									
	CDVCLS CDVCYL		DMKIOC DMKUNT									
RDI	CDVCTL	000001	DMKUNT									
		000002	DMKCPW	DMKMNT								
			2									

LY	LABEL	CO
LY20-0897-7 © Copyright IBM Corp. 1982, 1987	RDCFEAT RDCFLAG RDCFPNT RDCLENG RDCLENGC RDCLENGC RDCCPAGA RDCPAGGA RDCPAGGA RDCPAGGA RDCPAGGA RDCPAGGA RDCPAGCA RDCPAGCA RDCPAGCA RDCPAGCA RDCPAGCA RDCPAGCA RDCPAGCA RDCPAGCA RDCPAGCA RDCPAGCA RDCPAGCA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDCPACA RDC	000 000 000 000 000 000 000 000 000 00
	RDEVADFF RDEVADVF RDEVAINH RDEVAIOB	000 000 000
	RDEVAIRA	00
	RDEVALGN RDEVALLN	000
	RDEVALT	00
	RDEVAOF	000

LABEL COUNT REFERENCES DMKDSB DMKCPW RDCFEAT 000001 RDCFLAG 000005 DMKMNT 0004 DMKCPW DMKMNT 0005 DMKCCW DMKCPW DMKCPW DMKIOJ DMKIOC DMKALO DMKALO DMKALO DMKALO DMKCPW DMKCPW DMKCSB DMKCPW DMKCCW DMKCCW DMKCCP DMKCCP DMKCCP DMKCPW DMKMNT 0004 DMKMNT 0004 DMKMNT 0001 0002 DMKCKM DMKMNT DMKVSX 0015 DMKCPW DMKHVE DMKMNT DMKPGU DMKRSP DMKRSQ DMKVDG 0002 DMKCPW DMKMNT 0003 DMKCPW DMKMNT 0004 DMKPAG DMKSPK 0002 DMKUNT 0003 DMKMNT DMKVDG 0004 DMKMNT 0005 DMKCPW DMKMNT 0002 DMKMNT 0031 DMKGRD DMKACS DMKGRF DMKCCH DMKCPT DMKQCO DMKCFT DMKQVM DMKCKD DMKTTY DMKCKF DMKCQQ DMKDIB DMKGRG DMKACR DMKCPM DMKCCS DMKCKH DMKCKN DMKCPO DMKCPW DMKCQG DMKCQP DMKCQY DMKCQS DMKDIB DMKDMQ DMKDSP DMKENT DMKHPS DMKHPU DMKDID DMKDIF DMKDSB DMKHPT DMKJRL DMKSCN DMKVDT DMKIOQ DMKLOJ DMKMN I DMKMNL DMKMNT DMKMON DMKMOO DMKNES DMKNLD DMKPAG DMKVDR DMKGRC DMKGRC DMKGRD DMKACS DMKIOT DMKCFM DMKVCR DMKKCFM DMKACR DMKVCH DMKACR DMKVCH DMKACR DMKYCH DMKSSS DMKVDD DMKSSP DMKVCH DMKVDA DMKVDC DMKVDF DMKVDG DMKHPS DMKGRF DMKGRD 0007 DMKGRT DMKGRD DMKGRG DMKGRG DMKCFQ DMKDID DMKLOH DMKCFQ DMKVCS 0045 DMKGRE DMKGRG DMKHVE DMKRGA DMKVCP DMKVCX 0004 DMKCNS DMKGRD DMKQVM DMKCKD DMKDSP DMKOPE DMKCPS DMKGRF DMKDAD DMKHPT DMKCFR DMKDIF DMKCSO DMKHPS 0104 DMKDAS DMKDAU DMKHPU DMKIOS DMKNLD DMKRSE DMKVSJ DMKRGB 0034 DMKDIF DMKGRF DMKGRG DMKGR I DMKHPT DMKLOH DMKQVM DMKUSQ DMKVCT DMKVCV DMKVCX 00003 DMKCKF DMKPAG DMKCKV DMKVDG DMKCKT DMKCPZ DMKDAD DMKDAS DMKDAU DMKDMP DMKIDU DMKPGU DMKWRM DMKACS DMKVRR 0023 DMKCCS DMKCPM DMKCPN DMKCPT DMKCPW DMKCPZ DMKMNT DMKQVM DMKVSC 000009 000004 DMKCPO DMKCQQ DMKIOQ DMKMNL DMKMNT DMKSCN 
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LABEL	COUNT	REFERENC	CES									
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		DMKDSB	DMKDSP	DMKENT	DMKEPS	DMKERM	DMKEXT	DMKGIO	DMKGRC	DMKGRD	DMKGRE	
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		DMKRSP	DMKVCH	DMKVDA								
RDEVBZCH		DMKACS	DMKCNS	DMKCSO	DMKIOS	DMKIOT	DMKQVM					
RDEVCC3	000004	DMKIOT	DMKZTD	DMUUDO								
RDEVCFCB	000005	DMKCSO DMKDSB	DMKRSP DMKMNT	DMKURS DMKSSU								
RDEVCFLT RDEVCKDX	000004	DMKCCD	DMKCCW	DMKMNT	DMKSSP	DMKVDE						
RDEVCKP	000006	DMKCKH	DMKCKS	DMKWRM	DAIROOT	DIANADE						
RDEVCKPT	000007	DMKRNH	DMKWRM	<b>O</b> manuf								
RDEVCLAS RDEVCMDK	000011	DMKCFQ	DMKCKH	DMKCKR	DMKCKS	DMKCKV	DMKCSO	DMKRSP	DMKSSP	DMKURS	DMKWRM	
RDEVCMDK	000024	DMKCCO	DMKCCW	DMKDGD	DMKMNL	DMKMNT	DMKVDA	DMKVDE	DMKVSC			
RDEVCODE	000035	DMKALO	DMKATS	DMKCFG	DMKCFH	DMKCFS	DMKCKF	DMKCKM	DMKCKT	DMKCPP	DMKCPZ	
		DMKCQY	DMKHVD	DMKHVE	DMKHVF	DMKIDU	DMKIOG	DMKNLD	DMKOPE	DMKPAG	DMKPGT	
RDEVCON	000113	DMKSEG DMKCFM	DMKSNC DMKCFQ	DMKTCS DMKCNS	DMKVDG DMKD I F	DMKWRM DMKDSP	DMKGRD	DMKGRE	DMKGRF	DMKGRG	DMKGR I	
RDEVCON	000115	DMKNES	DMKQCO	DMKQVM	DMKRGA	DMKRNH	DMKTTX	DMKTTY	DMKVCQ	DMKVCR	DMKVCS	
		DMKVCV	DMKVCW	DMKVCX	DIMINOR	Dimmin	Drawny	Britter	Dimition	Dimeron	51111100	
RDEVCORD	000026	DMKCFT	DMKDIA	DMKDIB	DMKGRD	DMKGRE	DMKGRF	DMKGRG	DMKOPR	DMKQCN	DMKTTX	
		DMKTTY										
RDEVCORR	000003	DMKCNS	DMKTRM									
RDEVCPEX	000004	DMKIOT	DMKZTD									
RDEVCPIO	000003	DMK I OT DMKGR F	DMKZTD DMKGRG									
RDEVCPNA RDEVCRDC	000005	DMKBIO	DMKCCO	DMKCCS	DMKCPW	DMKMNT	DMKVDE					
RDEVCRUC	000009	DMKACS	DMKCFQ	DMKCFR	DMKDID	DMKIOT	DHRVDL					
RDEVCTL	000046	DMKACS	DMKCNS	DMKDIB	DMKGRD	DMKGRE	DMKGRF	DMKGRG	DMKGRH	DMKQVM	DMKVDS	
RDEVCTRS		DMKCPS	DMKCPW	DMKHPS	DMKIOE	DMKIOF	DMK I OJ	DMKNES	DMKNET			
RDEVCUA		DMKACR	DMKACS	DMKCAO	DMKCCO	DMKCFQ	DMKCKD	DMKCKH	DMKCKN	DMKCPM	DMKCPO	
		DMKCPS	DMKCPT	DMKCPZ	DMKCQP	DMKCQQ	DMKDIB	DMKDID	DMKDIF	DMKDMQ	DMKDSB	
		DMKENT	DMKGRG	DMKHPS	DMKIOC	DMKIOQ	DMKIOS	DMKMN I	DMKMNL	DMKMNT	DMKMON	
		DMKMOO DMKVCH	DMKNES DMKVDA	DMKNLD DMKVDE	DMKPRV DMKVRR	DMKQVM DMKVSC	DMKSCN	DMKSCO	DMKSSP	DMKSSS	DMKURS	
RDEVCUB	000035	DMKACS	DMKCAC	DMKCAO	DMKCFQ	DMKVSC	DMKCPZ	DMKCOO	DMKDAD	DMKDAS	DMKDAU	
NUEYCUD	000035	DMKDID	DMKDMQ	DMKDSB	DMKIOQ	DMKIOS	DMKMNL	DMKCQQ DMKMNT	DMKDAD DMKSCN	DMKSCO	DMKTAP	
		DMKVCH	DMKVDE	DMKVRR								
		2										

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LABEL	COUNT	REFERENC	FS								
RDEVCUP	000072	DMKALO	DMKCAC	DMKCAO	DMKCFH		DMKCKN	DMKCPT	DMKCPW	DMKCPZ	DMKCQP DMKMNT
		DMKCQQ DMKPAG	DMKDAS DMKVCH	DMKDEF DMKVDA	DMKDGD DMKVDB	DMKDSB DMKVDC	DMKG10 DMKVDD	DMKLNK DMKVDE	DMKLOJ DMKVDF	DMKMNL DMKVDG	DMKVRR
RDEVCURP	000003	DMKTCS	DMKVDR	DIARADA	DHIKYDD	DHRVDO	DHKVDD	DARTOL	DAINADI	DINKADO	Drittertter
RDEVCU11	000002	DMKCPZ	DMKMNT								
RDEVCU2	000010	DMKCPZ	DMKMNL	DMKVDT							
RDEVCYL RDEVDED	000005	DMKD I B DMKCFQ	DMKD I F DMKCFU	DMK I OQ DMKCKD	DMKIOS DMKCKH	DMKMON DMKCPJ	DMKCPM	DMKCPN	DMKCPO	DMKCPS	DMKCPV
KDEYDED	000113	DMKCPW	DMKCPZ	DMKCQP	DMKCQT	DMKCSB	DMKCSF	DMKCSO	DMKDAS	DMKDAU	DMKDEI
		DMKDIB	DMKDIF	DMKDSB	DMKDSP	DMKGRG	DMKHPT	DMKHPU	DMKIOQ	DMKIOS	DMKIOT
		DMKLNK	DMKLOJ	DMKMCC	DMKMNL	DMKMSW	DMKNEA	DMKNES	DMKNET	DMKNLD	DMKNLE
		DMKQVM	DMKRGB	DMKRNH	DMKRSP	DMKRST	DMKSCN	DMKSPT	DMKSSS	DMKSST	DMKVCH
RDEVDELP	000018	DMKVDA DMKCKH	DMKVDC DMKCPO	DMKVDD DMKCPS	DMKVDR DMKCPW	DMKVDS DMKRSE	DMKVDT DMKRSP	DMKVER DMKVDR	DMKVRR DMKVDS	DMKVRS	
RDEVDFCB		DMKCSO	DMKRSP	DMKURS	DMKOFW	DHKIGL	DHKIGF	DINKYDIX	DHKYDS		
RDEVDIIP	000007	DMKDIA	DMKDIF	DMKGRG	DMKHPS	DMKIOT					
RDEVDISA	000118	DMKACR	DMKACS	DMKCAC	DMKCFU	DMKCKH	DMKCKN	DMKCKR	DMKCKV	DMKCNS	DMKCPJ
		DMKCPM DMKCSB	DMKCPN DMKCSF	DMKCPO DMKCSO	DMKCPS DMKDAD	DMKCPT DMKDAS	DMKCPV DMKDAU	DMKCPW DMKDE I	DMKCPZ DMKDIB	DMKCQP DMKD I D	DMKCQT DMKDMP
		DMKDSB	DMKGRG	DMKIOS	DMKIOT	DMKLNK	DMKLOJ	DMKMCC	DMKMNI	DMKMNL	DMKMNT
		DMKNEA	DMKNES	DMKNET	DMKNLD	DMKNLE	DMKQVM	DMKRGA	DMKRGB	DMKRNH	DMKRSP
		DMKSCN	DMKSPT	DMKSSS	DMKSST	DMKVCH	DMKVDA	DMKVDC	DMKVDD	DMKVDE	DMKVDS
	000033	DMKVDT DMKACR	DMKVRR DMKCKD	DMKWRM DMKCNS	DMKCPV	DMKCQP	DMKDIB	DMKGRF	DMKNES	DMKNET	DMKRGA
RDEVDISB	000033	DMKRGB	DMKVDA	DHKCNS	DMKCPV	DMKCQF	DINKUTO	DMKGNF	DHANES	DRIVIET	DIAKINGA
RDEVDRAN	000036	DMKCKH	DMKCKR	DMKCKV	DMKCPO	DMKCPW	DMKCQP	DMKCSB	DMKCSO	DMKQVM	DMKRSP
		DMKRST	DMKURS	DMKVCH	DMKVDA	DMKVDC	DMKVDS	DMKWRM			
RDEVDROP		DMKCNS	DMKDID	DMKGRF	DMKQCO						
RDEVDUPL RDEVECKD	000004	DMKMNT DMKCCD	DMKCCW	DMKCKM	DMKCKN	DMKCKP	DMKDGD	DMKDGF	DMKDMP	DMKMON	DMKPAG
		DMKVDG	DMKZTD	DAKORA	Drinoni	DHINOICI	DIARDOD	DINIDOI	DINIDIN	Diminon	Dimarko
RDEVECOL	000014	DMKGRC	DMKGRD	DMKGRE	DMKGRG	DMKHVE	DMKQCN	DMKVCP	DMKVCX		
RDEVENLT		DMKGRC	DMKGRD	DMKGRE	DMKGRG	DMKHVE	DMKQCN	DMKVCP	DMKVCX	DMI/CBC	DMKHPS
RDEVENAB	000055	DMKACR DMKNES	DMKCKD DMKNET	DMKCNS DMKNLD	DMKCPO DMKOPE	DMKCPV DMKRGA	DMKCPW DMKRGB	DMKCQP DMKVCH	DMKGRF DMKVDA	DMKGRG DMKVDC	DMKVDS
		DMKVRR	DMKWRM	DARALD	DAROTE	DHINICA	DIAMOD	Drifteon	DIII()DA	DIN(100	Bringeo
RDEVEPDV	000024	DMKDIA	DMKDIB	DMKDIF	DMKNES	DMKNLD					
RDEVEPLN	000006	DMKCPO	DMKCPW	DMKDIF	DMKNES	DMKNLD	DMKVDS				
RDEVEPMD RDEVERR	000007	DMKCNS DMKGRF	DMKDIB	DMKDIF	DMKNES	DMKNLD					
RDEVEWO	000005	DMKGRC	DMKGRD	DMKHPS							
RDEVEXTN		DMKCKH	DMKCKS	DMKCKV	DMKCPW	DMKCSB	DMKCSF	DMKCSO	DMKMNT	DMKRSP	DMKSEP
		DMKTCS	DMKTCT	DMKURS	DMKWRM						
RDEVFCNS		DMKCNS	DMKGRF	DMKUSQ	DMIZODO			DMI/LIDE		DMKHPU	DMKIQQ
RDEVFIOB	000033	DMKACS DMKIOS	DMKCFQ DMKIOT	DMKCPI DMKNLD	DMKCPS DMKPAG	DMKDID DMKSSU	DMKDSP DMKVDG	DMKHPS	DMKHPT	DMKHPU	DINKIOQ
RDEVFLAG	000448	DMKACR	DMKALO	DMKBLD	DMKCFC	DMKCFG	DMKCFH	DMKCFM	DMKCFT	DMKCFU	DMKCKD
		DMKCKH	DMKCKR	DMKCKV	DMKCNS	DMKCPJ	DMKCPM	DMKCPO	DMKCPV	DMKCPW	DMKCPZ
		DMKCQP	DMKCQQ	DMKCSB	DMKCSF	DMKCSO	DMKDAD	DMKDAS	DMKDAU	DMKDEI	DMKDIB
		DMKDIF DMKIOS	DMKDSB DMKIOT	DMKEXT DMKLNK	DMKGRD DMKLOG	DMKGRF DMKLOJ	DMKGRG DMKLOM	DMKHPS DMKMCC	DMKHVD DMKMN I	DMKHVF DMKMNL	DMK I OQ DMKMNT
		DMKMON	DMKNES	DMKNET	DMKNLD	DMKNLE	DMKOPE	DMKPAG	DMKQCO	DMKQVM	DMKRGA
		DMKRGB	DMKRNH	DMKRSE	DMKRSP	DMKRST	DMKSCN	DMKSEP	DMKSNC	DMKSPS	DMKSPT
		DMKSSS	DMKSST	DMKSSV	DMKTCS	DMKTRM	DMKTTY	DMKURS	DMKVCH	DMKVCT	DMKVDA
RDEVFOFF	000005		DMKVDD DMKCPM	DMKVDG DMKCPN		DMKVDR	DMKVDS	DMKVDT	DMKVRR	DMKVRS	DMKWRM
RDEVFORC		DMKACR DMKD I D	DMKGRF	DMKGPN	DMKCPW						
RDEVFSEP		DMKCKH	DMKCKS	DMKCKV	DMKCSO	DMKTCS	DMKURS	DMKWRM			

LABEL	COUNT	REFERENC	ES								
RDEVFTR	000153	DMKACO DMKCQP DMKGRF DMKNLE DMKSSS DMKVDS	DMKALO DMKCSB DMKHVE DMKPAG DMKSST DMKVER	DMKBIO DMKDAD DMKIOE DMKPMA DMKTAP DMKVRR	DMKBSC DMKDAS DMKIOJ DMKRGA DMKTCT DMKVRS	DMKCCD DMKDAU DMKIOS DMKRGB DMKUNT DMKVSC	DMKCCO DMKDEI DMKIOT DMKRGC DMKVCN DMKZTD	DMKCCS DMKDGD DMKLNK DMKRSF DMKVDA	DMKCCW DMKDID DMKLOJ DMKSCN DMKVDC	DMKCFQ DMKDSB DMKMNT DMKSPK DMKVDE	DMKCPW DMKGRD DMKNLD DMKSSP DMKVDG
RDEVFTR2 RDEVFTR3 RDEVGRF1 RDEVGRTY	000012 000031 000002	DMKCPM DMKCPZ DMKGRD DMKOPR	DMKGRC DMKMNL DMKGRF DMKSSP	DMKMNT DMKMNT DMKGRG	DMKPAG DMKVDT DMKGR I	DMKVCN	DMKVCP	DMKVDE			
RDEVHIO RDEVHOLD RDEVHSTR RDEVHT	000020 000009 000008 000025	DMKCNS DMKGRD DMKGRD DMKD I A	DMKD I B DMKGRF DMKGRE DMKGRC	DMKEXT DMKGRG DMKGRF DMKGRD	DMKGRF DMKGRH DMKGRG DMKGRF	DMKGR I DMKGRG	DMKGRT	DMKHPS	DMKQCN	DMKSSP	DMKVCN
RDEVIDNT RDEVIMAG	000013	DMKVCP DMKCFC DMKCKH	DMKCFM DMKCKS	DMKCNS DMKCKV	DMKOPE DMKCPS	DMKTRM DMKCSO	DMKRSP	DMKTCS	DMKURS	DMKWRM	
RDEVIOAT RDEVIOBL RDEVIOCP RDEVIOCT	000012 000015 000005	DMKGRD DMKCPI DMKGRD DMKIOS	DMKGRE DMKIOQ DMKGRE DMKMNI	DMKGRF DMKIOS DMKGRF DMKMON	DMKGRG DMKPAG DMKGRG DMKMOO	DMKGR I DMKVDG DMKGR I					
RDEVIODE RDEVIOER	000009 000056	DMKGRD DMKBSC DMK10S	DMKGRE DMKCFQ DMKIOT	DMKGRF DMKCFR DMKNLD	DMKGRG DMKCPS DMKRSE	DMKGRI DMKCSO DMKRSF	DMKDAD DMKRSP	DMKDAS DMKTAP	DMKDAU DMKTPE	DMKD I D DMKTRK	DMKGRF
RDEVIRM RDEVISPL RDEVLCEP RDEVLDBG	000011	DMKCFU DMKRSP DMKACR DMKCSO	DMKIOE DMKCKD DMKRSP	DMKNES DMKCPW DMKURS	DMKNLD DMKCQP	DMKDIB	DMKNLD	DMKRNH			
RDEVLDMD RDEVLGDT RDEVLIOB	000003	DMKCSO DMKGRF DMKACS	DMKRSP DMKCP I	DMKURS DMKHPS	DMKIOQ	DMKVDG					
RDEVLLEN RDEVLNCP RDEVLNKS	000015 000023 000021	DMKBLD DMKACR DMKCPM DMKVDR	DMKCFT DMKCKD DMKCPO DMKVDS	DMKCQU DMKCPW DMKCPW	DMKHPS DMKCQP DMKCQP	DMKHVE DMKD I B DMKDE I	DMKSSP DMKNES DMKSCO	DMKVCP DMKNET DMKSSS	DMKVCS DMKNLD DMKTDK	DMKVCV DMKRNH DMKVDE	DMKVDS DMKVDF
RDEVLOAD RDEVLOG RDEVLOGC	000013	DMKČSO DMKCNS DMKUSQ	DMKRSP	DMKSEP DMKGRF	DMKVCP	DMKVDA	•				
RDEVLOW RDEVLTRM	000014	DMKCCD DMKGRF DMKACR	DMKDGD DMKUSQ DMKCKD	DMKDGF DMKCPJ	DMKPAG DMKCPW	DMKVDG DMKCQT	DMKNEA	DMKNES	DMKNET	DMKNLD	DMKRGA
RDEVMAXP	/	DMKRGB DMKRSP DMKACO	DMKRGC DMKALO	DMKRNH DMKB10	DMKVDS DMKCCD	DMKWRM DMKCCW	DMKCKF	DMKCKM	DMKCKN	DMKCPW	DMKDGD
•		DMKDGF DMKMON DMKVDE	DMKDMP DMKNES DMKVDG	DMKGRF DMKNLD DMKVER	DMKCCD DMKGRG DMKNLE DMKVSC	DMKHPS DMKPAG DMKZTD	DMKHVE DMKPMA	DMKIOC DMKSPK	DMKIOJ DMKSSP		
RDEVMDOO RDEVMDO2 RDEVMD13 RDEVM1D	000011	DMKCPW DMKALO DMKMNL DMKACS	DMKMNT DMKCCD DMKMNT DMKCFR	DMKPMA DMKDMP DMKVDE DMKDID	DMKHVE DMK10S	DMKPAG DMKIOT	DMKPMA	DMKVDE	DMKVDG	DMKVSC	
RDEVMORE RDEVMOUT	000010 000025	DMKGRF DMKCPM DMKVDS DMKCPW	DMKGRG DMKCPW DMKVDT	DMKGRH DMKCQP	DMKDAS	DMKDAU	DMKDSB	DMKSCN	DMKVDA	DMKVDD	DMKVDR
RDEVMOOO RDEVMOO4 RDEVNAME RDEVNATH	000004 000023	DMKCPW DMKCPW DMKCKN DMKALO	DMKMNT DMKIOC DMKCPV DMKCPN	DMKIOJ DMKGRG DMKCPW	DMKMNT DMKHPS DMKMNT	DMK10C DMKVDA	DMKMNT	DMKMOO	DMKMSW	DMKSSP	

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RDEVNCP	000010	DMKCKD	DMKCQP	DMKNLD	DMKRNH	DMKWRM					
RDEVNDLF		DMKCNS	DMKTTX	DMKTTY							
RDEVNICL	000078	DMKACO	DMKACR	DMKCFM	DMKCFQ	DMKCFT	DMKCFY	DMKCKD	DMKCKF	DMKCPJ	DMKCPW
		DMKCQG	DMKCQT	DMKCQU	DMKDEF	DMKDIA	DMKDIB	DMKERM	DMKEXT	DMKHVD	DMKHVE
		DMKLOG	DMKLOH	DMKNEA	DMKNES	DMKNET	DMKNLD	DMKQCN	DMKQCO	DMKRGA	DMKRGB
		DMKRGC	DMKRNH	DMKSCN	DMKVCN	DMKVDR	DMKVDS	DMKWRM			
RDEVNOCR		DMKTTX	DMKTTY								
RDEVNOHD		DMKCNS	DMITTY								
RDEVNOLF RDEVNOW	000009	DMKTTX DMKGRD	DMKTTY DMKGRE	DMKGRF	DMKGRG	DMKGRI					
RDEVNRDY		DMKCFQ	DMKCNS	DMKCPJ	DMKCPM	DMKCPN	DMKCPS	DMKCPW	DMKCQP	DMKCQT	DMKCSO
	000007	DMKDAD	DMKDAS	DMKDAU	DMKDIB	DMKDID	DMKIDU	DMKIOS	DMKIOT	DMKNES	DMKNET
		DMKNLD	DMKNLE	DMKRGA	DMKRGB	DMKRNH	DMKRSE	DMKRSP	DMKRST	DMKSST	DMKTAP
		DMKTPE	DMKURS	DMKVCN	DMKVDE	DMKVDS	5				
RDEVOFF	000004	DMKIOS	DMKIOT								
RDEVOVLY	000004	DMKCSO	DMKRSP	DMKURS	DMKVDR						
RDEVOWN	000047	DMKALO	DMKCFG	DMKCFH	DMKCPM	DMKCPW	DMKCPZ	DMKCQP	DMKCQQ	DMKDAD	DMKDAS
		DMKDAU	DMKDSB	DMKHVD	DMKHVF	DMKLOJ	DMKMNL	DMKNLD	DMKSNC	DMKSSS	DMKSST
		DMKTCS	DMKVCH	DMKVDA	DMKVDC	DMKVDD	DMKVDG	DMKVDH	DMKVDR	DMKVDS	DMKVDT
RDEVPBYP		DMKCPT									
RDEVPCHG		DMKBLD	DMKCFT	DMKTTX	DMKTTY						
RDEVPCNT		DMKCNS	DMUDDA								
RDEVPDLY		DMKNES	DMKRGA	DWIGDO	DHI/ODT	DMUGDU	DMI/ODZ	DMILOOT			DMKMCC
RDEVPEND	000028	DMKACR DMKNES	DMKCFU	DMKCPO DMKNLD	DMKCPT	DMKCPV DMKRGB	DMKCPZ DMKRNH	DMKCQT DMKSPT	DMKD I A DMKVDA	DMKD I D DMKVDS	DMKVDT
RDEVPIOB	000011	DMKCFQ	DMKNET DMKDID	DMKIOQ	DMKIOS	DMKIOT	UPIKANA	DMKSPT	DMKYDA	DIMINADO	DINKYDT
RDEVPPAG		DMKCAC	DMKCPM	DMKCPT	DMKCPW	DMKCPZ	DMKCQG	DMKCQP	DMKCQQ	DMKDSB	DMKLOJ
RECTINO	000032	DMKMNL	DMKMNT	DMKVCH	DMKVDA	DMKVDC	DMKVDD	DMKVDE	DMKVDT	DIMOOD	DIIILEOU
RDEVPRDV	000054	DMKALO	DMKCKM	DMKCKN	DMKCPT	DMKCPW	DMKCPZ	DMKCQG	DMKCQP	DMKCQQ	DMKDSB
		DMKLOJ	DMKMNT	DMKPAG	DMKVDA	DMKVDD	DMKVDE	DMKVDG	DMKVDT		
RDEVPREP	000014	DMKCNS	DMKDIB	DMKQVM	DMKVDA						
RDEVPRFG	000032	DMKCKH	DMKCKS	DMKCKV	DMKCNS	DMKCSO	DMKGRF	DMKRSP	DMKSEP	DMKTCT	DMKURS
		DMKUSQ	DMKWRM								
RDEVPROC	000033	DMKACS	DMKCKD	DMKCNS	DMKCPP	DMKDID	DMKENT	DMKEXT	DMKIOS	DMKIOT	DMKRSF
		DMKTPE	DMKVSJ								
RDEVPS	000052	DMKACO	DMKBLD	DMKCFQ	DMKCFR	DMKCKF	DMKCQG	DMKCQP	DMKCQQ	DMKCQS	DMKCQY
		DMKDIA	DMKDIB	DMKDID	DMKEXT	DMKGRD	DMKGRE	DMKGRF	DMKGRG	DMKHPS	DMKJRL
RDEVPSS	000005	DMKLOH DMKGRC	DMKPRV DMKGRE	DMKQCP DMKHVE	DMKSCN DMKVCP	DMKUSQ	DMKVDA	DMKVDS	DMKVIO	DMKVSI	DMKVSJ
RDEVPSOP	000005	DMKBLD	DMKCFT	DMKCNS	DMKHPS	DMKLOG	DMKTTX	DMKTTY	DMKVCT		
RDEVPT	0000020	DMKGRC	DMKHVE	DMKVCP	DMKIIFS	DHKLOG	DERITA	DHKITT	DHKYCI		
RDEVPTHS		DMKACR	DMKACS	DMKCFU	DMKCPM	DMKCPN	DMKCPO	DMKCPT	DMKCPW	DMKCPZ	DMKCQQ
NDETT THO	000012	DMKDID	DMKDIF	DMKIOQ	DMKMNL	DMKMNT	DMKNES	DMKNLD	DMKRSP	DMKSCN	DMKURS
		DMKVRR	DMKVRS	DMKWRM	0		5				
RDEVPTH1	000004	DMKACR	DMKCPM	DMKCPO	DMKVRR						
RDEVPTH2	000004	DMKACR	DMKCPM	DMKCPO	DMKVRR						
RDEVPTH3		DMKACR	DMKCPM	DMKCPO	DMKVRR						
RDEVPTH4		DMKACR	DMKCPM	DMKCPO	DMKVRR						
RDEVPTH5	000004	DMKACR	DMKCPM	DMKCPO	DMKVRR						
RDEVPTH6		DMKACR	DMKCPM	DMKCPO	DMKVRR						
RDEVPTH7	000004	DMKACR	DMKCPM	DMKCPO	DMKVRR						
RDEVPTH8		DMKACR	DMKCPM	DMKCPO	DMKVRR	DMITTOM	DMI/VOU	DMI/VOU			
RDEVPTTC RDEVPURG		DMKCNS DMKCSO	DMKNES DMKRSP	DMKNLD DMKTCS	DMKOPE DMKTCT	DMKTRM DMKURS	DMKVCU	DMKVCW			
RDEVPORG		DMKCSU	DMKDID	DMKIOQ	DMKIOS	DMKIOT					
RDEVQEST	000017	DMKACS	DMKENT	DMKIOQ	DMKIOS	DMKIOT	DMKMON	DMKPAG	DMKSSU		
RDEVQIOB		DMKCFQ	DMKCPS	DMKDID	DMKIOQ	DMKIOS	DMKIOT	Britti AU	Shirooo		
RDEVQREP		DMKGRC	DMKHPT	DMKHVE	DMKVCT	DMKVCW	DMKVCX				

LABEL	COUNT	REFERENC	ES						, ,		
RDEVQRY RDEVQUED		DMKGRF DMKCKM	DMKCKN								
RDEVRCNT		DMKCNS DMKCPO	DMICON	DMKDIB	DMKNES	DMKNLD	DMKNLE	DMKRNH	DMKVCH	DMKVDC	DMKVDS
RDEVRCVY RDEVRDC	000017 000043	DMKALO	DMKCPW DMKBIO	DMKCCF	DMKCCW	DMKCKM	DMKCPW	DMKDAU	DMKDMP	DMKDSB	DMKHVE
NULYNDO	000043	DMKIOC	DMKIOJ	DMKLNK	DMKMNT	DMKPAG	DMKPAH	DMKPGU	DMKPMA	DMKRSP	DMKRSQ
		DMKSPK	DMKTDK	DMKUNT	DMKVDG	DMKVSC	DMKVSX	DMKZTD			-
RDEVRDY	000003	DMKIOS	DMKIOT	DWVODO	DMULLOD	DMU/UOD	DMU/U/OU				
RDEVREAD RDEVREST		DMKGRD DMKCNS	DMKGRF	DMKGRG	DMKVCP	DMKVCR	DMKVCU				
RDEVREW	000002	DMKCPS									
RDEVRRES		DMKACR	DMKCCS	DMKCFR	DMKCPM	DMKCPN	DMKCPW	DMKCPZ	DMKDAD	DMKDAS	DMKDAU
		DMKDSB		D.444 1 0 T							
RDEVRSTA RDEVRSTR		DMKDID DMKCSF	DMKIOS DMKRSE	DMKIOT DMKRSP							
RDEVRSVD		DMKCPO	DMKCPW	DMKNES	DMKNET	DMKNLD	DMKNLE	DMKRGA	DMKRGB	DMKRNH	DMKVCH
NBETHOTB	000020	DMKVDC	DMKVDS								
RDEVRUN	000023	DMKDIB	DMKGRD	DMKGRF	DMKGRG	DMKGRH	DMKOPE				
RDEVSADN RDEVSCHD	000005	DMKCCT DMKACS	DMKCNS DMKCFQ	DMK I OC DMKCNS	DMKNES DMKCPO	DMKCPW	DMKCSO	DMKDID	DMKDSP	DMKGRF	DMKHPU
RDEVSCHU	000046	DMKIOQ	DMKIOS	DMKIOT	DMKPAG	DMKQVM	DMKRNH	DMKVCH	DMKD3r	DHKGKF	DMKHFU
RDEVSCRL	800000	DMKBLD	DMKCFT	DMKCQU	DMKTTX	DMKTTY	5				
RDEVSDR	000003	DMKNES				DUVOOT	D.44/0.01/				
RDEVSEL	000017	DMKCPW DMKIOS	DMKDSB DMKIOT	DMKLNK	DMKSSS	DMKSST	DMKSSV				
RDEVSENS RDEVSEP	000012	DMKCKH	DMKCSO	DMKRSP	DMKSEP	DMKURS	DMKWRM				
RDEVSEPF		DMKSEP	DMKTCT	Dimator	DIIIGEI	Dimono	Dimini				
RDEVSER	000104	DMKACR	DMKALO	DMKCFG	DMKCKT	DMKCPW	DMKCPZ	DMKCQG	DMKCQP	DMKDAD	DMKDAS
		DMKDAU	DMKDEI	DMKDSB	DMKHVC	DMKIOE	DMKIOS DMKTPE		DMKLOJ	DMKMN I DMKVDE	DMKMNT DMKVDG
		DMKMOO DMKVDH	DMKSCN DMKVDT	DMKSSS DMKVER	DMKSST DMKVRR	DMKVRS	DMKWRM	DMKVDA	DMKVDD	DHIKYDE	DMKYDG
RDEVSIZE	000032	DMKCCS	DMKCPI	DMKDID	DMKDSP	DMKHPS	DMKHPT	DMKLNK	DMKLOJ	DMKMNL	DMKPAG
•		DMKQVM	DMKSCN	DMKSSP	DMKSSS	DMKSSV	DMKVCT	DMKVCW	DMKVCX	DMKVDG	DMKVRS
RDEVSKUP		DMKIOQ	DMKMON	DMUDAUL							
RDEVSLOW RDEVSNA	000009	DMKCQP DMKBLD	DMKNES DMKCFC	DMKRNH	DMKCFQ	DMKCFT	DMKCFW	DMKCQI	DMKCQU	DMKDIA	DMKDIB
NULVSINA	000071	DMKGRC	DMKJRL	DMKLOG	DMKLOH	DMKLOM	DMKMON	DMKQCN	DMKQCO	DMKQCP	DMKQCQ
		DMKRGE	DMKTRD	DMKUSQ	DMKVCN	DMKVCT	DMKVCW	DMKVCX	DMKVDS		
RDEVSNRB	000101	DMKACO	DMKBLD	DMKCFM	DMKCFT	DMKCKF	DMKCQG	DMKCQQ	DMKCQS	DMKCQT	DMKCQU
		DMKCQY DMKQCP	DMKDIA DMKQCQ	DMKDIB DMKTRD	DMKD I F DMKUSQ	DMKDSP DMKVCN	DMKJRL DMKVCQ	DMKLOG DMKVCR	DMKLOH DMKVCS	DMKLOM DMKVCT	DMKQCO DMKVCU
		DMKVCV	DMKVCW	DMKVCX	DMKVDS	DHINYON	DIANUOQ	Drikton	DHINYOS	DIANACI	DHIN
RDEVSPAC		DMKCSF	DMKCSO	DMKRSP							
RDEVSPL	000044	DMKCKH	DMKCKR	DMKCPO	DMKCPW	DMKCSB	DMKCSF	DMKCSO	DMKRSE	DMKRSP	DMKRST
RDEVSPT	000005	DMKSPL DMKIOT	DMKURS DMKSPS	DMKVCH DMKSPT	DMKVDA	DMKVDC	DMKVDS	DMKWRM	DMKWRN		
RDEVSTAT		DMKACR	DMKACS	DMKCAC	DMKCCH	DMKCFQ	DMKCFU	DMKCKD	DMKCKH	DMKCKN	DMKCKR
	000011	DMKCKS	DMKCKV	DMKCNS	DMKCPJ	DMKCPM	DMKCPN	DMKCPO	DMKCPS	DMKCPT	DMKCPV
		DMKCPW	DMKCPZ	DMKCQP	DMKCQT	DMKCSB	DMKCSF	DMKCSO	DMKDAD	DMKDAS	DMKDAU
		DMKDE I DMKHPU	DMKD I A DMK I DU	DMKD I B DMK I OE	DMKD I D DMK I OQ	DMKD I F DMK I OS	DMKDMP DMKIOT	DMKDSB DMKLNK	DMKDSP DMKLOJ	DMKGRG DMKMCC	DMKHPT DMKMNI
		DMKMNL	DMKMNT	DMKMSW	DMKNEA	DMKNES	DMKNET	DMKNLD	DMKNLE	DMKQVM	DMKRGA
		DMKRGB	DMKRGC	DMKRNH	DMKRSE	DMKRSP	DMKRST	DMKSCN	DMKSPT	DMKSSS	DMKSST
		DMKTAP	DMKTPE	DMKURS	DMKVCH	DMKVCN	DMKVDA	DMKVDC	DMKVDD	DMKVDE	DMKVDR
RDEVSTA2	000072	DMKVDS DMKACR	DMKVDT DMKACS	DMKVER DMKCCS	DMKVRR DMKCNS	DMKVRS DMKCPM	DMKWRM DMKCPN	DMKZTD DMKCPT	DMKCPW	DMKCPZ	DMKCSO
NULVOIAL	000012	DMKDID	DMKGRF	DMKIOS	DMKIOT	DMKMNT	DMKNLD	DMKQCO	DMKQVM	DMKRSP	DMKTCS
		DMKTCT	DMKURS	DMKVCH	DMKVRR	DMKVRS	DMKVSC	•	•		

LABEL	COUNT	REFERENC	CES								
RDEVSTA3	000084	DMKACO DMKCQP DMKGRE DMKMNT DMKVIO	DMKBLD DMKCQQ DMKGRF DMKPRV DMKVSI	DMKCCD DMKCQS DMKGRG DMKQCP DMKVSJ	DMKCCW DMKCQY DMKHPS DMKSCN DMKZTD	DMKCFQ DMKDIA DMKIOS DMKSSP	DMKCFR DMKDIB DMKIOT DMKTAP	DMKCKF DMKDID DMKJRL DMKUSQ	DMKCNS DMKD I F DMKLOG DMKVDA	DMKCPT DMKEXT DMKLOH DMKVDE	DMKCQG DMKGRD DMKLOJ DMKVDS
RDEVSTA4	000160	DMKACR DMKCPO DMKDSP DMKNLD	DMKACS DMKCPW DMKENT DMKPAG	DMKCCH DMKCPZ DMKGRF DMKQVM	DMKCCS DMKCSO DMKHPS DMKRNH	DMKCFQ DMKDAD DMKHPT DMKRSE	DMKCFR DMKDAS DMKHPU DMKRSP	DMKCNS DMKDAU DMKIOQ DMKVCH	DMKCPB DMKDID DMKIOS DMKVDA	DMKCPM DMKDIF DMKIOT	DMKCPN DMKDSB DMKNES
RDEVSTA5	000139	DMKACS DMKCPT DMKGIO DMKVDA	DMKALO DMKCPW DMKGRF DMKVDB	DMKČAC DMKCPZ DMKIOS DMKVDC	DMKCAO DMKCQG DMKIOT DMKVDD	DMKCFH DMKCQP DMKLNK DMKVDE	DMKCKM DMKCQQ DMKLOJ DMKVDF	DMKCKN DMKDAS DMKMNL DMKVDG	DMKCPM DMKDEF DMKMNT DMKVDT	DMKCPN DMKDGD DMKPAG DMKVRR	DMKCPS DMKDSB DMKVCH
RDEVSTA6	000013	DMKBIO DMKNES DMKDID	DMKCCO DMKSSU DMKGRF	DMKCCS DMKUSQ DMKIOS	DMKCCW DMKVCH DMKIOT	DMKCPW DMKVDA DMKMNL	DMKDGD DMKVDE DMKMNT	DMKDSB DMKVRR DMKVDE	DMKGRF DMKVRS	DMKMNL DMKVSC	DMKMNT
RDEVSTMD RDEVSYNC RDEVSYS		DMKCFR DMKCNS DMKALO DMKDSB	DMKIOS DMKCFU DMKLOJ	DMKTAP DMKCPM DMKMCC	DMKCPO DMKMN I	DMKCPW DMKSPS	DMKCPZ DMKSPT	DMKCQP DMKSSS	DMKCQQ DMKSST	DMKDAS DMKVCH	DMKDAU DMKVDA
RDEVTALY RDEVTAPL RDEVTBTU	000007 000003	DMKVDC DMKVCH DMKCFT DMKNES	DMKVDD DMKCQU DMKRNH	DMKVDH DMKQCQ	DMKVDR DMKTTX	DMKVDS DMKVCQ	DMKVDT				
RDEVTCTL RDEVTERM RDEVTEXT RDEVTFLG	000016 000015	DMKNES DMKCSF DMKCFT DMKACS	DMKCSO DMKCQU DMKBLD	DMKRSE DMKGRF DMKCFT	DMKRSP DMKGRI DMKCNS					DMKGRD	DMKGRE DMKQVM
RDEVTMAT	000010	DMKGRF DMKTRM DMKVCX DMKACO	DMKGRG DMKTTX DMKVDA DMKCKF	DMKGRH DMKTTY DMKVDS DMKDIB	DMKGRI DMKVCP DMKDIF	DMKHPT DMKVCQ DMKVDR	DMKLOH DMKVCR DMKVDS	DMKNLD DMKVCS DMKVDT	DMKOPE DMKVCT	DMKQCQ DMKVCU	DMKQVM DMKVCV
RDEVTMCD	000079	DMKBLD DMKNLD DMKVCU	DMKCFT DMKOPE DMKVCV	DMKCNS DMKQCQ DMKVCW	DMKCQU DMKTRM	DMKGRF DMKTTX	DMKGRG DMKTTY	DMKGR I DMKVCN	DMKHVE DMKVCP	DMKIOC DMKVCQ	
RDEVTRQ RDEVTTYB	000029	DMKD I F DMKVCV DMKCNS	DMKGRD DMKVCX DMKTTX	DMKGRF DMKTTY	DMKGRG	DMKGR I	DMKHPT	DMKLOH	DMKVCR	DMKVCS	DMKVCT
RDEVTYPC		DMKACO DMKCFC DMKCKH DMKCQS DMKCQS DMKDID DMKEXT DMKIOC DMKLOG DMKNES	DMKACR DMKCFG DMKCKM DMKCQP DMKCQT DMKDIF DMKGRC DMKIOE DMKIOE DMKNET	DMKACS DMKCFH DMKCKN DMKCPS DMKCQU DMKCRG DMKCRG DMKIOF DMKLOJ DMKNLD	DMKALO DMKCFM DMKCKS DMKCQY DMKCQY DMKQRT DMKIOG DMKLOM DMKLOM DMKNLE	DMKATS DMKCFS DMKCKT DMKCPV DMKCSB DMKDRD DMKHPS DMKHOJ DMKMCC DMKOPE	DMKBLD DMKCFT DMKCKV DMKCPW DMKCSF DMKDRE DMKHVC DMKIOQ DMKNNI DMKOPR	DMKCCH DMKCFU DMKCNS DMKCQG DMKCSO DMKDSB DMKHVD DMKIOS DMKMNT DMKPAG	DMKCCS DMKCFY DMKCPJ DMKCQP DMKDEI DMKDEI DMKHVE DMKHVE DMKHON DMKPGT	DMKCCT DMKCKD DMKCPM DMKCQQ DMKDIA DMKEPS DMKHVF DMKJRL DMKMOO DMKPGU	DMKCCW DMKCKF DMKCPN DMKCQR DMKDIB DMKERM DMKERM DMKLNK DMKLNK DMKQCN
		DMKQCO DMKSEG DMKUDR DMKVDD DMKVSX	DMKQCP DMKSNC DMKURS DMKVDE DMKWRM	DMKQCQ DMKSPK DMKUSQ DMKVDF DMKZTD	DMKQVM DMKSPS DMKVCH DMKVDG	DMKRGA DMKSPT DMKVCN DMKVDR	DMKRNH DMKSSP DMKVCP DMKVDS	DMKRSE DMKSSS DMKVCR DMKVDT	DMKRSP DMKSST DMKVCS DMKVER	DMKRSQ DMKTCS DMKVDA DMKVRR	DMKSCN DMKTDK DMKVDC DMKVSC
RDEVTYPE	000963	DMKVSX DMKACO DMKCFH DMKCKN DMKCPS DMKCSB	DMKWRM DMKACR DMKCFM DMKCKS DMKCPT DMKCSC	DMKZTD DMKACS DMKCFQ DMKCKT DMKCPV DMKCSF	DMKALO DMKCFS DMKCKV DMKCPW DMKCSO	DMKATS DMKCFT DMKCNS DMKCQG DMKDAD	DMKBLD DMKCFU DMKCPJ DMKCQP DMKDAS	DMKCCS DMKCFY DMKCPM DMKCQQ DMKDAU	DMKCCT DMKCKD DMKCPN DMKCQS DMKDIA	DMKCFC DMKCKF DMKCPO DMKCQU DMKD1B	DMKCFG DMKCKH DMKCPP DMKCQY DMKDID

LABEL	COUNT	REFERENC	ES								
		DMKD1F DMKHPS DMKIOJ DMKMSW DMKQCP DMKSNC DMKTDK DMKVDC	DMKDMP DMKHPT DMKIOS DMKNES DMKQCQ DMKSPK DMKURS DMKVDE	DMKDMQ DMKHVC DMKIOT DMKNLD DMKQVM DMKSPS DMKUSQ DMKVDF	DMKDRD DMKHVD DMKJRL DMKNLE DMKRGA DMKSPT DMKVCH DMKVDG	DMKDRE DMKHVE DMKLNK DMKOPE DMKRSE DMKSSP DMKVCN DMKVDH	DMKDSB DMKHVF DMKLOG DMKOPR DMKRSF DMKSSS DMKVCP DMKVDR	DMKGRD DMKIOC DMKLOH DMKRSP DMKTAP DMKTAP DMKVCQ DMKVDS	DMKGRF DMKIOE DMKLOM DMKPGT DMKRST DMKTAQ DMKVCR DMKVCR	DMKGRG DMKIOF DMKMCC DMKQCN DMKSEG DMKTCS DMKVCU DMKVCU DMKWRM	DMKGRI DMKIOG DMKMNT DMKQCO DMKSEP DMKTCT DMKVDA DMKZTD
RDEVUNF RDEVUNSN	000004	DMKCPW DMK10S	DMKIOT								
RDEVUSC8 RDEVUSER	000007	DMKBLD DMKACS DMKCPT DMKDIF DMKLOH DMKQVM DMKVDD	DMKCFT DMKBLD DMKCPV DMKDMP DMKMCC DMKSPS DMKVDE	DMKCNS DMKCCT DMKCPZ DMKEXT DMKMNT DMKSPT DMKVDF	DMKNES DMKCFU DMKCQP DMKGRD DMKNES DMKSSS DMKVDG	DMKVCP DMKCKD DMKCQS DMKGRF DMKNET DMKUSQ DMKVDR	DMKCKH DMKCQT DMKGRI DMKNLD DMKVCH DMKVDS	DMKCNS DMKCSB DMKHPS DMKNLE DMKVCP DMKVDT	DMKCPI DMKCSF DMKHPT DMKOPE DMKVCV DMKVRR	DMKCPN DMKCSO DMKHPU DMKOPR DMKVCW DMKVCS	DMKCPO DMKDIA DMKIOT DMKPAG DMKVDA
RDEVVMNT RDEVVM2	000008	DMKDSB	DMKLOJ DMKCFT	DMKMNT	DMKSSV	DMKVDA DMKVCP	DMKVDR	DHR¥DT	DRAW	DARTICO	
RDEVVM21 RDEVWATT RDEVWATT	000004 000023	DMKTTX DMKRGA DMKNES	DMKVCQ DMKRGB DMKRNH	DMKRGC							
RDEVWIOB RDEVWSF	000021 000006	DMKGRD DMKGRE	DMKGRF DMKGRF	DMKGRG DMKGRG	DMKGR I						
RDEVWTH	000022	DMKBLD DMKSSP	DMKERM DMKVCN	DMKGRC DMKVCP	DMKGRD	DMKGRF	DMKGRG	DMKGRT	DMKHPS	DMKHVE	DMKQCN
RDEVXSEP RDEV12B RDEV14AD	000006 000002	DMKCKH DMKGRC DMKGRC	DMKCKS DMKHPS DMKHVE	DMKČKV	DMKCSO	DMKTCS	DMKURS	DMKWRM			
RDEV14B RDEV3101		DMKGRC DMKBLD	DMKGRD DMKCFT	DMKGRT DMKCQU	DMKTTX	DMKTTY					
RDEV333V RDEV741D		DMK10S DMKGRD	DMK I OT DMKGRE	DMKLOJ DMKGRF	DMKLOM	DMKPAG	DMKVDA	DMKVDR	DMKVRR		
RDIDX	000054	DMKACR DMKCKV DMKMNI DMKVDA	DMKACS DMKCPI DMKMNT DMKVDG	DMKCCH DMKCPJ DMKMOO DMKVDT	DMKCFS DMKCPP DMKNES DMKWRM	DMKCFU DMKCPZ DMKNET DMKWRN	DMKCKD DMKCSO DMKOPR	DMKCKF DMKDIF DMKPGU	DMKCKH DMKDMP DMKSCN	DMKCKM DMKGRG DMKSPS	DMKCKN DMKMCT DMKVCH
RDPTQRYG RDRCHN	000002 000030	DMKGRF DMKACO	DMKGRG DMKCKR	DMKCKS	DMKDMP	DMKMIA	DMKNLE	DMKSPL	DMKSPS	DMKTRT	DMKTRU
READ READMOD READNRM	000133 000001 000002	DMKVME DMKCCD DMKVCN DMKRNH	DMKVSD DMKCKP	DMKVSE DMKDAU	DMKWRM DMKDDR	DMKDGD	DMKDIR	DMKFMT	DMKLDOOE	DMKPAG	DMKSPK
RECBAK RECBLOK	000002 000015 000101	DMKRGT DMKCKF DMKRSP DMKCKF	DMKPGU DMKCKM DMKRSQ DMKCKH	DMKPST DMKCKT DMKSPK DMKWRM	DMKUSP DMKCKV DMKUSP	DMKVDG DMKDMP DMKVDG	DMKVDH DMK I DU DMKVDH	DMKXST DMKMOO DMKVSX	DMKPGT DMKWRM	DMKPGU DMKXST	DMKPST
RECCOPD	000007 000004 000070	DMKCKF DMKCKF DMKRSP	DMKCKH DMKCKM DMKRSQ		DMKCKV DMKVDH	DMKDMP DMKVSX	DMK I DU DMKWRM	DMKMOO DMKXST	DMKPGT	DMKPGU	DMKPST
RECDUMP RECFLAG1 RECFLAG2 RECFLG		DMKCKF DMKERP DMKIOG DMKCKF	DMKIOF	DMKIOG	DMKIOH						
RECFSZ	000003	DMKCKF	DMKWRM								
RECHULL	000083	DMKCKT DMKVSX	DMKCKV DMKXST	DMKDMP	DMKIDU	DMKPGT	DMKPGU	DMKPST	DMKRSP	DMKRSQ	DMKVDH

LABEL	COUNT	REFERENCI	ES								
RECMAX RECMODE	000034 000005	DMKCKF DMKIOG		DMKCKV DMKMCI	DMKDMP	DMKIDU	DMKPGT	DMKPGU	DMKPST	DMKVDH	DMKXST
RECNXT	000018	DMKERP	DMKIOF	DMKIOG	DMKIOH						
RECOVRPT RECPAG	000008 000008	DMKCPJ DMKERP	DMKMCH DMKIOF	DMKMCI DMKIOG	DMKIOH	DMKIOJ					
RECPAGDN	000001	DMKIOG		brinkroo	DINCION	21111100					
RECPAGFA RECPAGFL	000007	DMKIOG DMKERP	DMKIOH DMKIOF	DMKIOG	DMKIOH						
RECPAGFM	000003	DMKIOG	DAICION	DAILTOO	Drikton						
RECPAGFR RECPAGIU		DMKIOG DMKIOF	DMKIOG								
RECPNT	000066	DMKCKF	DMKCKT	DMKCKV	DMKDMP	DMKIDU	DMKPGT	DMKPGU	DMKPST	DMKRSP	DMKRSQ
RECQMSG	000010	DMKSPK DMKIUB	DMKUSP DMKIUE	DMKVDG DMKIUL	DMKVDH DMKIUN	DMKVSX DMKIUS	DMKWRM	DMKXST			
RECSIZE	000040	DMKCKT	DMKIDU	DMKPGT	DMKPGU	DMKPST	DMKRSP	DMKRSQ	DMKSPK	DMKUSP	DMKVDH
RECUSED	000050	DMKVSX DMKCKF		DMKXST DMKCKV	DMKDMP	DMKIDU	DMKPGT	DMKPGU	DMKPST	DMKRSP	DMKRSQ
		DMKVDG	DMKVDH	DMKVSX	DMKWRM	DMKXST	Drinki of	Drinki Go	Dintrol	Dimator	Dimatoq
REGNCODE	000002 000006	DMKPMA DMKFMT	DMKSAV								
REMOTE	000007	DMKACO	DMKCKF								
RENAME REPCNT	000001 000001	DMKIMG DMKERM									
REQUSER	000001	DMKSWM									
RESET	000027	DMKCFC DMKTRR	DMKCNS DMKVDE	DMKCPI DMKVDS	DMKEXT DMKVMD	DMKFMT	DMKMCT	DMKMID	DMKPEQ	DMKQCN	DMKTRP
RESINCHA		DMKCCS	DMKCCW		DHINAHD						
RETBUF RETDATA	000006 000004	DMKCFY DMKRET	DMKQCP	DMKRET							
RETDLEN	000004	DMKCFY	DMKQCP	DMKRET							
RETLEN1 RETLEN2	000012 000004	DMKRET DMKRET									
RETL132	000001	DMKCFY									
RETNXTD	000004 000004	DMKRET DMKRET									
RETRY	000092	DMKACO	DMKCKF								
RETRYSW REW	000003 000001	DMKCNS DMKGRC									
REWIND	000007	DMKCFC	DMKCPB	DMKSPT	DMKTAP	DMKTAQ	0.000	D11/2/05	D.11/1/100	011/1/01	
RHTBLOK	000029 000005	DMKCKR DMKCKW	DMKCKT DMKVSD		DMKCKW	DMKVSD	DMKVSE	DMKVSF	DMKVSG	DMKWRM	DMKWRN
RHTFLAG	000002	DMKVSE	DMKVSG								
RHTINDEX	000009	DMKVSD DMKVSD	DMKVSE DMKVSE								
RHTNUMEN		DMKVSD	DMKVSE								
RHTPAGNO RHTRSRV	000004 000016	DMKVSD DMKVSD	DMKVSE DMKVSE	DMKVSG							
RHTSFB1 RHTSPRF	000008	DMKVSD DMKVSE	DMKVSE DMKVSG								
RHTSPU1	000002 000028	DMKVSD	DMKVSE	DMKVSG							
RHTSPU3 RHTVIRT	000001 000008	DMKVSE DMKCKR	DMKCKT	DMKCKV	DMKCKW	DMKVSF	DMKWRN				
RHTVRFY	000001	DMKWRM		DUINONY	DEINONN	DEINAGI	DEINMINI				
RHXADDR RHXTABLE	000004 000003	DMKCKW DMKCKW	DMKWRM DMKWRM								
RM	000053	DMKGRD	DMKVCR								
ROUTE RPYQMSG	000006 000007	DMKDIR DMKIUB	DMKIUE	DMKIUG	DMKIUL	DMKIUS					
		5.11(100		5.11(100		5.11(100					

LABEL	COUNT	REFERENC	ES								
RQLKBASE	000002	DMKDSP	DMKWAI				DAMOTH				
RQLOCK	000015	DMKAPI	DMKDSP	DMKFRE	DMKPXA	DMKPXB	DMKSTK	DMKTRQ			
RSPBF1DC		DMKRSP	DHIVDOD								
RSPBF110		DMKCSO	DMKRSP								
RSPBF1VL RSPBF2DC	000011	DMKRSP DMKRSP									
		DMKCSO	DMKRSP								
RSPBF210 RSPBF2VL	000008	DMKRSP	DHKIGF								
RSPCLPRT	000009	DMKRSP									
RSPDEFER		DMKRSP									
RSPDPAGE		DMKRSP	DMKRST	DMKSPL	DMKTCS						
RSPDPAG2	000007	DMKRSP	2		2						
RSPERR	000007	DMKRST									
RSPFLAG1	000045	DMKCSO	DMKRSP								
RSPFLAG2	000087	DMKCSF	DMKCSO	DMKRSP	DMKRST						
RSPIMIDL		DMKRSP									
RSPLCTL	000025	DMKCKH	DMKCSF	DMKCSO	DMKRSP	DMKRSQ	DMKRST	DMKSPL	DMKTCS	DMKTCT	DMKURS
RSPMISC	000006	DMKCSF	DMKRSP		DUNTOO	DUNTOT					
RSPRPAGE	000047	DMKRSP	DMKRST	DMKSPL	DMKTCS	DMKTCT					
RSPRPAG2 RSPRSTRT	000011	DMKRSP DMKRSP	DMKTCS	DMKTCT							
RSPSEP	000012	DMKRSP									
RSPSFBLK		DMKCKH	DMKCSF	DMKCSO	DMKRSP	DMKRST	DMKSPL	DMKTCT	DMKURS		
RSPSFLOK		DMKCSF	DMKCSO	DMKRSP	Dimition	Diffusion		brinktor	Sintono		
RSPSIZE	000013	DMKRSP	DMKRST	DMKSPL							
RSPSWAP	000010	DMKRST	2								
RSPVPAGE	000012	DMKRSP	DMKRST	DMKSPL	DMKTCS						
RSPVPAG2	000026	DMKRSP									
RSPVPG2	000003	DMKRST									
RSPXAUTO		DMKCKV	DMKCSO	DMKRSP	DMKURS	DMKWRM					
RSPXBLOK	000055	DMKCKH	DMKCKS	DMKCKV	DMKCPW	DMKCSB	DMKCSF	DMKCSO	DMKMNT	DMKRSP	DMKSEP
		DMKTCS	DMKTCT	DMKURS	DMKWRM						
RSPXCHR	000003	DMKTCS DMKTCS	DMKTCT DMKTCT								
RSPXCHR1 RSPXCHR2	000002	DMKTCS	DMKTCT								
RSPXCHR3		DMKTCS	DMKTCT								
RSPXCMOD		DMKTCS	DMKTCT								
RSPXDEST		DMKCKH	DMKCKS	DMKCSO	DMKRSP	DMKWRM					
RSPXDST1		DMKRSP	DMKURS	011110000		5111111					
RSPXDST2		DMKMNT	DMKURS								
RSPXDST3	000002	DMKMNT	DMKURS								
RSPXDST4		DMKURS									
RSPXFCB	000012	DMKCKH	DMKCKS	DMKCKV	DMKCSB	DMKRSP	DMKTCS	DMKTCT	DMKWRM		
RSPXFILE	000004	DMKRSP									014/2/014
RSPXFLAG	000053	DMKCKH	DMKCKS	DMKCKV	DMKCPW	DMKCSB	DMKCSF	DMKCSO	DMKRSP	DMKURS	DMKWRM
RSPXFMNT		DMKCPW DMKCKV	DMKCSO DMKCSB	DMKRSP DMKWRM							
RSPXFOLD		DMKCKV	DMKCKS	DMKWKM	DMKCSO	DMKMNT	DMKRSP	DMKURS	DMKWRM		
RSPXFORM RSPXFPND		DMKCPW	DMKCSO	DMKRSP	DMKURS	DERMIN	DHKIGF	DHKUKS	DPICHINE		
RSPXINDX		DMKCKH	DMKCKS	DMKCKV	DMKCSB	DMKWRM					
RSPXMCHR		DMKTCS	Brinkonko	brintent	0111000	britani					
RSPXNOPL		DMKRSP									
RSPXOTRC	000004	DMKTCT									
RSPXPMNT		DMKCPW	DMKCSO	DMKRSP	DMKURS						
RSPXRECT		DMKCSO	DMKRSP	DMKURS							
RSPXSEQ	000006	DMKRSP	DMKSEP	DMKURS	DWWWDC	DMUUDIA					
RSPXSETU	000009	DMKCKV	DMKCSO		DMKURS DMKURS	DMKWRM					
RSPXSFIL	000013	DMKCSF	DMKCSO	DMKRSP	DUNKUKO						

LABEL	COUNT	REFERENC	ES								
RSPXSIZE RSPXVTRC RSRTNPSW RSRTOPSW RTCODE0 RTCODE1 RTCODE2 RTCODE3 RTCODE4 RTCODE5 RTCODE5 RTCODE7 RTNBLOCK RTNNOCTL RTNPE RUN RUNCR I NV RUNCRO	000002 000004 000022 000002	DMKMNT DMKRSP DMKAP I DMKMPO	DMKTCS DMKCCH	DMKCKD	DMKDMP	DMKDMQ	<b>DMKMC</b> Т	DMKQVM	DMKVRS		
RTCODE1 RTCODE2 RTCODE3 RTCODE4	000004 000004 000004 000007 000010	DMKEIG DMKEIG DMKEIG DMKEIG DMKEIG	DMKSEV DMKSEV DMKSEV DMKSEV DMKSEV	DMKSIX DMKSIX DMKSIX DMKSIX DMKSIX							
RTCODE5 RTCODE7 RTNBLOCK RTNNOCTL	000004 000004 000001 000002	DMKEIG DMKEIG DMKEIG DMKEIG DMKEIG DMKEIG DMKSEU DMKRGD DMKRGD DMKCFJ DMKAPI DMKAPI DMKAPI DMKAPI DMKAPI DMKAPI DMKAPI DMKCFC DMKCFR DMKCFC DMKCFR DMKCFR DMKCFR DMKCFR DMKCFR DMKCFR DMKCFR DMKCFR DMKCFR DMKCFR DMKCFR DMKCFR DMKCFR DMKCFR DMKCFR DMKCFR DMKCFR DMKCFR DMKCFR DMKCFR DMKCFR DMKCFR DMKCFR DMKCFR DMKCFR DMKCFR DMKCFR DMKCFR DMKCFR DMKCFR DMKCFR DMKCFR DMKCFF DMKCFR DMKCFF DMKCFR DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFFF DMKFFF DMKFFF DMKFFF DMKFFF DMKFFF DMKFFF D		DMKSIX							
	000003			DMKSVD	DMKTEE	DMKTRA					
RUNCRO	000072	DMKAPI		DMKDSP	DMKEXT	DMKFPS	DMKIOT	DMKPMA	DMKPRG	DMKPRV	DMKSTA
RUNCR1 RUNPSW RUNUSER	000020 000048 000050	DMKAPI DMKAPI	DMKCPI DMKDSP	DMKDSP DMKVFR DMKDSP DMKEXT DMKCFP	DMKFPS DMKFPS	DMKPMA DMKPMA DMKD I B	DMKPRG DMKPRG	DMKPRV DMKSVD	DMKSVD DMKWA I		
RUNUSER	000050	DMKAPI DMKLOH	DMKCDS DMKMCH	DMKCFP DMKMCT	DMKFPS DMKFPS DMKCP1 DMKMPO	DMKD I B DMKPMA	DMKPRG DMKPRG DMKD1F DMKPRG	DMKPRV DMKSVD DMKDSP DMKSCH	DMKEXT DMKSEL	DMK I OS DMKSVD	DMKIOT DMKTHI
RUN370E	000005 017418	DMKUSQ DMKDSP	DMKQCQ DMKCMD DMKCPI DMKCPI DMKCPI DMKCPI DMKCPI DMKCDS DMKCDS DMKCDS DMKCCS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCPV DMKCQQ DMKCSO DMKCPV DMKCQQ DMKCSO DMKCPV DMKCQQ DMKCSO DMKCPV DMKCQQ DMKCQQ DMKCQQ DMKCSO DMKCPV DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCCS DMKCQQ DMKCQQ DMKCCS DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCCS DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DM	DMIXAOS		DMKALO					DMKAPV
RO	017418		DMKACK	DMKACS DMKAPY DMKCCH	DMKALG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFU DMKCPJ DMKCPS DMKCQS DMKCQS DMKCQS DMKDAD DMKDAD DMKDGF DMKDGF DMKLOS DMKIDR DMKIDR DMKIDR DMKFPS DMKSGC DMKRSF DMKRSF DMKSPL DMKSSV DMKTRC DMKTX	DMKALO DMKATS DMKCFH DMKCFH DMKCFV DMKCFV DMKCPY DMKCQT DMKCQT DMKCQT DMKCSR DMKDIA DMKDSB DMKDIA DMKFRE DMKIOT DMKIOT DMKIOT DMKIOT DMKIOT DMKLOT DMKFRE DMKSEA DMKPGN DMKSEA DMKSFA DMKSFA DMKSTA DMKTCS DMKTCS DMKTCA DMKVCA	DMKAPI DMKCFJ DMKCFJ DMKCFJ DMKCFW DMKCFW DMKCFW DMKCPZ DMKCPZ DMKCQU DMKCST DMKCAU DMKDIB DMKDSP DMKFRT DMKISM DMKISM DMKISM DMKISM DMKISM DMKLOM DMKPEI DMKSEM DMKSEP DMKSEP DMKSFK DMKSTK DMKTCT DMKTRK DMKVCB DMKVCW	DMKAPS DMKCCW DMKCCFM DMKCCFY DMKCFY DMKCFY DMKCPO DMKCQC DMKCQC DMKCQC DMKCQC DMKCQC DMKCQC DMKCQC DMKCQC DMKCID DMKKICC DMKKICC DMKNES DMKVCP DMKSPS DMKSPS DMKSPS DMKTDK DMKTDK DMKKCH	DMKAPT DMKBSC DMKCBD DMKCFO DMKCKD DMKCKD DMKCKQ DMKCQG DMKCQG DMKCQG DMKCQC DMKCQC DMKCDT DMKENT DMKENT DMKLDOOE DMKLUB DMKLDOOE DMKLUB DMKNET DMKPEN DMKSFB DMKSFB DMKSFB DMKSFR DMKSFR DMKTES DMKTES DMKTRD DMKVCN DMKVCN DMKVCN	DMKCAC	DMKCAO
		DMKCFC	DMKCFD	DMKCCH DMKCFF DMKCFF DMKCPU DMKCPW DMKCPW DMKCQR DMKCQR DMKCQR DMKCQD DMKDRD DMKFMT DMKFMT DMKFMT DMKIUL DMKLOH DMKLOH DMKCPR	DMKCFG	DMKCFH	DMKCFJ	DMKCFM	DMKCFO	DMKCFP	DMKCFQ
		DMKCFR	DMKCFS	DMKCFT	DMKCFU	DMKCKS	DMKCFW	DMKCKV	DMKCKD	DMKCLK	DMKCNS
		DMKCNT DMKCPU	DMKCPB DMKCPV	DMKCPI DMKCPW	DMKCPJ DMKCPX	DMKCPM DMKCPY	DMKCPN DMKCPZ	DMKCPO DMKCQC	DMKCPP DMKCQG	DMKCPS	DMKCPI
		DMKCQP	DMKCQQ	DMKCQR	DMKCQS	DMKCQT	DMKCQU	DMKCQY		DMKCSB	DMKCSC
		DMKCSY	DMKCVT	DMKCVU	DMKDAD	DMKDAS	DMKDAU	DMKDDC	DMKDDR	DMKDEF	DMKDEG
		DMKDE I DMKDMQ	DMKDEX DMKDNC	DMKDGD DMKDRD	DMKDGF DMKDRE	DMKDIA DMKDSB	DMKDIB DMKDSP	DMKDID DMKEIG	DMKDTF DMKENT	DMKDIR	DMKDMP
		DMKERP	DMKEXT		DMKFPS		DMKFRT	DMKGIO			
		DMKHVD	DMKHVE	DMKHVF	DMKIDR	DMKIDU	DMKIMG	DMKIOC	DMKIOE	DMKIOF	DMKIOG
		DMKIOH DMKIUG	DMKIOJ DMKIUJ	DMKIOQ	DMKIOS DMKIUN	DMKIOT	DMKISM DMKIUS	DMKTUA DMKJRL	DMKTUB	DMKTUC	DMKTUE
		DMKLOC								DMKMCH	DMKMC I DMKMON
		DMKMOO	DMKMPO	DMKMSG	DMKMSW	DMKNEA	DMKNEM	DMKNES	DMKNET	DMKNLD	DMKNLE
		DMKNMT	DMKOPE DMKPGM DMKPTR	DMKOPR	DMKPAG	DMKPAH	DMKPMA	DMKPEL	DMKPEN	DMKPEQ	DMKPSA
		DMKPST	DMKPTR DMKRGA	DMKPTS DMKRGB	DMKPTT DMKRGC	DMKQCN DMKRGD	DMKQCO DMKRGE	DMKQCP DMKRND	DMKQCQ DMKRNH	DMKQVM DMKRPA	DMKREI DMKRPD
		DMKRPI	DMKRPW	DMKRSE	DMKRSF	DMKRSP	DMKRSQ	DMKRST	DMKSAD	DMKSAV	
		DMKSND	DMKSPC	DMKSPK	DMKSPL	DMKSPM	DMKSPR	DMKSPS	DMKSPT	DMKSRM	DMKSSP
		DMKSSS DMKSWA	DMKSST DMKSWM	DMKSSU DMKTAP	DMKSSV DMKTAQ	DMKSTA	DMKSTK	DMKSTP	DMKSTR DMKTES	DMKSVC	DMKSVD
		DMKTOD	DMKFTK DMKRGA DMKRPW DMKSCN DMKSPC DMKSST DMKSWM DMKTPE DMKTRU	DMKOPR DMKPGS DMKRGB DMKRGB DMKRSE DMKSCO DMKSPK DMKSSU DMKTAP DMKTRA DMKTRX DMKVAU		DMKTRD				DMKTRQ	DMKTRR
		DMKUSQ	DMKVAT DMKVCS	DMKVAU DMKVCT		DMKVCA	DMKVCB		DMKVCN	DMKVCP	DMKCAO DMKCDS DMKCFQ DMKCKH DMKCRS DMKCPT DMKCQI DMKCSC DMKCSC DMKCSC DMKCSC DMKCSC DMKCSC DMKCSC DMKLNG DMKHVC DMKLVC DMKLNM DMKKCI DMKLNM DMKKCI DMKLNM DMKKNLE DMKLNM DMKKSA DMKSBL DMKSBL DMKSSVD DMKSSVD DMKSVD DMKTRR DMKVCC
		DHKYCK	DIMINACO	DIMENT	DINKYCU		DINKACM	DHIKYUA	UPIKYUA	UPINYUD	DUIKADO

_____

LABEL	COUNT	REFERENC	ES								
		DMKVDD DMKVFD DMKVMI DMKVSP	DMKVDE DMKVFE DMKVRR DMKVSQ DMKVSQ	DMKVDF DMKVFR DMKVRS DMKVST DMKXST	DMKVDG DMKVFS DMKVSC DMKVSU DMKZTD	DMKVDH DMKVIO DMKVSD DMKVSV	DMKVDR DMKVMA DMKVSE DMKVSW	DMKVDS DMKVMC DMKVSF DMKVSX	DMKVDT DMKVMD DMKVSG DMKWA I	DMKVER DMKVME DMKVS I DMKWRM	DMKVFC DMKVMG DMKVSJ DMKWRN
R1	031954	DMKXAB DMKACO DMKAPW DMKCCD DMKCFC DMKCFR DMKCFR DMKCPU DMKCPU DMKCSF DMKCSF DMKCSY DMKDEI DMKDMQ	DMKVDE DMKVFE DMKVRR DMKVSQ DMKAQR DMKACR DMKCFD DMKCFD DMKCFD DMKCFD DMKCPV DMKCQQ DMKCVT DMKCQQ DMKCVT DMKDEX DMKDNC DMKDRC DMKDNC DMKLOH DMKIUL DMKLOH DMKMHV	DMKXSI DMKACS DMKAPY DMKCCH DMKCFF DMKCFT DMKCPI DMKCPI DMKCPW DMKCQR DMKCSP DMKCVU DMKDGD DMKDRD	DMKZID DMKALG DMKAPZ DMKCCO DMKCFG DMKCFU DMKCFJ DMKCPJ DMKCQS DMKCQS DMKCQS DMKCAD DMKDAD DMKDRE	DMKALO DMKATS DMKCCS DMKCFH DMKCFV DMKCPM DMKCPM DMKCPY DMKCQT DMKCQT DMKCSR DMKDIA DMKDSB	DMKAPI DMKBIO DMKCFJ DMKCFJ DMKCFW DMKCKT DMKCPN DMKCPZ DMKCQU DMKCQU DMKCAU DMKDAU DMKDAU DMKDSP DMKFRT DMKGRT DMKGRT	DMKAPS DMKBLD DMKCCW DMKCFM DMKCFY DMKCPO DMKCPO DMKCQC DMKCQC DMKCQU DMKCSU DMKCDID DMKEIG	DMKAPT DMKBSC DMKCDB DMKCFO DMKCKW DMKCKW DMKCQG DMKCQG DMKCQV DMKCSV DMKDDR DMKCSV DMKDIF DMKGRA DMKENT	DMKAPU DMKCAC DMKCCP DMKCFP DMKCKF DMKCLK DMKCPS DMKC9H DMKCSB DMKC9H DMKCSB DMKC9H DMKC5B DMKC9H DMKC9C DMKD1R DMKC9C DMKHPU	DMKAPV DMKCAO DMKCFQ DMKCFQ DMKCKH DMKCPT DMKCQI DMKCSX DMKCEG DMKDEG DMKDEG DMKDEG DMKERM DMKERD DMKFVC DMKIOH
		DMKVFD DMKVSP DMKXAB DMKACO DMKACO DMKCFC DMKCFC DMKCFR DMKCFC DMKCFR DMKCFC DMKCFR DMKCPU DMKCPU DMKCPU DMKCPU DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCFT DMKPET DMKPET DMKSCD DMKNDC DMKSSS DMKTES DMKVDA DMKVDA DMKVCA DMKVCA DMKVCA DMKVCA DMKVCA DMKCCQ DMKCCQ DMKCCQ DMKCCG DMKCCQ DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG DMKCCG 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DMKCG DMKCG DMKCG DMKCG DMKCG DMKCG DMKCG DMKCG DMKCG	DMKEXT DMKGRF DMKHVE DMKIOQ DMKIUL DMKLOH DMKMSG DMKOPR DMKPGM DMKPTR DMKRGA DMKSCN DMKSST DMKSST DMKSWH	DMKVDF DMKVFRS DMKVFRS DMKVST DMKXST DMKACS DMKACS DMKACFF DMKCCFF DMKCCFF DMKCCFF DMKCCPU DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQC DMKCQC DMKCQC DMKCCS DMKCCC DMKCCC DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS 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		DMKXAD	DMKXST	DMKZTD							

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R12	006079	DMKACO DMKACFC DMKCCFC DMKCCFC DMKCCFC DMKCCFC DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY 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DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY DMKCCSY	DMKACR DMKCFD DMKCFD DMKCFD DMKCFD DMKCFD DMKCPB DMKCVU DMKCQ DMKCQ DMKCQ DMKCQ DMKCQ DMKCQ DMKCQ DMKFMT DMKFRG DMKIOH DMKFMT DMKFRG DMKFFT DMKCPG DMKCPG DMKCPG DMKCPG DMKCPG DMKCPG DMKCPG DMKCPG DMKCPG DMKCPG DMKCPG DMKCC DMKCC DMKCC DMKCC DMKCCFD DMKVCE DMKVCE DMKVCF DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD DMKCCFD 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DMKCFD DMKCFD DMKCFD DMKCFD DMKCFD DMKCFD DMKCFD DMKCFD DMKCFD DMKCFD DMKCFD DMKCFD DMKCFD DMKCFD DMKCFD DMKCFD	DMKACS DMKACY DMKCCH DMKCCFF DMKCFF DMKCKP DMKCQP DMKCQP DMKCQP DMKCQP DMKCQP DMKCQP DMKCQP DMKCQP DMKCQP DMKCQP DMKCQP DMKLOJ DMKLOJ DMKLOJ DMKLOJ DMKKOVR DMKCVR DMKCVR DMKCVR DMKCVR DMKCVR DMKSSEG DMKSSPL DMKSSPL DMKSSP DMKVCC DMKVCC DMKVCC DMKVCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF DMKCCFF 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DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG	DMKALO DMKALS DMKCCS DMKCFH DMKCKS DMKCPM DMKCKS DMKCQPT DMKCQT DMKCQT DMKCQT DMKCQT DMKCQT DMKCQT DMKCQT DMKCQT DMKCQT DMKCQT DMKCQT DMKCQT DMKCQT DMKCQT DMKCQT DMKCQD DMKCQD DMKCQD DMKCQD DMKCQD DMKVCQ DMKVCQ DMKVCQ DMKVCQ DMKCCST DMKCCFT DMKCCST DMKCCST DMKCCST DMKCCST DMKCQF DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST DMKCCST	DMKAPI DMKBIO DMKCFJ DMKCFJ DMKCFT DMKCFT DMKCFT DMKCFZ DMKCPZ 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COUNT REFERENCES

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			D.4./11.5.	<b>B1111111111111</b>	0144150	0141/11ET	D	D141/411 F	DHIVODE	DUITON	DHILDAO
		DMKMSW	DMKNEA	DMKNEM	DMKNES	DMKNET	DMKNLD	DMKNLE	DMKOPE	DMKOVR	DMKPAG
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		DMKCKM	DMI/CI/N	DMKCKP	DMI/CI/D	DMI/CI/S	DMICHT	DMKCKN	DMI/CI/U	DMKCLK	DMKCNS DMKCPT DMKCQ1 DMKCSC
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		DEIKYDG		DINKYUK							
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		DMI/VOV	DMKVSW	DMKVSX	DMKWAI	DMKVSG DMKWRM	DMKWRN		DMKYAD	DMKXST	DMKZTD
015	000000	DMKVSV DMKACO	DINKYOW	DINKYOA	DINKWAL			DMKXAB DMKAPS	DMKXAD DMKAPT	DHILLER	DALLADY
R15	026616	DMKACO	DMKACR	DMKACS	DMKALG	DMKALO	DMKAPI	DMKAPS	UMKAPI	DMKAPU	DMKAPV
		DMKAPW	DMKAPX	DMKAPY	DMKAPZ	DMKATS	DMKBIO	DMKBLD	DMKBSC	DMKCAC	DMKCAO
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LABEL	COUNT	REFERENCE	S								
		DMKCFC DMKCFR DMKCKM DMKCRU DMKCQP DMKCSF DMKCSF DMKCSY DMKDEX DMKDEX DMKDEX DMKCRT DMKLOJ DMKHVE DMKIOJ DMKLOG DMKMHC DMKNPO DMKNPC DMKRET DMKRET DMKRET DMKSSU DMKSSU DMKTAP DMKTAP DMKTAR DMKTAR	DMKCCF DMKCCFS DMKCFS DMKCFS DMKCPB DMKCPQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKKQQ DMKKQQ DMKKQC DMKKSPL DMKKSSQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ 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		DMKVM I DMKVSP	DMKVRR DMKVSQ	DMKVRS DMKVST	DMKVSC DMKVSU	DMKVSD DMKVSV	DMKVSE DMKVSW	DMKVSF DMKVSX	DMKVSG DMKWA I	DMKVSI DMKWRM	DMKVSJ DMKWRN
R2	022358	DMKDAS DMKDIA DMKDSB DMKFRE DMKGRI DMKIDU DMKISM DMKIUS DMKLOM DMKLOM	DMKVCS DMKVCE DMKVFR DMKVFR DMKVRR DMKACR DMKACR DMKACR DMKCCS DMKCFJ DMKCCFV DMKCCFT DMKCCFT DMKCCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST DMKCST	DMKVST DMKXST DMKACS DMKATS DMKCFM DMKCFM DMKCFM DMKCFY DMKCFV DMKCFV DMKCQV DMKCQV DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCSU DMKDDC DMKDID DMKLDOOE DMKIUB DMKLDOOE DMKMNL DMKNET	DMKTTX DMKVBM DMKVCU DMKVDG DMKVCG DMKVSC DMKVSU DMKZTD DMKCB DMKCB DMKCBD DMKCFO DMKCKD DMKCKD DMKCKD DMKCKD DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCCP DMKCDD DMKCDD DMKCCP DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKCDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD DMKDD	DMKVSV DMKALO DMKBLD DMKCDM DMKCFP DMKCKF DMKCLK DMKCPS DMKCQH DMKCSW DMKCSW DMKCSW DMKCSW DMKDFF DMKDIR DMKDF DMKDF DMKFOG DMKIUE DMKIUE DMKNLE	DMKVSW DMKAPI DMKBSC DMKCFQ DMKCKH DMKCNS DMKCPT DMKCQI DMKCSX DMKCSX DMKDEG DMKCSX DMKDEG DMKCSX DMKCRD DMKCRD DMKIOH DMKIOG DMKIOC DMKNMT	DMKAPS	DMKWAI DMKCFD DMKCFD DMKCFD DMKCFS DMKCFB DMKCPB DMKCPV DMKCQQ DMKCVT DMKCQQ DMKCVT DMKDNC DMKCVT DMKDNC DMKCVT DMKCVT DMKCRF DMKLOH DMKHVE DMKLOH DMKMHV DMKMSG DMKOPR	DMKWRM DMKAPU DMKCCD DMKCFF DMKCFT DMKCFI DMKCPU DMKCQR DMKCVU DMKCQD DMKCVU DMKDGD DMKFMT DMKGRG DMKIOS DMKIOS DMKIOJ DMKMIA DMKMSW DMKOVR	DMKWRN DMKCPV DMKCCH DMKCFG DMKCFU DMKCPJ DMKCPJ DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCSQ DMKCQ DMKCQ DMKCQ DMK DMK DMK DMK DMK DMK DMK DMK DMK DMK

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LA	BEL	COUNT	REFERENC	ES								
			DMKPAH DMKQCN DMKQCN DMKSAD DMKSAD DMKSVD DMKSVD DMKTMR DMKTRR DMKUSP DMKVCQ DMKVCQ DMKVFC DMKVMC DMKVSJ	DMKPEI DMKPMA DMKQCO DMKRGE DMKSAV DMKSNC DMKSSS DMKSSA DMKTOD DMKTOD DMKVCR DMKVCR DMKVDD DMKVFD DMKVSP	DMKPEL DMKPRG DMKQCP DMKSCH DMKSCH DMKSSND DMKSST DMKSST DMKSWM DMKTPE DMKVTPE DMKVTE DMKVCS DMKVFE DMKVFE DMKVSQ DMKXST DMKACS	DMKPEN DMKPRV DMKQCQ DMKRNH DMKSCN DMKSSK DMKSSU DMKTAP DMKTRA DMKTRA DMKVAU DMKVCT DMKVDF DMKVFR DMKVRS DMKVST	DMKPEQ DMKPRW DMKQVM DMKSCO DMKSPL DMKSSV DMKTAQ DMKTAQ DMKTRC DMKVRM DMKVCU DMKVDG DMKVFS DMKVSC DMKVSC	DMKPER DMKPSA DMKREI DMKREG DMKSEG DMKSTA DMKTCS DMKTCS DMKTTY DMKVCA DMKVCA DMKVCH DMKVDH DMKVSD DMKVSD	DMKPET DMKPST DMKRET DMKRSE DMKSEL DMKSPR DMKSTK DMKTCT DMKTCT DMKTCK DMKVCB DMKVCB DMKVCB DMKVCB DMKVSE DMKVSE DMKVSE	DMKPGM DMKPTR DMKRGA DMKRSP DMKSPS DMKSPS DMKSTP DMKTDK DMKVDK DMKVCH DMKVCH DMKVCS DMKVSF DMKVA I	DMKPGS DMKPTS DMKRGB DMKRSQ DMKSEV DMKSTT DMKSTR DMKTES DMKTES DMKVR DMKVCN DMKVCN DMKVDA DMKVDT DMKVMD DMKVSG DMKWRM	DMKPGT DMKPTT DMKRGC DMKRST DMKSFB DMKSFB DMKSVC DMKTHI DMKURS DMKVCP DMKVCP DMKVER DMKVER DMKVSI DMKVSI DMKVRN
R3		015012	DMKVSJ DMKVSJ DMKACO DMKACO DMKCFF DMKCFF DMKCFF DMKCFJ DMKCPX DMKCPX DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKDIB DMKDIB DMKDIB DMKDSP DMKFRT DMKING DMKJRL DMKNES DMKVEI DMKNES DMKSVD	DMKVM1 DMKVSP DMKXAD DMKACR DMKACR DMKCFG DMKCFG DMKCFG DMKCFU DMKCFG DMKCPY DMKCQT DMKCPY DMKCQT DMKCPY DMKCQT DMKCPY DMKCQT DMKCPY DMKCPY DMKCPT DMKCDC DMKNLD DMKNET DMKNET DMKNET DMKRML DMKRML DMKRMH DMKSNC DMKSWA DMKSWA DMKSWA	DMKACFU DMKACFO DMKCFO DMKCFV DMKCFV DMKCPZ DMKCPZ DMKCPZ DMKCQU DMKCQU DMKCQU DMKCDR DMKCQU DMKCDIF DMKCDIF DMKLOF DMKLOF DMKLOF DMKLOF DMKKNLD DMKKPA DMKSCH DMKSST DMKSST DMKSST DMKSVM DMKT0T	DMKVST DMKALG DMKALG DMKACS DMKCCS DMKCFJ DMKCFW DMKCFW DMKCFW DMKCQC DMKCQC DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY DMKCY	DMKALO DMKBIO DMKCFW DMKCFY DMKCKV DMKCPP DMKCQG DMKCQG DMKCSW DMKCSW DMKDEG DMKDEG DMKDEG DMKLOC DMKLOC DMKIOC DMKNOT DMKNOT DMKNTT DMKRST DMKRST DMKRSE DMKSCO DMKSPL DMKSV DMKTAQ	DMKAPI DMKBLD DMKCFO DMKCFO DMKCKD DMKCKW DMKCPS DMKCPS DMKCSS DMKCSS DMKCSS DMKCSS DMKCSS DMKCST DMKHVD DMKIJJ DMKIJJ DMKLOG DMKMPC DMKRGA DMKSFF DMKSFG DMKSFA DMKSTA DMKTCS DMKTTY	DMKAPS DMKBSC DMKCFP DMKCFP DMKCRF DMKCQI DMKCQI DMKCQI DMKCSY DMKCSY DMKCSY DMKCSY DMKCSY DMKCSY DMKCSF DMKCSF DMKCSF DMKLOH DMKHVE DMKKOPR DMKKPGB DMKRGB DMKRSPR DMKSFL DMKSTCD DMKTCD	DMKAPT DMKCAC DMKCCS DMKCFQ DMKCKH DMKCPU DMKCQP DMKCQP DMKCSO DMKCOT DMKCGD DMKCOT DMKCGD DMKLOS DMKIOS DMKIOS DMKIOS DMKLOJ DMKKOYR DMKKOYR DMKKOYR DMKKSEP DMKSEPS DMKSTP DMKTDK DMKTRB	DMKAPU DMKCFC DMKCFC DMKCFR DMKCPB DMKCPB DMKCPU DMKCQQ DMKCQQ DMKCSP DMKCQQ DMKCSP DMKCQR DMKCQR DMKCPU DMKCQR DMKCPG DMKLOR DMKLOR DMKLOR DMKNEA DMKNEA DMKNEA DMKRGD DMKRSTR DMKSPT DMKSTR DMKTEM DMKTEM DMKTEM	DMKWRN DMKAPV DMKCCD DMKCFD DMKCFS DMKCFS DMKCPW DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQC DMKLOM DMKLOM DMKSFM DMKSFM DMKSFM DMKSFM DMKSFM DMKSFM DMKSFM DMKSFM DMKSFM DMKSFM DMKSFM DMKSFM DMKSFM DMKSFM DMKSFM DMKSFM DMKSFM DMKSFM DMKSFM DMKSFM DMKSFM DMKSFM DMKSFM DMKSFM DMKSFM DMKSFM DMKSFM DMKSFM
R4	ŀ	012277	DMKTHT DMKTRQ DMKVCQ DMKVCQ DMKVFC DMKVFC DMKVMG DMKVSJ DMKAC0 DMKAC0 DMKACF	DMKTRR DMKVCR DMKVCR DMKVDD DMKVFD DMKVFD DMKXSP DMKXST DMKACR DMKAPX DMKCCH	DMKVAT DMKVCS DMKVCS DMKVFE DMKVRR DMKVSQ DMKZTD DMKACS DMKAPY DMKCCO	DMKTKU DMKVAU DMKVCT DMKVDF DMKVFR DMKVST DMKALG DMKALG DMKACS	DMKTRX DMKVBM DMKVCU DMKVDG DMKVFS DMKVSC DMKVSV DMKALO DMKALO DMKBIO DMKCCW	DMKVCA DMKVCV DMKVDH DMKVIO DMKVSD DMKVSX DMKAPI DMKBLD DMKCDB	DMKTTY DMKVCB DMKVCW DMKVDR DMKVMA DMKVSE DMKVSE DMKAPS DMKBSC DMKCDM	DMKVCH DMKVCX DMKVDS DMKVMC DMKVSF DMKWRM DMKAPT DMKCAC DMKCDS	DMKVCN DMKVCA DMKVDA DMKVDT DMKVSG DMKVSG DMKVRN DMKAPU DMKCAO DMKCFC	DMKVCP DMKVDB DMKVER DMKVRE DMKVSI DMKXAB DMKAPV DMKCCD DMKCFD

LABEL	COUNT	REFERENC	ES								
R5	COUNT 010893	REFERENCE DMKCFF DMKCFF DMKCCFT DMKCCPX DMKCCPX DMKCCPX DMKCCPX DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKKDSP DMKFRT DMKKDSP DMKFRT DMKKNCJ DMKKNCJ DMKKNCJ DMKKNCJ DMKKNCJ DMKKNCJ DMKKNCJ DMKKNCJ DMKKNCJ DMKKSNC DMKKSNC DMKKSNC DMKKSNC DMKKSNC DMKKSNC DMKKSNC DMKKSNC DMKKCFC DMKKCFS DMKKCFS DMKKCFS DMKKCFS DMKKCFS DMKKCFS DMKKCGD DMKKCGD DMKKCGD DMKKCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCFS DMKCCFS DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD DMKCCGD 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DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO	DMKKCFVS DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT DMKKCPQUT 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DMKCCSP DMKCCSP DMKCCSP DMKKFRT DMKKPAGU DMKKPAGU DMKKPAGU DMKKPAGU DMKKVDA DMKKVDA DMKKVCSP DMKKVCSP DMKKVCSP DMKKVCSP DMKKVCSP DMKKVCSP DMKKCCFW DMKKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP 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LABEL

COUNT

						0141/0 00	0141/0 01	DUUDOE	DWVDOF	DWVDCD	DWI/DCO
		DMKRGD	DMKRGE	DMKRNH	DMKRPA	DMKRPD	DMKRPT	DMKRSE	DMKRSF	DMKRSP	DMKKSQ
		DMKRST DMKRST DMKSNC DMKSST DMKTAP DMKTMR	DMKSAD	DMKSAV	DMKSCH	DMKSCN	DMKRPI DMKSCO	DMKSEG	DMKSEL	DMKSEP	DMKRSQ DMKSFB
		DHICHO	DMICOND	DMKSPK DMKSSV DMKTCS DMKTPE	DMKSPL DMKSTA DMKTCT DMKTRA	DMKSPM DMKSTK DMKTDK DMKTRC	DMKSPR DMKSTP DMKTEE DMKTRD	DMKSPS DMKSTR DMKTEF DMKTRK	DMKSRM DMKSVD DMKTEM	DMKSSP DMKSWA DMKTES DMKTRP	DMIZCCC
		DMKSNG	DMKSND DMKSSU	DMKSPK	DMKSPL	DHKSPH	DHKOFK	Drikoro	Drikonni	DMKSSF	DMKSWM DMKTH1 DMKTRQ DMKURS
		DMKSST	DMKSSU	DMKSSV	DMKSTA	DMKSTK	DMKSTP	DMKSTR	DMKSVD	DMKSWA	DMKSWM
		DMITAD	DMKTAQ DMKTOD	DMITCS	DMITCT	DMITTN	DMKTEE	DMKTEE	DMKTEM	DMKTES	DMKTH1
		DMKTAP	DINKTAQ	Drikics	DERICI	DHINTON	DMATLE	DRIVIEI	DHKILM		DHITTOO
		DMKTMR	DMKTOD	DMKTPE	DMKTRA	DMKIRC	DMKIRD	DMKIRK	DMKTRM	DMKIRP	DMIKIKQ
		DMKTMR DMKTRR DMKUSP DMKVCQ DMKVCQ DMKVFC DMKVFC DMKVSP DMKXAD DMKXAD	DMKTOD DMKUSQ DMKVCR DMKVCR DMKVFD DMKVFD DMKVRR DMKVSQ DMKXST DMKACR DMKBIO DMKCCW DMKCCM	DMKTRU DMKVAT DMKVCS DMKVDE	DMKTRX DMKVAU DMKVCT DMKVDF	DMKTTX DMKVBM DMKVCU DMKVDG	DMKTTY DMKVCA DMKVCV DMKVDH	DMKUDR DMKVCB DMKVCW DMKVDR	DMKTRM DMKUDU DMKVCH DMKVCX DMKVDS DMKVMD DMKVSG DMKWRM	DMKIRF DMKUNT DMKVCN DMKVDA DMKVDT DMKVME DMKVSI DMKWRN	DMKURS
		DHININA	DHILLIOO	DIANAT	DINCINA	DHILLOM		DMIAVOD	DMI/VOU	DMI/VON	
		DMKUSP	DMKUSQ	UMKVAI	DMKVAU	DMKYBM	DMKYCA	DMIKYUB	DMKYCH	DIMINACIA	DHINYOP
		DMKVCQ	DMKVCR	DMKVCS	DMKVCT	DMKVCU	DMKVCV	DMKVCW	DMKVCX	DMKVDA	DMKVDB
		DMI/VDC	DMI/VDD	DMI/VDE	DMI/VDE	DMI/VDC			DMI/VDS	DMKVDT	DMKVER
		DIMINYUC	DINKYUU	DINKYDE	DINKYUF	DHINYDG	DHINYDH	DrikyDr	DMINYUS	DINKADT	
		DMKVFC	DMKVFD	DMKVFE DMKVRS DMKVST DMKZTD	DMKVFR DMKVSC	DMKV10 DMKVSD DMKVSW	DMKVMA DMKVSE	DMKVMC DMKVSF	DMKVMD	DMKVME	DMKVMG DMKVSJ
		DMIXVM1	DMI/VRR	DMKVRS	DMKVSC	DMKVSD	DMKVSE	DMKVSF	DMKVSG	DMKVST	DMKVS.1
		DITKATT	DITINUTION	DHINKING	DINKYOU	DINKAOU	DHILLION	DMICLAI	DMI/UDM	DMI/UDM	DMI/VAD
		DMKVSP	DMKVSQ	DMKVSI	DMKVSV	DMKVSW	DMKVSX	DMKWAI	DMKWKM	DINKWIKIN	DMKXAB
		DMKXAD	DMKXST	DMKZTD							
DC	000070	DMKACO DMKATS DMKCCT	DMILACD	DMKACS DMKBLD DMKCDB	DMKALO DMKBSC DMKCDM DMKCFP DMKCKF DMKCPT DMKCQI DMKCQI DMKCVU DMKCRD	DMKAPI DMKCAO DMKCDS DMKCFQ DMKCKH DMKCNT DMKCPU DMKCQP DMKCQP DMKCAD DMKDAD DMKDAD DMKDRE DMKERT	DMKAPS DMKCCD DMKCFC DMKCFR DMKCPB DMKCPB DMKCPV DMKCQQ DMKDAS DMKDAS DMKDIA DMKDSB DMKDIO	DMKAPT DMKCCF DMKCFD DMKCFS DMKCKN DMKCPI DMKCPW DMKCQR DMKCQR DMKDAU DMKDAU DMKDSP DMKDSP DMKCBA	DMKAPU DMKCCH DMKCFF DMKCFT DMKCKP DMKCPJ DMKCPX DMKCQS DMKCQS DMKCQS DMKDCC DMKDC DMKDC DMKDT DMKENT DMKGRC DMKIOF	DMKAPV DMKCCO DMKCFG DMKCFU DMKCKR DMKCPM DMKCPY DMKCQT DMKCQT DMKCDR DMKDDR DMKDDR DMKERM DMKERM DMKERD DMKHVC DMKIOG	DMKAPY DMKCCS DMKCCFH DMKCFV DMKCKS DMKCPN DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU D
R6	008272	UMIKACU	DMRAGR	DIFINAUS	DINKALU	DMINAPI	DMARS	DINKAFI	DMINAFU	DINKALY	DEINAFT
		DMKATS	DMKBIO	DMKBLD	DMKBSC	DMKCAO	DMKCCD	DMKCCF	DMKCCH	DMKCCO	DMKCCS
		DMICCT	DMKCCW	DMKCDB	DMKCDM	DMKCDS	DMKCEC	DMKCED	DMKCEE	DMKCEG	DMKCEH
		DHKOUT	DHKOOM	DMKODD	DINKODH	DHIKODO	DHIKOTO	DINKOTO	DHILOFT	DHILOTU	DMICOTY
		DMKCFJ	DMKCFM	DMKCFO	DMKCEP	DMKCFQ	DMKCFR	DMKCFS	DMKCFI	DMKCFU	DMKCFV
		DMKCEW	DMKCEY	DMKCKD	DMKCKE	DMKCKH	DMKCKM	DMKCKN	DMKCKP	DMKCKR	DMKCKS
		DINKOVA		DMICORD	DMICONO	DHILONIT	DMKODD	DMICODI	DMI/OD I	DMICOM	DMICON
		DMKCFJ DMKCFW DMKCKT DMKCQC DMKCQC DMKCQY DMKCSW DMKCSW DMKCSW DMKCSW DMKCSW DMKCSW DMKCQY DMKCQY DMKCQY DMKLOG DMKHV	DMKCFM DMKCFY DMKCKV DMKCPP	DMKCFO DMKCKD DMKCKW DMKCPS	DMIKCINS	DMKCNI	DMKCPB	DMKCPT	DMKCPJ	DMKCPM	DINKOPN
		DMKCPO	DMKCPP	DMKCPS	DMKCPT	DMKCPU	DMKCPV	DMKCPW	DMKCPX	DMKCPY	DMKCPZ
		DMI/COC	DMKCQG	DMI/COU	DMI/COI	DMI/COD	DMICOO	DMICOP	DMICOS	DMKCOT	DMKCOU
		DMKCQC	DMKGQG	DIFINGUE	DINKOQT	DHINGQP	DHINGQQ	Drinoun	DHINGQO	DINKOQT	DHIKOQU
		DMKCQY	DMKCRM DMKCSX DMKDE I	DMKCCA DMKCQH DMKCSB DMKCSY DMKDEX	DMKCSC	DMKCSO	DMKCSQ	DMKCSR	DMKCSI	DMKCSU	DMKCSV
		DMICSU	DMKCSY	DMKCSV	DMKCVII	DMKDAD	DMKDAS	DMKDAII	DMKDDC	DMKDDR	DMKDEE
		DITINGON	DINKUSA	Drikost	DHINGVU	DMINDAD	DINKDAS	DHKDAO		DHINDUN	DHINDLD
		DMKDEG	DMKDEI	DMKDEX	DMKDGD	DMKDGF	DMKDIA	DMKDIB	UMKUTU	DMKDTF	UMKUIK
		DMKDMP	DMKDLT DMKDMQ DMKFMT DMKGRG DMKHVF	DMKDNC DMKFPS	DMKDRD DMKFRE	DMKDRF	DMKDSB	DMKDSP	DMKENT	DMKERM	DMKERP
		DMICENT	DMICENT	DMUEDC	DMICEDE	DMKFRT DMKGRT DMKIMG	DMKGIO DMKHPS DMKIOC	DMKGRA DMKHPT	DMICORC	DMICOD	DMICOPE
		DMKEXI	DMKFMI	DMKFPS	DMKFRE	UMKERI	DMKGTU	DIFINGRA	DMINGRO	DIMINGRO	DHINGNE
		DMKGRF	DMKGRG	DMKGRH DMK I DR	DMKGR I DMK I DU	DMKGRT	DMKHPS	DMKHPT	DMKHPU	DMKHVC	DMKHVD
		DMI/UVE	DMILLIVE	DMI/ LDP	DMI/ LDU	DMICIMO	DMKIOC	DMKIOE	DMKIOF	DMKING	DMKIOH
		DINKIYE	DINKINY	DMATUN	DMKIDU	DHKING	DINKIUG	DHILLUD			DHILLIO
		DMKIOJ	DMKTOO	DMKIOS	DMKIOT			DMKIUB	DMKIUC	DMKIUE DMKLNM DMKMCT DMKMOO DMKOPR DMKPGM DMKRGB DMKRGB DMKRSQ DMKSEP DMKSRM DMKSWA DMKTMR	DMKTUG
		DMKIIII	DMKLOH	DMKIUN DMKLOK DMKMID	DMKIUP DMKLOM	DMKTSM DMKTUS DMKMCC DMKNNJ DMKNET DMKPEN DMKPRV DMKQVM DMKRPD DMKRCN	DMKIGA DMKJRL DMKMCD DMKMNL DMKNLD DMKPEQ DMKPRW DMKREI DMKRSE	DMKIOB DMKLDOOE DMKMCH DMKNLE DMKNLE DMKPER DMKPST DMKRET	DWKINK	DMKINM	DWKLOC
		DHILLOO		DINKION	DINKION	DINKIOO	DHIMMOD	DHIMOU	DMI/MOI	DMI/MOT	DMI/MUC
		DMKLOG	DMKLOH	DMKLOK	DMKLOM	DMKMCC	DMKMCD	DMKMCH	DMKMCI	DMIKMUT	DUNKUNC
		DMKMHV	DMKMIA	DMKMID	DMKLOM DMKMNI DMKNES DMKPEL DMKPRG	DMKMN.I	DMKMNI	DMKMNT	DMKMON	DMKMOO	DMKMPO
		DMKMSG		DMKNEA	DMI/NEC	DMI/NET	DMI/NIL D	DMI/NIE	DMI/NMT	DMI/ODD	DMKOVP
		DMKMSG	DMKMSW DMKPAH DMKPGU DMKQCO DMKRGE DMKSAV DMKSND DMKSND DMKTRQ DMKTRD DMKUDR DMKVCH DMKVCS	DMANEA	DMANES	DMANEI	DFINILD	DHKNLL	DERMET	DINKOFA	DINKOVIN
		DMKPAG DMKPGT DMKQCN	DMKPAH	DMKPEI DMKPMA DMKQCP DMKRND	DMKPEL	DMKPEN	DMKPEQ	DMKPER	DMKPET	DMKPGM	DMKPGS
		DMI/PCT	DMI/ POIL	DMIZDMA	DMIZPRC	DMKDDV	DMKDRW	DMKPST	DMKPTR	DMKPTS	DMKPTT
		DEREGI	DMKFGU	DrikrmA	DMKFNO	DEIKEIN	DINKINA	DHILLOST	DIANDOA	DHILLIO	
		DMKQCN	DMKQCO	DMKQCP	DMKQCQ DMKRNH	DMKQVM	DMKREI	DMKREI	DMKRGA DMKRSP	DMKRGB	DMKKGC
		DMKRCCN DMKRGD DMKSAD DMKSNC DMKSSS DMKTAP DMKTRC DMKTTY DMKVCB DMKVCW DMKVCP	DMKRGE	DMKRND	DMKRNH	DMKRPD	DMKRSE	DMKRSF	DMKRSP	DMKRSQ	DMKRST
		DHKALD	DINKICL	DMKSBL DMKSPK DMKSSV DMKTCS	DMKKNH DMKSCH DMKSPL DMKSTA DMKTCT DMKTCT DMKVCP DMKVDB DMKVDB	DMKKPD DMKSCN DMKSPM DMKSTK DMKTDK DMKTRQ DMKVCQ DMKVCQ DMKVCC	DMIKCOO	DMKKSFG DMKSEG DMKSPS DMKTES DMKTES DMKTRT DMKUSQ DMKVCS DMKVDE	DMKSEL DMKSPT DMKSVD DMKTHI	DMILCED	DMICED
		UMKSAU	DMKSAV	DMKSBL	DMKSCH	DMKSUN	DMKSCU	DHINGEG	DUNKSEL	DINKOLF	DINKOFD
		DMKSNC	DMKSND	DMKSPK	DMKSPL	DMKSPM	DMKSPR	DMKSPS	DMKSPT	DMKSRM	DMKSSP
		DMILESS	DMICST	DMICSOV	DMISTA	DMVCTV	DMISTD	DMKSTP	DMKSVD	DMKSWA	DMKSWM
		DHKSSS	DHK33T	DMKSSV	DIANSTA	Drikstk	DHKSTT	DHKOTK	DINKOVD		DINICOMIN
		DMKTAP	DMKTAQ	DMKICS	DMKICT	DMKIDK	DMKIEE	DMKIES	DMKIHI	DMKIMK	DMKTOD
		DMKTRC	DMKTRD	DMKTRK DMKUDU DMKVCN DMKVDA	DMKTRP	DMKTRO	DMKTRR	DMKTRT	DMKTRU DMKVAT DMKVCT DMKVDF	DMKTRX DMKVAU DMKVCU DMKVDG	DMKTTX DMKVCA DMKVCV
		DINICTIC	DMUUDD		DANKINA	DMI/UDC	DMI/UCD	DHILLICO	DMINIAT	DMI/V/AU	DMI/V/CA
		DMKITY	DMKUUK	DMKUDU	UMKUNI	DMKUKS	DMKUSP	DMKUSQ	UMIKVAI	DMKVAU	DINKYCA
		DMKVCB	DMKVCH	DMKVCN	DMKVCP	DMKVCQ	DMKVCR	DMKVCS	DMKVCT	DMKVCU	DMKVCV
		DMIAVOU	DMIAVOV	DMILLOA	DMI/VDD	DMILLIOC	DMI/VDD	DMILLIDE	DMI/VDE	DMIXVDC	DMKVDH
		MUNNUM	UMINYUN	UMINYUA	UMIKYUD	UMKYUU	UUVAINO	UMINYUE	טוייגעטר	UNINVUG	חטיאוייט
		DMKVDR	DMKVDS	DMKVDT	DMKVER	DMKVFC	DMKVFD	DMKVFE	DMKVFR	DMKVFS	DMKVIO
		DMI/\/MA	DMI/VMC	DMI/\/MD	DMI/VME	DMI/VMC	DMIZVMI	DMKVRR	DMKVSC	DMKVFS DMKVSD	DMKV10 DMKVSE
		DINKYINA	DHINYHO	DIANATAD	DHIKTHE	DHIKWHO		DINGTOT			DINICIOL
		DMKVDR DMKVMA DMKVSF	DMKVDS DMKVMC DMKVSG	DMKVDT DMKVMD DMKVSI	DMKVER DMKVER DMKVME DMKVSJ DMKWRN	DMKVFC DMKVMG DMKVSP	UMKVSQ	DMKVFE DMKVRR DMKVST	DMKVFR DMKVSC DMKVSU	DMKVSV	DMKVSW
		DMKVSX	DMKWAI	DMKWRM	DMKWRN		DMKXAD		DMKZTD		
07	000005	DMILLOO	DMILAOD	DMILAGO		DMI/ADC	DMKRSE DMKSCO DMKSPR DMKSTP DMKTEE DMKTRR DMKVSP DMKVCR DMKVFD DMKVFD DMKVMI DMKVSQ DMKXAD DMKXAD		DMICADIC	DMI/ADL/	DMIZADY
R7	008835	DMKACU	DMKACK	DMKAUS	DMKALG	UMKAPS	DIMKAPT	DMKAPU	DITKAPY	DINKAPW	DMKAPX
		DMKAPY	DMKATS	DMKBIO	DMKALG DMKBLD	DMKBSC	DMKAPT DMKCAC	DMKCAO	DMKCCD	DMKCCF	DMKCCH
		DMKVSX DMKACO DMKAPY DMKCCO	DMKACR DMKATS DMKCCS	DMKACS DMKBIO DMKCCT	DMKCCW	DMKCDB	DMKCDM	DMKCDS	DMKAPV DMKCCD DMKCFC	DMKAPW DMKCCF DMKCFD	DMKCFF DMKCFU
		UNINCLU	DINKUUS	DINKUUI	DHINGUW	DINKUDD		DINKUUS	DHKOFU	DHINGFU	
		DMKCFG	DMKCFH	DMKCFM	DMKCFO	DMKCFP	DMKCFQ	DMKCFR	DMKCFS	DMKCFT	DMKCFU
		DMKCEV	DMKCEW	DMKCEV	DMKCKD	DMKCKE	DMKCKM	DMKCKN	DMKCKR	DMKCKS	DMKCKT
				DMKCFY	DMICONT	DMI/ODD	DMIXOD	DMICODM	DMICON	DMKCKS DMKCPO	DMKCKT DMKCPP
		DMKCKV	DMKCKW	DMKCNS	DMKCFO DMKCKD DMKCNT	DMKCAR	UMIKCPJ	DMKCPM	DMKCFS DMKCKR DMKCPN	DMIKCPU	UMKUPP
		DMKCPS	DMKCPT	DMKCPU	DMKCPV	DMKCPW	DMKCPX	DMKCPY	DMKCPZ	DMKCOC	DMKCQG
		DMKCFG DMKCFV DMKCFV DMKCPS DMKCQP	DMKCCFH DMKCFW DMKCFW DMKCPT DMKCQQ	DMKCPU DMKCQR	DMKCPV DMKCQS	DMKXAB DMKAPS DMKBSC DMKCDB DMKCFP DMKCFF DMKCPB DMKCPW DMKCQT	DMKCFQ DMKCKM DMKCPJ DMKCPX DMKCQU	DMKAST DMKAPU DMKCAO DMKCDS DMKCFR DMKCFR DMKCPM DMKCPY DMKCQY	DMKCRM	DMKCQC DMKCSB	DMKCQG DMKCSC
		DMKCQP	DMKCQQ	DMKUQK	DMKCQS	DIFIKUQI	DIFIKUQU	DMKGQY	DMKCKM	DHIKUOD	UMAGOG

REFERENCES

LABEL	COUNT	REFERENC	ES								
R8	006535	DMKCSF DMKCVU DMKDIA DMKDIA DMKDSB DMKDSB DMKLOF DMKIOF DMKIOF DMKIOF DMKLOF DMKKAGD DMKPAG DMKSAG DMKSAD DMKSAD DMKSAD DMKSST DMKVCW DMKVCW DMKVCW DMKVCW DMKVCW DMKVCW DMKVCW DMKCVCW DMKCVCW DMKCCF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKFFF DMKFFF DMKFFF DMKFFF DMKFFF DMKFFF DMKFFF DMKFFF DMKFFF DMKFFF DMKFFF DMKFFF DMKFFFF DMKFFFF DMKFFFF DMKFFFF DMKFFFFFFFFFF	DMKCSO DMKDAD DMKDAD DMKDAD DMKDIB DMKCSR DMKHPU DMKIOG DMKIOC DMKIOC DMKLOC DMKASW DMKPAH DMKPGU DMKCCN DMKSSV DMKSSV DMKSSV DMKSSV DMKSSV DMKCCN DMKVCH DMKVCK DMKVCK DMKVCK DMKVCK DMKVCK DMKVCK DMKVCK DMKVCK DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCFG DMKCSR DMKCSR DMKCDR DMKCDR DMKCSR DMKCSR DMKCSR DMKCSR DMKCSR DMKCSR DMKCSR DMKCSR DMKCSR DMKCAT DMKCAT DMKCAT DMKCAT DMKCAT	DMKCSQ DMKDAS DMKDID DMKENT DMKENT DMKENT DMKKVC DMKIOH DMKIOG DMKNID DMKNEA DMKPEA DMKPEA DMKPEA DMKSBL DMKSBL DMKSBL DMKSBL DMKSBL DMKSBL DMKSBL DMKSBL DMKCO DMKVDA DMKVDA DMKVDA DMKVDA DMKVDA DMKVCN DMKVCN DMKVCN DMKVCN DMKCFW DMKCFW DMKCFW DMKCFW DMKCFW DMKCFW DMKCFF DMKCFW DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFF DMKFFF DMKFFF DMKFFF DMKFFF DMKFFF DMKFFFF DMKFFFF DMKFFFFFFFFFF	DMKCSR DMKDAU DMKDAU DMKERM DMKGRD DMKHVD DMKIOJ DMKIOJ DMKIOJ DMKIOH DMKNES DMKPEL DMKVER DMKSCH DMKSCH DMKSCH DMKSTK DMKVCP DMKVDB DMKVCP DMKVDB DMKVCP DMKVCP DMKVCP DMKVCP DMKVCP DMKVCP DMKVCP DMKVCP DMKVCP DMKVCP DMKVCP DMKVCP DMKVCP DMKVCP DMKCSI DMKCFY DMKCFS DMKCFY DMKCCSB DMKCCFY DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSB DMKCCSC DMKCCSB DMKCCSB DMKCCSB DMKCCSC DMKCCSC DMKCCSC DMKCCSB DMKCCSC DMKCCSB DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCSC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCCC DMKCC	DMKCST DMKDIR DMKERP DMKERP DMKERP DMKIUQ DMKIUQ DMKIUQ DMKIUJ DMKNET DMKPEN DMKSCN DMKSCN DMKSCN DMKSCN DMKSTP DMKSTP DMKVCQ DMKVCQ DMKVCQ DMKVFC DMKVFC DMKVFC DMKVFC DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DMKCFO DM	DMKCSU DMKCDR DMKDDR DMKEXT DMKLDR DMKKVF DMKIOS DMKIOM DMKNLD DMKNLD DMKNLD DMKSCO DMKSCS DMKSTR DMKSTR DMKVCR DMKVCR DMKVCR DMKVCR DMKVCR DMKVCR DMKVCR DMKVCR DMKVCR DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF DMKCSF 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DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS DMKCFS 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LABEL	COUNT	REFERENC	CES								
		DMKVCS DMKVDE DMKVFE DMKVSC DMKVSU DMKZTD	DMKVCT DMKVDF DMKVFS DMKVSD DMKVSV	DMKVCU DMKVDG DMKVIO DMKVSE DMKVSW	DMKVCV DMKVDH DMKVMA DMKVSF DMKVSX	DMKVCW DMKVDR DMKVMC DMKVSG DMKWA I	DMKVCX DMKVDS DMKVMD DMKVS I DMKWRM	DMKVDA DMKVDT DMKVME DMKVSJ DMKWRN	DMKVDB DMKVER DMKVMI DMKVSP DMKXAB	DMKVDC DMKVFC DMKVRR DMKVSQ DMKXAD	DMKVDD DMKVFD DMKVRS DMKVST DMKXST
R9 SAS SASPF SAVCNT SAVCREGS	006154 000014 00002 00002 00002	DMKACU DMKACPY DMKCPM DMKCFR DMKCCFR DMKCCRM DMKCQG DMKCQG DMKCQG DMKCQG DMKCQG DMKCQG DMKCQG DMKCQG DMKCQG DMKJRL DMKJRL DMKJRL DMKJRL DMKNLE DMKNLE DMKNCC DMKSAV DMKSAV DMKSAV DMKSAV DMKTRA DMKTCA DMKTCA DMKVCV DMKVCD DMKVCSJ DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCS DMKCCS DMKCCS DMKCCS DMKCS DMKCCS DMKCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKCS DMKC	DMKACR DMKATS DMKCDS DMKCCS DMKCCS DMKCQH DMKCQH DMKCQH DMKCSS DMKDGD DMKDRE DMKFRE DMKFRE DMKFRE DMKFRE DMKLD00E DMKMNT DMKNMT DMKNMT DMKNMT DMKNMT DMKNMT DMKSCH DMKSCH DMKSCH DMKSCH DMKSCH DMKSCB DMKVCB DMKVCB DMKVCB DMKVCB DMKVCB DMKVCB DMKVCB DMKVCB DMKVCB DMKVCB DMKVCB DMKVCB DMKVCB DMKVCB DMKVCB DMKVCB DMKVCB DMKVCB DMKVCB DMKVCB DMKVCB DMKVAT	DMKACS DMKBIO DMKCFC DMKCFU DMKCKS DMKCQI DMKCQI DMKCSF DMKCSF DMKDSP DMKFRT DMKHPS	DMKALG DMKBLD DMKCFF DMKCFV DMKCFV DMKCQP DMKCQP DMKCQD DMKCQD DMKCQD DMKCIG DMKFIG DMKFIG DMKIOC DMKFOC DMKAPA DMKQCO DMKSPK DMKSCO DMKSPK DMKSCO DMKSPK DMKSCO DMKSPK DMKSCO DMKSPK DMKVCN DMKVCN DMKVCN DMKVCN DMKVCN DMKVCN DMKVCN DMKVFPS	DMKAPI DMKBSC DMKCFG DMKCFW DMKCFW DMKCFW DMKCQQ DMKCQQ DMKCQD DMKDID DMKENT DMKINF DMKIOC DMKNCT DMKKIOC DMKKVCP DMKVSCP DMKSFL DMKVCP DMKVCP DMKVCP DMKVCP DMKVCP DMKVCP DMKVSU DMKIOT	DMKAPS DMKCFH DMKCFY DMKCFY DMKCFY DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKKISG DMKLOG DMKVICG DMKVFRG DMKVFR DMKVCQ DMKVFE DMKVSC DMKVSC DMKVSC DMKVSC DMKVSC DMKVSC DMKVSC DMKVSC DMKVSC DMKVSC DMKVSC DMKVSC DMKVSC DMKVSC DMKVSC	DMKAPT DMKCAO DMKCFM DMKCKD DMKCFM DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKCQS DMKLOH DMKLOH DMKLOH DMKLOH DMKKLOH DMKKLOH DMKKLOH DMKKPRV DMKKSP DMKSPR DMKSPR DMKSPR DMKVSR DMKVSE DMKVSE DMKVSE DMKVSW DMKPSA	DMKAPU DMKCCH DMKCCFO DMKCKF DMKCPI DMKCQT DMKCQT DMKCQT DMKCQT DMKLOJ DMKLOJ DMKLOJ DMKLOJ DMKLOJ DMKLOJ DMKLOJ DMKKEQ DMKKPRW DMKKPRW DMKSPS DMKSPS DMKSPS DMKVSF DMKVSF DMKVSF DMKVSF DMKSVD	DMKAPV DMKCFP DMKCFP DMKCFJ DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKCQU DMKVFF DMKSFB DMKSFB DMKSFB DMKSFB DMKSFB DMKSFB DMKSVAT DMKVAT DMKVAT DMKVAT DMKVSG DMKV10 DMKVSG	DMKAPX DMKCCW DMKCCW DMKCFQ DMKCCPZ DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKCQY DMKFMT DMKIDR DMKIDR DMKIDR DMKLOM DMKVLO DMKVFT DMKSAD DMKSAD DMKSSP DMKTAP DMKTAP DMKTAP DMKTAP DMKTAD DMKVAU DMKVCU DMKVCU DMKVKSI DMKWRM
SAVCRÈGS SAVDATE SAVEAR SAVEAREA	000005 000007 000526	DMKCFF DMKCFF DMK1MG DMKACO DMKACH DMKCCH DMKCFF DMKCFT DMKCNS	DMKCFH DMKCFH DMKRND DMKALG DMKAPZ DMKCCO DMKCFG DMKCFG DMKCFB	DMKALO DMKATS DMKCCS DMKCFH	DMKCQY DMKAPI DMKBIO DMKCCT DMKCFJ DMKCFW DMKCFW	DMKAPS DMKBLD DMKCCW DMKCFM DMKCFY DMKCPZ	DMKAPT DMKBSC DMKCDB DMKCFO DMKCKR DMKCPO	DMKAPU DMKCAC DMKCCM DMKCFP DMKCKS DMKCPP DMKCRM DMKCSV DMKCEI DMKEIG DMKHPS	DMKAPV DMKCAO DMKCDS DMKCFQ DMKCKT DMKCPS DMKCSB DMKCSB DMKCSW DMKEST DMKHPT	DMKAPW DMKCCD DMKCFC DMKCFR DMKCKV DMKCQI DMKCSC DMKCSX DMKCGD DMKEPS DMKHPU	DMKAPX DMKCCF DMKCFS DMKCLK DMKCPU DMKCQP DMKCSF DMKCSF
		DMKCNY DMKCQQ DMKCQQ DMKCSO DMKCVU DMKDIB DMKGIO	DMKCPW DMKCQW DMKCQP DMKCSP DMKDAD DMKDID DMKGRA	DMKCFV DMKCPJ DMKCQS DMKCQS DMKCSQ DMKDAS DMKDIF DMKGRC	DMKCPY DMKCQT DMKCQT DMKCSR DMKDAU DMKDRD DMKGRG	DMKCPZ DMKCQU DMKCQU DMKCST DMKDEF DMKDRE DMKGRI	DMKCQC DMKCQC DMKCQY DMKCSU DMKDEG DMKDSB DMKGRT	DMKCRM DMKCSV DMKDEI DMKEIG DMKHPS	DMKCSB DMKCSB DMKCSW DMKDEX DMKENT DMKHPT	DMKCSC DMKCSX DMKDGD DMKEPS DMKHPU	DMKCSF DMKCSF DMKCSY DMKDIA DMKERM DMKHVD

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	LABEL	COUNT	REFERENC	ES								
,			DMKHVE DMKIOT DMKIOT DMKMCC DMKMNT DMKNLE DMKREI DMKRSE DMKSEV DMKSPS DMKSVC DMKVEM DMKVCV DMKVDH DMKVCA DMKVAB	DMKHVF DMKISM DMKISM DMKMCD DMKOPE DMKPST DMKRGA DMKSFB DMKSFB DMKSFT DMKSVA DMKTTY DMKVCA DMKVCA DMKVCA DMKVCA DMKVCA DMKVCA DMKVCA DMKVCA DMKVCA DMKVCA DMKVCA DMKVAC	DMKIDR DMKIDR DMKJRL DMKMCI DMKPTR DMKPTR DMKRGB DMKSTR DMKSRM DMKSRM DMKSRM DMKVCB DMKVCB DMKVCS DMKVDS DMKVST	DMKIDU DMKIUB DMKLNK DMKMSG DMKPEL DMKPTS DMKRGC DMKSNC DMKSSP DMKTAP DMKVDU DMKVCH DMKVDA DMKVDT DMKVSR DMKZTD	DMKIOE DMKIUC DMKLNM DMKMSW DMKPEN DMKPEN DMKPEN DMKRGE DMKSSS DMKTAQ DMKSSS DMKTAQ DMKVCP DMKVCP DMKVCP DMKVER DMKVER DMKVST	DMKIOF DMKIUE DMKLOC DMKNEA DMKNEA DMKPEQ DMKQCN DMKSBL DMKSST DMKTCS DMKTCS DMKTCS DMKVCQ DMKVCQ DMKVFC DMKVRR DMKVSU	DMKIOG DMKIUG DMKLOG DMKNEM DMKPER DMKQCO DMKSPA DMKSCO DMKSPK DMKSSU DMKTCT DMKUSP DMKVCR DMKVCR DMKVFD DMKVSC DMKVSW	DMKIOH DMKIUJ DMKLOH DMKNES DMKPET DMKQCP DMKSEG DMKSEG DMKSSV DMKTDK DMKTDK DMKVCS DMKVCS DMKVFE DMKVSX	DMKIOQ DMKIUL DMKLOJ DMKNET DMKPGM DMKQCQ DMKSPI DMKSEL DMKSPM DMKSTP DMKTHI DMKVAT DMKVAT DMKVCT DMKVFR DMKVFR DMKVSE DMKVSE	DMKIOS DMKIUN DMKLOM DMKNLD DMKPGS DMKQVM DMKSEP DMKSFR DMKSTR DMKSTR DMKTOD DMKVCU DMKVCU DMKVCU DMKVSF DMKVSF
	SAVEPROC		DMKXAB DMKACO DMKACO DMKACY DMKCCW DMKCFY DMKCFY DMKCQC DMKCQC DMKCQC DMKCQU DMKDSB DMK1OS DMK1OS DMK1OS DMK1UP DMKMCC DMKNLE DMKNLE DMKNLE DMKSQC DMKSSV DMKSVC DMKVCH	DMKXAD DMKSVC DMKALG DMKAPZ DMKCDB DMKCFO DMKCFO DMKCFO DMKCRM DMKCRM DMKCSV DMKDE I DMKENT DMKENT DMKNCD DMKNCD DMKMON DMKNCD DMKNCD DMKSTF DMKSFF DMKSFF DMKSTCD DMKTCD DMKTCD DMKVCA DMKVCA DMKVCR	DMKXST DMKALO DMKATS DMKCDM DMKCFP DMKCFS DMKCPS DMKCPS DMKCSW DMKCSW DMKCSW DMKCSW DMKCSW DMKCSW DMKCSW DMKCSW DMKL DMKCI DMKFTR DMKNCI DMKSPL DMKSPL DMKSPL DMKSPL DMKSPL DMKSPL DMKVCS DMKVCS DMKVCS	DMKZTD DMKAPI DMKBIO DMKCDS DMKCCS DMKCFQ DMKCFT DMKCQI DMKCSX DMKDGD DMKERM DMKIUB DMKLNK DMKHC DMKNFS DMKPTS DMKSPA DMKSPA DMKSPA DMKSWA DMKVCH DMKVDA DMKVDT	DMKAPS DMKBLD DMKCFC DMKCFC DMKCFV DMKCVP DMKCVP DMKCSF DMKCSY DMKCSY DMKIDR DMKIDR DMKFPT DMKRPEN DMKSEG DMKSPR DMKSEG DMKSPR DMKSEG DMKSPR DMKVCP DMKVCB DMKVER	DMKAPT DMKBSC DMKCFD DMKCFS DMKCLK DMKCQQ DMKCQQ DMKCVU DMKDIB DMKCVU DMKIIB DMKGRA DMKIDU DMKIIB DMKKRA DMKKPCN DMKSPT DMKSPT DMKTRD DMKVCQ DMKVCC DMKVFC	DMKAPU DMKCAC DMKCFF DMKCFF DMKCPB DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKCQR DMKVCR DMKVCD DMKVFD	DMKAPV DMKCAO DMKCFG DMKCFJ DMKCPJ DMKCQS DMKCQS DMKDAS DMKCQS DMKLOF DMKIOF DMKIOF DMKIOF DMKKSS DMKKSFB DMKSSS DMKTCS DMKVCS DMKVCE DMKVFE	DMKAPW DMKCCH DMKCFH DMKCFY DMKCPY DMKCQT DMKCQT DMKCQT DMKDRD DMKHPS DMKIOJ DMKHPS DMKIOJ DMKNET DMKKQVM DMKRSF DMKSST DMKSTCT DMKVCT DMKVDF DMKVDF	DMKAPX DMKCCS DMKCFJ DMKCFW DMKCPN DMKCQU DMKCQU DMKCST DMKDRE DMKDRE DMKHPT DMKION DMKNLD DMKNLD DMKRSP DMKRSP DMKSSU DMKSSU DMKTRU DMKVAU DMKVCU DMKVFS
	CAVEDET	000021	DMKVMC DMKVSQ DMKZTD DMKDDR	DMKVMD DMKVST DMKDIR	DMKVME DMKVSU	DMKVMG DMKVSW	DMKVRR DMKVSX	DMKVSD DMKWRM	DMKVSE DMKWRN	DMKVSF DMKXAB	DMKVSG DMKXAD	DMKVSP DMKXST
	SAVERET SAVERETN		DMKCFG DMKPGM DMKVCR	DMKCFJ DMKPTR DMKVCS	DMKCFQ DMKQCO DMKVER	DMKCPB DMKSSS	DMKD I F DMKSTP	DMKIOS DMKSVC	DMKTUA DMKVAT	DMKLNK DMKVCA	DMKLOC DMKVCP	DMKLOG DMKVCQ
	SAVERTN SAVERO	000003 000166	DMKSVC DMKCKS DMKDEG	DMKVSQ DMKCKT DMKDIA	DMKCNS DMKD I B	DMKCQG DMKDRD	DMKCQQ DMKDRE	DMKCQR DMKEPS	DMKCQY DMKERM	DMKDAD DMKGRG	DMKDAS DMKGRT	DMKDAU DMKHVD

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REFERENCES

DMKHVF

DMKIUA

DMKHVE

		DIMIKHVE		DMKTUA	DMKIOC	DMICHON	DMILMOU	DMINEM	DMICDOM	DMICDMA
		DMKJRL	DMKLNK	DMKLNM	DMKLOH	DMKMON	DMKMSW	DMKNEM	DMKPGM	DMKPMA
		DMKQCN	DMKQCO	DMKRGC	DMKRNH	DMKSCO	DMKSFB	DMKSNC	DMKSTP	DMKSTR
		DMKTAP	DMKTDK	DMKTPE	DMKTRC	DMKTRD	DMKUDR	DMKVAU	DMKVBM	DMKVCA
		DMKVCR	DMKVCT	DMKVCV	DMKVDA	DMKVDC	DMKVFE	DMKVSD	DMKVSF	DMKVSG
SAVER1	000231	DMKACR	DMKBLD	DMKCCW	DMKCFC	DMKCFR	DMKCKS	DMKCKT	DMKCNS	DMKCPO
		DMKCPU	DMKCPZ	DMKCQQ	DMKCSB	DMKCSC	DMKDEG	DMKERM	DMKHPU	DMKHVF
		DMKIOE	DMKIUA	DMKIŬĜ	DMKIUJ	DMKIUP	DMKLNK	DMKLNM	DMKMHC	DMKMIA
		DMKMSG	DMKPGM	DMKPGS	DMKPMA	DMKPTR	DMKPTT	DMKQCN	DMKQCO	DMKRNH
		DMKSSS	DMKSTP	DMKSTR	DMKTDK	DMKTRC	DMKTRD	DMKTRK	DMKVAU	DMKVBM
				DMKSIK						
		DMKVCP	DMKVCV	DMKVCX	DMKVDE	DMKVDF	DMKVDR	DMKVDS	DMKVDT	DMKVFC
		DMKVSD	DMKVSE	DMKVST	DMKVSU					
SAVER10	000050	DMKACO	DMKBLD	DMKCCD	DMKCCF	DMKCCH	DMKCCO	DMKCCS	DMKCCW	DMKHPU
		DMKLOG	DMKLOJ	DMKSEP	DMKVBM	DMKVCA	DMKVCH	DMKVDC	DMKVDE	DMKVDG
		DMKVFE								
SAVER11	000165	DMKALG	DMKAPI	DMKBLD	DMKCAO	DMKCFM	DMKCFO	DMKCFQ	DMKCFR	DMKCPN
0/1/2/11/	000.05	DMKCPP	DMKCPS	DMKCPU	DMKCPY	DMKCQG	DMKCQQ	DMKCSR	DMKCSU	DMKCSX
		DMKDAS	DMKDAŬ	DMKDIA	DMKDIB	DMKDIF	DMKEPS	DMKHPS	DMKHPT	DMKHVF
		DMKIOS	DMKLNK	DMKLOG	DMKLOH	DMKLOJ	DMKMID	DMKMSG	DMKMSW	DMKNES
						DMKSEL	DMKSND	DMKSPK	DMKSPL	DMKSPM
		DMKPGM	DMKQCN	DMKQCO	DMKSCO					
		DMKSTR	DMKSWA	DMKTCS	DMKTCT	DMKTHI	DMKUNT	DMKVCA	DMKVCB	DMKVCH
		DMKVCS	DMKVCT	DMKVDA	DMKVDB	DMKVDD	DMKVDE	DMKVDT	DMKVMC	
SAVER12	000034	DMKCCS	DMKCCW	DMKCFQ	DMKCPV	DMKDGD	DMKIDR	DMKLNK	DMKMCC	DMKMCD
		DMKPGS	DMKPTR	DMKSTP	DMKSVC	DMKVAT	DMKVCA	DMKVER		
SAVER13	000046	DMKCFF	DMKCFR	DMKCPN	DMKCPW	DMKCQT	DMKCSY	DMKDIF	DMKHPU	DMKLNK
		DMKPGM	DMKPGS	DMKPTR	DMKSEL	DMKSTP	DMKSVC	DMKTTX	DMKVAT	DMKVCU
		DMKVDC	DMKVDE	DMKVDF	DMKVDS					
SAVER14	000002	DMKCFU	DINCTOL	DINCOL	51111100					
SAVER2	000417	DMKACO	DMKALG	DMKBLD	DMKCCW	DMKCDB	DMKCDS	DMKCFC	DMKCFM	DMKCFS
SAVENZ	000417			DMKCNS			DMKCQH	DMKCQP	DMKCQQ	DMKCSR
		DMKCKS	DMKCKV		DMKCPT	DMKCQG				
		DMKCSY	DMKDEF	DMKDEG	DMKDEI	DMKDGD	DMKDIA	DMKDIB	DMKDRD	DMKDRE
		DMKGIO	DMKGR I	DMKHVF	DMKIUG	DMKJRL	DMKLNK	DMKLNM	DMKLOG	DMKLOH
		DMKLOM	DMKMHC	DMKMTA	DMKMNJ	DMKMSG	DMKNEA	DMKNES	DMKNET	DMKNLD
		DMKPEI	DMKPEL	DMKPEN	DMKPEQ	DMKPET	DMKPGS	DMKPMA	DMKPTR	DMKPTT
		DMKQCO	DMKQCQ	DMKREI	DMKRGA	DMKRGB	DMKRGC	DMKRNH	DMKRPA	DMKRSQ
		DMKSFB	DMKSNC	DMKSPT	DMKSSS	DMKSST	DMKSTR	DMKSWA	DMKTRA	DMKTRC
		DMKTRP	DMKTTX	DMKUDR	DMKVAU	DMKVCH	DMKVCQ	DMKVCR	DMKVCS	DMKVCU
		DMKVDC	DMKVDD	DMKVDE	DMKVDG	DMKVDS	DMKVDT	DMKVMC	DMKVSD	DMKVSE
		DMKVSQ	DMKVST	DMKVSU	DMKWRM	51111100	5111101	Brittene	5	
SAVER3	000044	DMKCFC	DMKCPN	DMKCSW	DMKERM	DMKLNK	DMKLOH	DMKPGS	DMKPTR	DMKPTT
SAVENS	000044						DMKTTX	DMKUDU	DMKVAU	DMKVDT
a hurah	000016	DMKQCQ	DMKRPA	DMKSCO	DMKSST	DMKSTR				
SAVER4	000016	DMKCCH	DMKCFC	DMKCFQ	DMKCPN	DMKQCN	DMKQCO	DMKQCQ	DMKTRC	DMKVDA
		DMKVDG						<b></b>		
SAVER5	000023	DMKAPT	DMKAPV	DMKCFG	DMKCPN	DMKHPT	DMKHVE	DMKLOH	DMKLOJ	DMKMON
		DMKTRC	DMKVCB	DMKVDT	DMKVMC					
SAVER6	000036	DMKACO	DMKCCH	DMKCFF	DMKCFG	DMKCSR	DMKCSW	DMKDRD	DMKDRE	DMKGRA
		DMKHVF	DMKIOF	DMKLNK	DMKLOH	DMKRSF	DMKSCO	DMKSNC	DMKSST	DMKVCV
		DMKVMC	DMKVSW	DMKVSX						
SAVER7	000028	DMKCCH	DMKCSW	DMKLNK	DMKLOH	DMKMSW	DMKPGM	DMKPTR	DMKQCN	DMKRSF
	000020	DMKTRK	DMKVCQ	DMKVCV	DMKVDE	DMKVSD	DMKVSG	DMKVSP	DMKVSQ	DMKVST
			DHILLOQ	DHRVOV	DHRYDL	DHKVJD	DHINY SO	DHAYOT	DHINYOQ	DRINGOT
CAVEDO	000076	DMKVSX	DMKOOL	DMI/COC	DMI/COU	DMIZOEO	DMVCED	DMI/CI/S		
SAVER8	000076	DMKBLD	DMKCCH	DMKCCS	DMKCCW	DMKCFQ	DMKCFR	DMKCKS	DMKCPZ	DMKDIB
		DMKHPT	DMKLNK	DMKQCN	DMKQCO	DMKSCO	DMKSEP	DMKSPL	DMKTCT	DMKTDK
		DMKTRK	DMKUNT	DMKVDG	DMKVDS	DMKVDT	DMKVSP	DMKVSQ	DMKVST	DMKVSW
		DMKZTD								
SAVER9	000041	DMKALG	DMKCCW	DMKCPT	DMKCPW	DMKCQP	DMKCQQ	DMKCSR	DMKGRT	DMKNEA
		DMKNET	DMKPGM	DMKSND	DMKSPL	DMKUSQ	DMKVCV	DMKVCX	DMKVDR	DMKVSP
		DMKVST	DMKVSW	DMKZTD						
		5111101	21111101							

DMKIUC

DMKIUE

DMKIUS DMKPTR DMKSVC DMKVCQ DMKVST

DMKCPP DMK1DR DMKMON DMKRPA DMKVCB DMKVFE

DMKLNK DMKVDS

DMKCPO DMKDAD DMKIOQ DMKNLD DMKSSS DMKVCP DMKPGM DMKLNM DMKLNB

DMKCKR DMKCSW DMKERM DMKLOJ DMKNLE

DMKQCN DMKSEL DMKTRD DMKVSF DMKVSF DMKQCN DMKVDE DMKSST DMKHVE DMKVDE

DMKSPL DMKVSW

DMKGRT DMKTRD DMKVSX

DMKNES DMKVSQ

DMKIUL

DMKIUJ

DMKIUG

DMKIUP

L	ABEL	COUNT	REFERENC	ES								
S	SAVESIZE	000036		DMKIUA DMKVCS	DMKLNK DMKVCT	DMKLNM DMKVDA	DMKQCO	DMKSTA	DMKSVC	DMKSWA	DMKVCP	DMKVCQ
9 9 9 9	SAVETRAC SAVEVAC SAVEWRK1	000007 000005 002746	DMKVCR DMKACO DMKACO DMKACO DMKCDB DMKCFQ DMKCFZ DMKCFZ DMKCSB DMKDAD DMKIOQ DMKIOQ DMKIOQ DMKIOQ DMKIOQ DMKIOQ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DMKCPZ DM	DMKCKF DMKALG DMKCDM DMKCPR DMKCPN DMKCQC DMKCQC DMKCQSF DMKDAS DMKJRL DMKFRN DMKSFB DMKSFB DMKSTR DMKSTR DMKVAU DMKVAU DMKVCV DMKVDR DMKVMD	DMKAPX DMKCDS DMKCPS DMKCQG DMKCQG DMKCQG DMKCQG DMKLOAU DMKEIG DMKLOAU DMKFIG DMKSIX DMKSIX DMKSIX DMKVCA DMKVCA DMKVCS DMKVDS DMKVME	DMKAPY DMKCFC DMKCFT DMKCPS DMKCQH DMKCQH DMKDEF DMKEPS DMKIUB DMKIUB DMKLNM DMKMSG DMKPER DMKSNC DMKTAP DMKTAP DMKTAP DMKVCB DMKVCSE	DMKATS DMKCFD DMKCFU DMKCQI DMKCQI DMKCQG DMKLCC DMKLOC DMKLOC DMKLOC DMKNEA DMKYCQ DMKTAQ DMKTAQ DMKTAQ DMKVCH DMKVCH DMKVCA DMKVSG	DMKBLD DMKCFF DMKCFV DMKCQP DMKCQP DMKCQF DMKDEI DMKBRC DMKFC DMKNES DMKRPA DMKRPA DMKTCS DMKTTX DMKVCP DMKVCP DMKVCDB DMKVSU	DMKCAC DMKCFG DMKCFY DMKCQQ DMKCQQ DMKCQU DMKDEX DMKHPS DMKLOH DMKNET DMKRSE DMKSRM DMKTCT DMKTTY DMKVCQ DMKVFD DMKVFD	DMKCAO DMKCFH DMKCFW DMKCQS DMKCQS DMKCQS DMKDIA DMKHPU DMKLOJ DMKNLD DMKRSS DMKRSS DMKTDK DMKVCR DMKVCR DMKVFE DMKXAB	DMKCCH DMKCFO DMKCKS DMKCQT DMKCQT DMKCQT DMKLOB DMKLIB DMKLOM DMKLOM DMKNLE DMKSST DMKVST DMKVTH DMKVCS DMKVDE DMKVFR DMKXAD	DMKCCW DMKCFP DMKCKT DMKCQY DMKCQY DMKCQY DMKIDR DMKIDP DMKFTR DMKSSU DMKTRA DMKSSU DMKTRA DMKVCT DMKVCT DMKVFS DMKXST
S	SAVEWRK2	001566	DMKZID DMKCCM DMKCCFQ DMKCFY DMKCSB DMKCSB DMKCSB DMKLIE DMKLUC DMKLOH DMKLOH DMKPTT DMKSSU DMKTHI DMKVST DMKVCT DMKVCT DMKVSD	DMKAPI DMKCDS DMKCFR DMKCQC DMKCQC DMKDAD DMKDIF DMKIUE DMKIUE DMKPEL DMKSEL DMKSEL DMKSEL DMKSEL DMKVAU DMKVAU DMKVCU DMKVSE	DMKATS DMKCFC DMKCFC DMKCPO DMKCQG DMKCSO DMKDAS DMKDRD DMKDRD DMKIUJ DMKFEN DMKFEN DMKSEP DMKSEP DMKSEP DMKSEP DMKVCV DMKVCV DMKVCV DMKVSF	DMKBIO DMKCFD DMKCFP DMKCPP DMKCSP DMKDAU DMKDRE DMKIOE DMKIUL DMKNON DMKPEQ DMKSNC DMKSTR DMKTRD DMKVCA DMKVCA DMKVCR DMKVSG	DMKBLD DMKCFF DMKCFU DMKCQI DMKCSQ DMKCSQ DMKDEF DMKIUN DMKIOF DMKIOF DMKIOF DMKSPL DMKSPL DMKSPL DMKSPL DMKVCB DMKVCB DMKVCB DMKVFC DMKVSP	DMKCAC DMKCFG DMKCFY DMKCPT DMKCSR DMKDEG DMKIUP DMKRCC DMKIUP DMKNEA DMKSPM DMKSPM DMKSPM DMKSVCH DMKVDC DMKVFE DMKVST	DMKCAO DMKCFH DMKCFY DMKCPU DMKCQQ DMKCST DMKDEI DMKHPS DMKIOQ DMKIUS DMKNES DMKRPD DMKSPT DMKSPT DMKSVCP DMKVCP DMKVCP DMKVFR DMKWRM	DMKCCH DMKCFJ DMKCKS DMKCPV DMKCQR DMKCSU DMKDEX DMKHPT DMKIOS DMKLNK DMKNET DMKSRM DMKRSE DMKSRM DMKTCS DMKUDR DMKVDE DMKVDE DMKVMC DMKXAB	DMKCCW DMKCFM DMKCFW DMKCQT DMKCSV DMKDGD DMKHPU DMKIOT DMKLNM DMKNLD DMKSSS DMKSSS DMKTCT DMKSSS DMKVCR DMKVDG DMKVMD DMKXAD	DMKCDB DMKCFO DMKCPX DMKCPX DMKCSW DMKCSW DMKDIA DMKLUB DMKLUB DMKLUB DMKLUB DMKLUB DMKLUB DMKLOG DMKNLE DMKSST DMKVCS DMKVCS DMKVCS DMKVDH DMKVME DMKXST
s	SAVEWRK3	000628	DMKAZO DMKCFC DMKCPB DMKCPZ DMKDAD DMKDRE DMKIOT DMKPEN DMKRPA DMKRPA DMKSWA	DMKAPI DMKCFD DMKCQG DMKDAS DMKDAS DMKJUC DMKILO DMKPEQ DMKRSE DMKSWM DMKUSP	DMKAPX DMKCFF DMKCPN DMKDAU DMKHPS DMKHPS DMKNSG DMKPER DMKRSQ DMKTCS DMKVCB	DMKATS DMKCFR DMKCPO DMKCQQ DMKDEF DMKHPT DMKNEA DMKPET DMKSEL DMKSEL DMKVCT	DMKBIO DMKCFS DMKCQS DMKDEG DMKHPU DMKIUL DMKNES DMKPGM DMKSNC DMKSNC DMKVCV	DMKCAC DMKCFT DMKCPS DMKCQT DMKDEI DMKHVD DMKLNK DMKNET DMKPGS DMKSSS DMKSSS DMKTHI DMKVCW	DMKCAO DMKCFU DMKCPT DMKCQY DMKDEX DMKHVE DMKNLO DMKNLD DMKPMA DMKSST DMKTRC DMKVDA	DMKCDB DMKCKR DMKCPV DMKCSO DMKDIA DMKHVF DMKLOH DMKNLE DMKPTR DMKSSU DMKSSU DMKTRK DMKVDB	DMKCDM DMKCKS DMKCFW DMKCST DMKDIB DMKIDR DMKNCC DMKPEI DMKQCN DMKSSV DMKTRP DMKVDC	DMKCDS DMKCKV DMKCRX DMKCSW DMKDRD DMKIOQ DMKPEL DMKQCO DMKSTR DMKUNT DMKVDD

L	ABEL	COUNT	REFERENC	CES								
			DMKVDE	DMKVDG DMK₩RM		DMKVDS DMKXAD	DMKVDT		DMKVFC	DMKVFD	DMKVMD	DMKVME
S	AVEWRK4	000724	DMKVSG DMKACO	DMKATS	DMKXAB DMKB10	DMKCAC	DMKXST DMKCAO	DMKZTD DMKCCW	DMKCDB	DMKCDM	DMKCDS	DMKCFC
0,		000724	DMKCFD	DMKCFG	DMKCEH	DMKCFO	DMKCES	DMKCET	DMKCFU	DMKCFV	DMKCFY	DMI/CI/D
			DMKCKS	DMKCKT	DMKCPB	DMKCPM	DMKCPN	DMKCPO	DMKCPP	DMKCPT	DMKCPU	DMKCPV
			DMKCPW	DMKCPY	DMKCQC	DMKCQG	DMKCQH	DMKCQI	DMKCQP	DMKCQQ	DMKCQY	DMKCSB
			DMKCFD DMKCKS DMKCPW DMKCSF	DMKCSO	DMKCPB DMKCQC DMKCSQ	DMKCFO DMKCFM DMKCQG DMKCST DMKCGD	DMKCPN DMKCQH DMKCSU	DMKCPO DMKCQI DMKCSV DMKDIB	DMKCFU DMKCPP DMKCQP DMKCSX DMKDRD	DMKDAD	DMKCCFY DMKCCFY DMKCQY DMKCQY DMKDAS DMKERM DMKIUE	DMKCRV DMKCSB DMKDAU DMKHPT
			DMKDEE	DMKDEG	DMKDFI	DMKDGD	DMKDIA	DMKDIB	DMKDRD	DMKDRE	DMKERM	DMKHPT
			DMKHPU	DMKHVD	DMKHVF	DMKTDR	DMKIOQ	DMKTOS	DMKIIIA	DMKIUC	DMKIUE	DMKIUL
			DMKIUP DMKNET DMKPST	DMKLNK DMKNLD	DMKLNM DMKNLE	DMKLOG DMKPE1	DMKLOH DMKPEL	DMKMCD	DMKMC I DMKPER	DMKCDM DMKCFV DMKCQQ DMKDAD DMKDAD DMKDRE DMKIUC DMKMSG DMKPET DMKSNC	DMKTGE DMKNEA DMKPGM DMKSPT DMKTCS DMKUSQ DMKVCX DMKVMD	DMKNES
				DMKNLD	DMKNLE	DMKPET		DMKPEQ DMKRSQ	DMKPER	DMKPEI		DMKNES DMKPGS DMKSRM DMKTCT DMKVCB DMKVDE DMKVME
			DMKSSS	DMKSST	DMKQCO DMKSSU	DMKQCQ DMKSTP	DMKRSF DMKSTR DMKTRK	DMKSWM	DMKTAP	DMKTAQ	DMKTCS	DMKTCT
			DMKSSS DMKTDK DMKVCH DMKVDG	DMKTHI	DMKTRC	DMKTRD	DMKTRK	DMKTRP	DMKUDR	DMKUSP	DMKUSQ	DMKVCB
			DMKVCH	DMKVCP DMKVDR	DMKVCQ	DMKVCR DMKVDT	DMKVCS	DMKVCT DMKVFC		DMKUSP DMKVCW DMKVFE	DMKVCX	DMKVDE
			DMKVDG	DMKVDR	DMKVDS	DMKVDT	DMKVER	DMKVFC	DMKVFD	DMKVFE	DMKVMD	DMKVME
			nmuven	DMKWRM	DMKVCQ DMKVDS DMKXAB	DMKXST	DMKVCS DMKVER DMKZTD					
S	AVEWRK5	000552	DMKCSD DMKATS DMKCFG DMKCPB DMKCSO	DMKCAC	DMKCAO	DMKCCH	DMKCDB	DMKCDM	DMKCDS	DMKCFC DMKCKS DMKCQQ DMKCSX	DMKCFD	DMKCFF
			DMKCFG	DMKCFH	DMKCFO DMKCPN	DMKCFS DMKCPS	DMKCFU DMKCPT	DMKCFV DMKCPW	DMKCFY DMKCQG	DMKCKS	DMKCKT	DMKCKV
			DMKCPB	DMKCPM DMKCSP	DMKCPN	DMKCPS	DMKCPT	DMKCPW	DMKCQG	DMKCQQ	DMKCQY	DMKCSB
			DMKCSO	DMKCSP	DMKCSQ DMKDIA	DMKCST	DMKCSU DMKD1F	DMKCSV DMKDRD	DMKCSW			
			DMKIDR	DMKIUA	DMKIUB	DMKIUE			DMKLNM			
			DMKMCC	DMKMID	DMKMSG	DMKNEA	DMKNES		DMKNLD		DMKCFD DMKCKT DMKCQY DMKDAD DMKHVE DMKLOJ DMKPEI DMKRSE	DMKCFF DMKCKV DMKCSB DMKDAS DMKHVF DMKLOM DMKPEL DMKSEP
			DMKMCC DMKPEQ DMKSNC	DMKPER	DMKPET	DMKPGM	DMKPGS DMKSTR	DMKPIR	DMKQCN	DMKQCO	DMKRSE	DMKSEP
			DMKSNC	DMKPER DMKSSS	DMKPET DMKSST	DMKPGM DMKSSU	DMKSTR	DMKSWA	DMKQCN DMKSWM	DMKTCS		DMKTHI DMKVCR
				DMKTRK	DMKTRP	DMKUNT	DMKURS	DMKUSQ	DMKVCH	DMKVCP	DMKVCQ	DMKVCR
			DMKVCS	DMKVCT	DMKVCU	DMKVCV DMKVDT	DMKVCW DMKVER	DMKUSQ DMKVCX DMKVFC	DMKVDA DMKVFD	DMKVDB	DMKVCQ DMKVDC DMKVMC	
			DMKTRCS DMKVDE DMKVDE DMKACO DMKCFG DMKCFG DMKCQT DMKCSX	DMKVCT DMKVDG DMKVSG	DMKVCU DMKVDS DMKXAB	DMKVDT	DMKVER	DMKVFC	DMKVFD	DMKHFOH DMKLOH DMKNLE DMKQCO DMKTCS DMKVCP DMKVDB DMKVFE	DMKVMC	DMKVMD
c	AVEWRK6	000522		DMKVSG DMKATS	DMKXAB	DMKXST DMKCAC	DMKZTD DMKCAO	DMI/COU	DMICOR		DMI/CEC	DMICEE
3/	AVEWRKO	000523	DMKACO	DMKCFH	DMKCFM	DMKCAC	DMKCFR	DMKCCH DMKCFS	DMKCDB DMKCKR	DMKCDM DMKCKS DMKCQH DMKCSU	DMKCFC DMKCKT DMKCQI DMKCSV DMKDIB DMKIUE	DMKCFF DMKCKV DMKCQQ DMKCSW DMKDIF DMKIUJ
			DMKCPM	DMKCPP	DMKCPS	DMKCFQ DMKCPT DMKCSF	DMKCPY	DMKCOC	DMKCOG	DMKCOH	DMKCQI	DMKCOO
			DMKCQT	DMKCQY	DMKCSB	DMKCSF	DMKCSO	DMKCQC DMKCSQ	DMKCQG DMKCST	DMKCSU	DMKCSV	DMKCSW
			DMKCSX	DMKDAD	DMKDAS DMKEPS	DMKDEF DMKERM	DMKDEG DMKHPU	DMKDE I DMK I DR	DMKDEX	DMKDIA	DMKDIB	DMKDIF
			עזעאויוע	DMKDRE	DMKEPS	DMKERM	DMKHPU	DMKIDR	DMKIOF	DMKIOQ	DMKIUE	DMKIUJ
			DMKIUL	DMKIUN	DMKLNK	DMKLNM	DMKLOG	DMKLOH DMKPGM	DMKMCD DMKPGS DMKTCS	DMKMC I DMKPTR	DMKNOO DMKRSF DMKTRC DMKVCH	DMKMSG DMKSNC DMKTRD DMKVCP
			DMKNEA	DMKNLD	DMKNLE	DMKPEQ DMKSTP DMKURS	DMKPET	DMKPGM	DMKPGS	DMKPTR	DMKRSF	DMKSNC
			DMKSSS DMKTRK	DMKSST DMKUDR	DMKSSU DMKUNT	DMKSTP	DMKSWA	DMKSWM DMKUSQ	DMKTCS	DMKIHI	DMKTRC	DMKIRD
				DMKUDK		DMKURS	DMKUSP	DMKUSQ DMKVDA				
			DMKVDT	DMKVER	DMKVEC	DMKVEF		DMKVMD	DMKVDC DMKVME	DMKTHI DMKVCB DMKVDD DMKVSD	DMKVDR DMKVSG	DMKVDS DMKVSP
			DMKVCR DMKVDT DMKWRM	DMKVCS DMKVER DMKXAB	DMKVCT DMKVFC DMKXST	DMKVCU DMKVFE DMKZTD		DINKIND	DHINTHE			
S	AVEWRK7	000445	DMKACO	DMKATS	DMKCAC	DMKCCH	DMKCCS DMKCFY DMKCPY	DMKCFD	DMKCFF	DMKCFG DMKCKT DMKCQQ DMKDAD DMKERM	DMKCFH	DMKCFO
			DMKCFQ DMKCPO	DMKCFS	DMKCFU DMKCPS	DMKCFV DMKCPT	DMKCFY	DMKCFD DMKCKR	DMKCKS	DMKCKT	DMKCKV	DMKCPN
			DMKCPO	DMKCPP	DMKCPS	DMKCPT	DMKCPY	DMKCQG	DMKCQP	DMKCQQ	DMKCQT	DMKCQY
			DMKCSB	DMKCSF	DMKCSO	DMKCSP	DMKCSQ	DMKCST	DMKCSW	DMKDAD	DMKDAS	DMKDEF
			DMKDEG	DMKDEI	DMKDIA	DMKDIB	DMKCSQ DMKDIF DMKIUC	DMKCQG DMKCST DMKDRD DMKIUE	DMKCFF DMKCQP DMKCQP DMKCSW DMKDRE DMKIUJ	DMKERM	DMKHPU	DMKHVF
			DMKIDR	DMKI OE DMKLOM	DMKIOG	DMK I OQ DMKM I D	DMKTUC	DMKTUE	DMKTUJ DMKNET	DMKIUL		
				DMKPCS		DMKSEP	DMKNLA	DMKNES	DMKSSS	DMKSST	DMKNLE	DMKSWM
			DMKLOH DMKPGM DMKTCS DMKVCR	DMKPGS DMKTRA	DMKQCN DMKTRC	DMKTRD	DMKTRK	DMKSPT	DMKSSS DMKUDR	DMKNLD DMKSST DMKUSP	DMKCFH DMKCKV DMKCQT DMKDAS DMKHPU DMKLNK DMKNLE DMKSSU DMKVDS DMKVDS DMKVSG	DMKCFO DMKCPN DMKCQY DMKDEF DMKLVF DMKLNM DMKPET DMKSWM DMKVCP
			DMKVCR	DMKVCT	DMKVCU	DMKVCV	DMKVCW	DMKVDA	DMKVDC	DMKVDD	DMKVDS	DMKVDT
			DMKVFR	DMKVFC	DMKVFD	DMKVMC	DMKVMD	DMKVDA DMKVME	DMKVDC DMKVSD	DMKVDD DMKVSE	DMKVSG	DMKXAB
			DMKXST DMKALG									
S	AVEWRK8	000696	DMKALG	DMKATS	DMKCAC	DMKCAO	DMKCCH	DMKCDB	DMKCDM	DMKCDS	DMKCFC	DMKCFF
			DMKCFG	DMKCFH	DMKCFO	DMKCFQ	DMKCFR DMKCPN	DMKCFS	DMKCFT	DMKCFU	DMKCFW	DMKCFY
			DMKCKR	DMKCKS	DMKCKV	DMKCPM	DMKCPN	DMKCPP	DMKCPS	DMKCPT	DMKCPU	DMKCPV

LABEL	COUNT	REFERENC	ES								
		DMKCPW DMKCSB DMKDAD DMKERM DMKIUN DMKNEA DMKSEL DMKSWA DMKURS DMKVDA	DMKCPY DMKCSF DMKDAS DMKHPS DMKIUP DMKNES DMKSEP DMKSEP DMKSWM DMKUSQ DMKVDC	DMKCPZ DMKCSO DMKDEF DMKHPU DMKLNK DMKNLD DMKSNC DMKTCS DMKVCH DMKVDD	DMKCQG DMKCSP DMKDEG DMKHVF DMKLNM DMKNLE DMKSND DMKTHI DMKVCP DMKVDS	DMKCQH DMKCSQ DMKDEI DMKIDR DMKLOG DMKPEL DMKSPL DMKTRC DMKVCR DMKVDT	DMKCQI DMKCST DMKDEX DMKIOQ DMKLOH DMKPEQ DMKPEQ DMKTRD DMKVCS DMKVER	DMKCQP DMKCSU DMKDIA DMKIUC DMKLOJ DMKPET DMKSSS DMKTRK DMKVCT DMKVFE	DMKCQQ DMKCSV DMKDIB DMKIUE DMKLOM DMKPGM DMKSST DMKTRP DMKVCV DMKVFR	DMKCQU DMKCŚW DMKDIF DMKIUJ DMKMHV DMKPMA DMKSSU DMKTXX DMKVCW DMKVMD	DMKCQY DMKCSX DMKDRD DMKIUL DMKMSG DMKPTR DMKSTP DMKUNT DMKVCX DMKVME
SAVEWRK9	000539	DMKVSD DMKATS DMKCFQ DMKCPW DMKDAS DMKEIG DMKIUL DMKNET DMKSEV DMKTRD DMKVCU DMKVCU DMKVCU	DMKVSE DMKBIO DMKCFR DMKCPY DMKDAU DMKERM DMKIUN DMKIUN DMKNLD DMKSIX DMKTTY DMKVCY DMKVCR	DMKVSG DMKBLD DMKCFS DMKCFS DMKCPZ DMKDEF DMK1UP DMKNLE DMKSNC DMKVCX DMKVCX DMKVMD	DMKWRM DMKCAC DMKCKS DMKCQP DMKDEG DMKHPU DMKIUS DMKPEQ DMKSPT DMKVDA DMKVDA DMKVME	DMKXAB DMKCAO DMKCPN DMKCQY DMKDEX DMKLQY DMKLNK DMKLOK DMKVCB DMKVCB DMKVSG	DMKCCH DMKCPO DMKCSP DMKIDR DMKIDR DMKINM DMKPGS DMKSST DMKVCH DMKVDC DMKVSQ	DMKCCW DMKCPP DMKCSQ DMKLOIA DMKLOJ DMKPST DMKSSV DMKVCP DMKVCP DMKVDD DMKXAB	DMKCFF DMKCPS DMKCST DMKIUC DMKIUC DMKFTR DMKSTP DMKVCR DMKVDE DMKVDE DMKVST	DMKCFG DMKCPT DMKCSV DMKDIF DMKIUE DMKNEA DMKSEL DMKTCS DMKVCS DMKVDR DMKZTD	DMKCFH DMKCPU DMKDAD DMKDRD DMKIUJ DMKNES DMKSEP DMKTRC DMKVCT DMKVDS
SAVFPRES SAVGREGS SAVKEYS SAVNAME SAVPSW SAVREG14 SAVTABLE SAVTIME	000004 000004 000004 000003	DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF DMKCFF	DMKCFH DMKCFH DMKCFH DMKCFH DMKCFH DMKCFH DMKCFH	DMKCKM DMKCKM DMKCKM DMKCKM DMKCKM DMKCFS DMKCFS	DMKCQY	DMKCQY					·
SAVUSER SBA SCAN SCCBCTL SCCBIBLK SCCBIFE SCCBIMAP SCCBINUM	000004 000062 000024 000002 000003 000001 000002 000002	DMKCFH DMKGRD DMKDDR DMKMHV DMKMHC DMKMHC DMKPST DMKPST DMKMHC	DMKCFS DMKGRE DMKDID DMKPST DMKXST DMKXST	DMKCKM DMKGRG DMKDIR	DMKFMT	DMKLOC	DMKSSP	DMKSSV	DMKVDC		
SCCBISIZ SCCBKMAX SCCBLEN SCCBLOK SCCBMDRT SCCBMODP SCCBMXID	000006 000012 000001 000001	DMKMHC DMKMHV DMKMHC DMKMHV DMKMHV DMKMHC	DMKPST DMKPST DMKMHV	DMKVFC DMKPST	DMKXST DMKVFC	DMKXST					
SCCBRESP SCCBRMAX SCCB0010 SCCB0020 SCCB0100 SCCB0120 SCCB0120	000015 000002 000007 000003 000001 000002 000001	DMKMHC DMKMHC DMKMHC DMKMHV DMKMHV DMKVFC DMKVFC	DMKMHV DMKMHV DMKMHV DMKVFC	DMKPST DMKPST	DMKVFC DMKXST	DMKXST					
SCHEDCL SCRCMDR SDRBLOK SDRBSIZE	000001 000011 000006	DMKMCC DMKCFW DMKIOE DMKIOE	DMKGRD DMK10F	DMKLOM DMK I OJ	DMKQCN	DMKQCQ	DMKRGB				
SDRCPID SDRCTRS	000002 000016	DMKIOE	DMKIOF	DMKIOJ							

Y2	LABEL	COUNT	REFERENC	ES
LY20-0897-7 © Copyright IBM Corp. 1982, 1987	SDRCTR8 SDRCTR9 SDRCUA SDRFLAGS	000003 000001 000003 000009	DMKIOF DMKIOF DMKIOE DMKIOE	DMKIOF DMKIOJ
© Copy	SDRFLCT SDRLNGTH SDRMAX SDROVFWK	000004 000009 000003 000006	DMKIOF DMKIOE DMKIOF DMKIOF	DMKIOF
/right I	SDRPRMCT SDRRDEV SDRRECD SDRSHRT	000004 000002 000005 000009	DMKIOF DMKIOF DMKIOE DMKIOE	DMKIOF DMKIOF
BM Co	SDRSIZE SDRSIZE1 SECUSER SEEKOFF	000001 000001 000006 000002	DMKIOF DMKIOF DMKGRD DMKCCD	DMKQCN
rp. 198;	SEGENQ SEGFLAG SEGINV	000006 000013 000064	DMKBLD DMKATS DMKATS DMKHVD	DMKSTR DMKPGM DMKBLD DMKPGS
2, 1987	SEGMIG SEGMIGPG SEGMIGPP	000007 000002 000002	DMKVFR DMKCKM DMKPGM DMKPGM	DMKVMA DMKPGM DMKSTR DMKSTR
	SEGPAGE	000146	DMKATS DMKPGM DMKSWM DMKBLD	DMKBLD DMKPGS DMKVMA DMKSTR
	SEGPROT SEGTABLE SEG1M SELDATA	000022 000024 000002 000005	DMKATS DMKATS DMKPRV DMKCQC	DMKCFF DMKBLD DMKPTT DMKTRP
	SELDISP SELECT SELENTRY	000014 000030 000012	DMKCQC DMKCQC DMKCQC	DMKTRP DMKGRD DMKTRP
	SELFORW SELLTH SELRB SELRSP	000010 000005 000002 000002	DMKCQC DMKCQC DMKGRD DMKGRE	DMKTRP DMKTRP
	SELRM SELRMP SELSIZE SELWRT	000001 000002 000004 000002	DMKGRD DMKGRE DMKTRP DMKGRD	DMKTRR
	SENDOMSG SENSE	000006 000123	DMKIUE DMKCCD DMKFMT	DMKIUG DMKCCO DMKOPR
CP Di	SENSEA4 SENSEE4 SENSEID SENSEI0	000001 000005 000004 000001	DMKCCO DMKCCO DMKGRF DMKCCT	DMKCCS
CP Directories	SENSEPID SENSE04 SENSE44 SEQMSK	000004 000002 000001 000007	DMKCCO DMKCCO DMKCCO DMKFRE	DMKCCS DMKCCS
<b>M</b>	SETID	000002	DMKCCO	DMILOVE

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SURRDEV	000002	DMKIOF				
SDRRECD	000005	DMKIOE	DMKIOF	DMKIOJ		
SDRSHRT	000009	DMKIOE	DMKIOF	DMK10J		
SDRSIZE	000001	DMKIOF				
SDRS IZE1	000001	DMKIOF				
SECUSER			DMIZOON	DMILOCO	DMKQCQ	
	000006	DMKGRD	DMKQCN	DMKQCO	UNINGCO	
SEEKOFF	000002	DMKCCD				
SEGENQ	000006	DMKBLD	DMKSTR			
SEGFLAG	000013	DMKATS	DMKPGM	DMKPGS	DMKPSA	DMKSEL
SEGINV	000064	DMKATS	DMKBLD	DMKCFF	DMKCFG	DMKCFH
0201111	000004	DMKHVD	DMKPGS	DMKPMA	DMKPSA	DMKPTR
		DMKVFR	DMKVMA	Dentring	Drifti OA	Dritti III
COMIO	000007			DMUCTO	DMKSWM	
SEGMIG	000007	DMKCKM	DMKPGM	DMKSTR	DMKSWM	
SEGMIGPG	000002	DMKPGM	DMKSTR			
SEGMIGPP	000002	DMKPGM	DMKSTR			
SEGPAGE	000146	DMKATS	DMKBLD	DMKCFF	DMKCFG	DMKCKM
		DMKPGM	DMKPGS	DMKPMA	DMKPTR	DMKPTS
		DMKSWM	DMKVMA		2	
SEGPLEN	000007	DMKBLD	DMKSTR	DMKVMA		
					DMU/DDU	
SEGPROT	000022	DMKATS	DMKCFF	DMKPGS	DMKPRW	DMKPSA
SEGTABLE	000024	DMKATS	DMKBLD	DMKCFG	DMKCKM	DMKCKP
SEG1M	000002	DMKPRV	DMKPTT			
SELDATA	000005	DMKCQC	DMKTRP	DMKTRR	DMKTRT	
SELDISP	000014	DMKCQC	DMKTRP	DMKTRR	DMKTRT	
SELECT	000030	DMKCQC	DMKGRD	DMKTRP	DMKTRR	DMKTRT
SELENTRY	000012	DMKCQC	DMKTRP	DMKTRR	DMKTRT	Dimente
SELFORW	000010	DMKCQC	DMKTRP	DMKTRR	DMKTRT	
SELLTH	000005	DMKCQC	DMKTRP	DMKTRR	DMKTRT	
SELRB	000002	DMKGRD				
SELROP	000002	DMKGRE				
SELRM	000001	DMKGRD				
SELRMP	000002	DMKGRE				
SELSIZE	000004	DMKTRP	DMKTRR			
			DERTUN			
SELWRT	000002	DMKGRD	<b>D</b> 111110		<b>D1</b> 11110	
SENDQMSG	000006	DMKIUE	DMKIUG	DMKIUN	DMKIUS	
SENSE	000123	DMKCCD	DMKCCO	DMKCCT	DMKCCW	DMKCPP
		DMKFMT	DMKOPR	DMKOVR	DMKSAD	DMKSSP
SENSEA4	000001	DMKCCO				
SENSEE4	000005	DMKCCO	DMKCCS			
SENSEID	000004	DMKGRF	UNIXOOO			
SENSEIO	000001	DMKCCT				
SENSEPID	000004	DMKCCO	DMKCCS			
SENSE04	000002	DMKCCO	DMKCCS			
SENSE44	000001	DMKCCO				
SEQMSK	000007	DMKFRE				
SETID	000002	DMKCCO				
SETTOK	000002	DMKACO	DMKCKF			
SETUP	000075	DMKACO	DMKCKF			
SETUP1	000002	DMKACO	DMKCKF			

DMKIOJ

DMKSTR DMKCKM DMKPTS

DMKCKP DMKPTT

DMKVAT DMKPGM

DMKDDR DMKVM I

DMKSWM DMKCKW DMKPTT

DMKDAD DMKSEG

DMKVAU DMKPGS

DMKD I R DMKVRR

DMKDAD DMKSEL

DMKDAS DMKSEL

DMKPSA

DMKDMP DMKVRS

DMKDAS DMKSTR

DMKDAU DMKSTR

DMKVMA

DMKDMQ

DMKDAU DMKSWA

DMKHVD DMKSWA

DMKEXT

LABEL	COUNT	REFERENCI	ES								
SETUP2	000004	DMKACO	DMKCKF								
SEVER	000055	DMKAPS	DMKAPT	DMKAPV	DMKBIO	DMKCRM	DMKIDR	DMKIOF	DMKIUJ	DMKIUP	DMKMSG
		DMKRPI	DMKVCW	DMKVCX	DMKVMG						
SF	000059	DMKBOX	DMKGRD	DMKQCQ							
SFBACNT	000002	DMKACO	DMKCQI								
	000016	DMKAPS	DMKAPU	DMKAPV	DMKCQH	DMKCQ I	DMKCSV	DMKCSW	DMKCSY	DMKSPS	DMKWRM
SFBCDMP	000003	DMKIDU	DMKWRN								
	000002	DMKCKS	DMKIDU		DMILOUE	DMI/OOU	DMIZOOL	DWYOCO	DHIVOOD	DMILOCH	DMILOCU
SFBCLAS	000039	DMKACO	DMKAPU	DMKAPV	DMKCKF DMKHVF	DMKCQH DMKMIA	DMKCQI	DMKCSQ DMKRSP	DMKCSR DMKRST	DMKCSV DMKSEP	DMKCSW DMKSPL
		DMKCSX DMKSPS	DMKCSY DMKSPT	DMKDRD DMKTRT	DMKTRU	DMKURS	DMKVME	DMKVSP	DMKVSQ	DMKVSW	UMKSPL
SFBCONTO	000003	DMKCKF	DMKWRN	DHKINI	DEKTING	DHKUNS	DHKYHL	DHKYSF	DHILADA	DHIL	
SFBCONV	000016	DMKAPU	DMKAPV	DMKCQI	DMKCSV	DMKCSW	DMKCSY	DMKRSP	DMKSPS	DMKWRM	DMKXAB
SFBCOPY	000040	DMKACO	DMKAPU	DMKAPV	DMKCKF	DMKCKR	DMKCQI	DMKCSF	DMKCSW	DMKDRD	DMKHVF
0100011	000040	DMKMIA	DMKNLE	DMKRSP	DMKSPL	DMKTCS	DMKTRT	DMKTRU	DMKURS	DMKVME	
SFBDATE	000023	DMKACO	DMKCKH	DMKCKV	DMKCQI	DMKDMP	DMKMIA	DMKNLE	DMKSAD	DMKSPL	DMKTRT
		DMKTRU	DMKVMD	DMKVME	DMKWRM						
SFBDEST	000031	DMKAPU	DMKAPV	DMKCKF	DMKCQH	DMKCQI	DMKCSQ	DMKCSV	DMKCSW	DMKCSY	DMKHVF
		DMKRSP	DMKSEP	DMKSPL	DMKSPS	DMKSPT	244/2014	DHIVDHD	DUUUUUE	DAUGALLA	
SFBDIST	000045	DMKACO	DMKCKF	DMKCKR	DMKCQI	DMKCSQ	DMKCSW	DMKDMP	DMKHVF	DMKMIA	DMKNLE
CEDDUMD	000010	DMKSAD	DMKSEP	DMKSPK DMKCSY	DMKSPL DMKDMP	DMKTRT DMKDRD	DMKTRU DMKNLE	DMKURS DMKSPK	DMKVME DMKSPS	DMKTRR	DMKTRT
SFBDUMP	000018	DMKCKV DMKTRU	DMKCQI DMKVME	DMKVSW	DPIKUPIP	DMKDKD	DHINNLE	DHKSFK	DMK3F3	Derchan	DERTIT
SFBEOF	000015	DMKCKR	DMKDRD	DMKVSW	DMKVSX	DMKWRM					
SFBFCB	000009	DMKRSP	DMKVSP	DMKVSQ	Dimerore	Britana					
SFBFCBNL		DMKRSP	DMKVSQ								
SFBFCBXL		DMKRSP	DMKVSQ								
SFBFILID	000103	DMKAPT	DMKAPU	DMKAPV	DMKAPW	DMKCFU	DMKCKS	DMKCKT	DMKCKV	DMKCQI	DMKCSQ
		DMKCSR	DMKCST	DMKCSV	DMKCSW	DMKCSY	DMKDRD	DMKDRE	DMKHVF	DMKMIA	DMKMNJ
		DMKNLE	DMKRSE	DMKRST	DMKSEP	DMKSFB	DMKSPK	DMKSPL	DMKSPS	DMKTCS	DMKTCT
		DMKTRT	DMKTRU	DMKURS	DMKVME	DMKVSD	DMKVSF	DMKVSG	DMKVSP	DMKVST	DMKVSW
COPUSA	000000	DMKWRN DMKCKF	DMKXAB DMKSPK								
SFBF1RST SFBFLAG	000198	DMKAPS	DMKAPU	DMKAPV	DMKCKH	DMKCKR	DMKCKV	DMKCQH	DMKCQI	DMKCQR	DMKCSF
SFDFLAG	000190	DMKCSQ	DMKCSR	DMKCST	DMKCSV	DMKCSW	DMKCSX	DMKCSY	DMKDRD	DMKDRE	DMKHVF
		DMKMIA	DMKNLE	DMKRSE	DMKRSP	DMKRSQ	DMKSFB	DMKSPK	DMKSPL	DMKSPS	DMKSPT
		DMKTRR	DMKTRT	DMKTRU	DMKUSP	DMKVME	DMKVSP	DMKVSU	DMKVSW	DMKVSX	DMKWRM
		DMKXAB									
SFBFLAG2	000054	DMKCKF	DMKCKH	DMKCKR	DMKCKV	DMKCQI	DMKCSO	DMKCSQ	DMKCSY	DMKDRD	DMKMIA
		DMKRSP	DMKSEP	DMKSPK	DMKSPL	DMKVSP	DMKVSQ	DMKVSW	DMKWRM		0.000
SFBFLAG3	000045	DMKACO	DMKAPU	DMKCQI	DMKCSF	DMKCSY	DMKDRD	DMKDRE	DMKHVF	DMKRSP	DMKVSP
	000100	DMKVSQ	DMKVSX	DMIADV	DMKCQH	DMKCQI	DMKCST	DMKCSV	DMKCSW	DMKCSY	DMKDRD
SFBFLAG4	000108	DMKAPS DMKDRE	DMKAPU DMKHVF	DMKAPV DMKMIA	DMKRSP	DMKSPK	DMKSPS	DMKSPT	DMKVMD	DMKVSD	DMKVSE
		DMKVSQ	DMKWRM	DMKWRN	DMKXAB	DMKXAD	Difficor o	DIAKOTI	Driktino	Drifty OD	DIIIXVOL
SFBFLAG5	000006	DMKCKF	DMKIDU	DMKWRN	51110018	01110010					
SEBELASH		DMKAPU	DMKCQI	DMKCSW	DMKHVF	DMKRSP	DMKSPK	DMKSPL			
SFBFLNMT	000003	DMKVSP	DMKVSQ								
SFBFNAME	000025	DMKACO	DMKCKH	DMKCQI	DMKCSQ	DMKCSW	DMKHVF	DMKMIA	DMKNLE	DMKRSP	DMKRST
		DMKSPL	DMKSPS	DMKTRT	DMKTRU	DMKVMD	DMKWRM	DMUTOU	D11/0 /01D	D14/0 015	
SFBFTYPE		DMKCKH	DMKCQI	DMKM!A	DMKNLE	DMKRST	DMKTRT	DMKTRU	DMKVMD	DMKVME	DMKWRM
SFBHOLD	000011	DMKCSQ DMKAPS	DMKSPL DMKAPU	DMKVSP DMKAPV	DMKVSW DMKCKR	DMKCQH	DMKCQI	DMKCSQ	DMKCSR	DMKCST	DMKCSV
SFBINUSE	000073	DMKCSW	DMKCSY	DMKDRD	DMKDRE	DMKMIA	DMKRSP	DMKSFB	DMKSPS	DMKSPT	DMKUSP
		DMKVSW	DMKWRM	DMKXAB	DHINDINE		Brittion	5 mor 5	DIROT 0	DINGTI	Difficult
SFBINVS	000014	DMKCSV	DMKCSY	DMKDRD	DMKDRE	DMKVSD	DMKVSE	DMKWRN			
SFBLAST	000096	DMKACO	DMKCFU	DMKCKF	DMKCKH	DMKCKV	DMKCSV	DMKCSW	DMKCSY	DMKDMP	DMKDRD
		DMKDRE	DMKMIA	DMKNLE	DMKRSP	DMKRST	DMKSPK	DMKSPL	DMKSPS	DMKTRT	DMKVME

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LABEL COUNT REFERENCES

SFBLDBEG SFBLDMID SFBLOK		DMKVSP DMKAPU DMKAPU DMKACO DMKCKS DMKCST DMKIDU DMKSFB	DMKVSQ DMKCQI DMKCQI DMKAPS DMKCKT DMKCSU DMKMIA DMKSPK	DMKVSX DMKCSF DMKCSF DMKAPT DMKCKV DMKCSV DMKMNJ DMKSPL	DMKHVF DMKHVF DMKCQH DMKCSW DMKNLE DMKSPS DMKVMD	DMKRSP DMKRSP DMKCQI DMKCSX DMKCSX DMKRSE DMKSPT DMKVME	DMKVSP DMKVSP DMKAPW DMKCQR DMKCSY DMKRSP DMKTCS	DMKVSX DMKVSX DMKCFU DMKCSF DMKCMP DMKRSQ DMKTCT DMKVSE	DMKCKF DMKCSO DMKDRD DMKRST DMKTRP DMKVSF	DMKCKH DMKCSQ DMKDRE DMKSAD DMKTRR DMKVSG	DMKCKR DMKCSR DMKHVF DMKSEP DMKTRT DMKVSP
SFBMISC1 SFBMON SFBNOHLD SFBNORET	000010 000014	DMKTRU DMKVSQ DMKCKV DMKCKR DMKCQH DMKCSY	DMKURS DMKVST DMKDRD DMKCKV DMKCQ1 DMKVMD	DMKUSP DMKVSU DMKSPK DMKCQ I DMKCSQ	DMKVMD DMKVSW DMKSPS DMKCSY DMKSPL	DMKVME DMKVSX DMKTRR DMKDRD DMKVSW	DMKVSD DMKWRM DMKTRT DMKMIA	DMKVSE DMKWRN DMKTRU DMKSPK	DMKVSF DMKXAB DMKVME DMKVSW	DMKVSG DMKXAD DMKVSQ	UMKVSP
SFBOFORM		DMKACO DMKCSY DMKVMD	DMKAPU DMKMIA DMKWRM	DMKAPV DMKNLE	DMKCKF DMKRSP	DMKCKH DMKSEP	DMKCKR DMKSPL	DMKCQH DMKSPS	DMKCQI DMKSPT	DMKCSV DMKTRT	DMKCSW DMKTRU
SFBOPEN SFBORIG SFBPNT	000007 000029 000199	DMKCKR DMKACO DMKNLE DMKAPS	DMKDRD DMKCFU DMKRST DMKAPT	DMKVSW DMKCKF DMKSAD DMKAPU	DMKVSX DMKCKS DMKSEP DMKAPV	DMKWRM DMKCQI DMKSPL DMKCKF	DMKCSY DMKTRT DMKCKH	DMKDRD DMKTRU DMKCKR	DMKDRE DMKVME DMKCKV	DMKIDU DMKVSF DMKCQH	DMKMIA DMKWRN DMKCQI
		DMKCST DMKRSQ DMKVSF	DMKCSV DMKRST DMKVSG	DMKCSW DMKSFB DMKVSP	DMKCSÝ DMKSPK DMKVSQ	DMKDMP DMKSPL DMKWRM	DMKDRD DMKSPS DMKXAB	DMKDRE DMKURS	DMKM I Å DMKVME	DMKRSE DMKVSD	DMKRSP DMKVSE
SFBPURGD SFBPURGE SFBRECER	000009	DMKAPU DMKCKF DMKCKH DMKVSX	DMKCQH DMKCKH DMKCKR DMKWRM	DMKCQ I DMKSPK DMKCSF	DMKCSV DMKVSP DMKRSE	DMKCSW DMKVSQ DMKRSP	DMKCSY DMKWRM DMKRSQ	DMKWRM DMKSPK	DMKXAB DMKVSP	DMKVSU	DMKVSW
SFBRECNO		DMKÁCO DMKSEP DMKVSW	DMKCKF DMKSPK	DMKCKV DMKSPS	DMKCQ I DMKTRP	DMKHVF DMKTRT	DMKMIA DMKURS	DMKNLE DMKVME	DMKRSP DMKVSP	DMKRST DMKVSQ	DMKSAD DMKVST
SFBRECOK SFBRECS SFBRECSZ SFBREQUE SFBRSTRT	000035 000015 000008	DMKCSF DMKCKF DMKACO DMKCSO DMKCKR	DMKRSP DMKCKH DMKNLE DMKRSP DMKRSP	DMKRSQ DMKCKR DMKSPL DMKSPL DMKSEP	DMKCKS DMKTRT DMKSPL	DMKRSP DMKTRU DMKWRM	DMKRSQ DMKVME	DMKSPK DMKVSP	DMKSPS DMKVST	DMKVSX DMKVSX	DMKWRM
SFBSEEN SFBSHOLD	000009	DMKCSY DMKAPU DMKRSE	DMKDRD DMKAPV DMKRSP	DMKDRE DMKCQH DMKSPL	DMKCQ I DMKSPS	DMKCQR DMKSPT	DMKCSF	DMKCSQ	DMKCSW	DMKCSY	DMKHVF
SFBSIZE	000111	DMKACO DMKCSW DMKSPK DMKVSW	DMKAPT DMKCSY DMKSPL DMKWRM	DMKCKF DMKDMP DMKSPS DMKWRN	DMKCKH DMKDRD DMKTRT	DMKCKR DMKDRE DMKTRU	DMKCKS DMKIDU DMKUSP	DMKCKT DMKMIA DMKVMD	DMKCPI DMKNLE DMKVME	DMKCSQ DMKRSP DMKVSD	DMKCSV DMKRST DMKVSP
SFBSIZEB SFBSP3 SFBSTART	000001	DMKCKR DMKWRM DMKACO DMKCSW	DMKCKT DMKAPT DMKCSY	DMKCKV DMKAPU DMKDMP	DMKCSY DMKCFU DMKDRD	DMKSFB DMKCKF DMKDRE	DMKVSE DMKCKH DMKHVF	DMKVSF DMKCKV DMKIDU	DMKWRM DMKCQI DMKMIA	DMKWRN DMKCST DMKNLE	DMKCSV DMKRSP
SFBSTCPY		DMKCSW DMKRST DMKVSP DMKSPL	DMKSEP DMKVSQ DMKTCS	DMKDMP DMKSPK DMKVST	DMKDRD DMKSPL DMKVSW	DMKSPS	DMKTCS	DMKTRR	DMKTRT	DMKTRU	DMKVME
SFBSYSID	000089	DMKACO DMKCSV DMKSPK DMKVSW	DMKCFU DMKCSW DMKSPL DMKWRM	DMKCKF DMKCSY DMKSPS DMKWRN	DMKCKH DMKDRD DMKSPT	DMKCKR DMKDRE DMKTRU	DMKCKS DMKIDU DMKVME	DMKCKT DMKMIA DMKVSD	DMKCQH DMKNLE DMKVSE	DMKCQI DMKRST DMKVSP	DMKCSR DMKSFB DMKVSQ
SFBTICER SFBTIME	000004 000033	DMKRSP DMKACO DMKVMD		DMKCQ I DMKVSP	DMKDMP DMKVSQ	DMKMIA	DMKNLE	DMKSAD	DMKSPL	DMKTRT	DMKTRU
SFBTUSE	000041	DMKCKR	DMKCQI	DMKCST	DMKCSW	DMKCSY	DMKDRD	DMKDRE	DMKHVF	DMKMIA	DMKRSP

LABEL	COUNT	REFERENC	ES								
SFBTYPE	000044	DMKSPS DMKACO DMKNLE DMKVSQ	DMKSPT DMKAPU DMKRSP DMKVSX	DMKWRM DMKAPV DMKSPK DMKXAB	DMKXAB DMKCKT DMKSPL	DMKCQI DMKTRT	DMKCSY DMKTRU	DMKDMP DMKURS	DMKDRD DMKVME	DMKHVF DMKVSG	DMKMIA DMKVSP
SFBUFORM	000035	DMKACO	DMKAPV	DMKCKF	DMKCKR	DMKCQH	DMKCQI	DMKCSQ	DMKCSV	DMKCSW	DMKCSY
SFBUHOLD	000052	DMKHVF DMKAPU DMKHVF	DMKMIA DMKAPV DMKRSP	DMKNLE DMKCKR DMKSPL	DMKSPL DMKCQH DMKSPS	DMKTRT DMKCQI DMKSPT	DMKTRU DMKCSR DMKVSW	DMKVMD DMKCSW DMKXAB	DMKCSX	DMKCSY	DMKDRD
SFBUSER	000153	DMKACO DMKCSV DMKRST DMKTRU	DMKAPT DMKCSW DMKSAD DMKURS	DMKCFU DMKCSY DMKSEP DMKVMD	DMKCKF DMKDRD DMKSFB DMKVME	DMKCKT DMKDRE DMKSPK DMKVSD	DMKCKV DMKHVF DMKSPL DMKVSE	DMKCQH DMKIDU DMKSPS DMKVSG	DMKCQI DMKMIA DMKTCS DMKVSW	DMKCSQ DMKNLE DMKTCT DMKWRN	DMKCST DMKRSP DMKTRT DMKXAB
SFBVLEN	000001	DMKVSQ									
SFBXAB SFBXABER	000021 000005	DMKCSV DMKSPK	DMKCSW DMKWRM	DMKCSY DMKXAD	DMKSPK	DMKSPS	DMKWRM	DMKXAB	DMKXAD		
SFBXABL SFBXFER SFLAG	000014 000001 000009	DMKCSV DMKCSY DMKXAB	DMKCSW	DMKCSY	DMKSPK	DMKXAB	DMKXAD				
SFPBLOK SFPBOT SFPCLASS	000002	DMKSEP DMKVSQ DMKSEP	DMKVSQ DMKVSQ								
SFPEND	000002	DMKSEP	DMKVSQ								
SFPEND SFPLEN	000005	DMKSEP	DMKVSQ								
SFPOPTS SFPTITLE SFPTOP	000002	DMKVSQ DMKSEP DMKVSQ	DMKVSQ								
SHPSLIM	000003	DMKCCD	DMKCCO	DWV000	011/001/	DMIKODI	DMUUDM	DMUUDA			
SHQBLOK SHQBSIZE SHQCKPMP	000014 000011 000001	DMKCKS DMKCKF DMKCSQ	DMKCQR DMKCKS	DMKCSQ DMKCKV	DMKCSY DMKCSQ	DMKSPL DMKWRM	DMKWRM	DMKWRN			
SHQCKPT SHQFLAGS	000004 000001	DMKCKS DMKCSQ	DMKCSQ	DMKWRN							
SHQPNT SHQSHOLD	000003	DMKCSQ DMKCQR	DMKCSY DMKCSQ	DMKSPL DMKCSY	DMKSPL						
SHQUSER	000007	DMKCQR	DMKCSQ	DMKCSY	DMKSPL						
SHRBPNT	000007	DMKATS	DMKCFF	DMKCFH	DMKPGS						
SHRFLAG	000035	DMKATS DMKPTS	DMKCCW DMKPTT	DMKCFF DMKSEL	DMKCPP	DMKCPU	DMKPGS	DMKPRV	DMKPRW	DMKPSA	DMKPTR
SHRFPNT SHRLKCNT	000022 000036	DMKATS DMKAPI	DMKCFF DMKCCW	DMKCFH DMKCPM	DMKCPP DMKCPO	DMKCPU DMKCPS	DMKPGS DMKCPU	DMKDGD			
SHRNAME SHRNOPRT	000022	DMKATS DMKATS	DMKCFF DMKCCW	DMKCFG DMKCFF	DMKCFH DMKCPP	DMKCPP DMKCPU	DMKPGS DMKPGS	DMKVMA DMKPRV	DMKPRW	DMKPSA	DMKPTR
		DMKPTS	DMKPTT	DMKSEL DMKCPP	DMKCPU	Drikor o	brind 66	Dintritt	Dintriti	Diniti Ort	DIRGING
SHRPAGE SHRSEGCT	000025	DMKATS	DMKCFF	DMKCPP	DMKCPU	DMKPGS	DMKPTR	DMKPTS	DMKVMA		
SHRSEGNM	000019	DMKATS	DMKCFF	DMKCPP	DMKCPU	DMKPGS	DMKVMA				
SHRSGPRT SHRTABLE		DMKATS DMKATS DMKPSA	DMKCFF DMKCCW DMKPTR	DMKCPP DMKCFF DMKPTS	DMKCPU DMKCFG DMKPTT	DMKPGS DMKCFH DMKSEL	DMKPTR DMKCPP DMKVMA	DMKPTS DMKCPU	DMKPTT DMKPGS	DMKSEL DMKPRV	DMKPRW
SHRTSIZE SHRUSECT SIGAPR SIGCLK		DMKATS DMKATS DMKMCT DMKCLK	DMKCFF DMKCFF	DMKPGS DMKPGS	•••••						
SIGCR SIGDISP	000002 000004	DMKCFP DMKDSP	DMKDMP DMKEXT	DMKFRE	DMKSTK						
SIGEMS	000016	DMKACO DMKSEL	DMKCLK	DMKCPO	DMKCPS	DMKEXT	DMKFRE	DMKMPO	DMKPMA	DMKPTR	DMKPTT
SIGEXT	000002	DMKEXT	DMKPTR								

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LABEL	COUNT	REFERENC	ES								
SIGIPR SIGMASK SIGPR	000006 000001 000001	DMKCKP DMKDSP DMKACS	DMKCPI	DMKCPP	DMKCPU	DMKDMP					
SIGQUI SIGRES	000009 000010	DMKACO DMKACO	DMKCLK DMKCLK	DMKCPO DMKCPP	DMKCPS DMKEXT	DMKEXT DMKPTR	DMKPTR DMKPTT	DMKPTT DMKSEL	DMKSEL		
SIGREST	000013	DMKAPI	DMKCCH	DMKCKD	DMKDMP	DMKDMQ	DMKMCT	DMKMPO	DMKVRS		
SIGSAVE SIGSENSE	000010 000021	DMKCKW DMKCFP DMKLOJ	DMKEXT DMKCKD DMKMHC	<b>ДМКСКР</b> ДМКМРО	DMKCP I DMKVRR	DMKCPP	DMKCPS	DMKCPU	DMKDMP	DMKDMQ	DMKEXT
SIGSHD SIGSSS	000001 000011	DMKCPS DMKCCH	DMKCKD	DMKCPI	DMKDMP	рмкмст	<b>ДМКМРО</b>				
SIGSTART SIGSTOP	000007 000010	DMKCPP DMKCCH	DMKLOJ DMKCPO	DMKMCH	DMKMHC DMKDMP	DMKMPO DMKMCH	DMKVRR DMKMCT	DMKMHC	<b>DMKMPO</b>		
SIGSYNC SIGWAKE	000001 000010	DMKCLK DMKCPI	DMKCPU	DMKEXT	DMKFRE	DMKIOS	DMKMOO	DMKSCH			
SIGXC	000026 000001	DMKACO DMKMOO DMKFRE	DMKCLK DMKMPO	DMKCPI DMKPMA	DMKCPP DMKPTR	DMKCPU DMKPTT	DMKDSP DMKSCH	DMKEXT DMKSEL	DMKFRE DMKSTK	DMKIOS	DMKMCT
SILI	002279	DMKACO	DMKACS	DMKBSC	DMKCAC	DMKCCD	DMKCCT	DMKCCW	DMKCFQ	DMKCKD DMKCPW	DMKCKH DMKCPZ
		DMKCKN DMKCQT	DMKCKP DMKCSB	DMKCNS DMKCSC	DMKCPB DMKDAD	DMKCPI DMKDAS	DMKCPM DMKDAU	DMKCPN DMKDDR	DMKCPS DMKDEX	DMKDGD	DMKDGF
		DMKD I B DMKGRH	DMKD I R DMKHPS	DMKDMP DMKHPU	DMKDMQ DMK10S	DMKDSB DMKIOT	DMKFMT DMKLDOOE		DMKGRE DMKMNL	DMKGRF DMKMNT	DMKGRG DMKNLD
		DMKNLE DMKRGC	DMKOPE DMKRGD	DMKOPR DMKRNH	DMKOVR DMKRSF	DMKPAG DMKRSP	DMKPAH DMKRSQ	DMKPIA DMKRST	DMKP I B DMKSAD	DMKRGA DMKSAV	DMKRGB DMKSEP
		DMKSPK	DMKSPS	DMKSPT	DMKSSP	DMKTAP	DMKTAQ	DMKTCS DMKUDR	DMKTCT DMKVCA	DMKTPE DMKVCB	DMKTRK DMKVCN
		DMKVDE	DMKTTY DMKVDR	DMKTTZ DMKVDT	DMKUCB DMKVM I	DMKUCC DMKVRR	DMKUCS DMKVRS	DMKVSP	DMKVSQ	DMKVST	DMKVSX
SIOCCH	000003	DMKZTD DMKACS	DMKCCH								
SIZE SKALTCYL	000022	DMKFRE DMKCCD									
SKCECYL	000007 000349	DMKCCD DMKBSC	DMKCCD	DMKCCF	DMKCCO	DMKCCS	DMKCCT	DMKCCW	DMKCKH	<b>ДМКСКР</b>	DMKCNS
SKIF	000349	DMKCSB	DMKCSC	DMKCST	DMKDAD	DMKDAS	DMKDAU	DMKDDR	DMKDIB	DMKDIR	DMKDMP
		DMKDRD DMKRGE	DMKDRE DMKRSF	DMKFMT DMKRSP	DMKHPS DMKRSQ	DMKHPU DMKRST	DMKIOS DMKSAV	DMKIOT DMKSEP	DMKPAG DMKSPL	DMKPAH DMKTAQ	DMKRGA DMKTCS
		DMKTCT DMKVSC	DMKTRK DMKVSP	DMKTTX DMKVSQ	DMKTTY DMKVST	DMKTTZ DMKVSX	DMKUNT	DMKVCA	DMKVCN	DMKVDE	DMKVMI
SKIPADD SKIPSENS	000003 000002	DMKACO DMKCCS	DMKCKF DMKCCW	J. M. O.	5						
SKIPSUP	000005	DMKDMQ	DHROOM								
SLNONPOS SM	000002	DMKSCH DMKACS	DMKCNS	DMKCPM	DMKDDR	DMKDMP	DMKDMQ	DMKDSP	DMKEXT	DMKFMT	DMKIOS
		DMKIOT	DMKMNT DMKVSI	DMKNLD DMKVSJ	DMKNLE	DMKOPR	DMKRSE	DMKSAD	DMKSAV	DMKSSP	DMKVIO
SMCOM SNACARD	000018 000002	DMKCCD DMKACO	DMKCCO DMKCKF	DMKCCT	DMKCCW						
SNAENBLE	000003	DMKVCT	DMKVCW								
SNARACO SNARBACH		DMKVCT DMKDSP	DMKVCW DMKVCP	DMKVCR	DMKVCS	DMKVCT	DMKVCU				
SNARBLOK	000109	DMKACO DMKCQY	DMKBLD DMKDIA	DMKCFM DMKD I B	DMKCFT DMKD1F	DMKCKF DMKDSP	DMKCQG DMKJRL	DMKCQQ DMKLOG	DMKCQS DMKLOH	DMKCQT DMKLOM	DMKCQU DMKQCO
		DMKQCP DMKVCU	DMKQCQ DMKVCV	DMKTRD DMKVCW	DMKUSQ DMKVCX		DMKVCP	DMKVCQ	DMKVCR	DMKVCS	DMKVCT
SNARBTCT		DMKVCS		DHINYON	DHILYUX	DUIVADO					
SNARCNTX SNARCONQ		DMKVCV DMKVCR	DMKVCX DMKVCS	DMKVCV							

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LABEL	COUNT	REFERENC	ES								
SNARCPFD SNARCPT SNARDIAL	000009	DMKVCP DMKVCP DMKCQS DMKVCW	DMKVCU DMKVCT DMKCQT DMKVCX	DMKVCV DMKD I F	DMKVCW DMKVCP	DMKVCX DMKVCQ	DMKVCR	DMKVCS	DMKVCT	DMKVCU	DMKVCV
SNARD I PG SNARD I S SNAREXWT	000020	DMKDIA DMKDIB DMKVCQ	DMKDIF DMKLOG DMKVCS	DMKVCS DMKVCP DMKVCX	DMKVCQ	DMKVCR	DMKVCS	DMKVCT	DMKVCV	DMKVCW	DMKVCX
SNARFG1	000047	DMKCFM	DMKDIB	DMKDSP	DMKLOG	DMKVCP	DMKVCQ	DMKVCR	DMKVCS	DMKVCT	DMKVCU
SNARFG2	000090	DMKCQS DMKVCS	DMKCQT	DMKCQU	DMKD I F DMKVCV	DMKQCO DMKVCW		DMKVCN	DMKVCP	DMKVCQ	DMKVCR
SNARFG3 SNARFORC SNARFSS SNARIF	000021 000005 000006 000002	DMKCFM DMKVCQ DMKVCR DMKVCR	DMKDIA DMKVCR DMKVCS	DMKDIF DMKVCU DMKVCU		DMKVCR	DMKVCS	DMKVCT	DMKVCW	DMKVCX	
SNAR I LER SNAR I NN SNARLUN	000003 000028 000020	DMKCFM DMKQCO DMKACO	DMKVCR DMKVCP DMKBLD	DMKVCQ DMKCKF			DMKVCT DMKCQS	DMKVCU	DMKVCV DMKD I A	DMKVCW DMKD I B	DMKVCX DMKJRL
SNARMDE SNARNOPR	000006	DMKLOH DMKVCR DMKVCV	DMKLOM DMKVCS	DMKQCP DMKVCU	DMKTRD	DMKUSQ	DMKVCV	DMKVCW			
SNARNXT SNAROUT	000018 000022	DMKCQS DMKLOH DMKVCX	DMKCQT DMKQCO	DMKVCT DMKVCP	DMKVCV DMKVCQ	DMKVCW DMKVCR	DMKVCX DMKVCS	DMK∨CT	DMKVCU	DMKVCV	DMKVCW
SNARPASS SNARPCT SNARPFIM SNARPFKI	000016 000003	DMKVCR DMKVCP DMKVCR DMKVCU	DMKVCS DMKVCQ DMKVCU	DMKVCR	DMKVCS						
SNARPRMT SNARPVL SNARRSE	000003 000006 000001	DMKCFM DMKVCP DMKVCW	DMKVCS DMKVCQ	DMKVCR	DMKVCS						
SNARSIZ SNARSPN SNARSPT	000005 000002 000004	DMKVCT DMKVCW DMKVCP	DMKVCW DMKVCV	DMKVCX							
SNARTTY	000028	DMKCQU	DMKQCO DMKVCR	DMKQCQ DMKVCU	DMKVCN DMKVCV	DMKVCP	DMKVCQ	DMKVCR	DMKVCS	DMKVCV	
SNARVMB SNASTATS	000017	DMKCFM DMKVCV DMKCPP	DMKCFT DMKVCW DMKVCT	DMKCQG DMKVDS DMKVCW	DMKDIA	DMKJRL	DMKLOH	DMKVCN	DMKVCR	DMKVCS	DMKVCT
SNAUSER SNSSUBCT SNSSUBST	000004	DMKACO DMKCCO DMKCCO	DMKCKF DMKCCS DMKCCS	DMKDVA	DMI/DVD						
SPBOUND SPBTSTAC		DMKFRE DMKSPL	DMKFRT	DMKPXA DMKDRD	DMKPXB DMKDRE	DMKHVF	DMKSPL	DMKTCS			
SPCHAR SPCHAR1 SPCHAR2 SPCHAR3 SPCMCHR SPCMOD SPCOPYFG	000015 000006 000006 000008 000008 000009 000007	DMKCQI DMKCQI DMKCQI DMKCQI DMKCQI DMKCQI DMKCQI	DMKCSW DMKCSW DMKCSW DMKCSW DMKCSW DMKCSW DMKCSW	DMKHVF DMKHVF DMKHVF DMKHVF DMKHVF DMKHVF DMKSPL	DMKDRE DMKSPL DMKSPL DMKSPL DMKSPL DMKSPL DMKTCS	DMKTCS DMKTCS DMKTCS DMKTCS DMKTCS	DMKSFL	DMRTUS			
SPCPTRAP SPECIALV SPFCB		DMKSPS DMKBLD DMKCQ1	DMKCPY DMKCSW	DMKERM DMKHVF	DMKHPS DMKSPL	DMKPTR DMKTCS	DMKRPA	DMKSTR	DMKVSE	DMKVSG	DMKWRM
SPFILID SPFLAG1	000014 000006	DMKACO DMKCQ I	DMKDRD DMKCSW	DMKDRE DMKSPL	DMKMIA DMKTCS	DMKTRR	DMKTRT	DMKTRU	DMKVSP	DMKVSQ	
SPFLSHC SPLINK	000006 000064	DMKCQI DMKACO	DMKCSW DMKAPT	DMKHVF DMKCKF	DMKSPL DMKCKV	DMKTCS DMKCQI	DMKCSV	DMKCSW	DMKCSY	DMKDRD	DMKDRE

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REFERENCES DMKHVF

DMKMIA

DMKSPS

DMKSPS

DMKSPT

DMKIOT

DMKSPR

DMKTRT DMKTRU DMKVDD DMKVME DMKVSP DMKVSQ DMKVST DMKVSW DMKVSX DMKXAD DMKCKM SPM 000002 DMKCPI DMKCCW DMKCDB SPMPFX 000074 DMKCDM DMKCDS DMKCFG DMKCFP DMKCPP DMKCPS DMKCPU DMKFPS DMKHVD DMKMPO DMKPGS DMKPMA DMKPSA DMKQVM DMKSAD DMKVIO DMKVME DMKVRR SPNTR 000009 DMKFRE SPNXTPAG 000084 DMKACO DMKAPT DMKRST DMKCKF DMKCKV DMKCSV DMKCSW DMKDRD DMKCSY DMKRSQ DMKSPS DMKTCS DMKVDD DMKVME DMKTRR DMKTRT DMKVSX DMKXAB DMKXAD 000036 000003 SPOOLED SPPGLEN DMKCPS DMKDMQ DMKMCC DMKMCD DMKMIA DMKMN I DMKMON DMKSPL DMKCKF DMKCKV DMKRSP DMKSPS SPPREPAG 000050 DMKACO DMKDRD DMKMIA DMKRSQ DMKRST DMKTRR DMKTRT DMKXAB DMKVME DMKVSP DMKVSQ DMKXAD SPRECMAX 000001 DMKSPL DMKCKV DMKRSQ DMKMON SPRECNUM 000026 DMKACO DMKDRD DMKRSP DMKVSQ DMKMIA DMKRST DMKTRU DMKVME DMKRSP SPRMISC 000036 DMKRST DMKSPK DMKSPS DMKTRR DMKTRT DMKVSP DMKVSQ SPROFCL DMKMCC 000003 000052 DMKDRE SPSIZE DMKDRD DMKRSP DMKRSQ DMKRST DMKSAD DMKSEP DMKTRR DMKTRT DMKVME DMKVSQ DMKVSX SPSPLNKC 000006 SPTBLOK 000006 SPTBUFR1 000012 DMKCQ I DMKCSV DMKCSW DMKCSY DMKSPL DMKHVF DMKSPR DMKSPT DMKSPT DMKIOT DMKSPS DMKSPT DMKSPS SPTBUFR2 000004 DMKSPS SPTBUFV1 000007 SPTBUFV2 000005 DMKSPT DMKSPS DMKSPT DMKSPS DMKSPT DMKSPT DMKSPS DMKSPS SPTCAN 000008 SPTCLAS 000003 SPTCLASS 000004 DMKSPS DMKSPT SPTCODE 000005 DMKSPS SPTDEST 000005 DMKSPS DMKSPT SPTDESTS 000003 SPTDONE 000003 DMKSPS DMKSPT DMKSPS SPTENTPT 000004 DMKSPS DMKSPT SPTFILES 000005 DMKSPS DMKSPT SPTFLAG 000016 SPTFLAG1 000030 DMKSPS DMKSPT DMKSPS SPTFLAG2 000032 DMKSPS DMKSPT SPTFLAG3 000006 DMKIOT DMKSPS DMKSPT SPTFORM DMKSPT 000004 DMKSPS SPTFRMST 000003 DMKSPS DMKSPT SPTIME 000013 SPTINTAD 000002 DMKACO DMKCKV DMKFRE DMKMIA DMKTRU DMKVSP DMKVSQ DMKSPS DMKSPT DMKIOT SPTINTR DMKSPS 000011 DMKSPT SPT10BAD 000004 DMKSPS SPTISSUR 000007 DMKSPR DMKSPS DMKSPT SPTLAST 000004 SPTLDPRT 000007 SPTLDRDR 000005 DMKSPS DMKSPS DMKSPS SPTLINK DMKSPS 000025 SPTLKLST 000003 DMKSPS SPTLOAD SPTMODE 000003 DMKSPS DMKSPT 000002 DMKSPT SPTMSGAD DMKSPT DMKSPT 000005 DMKSPS SPTNOH DMKSPS 000006

DMKRSP

DMKRSQ

DMKRST

DMKSPK

DMKSPL

DMKSPS

DMKTCS

DMKSPM

DMKMIA

DMKVSP

DMKTRR

DMKXAB

DMKDMP

DMKVAT

DMKRSP

DMKVSQ

DMKTCS

DMKVSX

DMKTRU

SPTOFLOW 000006

SPTOSFID 000002

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<u>S</u>	LABEL	COUNT	REFERENCE	ES						
'n	SPTOUSER	000002	DMKSPR	DMKSPS						
	SPTPRT	000006	DMKSPS	DMKSPT						
vetp	SPTPUR	000003	DMKSPS	DMKSPT						
ž	SPTRADDR		DMKSPR	DMKSPS	DMKSPT					
-	SPTRDEV	000004	DMKSPS	DMKSPT						
	SPTRDR	000012	DMKSPS	DMKSPT						
	SPTREAD	000003	DMKSPS	DINKODT						
כ	SPTREW	000003	DMKSPS	DMKSPT			÷			
ע	SPTRUN	000004	DMKSPS	DMKSPT						
7 2	SPTSAD	000003	DMKSPS	DMKSPT						
7	SPTSFB	000014	DMKSPS DMKSPS	DMKSPT						
Problem	SPTSHOLD	000006	DMKSPS	DMKSPT						
2	SPTSIZE SPTSPID1	000004	DMKSPS	DMKSPT						
P	SPTSPID2	000005	DMKSPS	DMKSPT						
	SPTSTOP	000005	DMKSPS	DMKSPT						
7	SPTSYS	000004	DMKSPT	Drintor 1						
Determin	SPTTM	000002	DMKSPS							
1	SPTUHOLD		DMKSPS	DMKSPT						
3	SPTUNLD	000005	DMKSPS	brinter i						
3	SPTUSER	000007	DMKSPS	DMKSPT						
ation	SPTXAB	000012	DMKSPS							
5	SPTXABLK		DMKSPS						-	
3	SPUBLOK	000043	DMKCKR	DMKVSD	DMKVSE	DMKVSF	DMKVSG	DMKWRM		
<b>.</b> .		000019	DMKVSD	DMKVSE	DMKVSF	DMKVSG				
Ξ.	SPUIND	000004	DMKCKR	DMKVSG	DMKWRM					
7	SPULASGN		DMKVSG							
Í	SPULAST	000019	DMKVSD	DMKVSE	DMKVSG					
Ĵ	SPUMAP	000011	DMKVSG							
3	SPUMAPSZ	000011	DMKVSG							
	SPUNEXT	000015	DMKVSE	DMKVSG						
	SPURCNT	000013	DMKVSD	DMKVSF	DMKVSG					
2	SPUSIZEB	000003	DMKVSE	DMKVSG						
3	SPUSRTID	000024	DMKVSG	014/4 1014						
7080-00	SPUSYSID		DMKVSG	DMKWRM						
78	SPUUSER	000003	DMKVSD	DMKVSG		DMIZOVA				
3	SPXCR6	000005	DMKCPS	DMKDMP	DMKPMA	DMKQVM				
L-1	SRTBLOK	000021	DMKCRM	DMK I DR DMK I DR						
	SRTFLAG	000032	DMKCRM DMKCRM	DMKIDR						
3	SRTGIND	000004	DMKCRM	DMKIDR						
Ú.	SRTIPND	000013 000008	DMKCRM	DMKIDR						
Convright	SRTPATH	000010	DMKIDR	DHKIDK						
	SRTPREV	000005	DMKIDR							
<u>.</u> .	SRTRESID		DMKIDR							
5	SRTRRPND	000005	DMKIDR							
-	SRTRVPND		DMKCRM	DMKIDR						
7	SRTSIZE	000008	DMKIDR	ormeren						
ζ	SRTSPND	000005	DMKIDR							
<b>`</b>		000003	DMKIDR							
IRM Com	SSAVE	000004	DMKIMG							
÷	SSBALLOC		DMKPGM	DMKPGS	DMKPTR	DMKSTR	DMKSWM			
	SSBBPNT	000007	DMKPGM	DMKPTR	DMKSEL	DMKSWA				
õ	SSBCODE	000007	DMKMON	DMKPAH	DMKPGM	DMKPTR	DMKSWA			
1987	SSBCPG	000004	DMKSEL							
	SSBCYL	000017	DMKCKM	DMKMON	DMKPAG	DMKPGM	DMKPGU	DMKPTR	DMKSWA	DMKSWM
987	SSBEFLG	000036	DMKPAG	DMKPGM	DMKPGU	DMKPTR	DMKSEL	DMKSWA	DMKSWM	
3	SSBEINVL	000028	DMKPAG	DMKPGM	DMKPGU	DMKPTR	DMKSEL	DMKSWA	DMKSWM	

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LADEL	000111		20						
SSBENTRL	000022	DMKBLD	DMKCKM	DMKLOG	DMKPAG	DMKPGM	DMKPGS	DMKPGU	DMKPTR
SODENTRE	000033	DMKSWA	DMKSWM	DMKVBM	DHILLAG	DHKTOH	DAINI 00	Dianti ou	DIRE
CODENTON	000001				DMI/DOC	DMI/DOU	DMKPTR	DMKSEL	DMKSTR
SSBENTRY		DMKCKM	DMKPAG	DMKPGM	DMKPGS	DMKPGU			
SSBENUM	000012	DMKCKM	DMKPAG	DMKPGM	DMKPGS	DMKPGU	DMKPTR	DMKSTR	DMKSWA
SSBEPGS	000027	DMKMON	DMKPAG	DMKPGM	DMKPTR	DMKSWA	DMKSWM		
SSBEXTND		DMKPTR	DMKSWA	DMKSWM					
SSBFLAG	000047	DMKMON	DMKPGM	DMKPGS	DMKPTR	DMKPTS	DMKSEL	DMKSTR	DMKSWA
SSBFLUSH	000002	DMKSEL	DMKSWA						
SSBFPNT	000019	DMKMON	DMKPGM	DMKPTR	DMKPTS	DMKSEL	DMKSWA		
SSBHEADL		DMKBLD	DMKCKM	DMKLOG	DMKPAG	DMKPGM	DMKPGS	DMKPGU	DMKPTR
CODITERIOL	000021	DMKSWA	DMKSWM	DMKVBM					
SSBLOK	000044	DMKCKM	DMKMON	DMKPAG	DMKPAH	DMKPGM	DMKPGS	DMKPGU	DMKPTR
JULIA	000044	DMKSTR	DMKSWA	DMKSWM	DRIVE	Drifter On	DINKI OO	Drifter 00	Dinki ik
CONCOL	000000			DHIKOWH					
SSBMGREF		DMKPGM	DMKSWA	DMI/OUA	DMI/OUM				
SSBNDLCT		DMKPGM	DMKPTR	DMKSWA	DMKSWM		DUVOEL		DMUCUM
SSBNPGS	000023	DMKMON	DMKPAG	DMKPGM	DMKPGU	DMKPTR	DMKSEL	DMKSWA	DMKSWM
SSBOLD	000006	DMKMON	DMKPTR	DMKPTS	DMKSWA				
SSBPGSP	000002	DMKMON	DMKSWA						
SSBPSTOR	000005	DMKMON	DMKPGM	DMKPTR	DMKSWA				
SSBSWAP	000001	DMKSWA							
SSBTRANS		DMKPGM	DMKPGS	DMKPTR	DMKSEL	DMKSTR	DMKSWA	DMKSWM	
SSBUPGS	000027	DMKMON	DMKPAG	DMKPGM	DMKPTR	DMKSWA	DMKSWM		
SSBVMSCB		DMKPTR	DMKSEL	DMKSWA	5				
SSBVPAGE	000000	DMKMON	DMKPAG	DMKPGM	DMKPGU	DMKPTR	DMKSEL	DMKSWA	DMKSWM
SSCBADDR		DMKCPJ	DMKCRM	DMKIDR	DHILLOU	DHILLIN	DHILOCL	DIMONA	orncomi
		DMKOPJ	DMKCRM	DMKIDR					
SSCBLOK	000017	DMKCPJ							
SSCIXADD	000014	DMKCPJ	DMKCRM	DMKIDR					
SSCIXLST		DMKCPJ	DMKCRM	DMKIDR					
SSCRTADD		DMKCPJ	DMKCRM	DMKIDR					
SSCRTCNT		DMKCRM	DMKIDR						
SSCRTLST	000002	DMKCPJ	DMKIDR						
SSCSIZE	000002	DMKCPJ							
SSCTSFPT	000006	DMKCPJ	DMKCRM	DMKIDR					
SSCTSFVM	000009	DMKCRM	DMKIDR						
SSEB	000002	DMKPRG	DMKVAT						
SSEBC	000001	DMKPRG	2						
SSEF	000001	DMKPRG							
SSM	000009	DMKCCD	DMKCCO						
STACKVM	000013	DMKCPP	DMKDSP	DMKLOH					
			DHKDSP	DHIKLON					
STAKSIZE		DMKFRT	DHIVOVE	DMI/COUL	DMUGTO	DHITOD	DMI/UDM		
STARTIME		DMKBLD	DMKCKF	DMKSCH	DMKSTP	DMKTOD	DMKWRM		
STCODE	000004	DMKACO	DMKCKF		<b>B</b>				
STOACTV	000017	DMKFPS	DMKPRG	DMKVAT	DMKVAU				
STOBLKLN		DMKVAT							
STOBLOK	000053	DMKFPS	DMKPRG	DMKVAT	DMKVAU				
STOFLAG	000038	DMKFPS	DMKPRG	DMKVAT	DMKVAU				
STOFSTUS	000005	DMKVAT	DMKVAU						
STOLAST	000016	DMKFPS	DMKVAT						
STONEXT	000033	DMKFPS	DMKVAT	DMKVAU					
STONXTUS		DMKVAT	DMKVAU						
STOPAGVR		DMKVAT	Dentind						
STORSIZE		DMKDMQ							
			DMI/VAU						
STOSEGCT	000000	DMKVAT		DMI/MAIL					
STOSEGVR		DMKFPS	DMKVAT	DMKVAU	DMIN				
STOSHCR1	000035	DMKFPS	DMKPRG	DMKVAT	DMKVAU				
STOSHLEN		DMKVAT							
STOSHSEG		DMKVAT							
STOSIZE	000001	DMKVAT							

REFERENCES

DMKSEL

DMKSWA

DMKSWM

DMKSEL

DMKPTS

DMKSTR

DMKSWM

DMKSTR

DMKSEL

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STOUSPT STOUSPTL STOVCR1 STOVLEN STOVPPG STOVPSG	000010	DMKFPS DMKFPS DMKFPS DMKVAT DMKFPS DMKFPS	DMKVAT DMKVAT DMKVAT DMKVAU DMKVAT DMKVAT	DMKVAU							
STOGCPG STOGCSG STPDRP STRTADDR STRTNEW STX	000002 000005 000004	DMKFPS DMKFPS DMKMON DMKIMG DMKCCW DMKCCU	DMKVAT DMKVAT DMKSTP								
STYPRDPT SUBADDR SUBBDTE	000002 000015 000010	DMKRGD DMKGRF DMKTEE DMKTEE DMKMOO DMKTEE	DMKGRG DMKTEF DMKTEF DMKTEF								
SUBCNI SUBCSFLG SUBCSWPT SUBFBYTE SUBHEAD SUBLEVEL SUBLIST	000006 000010 000008	DMKTEE DMKTEE DMKFRT	DMKTEF DMKTEF DMKMOO	DMKPXA	DMKPXB						
SUBLEVEL SUBLIST SUBREAL SUBSIZES SUBSTAT SUBSTLA SUBSTLS SUBTABLE	000003 000031 000003 000028	DMKTEE DMKTEE DMKTEE DMKFRE	DMKTEF DMKTEF DMKTEF								
SUBSTAT	000005	DMKTEE DMKFRE DMKFRE	DMKTEF DMKMOO	DMKPXA	DMKPXB						
SUBTABLE SUBTABMX SUBTOP SUBVIRT SUBVMBAD SUBVMFLG	000003 000003	DMKFRT DMKFRE DMKFRE DMKTEE DMKTEE DMKTEE	DMKMOO DMKFRT DMKFRT DMKTEF DMKTEF DMKTEF	DMKPXA	DMKPXB						
SUSPEND SVCNPSW SVCOPSW SVCREGS	000017 000022 000045 000021	DMKMIA DMKAPI DMKPRG DMKSVC	DMKMNI DMKCPI DMKSAD DMKSVD	DMKMON DMKMCT DMKSVC	DMKPRG DMKSVD	DMKSAD DMKTRC	DMKSVC	DMKSVD	DMKTRC		
SVMNOUPD	000005 000001	DMKCFO DMKLOK	DMKLOK	DMKSPR	DMKSPS						
SVMSTAY SVMUNLOK SVSEN SWPALLOC	000011 000004	DMKACR DMKCCO	DMKCNS DMKCCW DMKPGM	DMKCPY DMKPGS	DMKFRE DMKPTR	DMKLOK DMKSEL	DMKRGA DMKSTR	DMKRNH	DMKSPR	DMKSPS	
SWPAPP SWPCHG1	000005 000032	DMKATS DMKATS DMKATS DMKSEG	DMKCFF DMKCFF DMKSEL	DMKPTR DMKCPP DMKSWA	DMKSEL DMKHVD DMKSWM	DMKMCH DMKVSE	DMKPAG DMKWRM	DMKPGM	DMKPRW	DMKPTR	DMKRPA
SWPCHG2 SWPCODE SWPCYL	000011 000013 000083	DMKHVD DMKATS DMKATS DMKHVD	DMKMCH DMKCKW DMKCDS DMKHVF	DMKRPA DMKPAH DMKCFF DMKPAG	DMKSEL DMKPGM DMKCFG DMKPGM	DMKSWA DMKPGS DMKCFU DMKPGS	DMKSWM DMKPTR DMKCKM DMKPGU	DMKSEL DMKCKW DMKPTR	DMKVSE DMKCPP DMKRPA	DMKVSG DMKDRD DMKSEG	DMKDRE DMKSEL
SWPFLAG	000297	DMKSTR DMKATS DMKCPP DMKPGS DMKSEG DMKVSE	DMKSWA DMKBLD DMKDGD DMKPGU DMKSEL DMKVSG	DMKUDU DMKCCW DMKDRD DMKPRV DMKSPM DMKWRM	DMKCDS DMKDRE DMKPRW DMKSTR	DMKCFF DMKHVD DMKPSA DMKSWA	DMKCFG DMKHVF DMKPTR DMKSWM	DMKCFH DMKMCH DMKPTS DMKUDU	DMKCFU DMKPAG DMKPTT DMKVAT	DMKCKM DMKPAH DMKQVM DMKVMA	DMKCKW DMKPGM DMKRPA DMKVME
SWPFLAG2 SWPKEY1	000005 000046	DMKVSE DMKATS DMKATS DMKQVM	DMKCFF DMKCFF DMKSEL	DMKWRM DMKPTR DMKCFH DMKSPM	DMKSEL DMKCKM DMKVME	DMKDRD	DMKMCH	DMKPGS	DMKPRV	DMKPRW	DMKPTR

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SWPKEY2	000029	DMKDRD	DMKMCH	DMKPRW	DMKQVM	DMKSEL	DMKSPM	DMKVME			
SWPPAG	000013	DMKATS	DMKBLD	DMKCCW	DMKCFF	DMKPSA	DMKPTR	DMKPTT	DMKSEL	DMKSTR	
SWPPSTOR	000013	DMKATS	DMKCKW	DMKPAG	DMKPGM	DMKPGS	DMKPGT	DMKPGU	DMKPTR	DMKSEL	DMKVSE
0.0005000		DMKVSG		DMIXODO	DWYOEF	DHIVOED	DMUOCU	DMIZOVM	DMI/ODD	DMKDRD	DMKDRE
SWPRECMP	000066	DMKATS DMKHVD	DMKBLD DMKHVF	DMKCDS DMKPAG	DMKCFF DMKPAH	DMKCFG DMKPGM	DMKCFU DMKPGS	DMKCKM DMKPGU	DMKCPP DMKPTR	DMKRPA	DMKSEL
		DMKSTR	DMKSWA	DMKPAG	DMKUDU	DMKVSE	DMKWRM	DHKPGU	DPIKPIK	DHKKFA	DHROEL
SWPREF1	000005	DMKHVD	DMKPRW	DMKPTS	DMKSEL	DHKYJL	DEINMINE				
SWPREF2	000004	DMKHVD	DMKPTS	DMKSEL	DIINOLL						
SWPSEGNO		DMKBLD	DMKPGS	DMKPTT	DMKSEL	DMKSTR	DMKSWA				
SWPSHR	000017	DMKATS	DMKCFF	DMKDGD	DMKPGS	DMKPRV	DMKPRW	DMKPTR	DMKRPA	DMKSEL	DMKVAT
SWPTABLE	000055	DMKATS	DMKBLD	DMKCFF	DMKCPP	DMKPGM	DMKPGS	DMKPTR	DMKPTT	DMKSEL	DMKSTR
		DMKSWA	DMKVAT	DMKVMA	DMKWRM			0.000	DUVOTO	0.000	D.4.(0) #4
SWPTRANS	000049	DMKCDS	DMKPAG	DMKPGM	DMKPGS	DMKPTR	DMKRPA	DMKSEL	DMKSTR	DMKSWA	DMKSWM
SH DVM	000019	DMKVMA DMKATS	DMKBLD	DMKCFF	DMKCPP	DMKPGS	DMKSEL	DMKSTR			
SWPVM SWPVPAGE		DMKATS	DMKCCW	DMKPGM	DMKPGS	DMKPSA	DMKPTR	DMKPTS	DMKPTT	DMKSEL	DMKSTR
SHI VI AOL	000037	DMKSWA	DMKVMA	DIANA ON	Drift 00	DINGOA	Unit	Drakting	Dinki i i	DIINOLL	brind fit
SWTHSAVE	000010	DMKSTK	DMKVMA								
SYNCLOG	000002	DMKCPI	DMKCPJ								
SYNCMASK		DMKCLK	DMKDSP	DMKEXT	DMKIOT	DMKPMA					
SYSCPRD	000001	DMKMHV									
SYSCPWT	000001	DMKMHV									
SYSDFLT SYSIPLDV	000001	DMKLOH DMKCPI	DMKMNT	DMKSEG							
SYSLOCS	000032	DMKACO	DMKBLD	DMKGET	DMKCFU	DMKCFV	DMKCKF	DMKLOC	DMKLOJ	DMKLOM	DMKUDR
	000032	DMKUDU	DMKUSQ	DMKVCT	DMKVCW	Different	DIRICINI	DIIICEOO	DIIICEOU	DIREON	DIMODIN
SYSNAME SYSOPER	000025	DMKATS	DMKCFF	DMKCFG	DMKCFH	DMKCFS	DMKCKM	DMKCPP	DMKCQY		
SYSOPER	000002	DMKLOH									
SYSPALL	000004	DMKSRM	DMKSTP								
SYSPALOC	000030	DMKMOO	DMKPGT	DMKPST	DMKSTP	DMKUSP	DMKVDG	DMKVDH	DMKXST		
SYSPATYP Syspbpnt	000002	DMKPST DMKPGT	DMKVDH DMKVDG								
SYSPENT	000002	DMKPST	DMKVDG	DMKVDH							
SYSPCYL1		DMKPST	DMKVDG	DMKVDH	DMKXST						
SYSPCYL2	000015	DMKPST	DMKVDG	DMKVDH	DMKXST						
SYSPDASD	000001	DMKVDH									
SYSPEXT	000012	DMKSRM	DMKSTP								
SYSPEXTN	000005	DMKSRM	DMKSTP	DMUGDM	DWYOTD	DMUCH	DM/// (DO	DMI/VOT			
SYSPFLG SYSPFLG2	000035	DMKPGT DMKPGM	DMKPST DMKPST	DMKSRM DMKVDH	DMKSTP DMKXST	DMKSWM	DMKVDG	DMKXST			
SYSPFPNT	000000	DMKMOO	DMKPGM	DMKPGT	DMKPST	DMKSRM	DMKSTP	DMKSWM	DMKUSP	DMKVDG	DMKVDH
SYSPFULL		DMKSRM	DMKSTP	brind of	oniti or	Dimonal	Dimoiri	ormonit	Dimoor	51111100	
SYSPLIST	000015	DMKMOO	DMKPGM	DMKPGT	DMKPST	DMKSRM	DMKSTP	DMKSWM	DMKUSP	DMKVDG	DMKVDH
		DMKXST									
SYSPLVNO		DMKSRM									
SYSPMGOU	000002	DMKSTP									
SYSPOVFL SYSPPAG	000002	DMKSTP DMKPST									
SYSPPCNT	000001	DMKSTP									
SYSPPST	000002	DMKPGM	DMKPST								
SYSPPUSE	000002	DMKSTP									
SYSPRIV	000001	DMKCCT									
SYSPTOTL	000002	DMKSRM	DMKSTP								
SYSPUSER SYSPVLEN	000001	DMKVDH	DMI/VDO	DMI/VDU							
SYSPVLEN	000004	DMKPST DMKPST	DMKVDG DMKVDH	DMKVDH							
SYSPVOL	000010	DMKPST	DMKVDG	DMKVDH							

**CP** Directories

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LABEL	COUNT	REFERENC	ES								
SYSPXST SYSSERV SYSSIZE	000003 000005 000004	DMKPST DMKCCD DMKVMD	DMKXST DMKCCF	DMKCCO	DMKCCS						
SYSTEM	000622	DMKACO DMKCKV DMKCPW DMKCSY DMKNVF DMKNLD DMKRST DMKRSS DMKTTX DMKVSQ	DMKATS DMKCNS DMKCPY DMKDRD DMK1DU DMKNLE DMKSEG DMKSST DMKUDR DMKVST	DMKCFC DMKCPB DMKCQI DMKDRE DMKIOF DMKOPE DMKSVC DMKSVC DMKUDU DMKVSW	DMKCFG DMKCQS DMKCQS DMKERM DMKIOG DMKPMA DMKSNC DMKVSM DMKVSX	DMKCFH DMKCQJ DMKCQY DMKGRA DMKIOH DMKPTR DMKSPK DMKSPK DMKYCH DMKWRM	DMKCFM DMKCPM DMKCSC DMKIOT DMKRGC DMKRGC DMKSPL DMKYCX DMKVCX DMKWRN	DMKCFS DMKCPO DMKCSO DMKLNK DMKRNH DMKRNH DMKSPS DMKTOD DMKVDD DMKXAB	DMKCFU DMKCPP DMKCST DMKMCC DMKRPA DMKSPT DMKVDR DMKVDR DMKXAD	DMKCKR DMKCPS DMKCSV DMKHVD DMKHVD DMKRSP DMKSRM DMKSRM DMKVME	DMKCKS DMKCPU DMKCSW DMKHVE DMKMNJ DMKRSQ DMKSSP DMKTRU DMKVSP
SYSTEMID SYSVIRT	000007 000047	DMKVMC DMKBIO DMKDSB	DMKCCO DMK10S	DMKCCW DMKIOT	DMKCFQ DMKLNK	DMKCPW DMKLOJ	DMKCQP DMKMNT	DMKDEF DMKSSS	DMKDE I DMKSST	DMKDGD DMKVDA	DMKD I D DMKVSC
SYSXCDEL SYSXCKP SYSXLONG SYSXMGCT SYSXOVCT SYSXPDEL SYSXRWCT SYSXSHRT SYSXSIZE	000002 000003 000001 000001 000002 000002 000002 000002 000002	DMKSTP DMKSTP DMKSRM DMKSRM DMKSRM DMKSRM DMKSRM DMKSRM DMKSTP	DMKSTP DMKSTP		Jinchi	5111200					
S12 S15 S18 S21 S24 S27 S3 S30 S33 S33 S33 S33 S6 S9	000002 000002 000002 000002 000002 000002 000002 000002 000002 000002	DMKFRE DMKFRE DMKFRE DMKFRE DMKFRE DMKFRE DMKFRE DMKFRE DMKFRE DMKFRE									
TAB TABWDTH TABWDTH\$		DMKGR1 DMKMOO DMKFRE	DMKRGB DMKFRT	DMKRGC	DMKVCU	DMKVMI		<b>D</b>			244/22 5
TEMPRO	000126	DMKAPI DMKGRG DMKVFR	DMKATS DMKGRI DMKVMA	DMKCFM DMKGRT DMKVSR	DMKCFV DMKPRV DMKVSX	DMKCNT DMKPTS	DMKCP I DMKQCQ	DMKCSX DMKRGC	DMKDSP DMKSCH	DMKGRC DMKSTP	DMKGRF DMKVCA
TEMPR1 TEMPR10 TEMPR11	000073 000020 000008	DMKATS DMKPRG DMKATS DMKEXT	DMKCFM DMKPTR DMKCCW DMKPMA	DMKCKV DMKQCQ DMKDSP DMKSCH	DMKCPI DMKRGA DMKPRG	DMKGRC DMKRSP DMKSCH	DMKGRT DMKRSQ DMKSSU	DMKIDU DMKSTP DMKSTP	DMKMHC DMKVSQ	DMKMN I DMKVSR	DMKPGS DMKVSX
TEMPR12 TEMPR13	0000011	DMKCKP	DMKFPS	DMKPRG	DMKSCH	DMKSSU	DMKVSR				
TEMPR14	000049	DMKATS DMKSSU	DMKCCW DMKSVC	DMKCDS DMKVCR	DMKCPJ	DMKFPS	DMKGRC	DMKMNT	DMKPRG	DMKSCH	DMKSEL
TEMPR15 TEMPR2	000028 000104	DMKATS DMKAPI DMKIUS	DMKCCW DMKATS DMKMHC	DMKCDS DMKCCW DMKPMA	DMKGRC DMKCKP DMKPTR	DMKMNT DMKCPI DMKRGA	DMKPRG DMKCSU DMKSAV	DMKRGA DMKCSV DMKSTP	DMKSCH DMKCSX DMKVCA	DMKVSC DMKEXT DMKVCX	DMKVSQ DMKGRC DMKVMA
TEMPR3	000044	DMKAPI	DMKCCW	DMKCPI	DMKCSW	DMKGRC	DMKGRG	DMKMNI	DMKPTR	DMKRGC	DMKSTP
TEMPR4	000093	DMKAPI DMKSTP	DMKCKP DMKVCA	DMKCP I DMKVFR	DMKCSW DMKVSG	DMKGRC DMKVSR	DMKPMA	DMKPRV	DMKPTS	DMKSAV	DMKSCH
TEMPR5	000048	DMKAPI	DMKCPI	DMKPRV	DMKRGA	DMKSTP	DMKVCA	DMKVFR	DMKVMA	DMKVSI	

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LABEL	COUNT	REFERENC	ES								
TEMPR6	000035	DMKAPI	DMKCCO	DMKCKP	DMKCPI	DMKDSP	DMKFRE	DMKGRT	DMKHVC	DMKRGA	DMKSTP
	000014 000007	DMKVFR DMKAPI DMKHVC	DMKVMA DMKSCH DMKPTT	DMKSTP DMKVFR	DMKSVD	DMKVCR					
TEMPR8 TEMPR9 TEMPSAVE	000011	DMKGRC DMKACO DMKCNT DMKFPS DMKNLD DMKSCH DMKVSD	DMKFTT DMKIOT DMKAPI DMKCPJ DMKFRE DMKOPE DMKSSU DMKVSJ	DMKYFK DMKCCT DMKCPM DMKGRG DMKPGM DMKSTP	DMKSCH DMKCFS DMKCPZ DMKIDU DMKPGU DMKTOD	DMKVFR DMKCFY DMKCVT DMKIOS DMKPRG DMKTRQ	DMKCKF DMKCVU DMKIUS DMKPRV DMKTTY	DMKCKH DMKDAD DMKLOJ DMKQCQ DMKVDG	DMKCKP DMKDAS DMKMID DMKRGA DMKVIO	DMKCKW DMKDSP DMKMNT DMKRNH DMKVRR	DMKCNS DMKEXT DMKNET DMKSAV DMKVSC
TERMRMID TERMSYS	000015 000002 000007	DMKCCH DMKACO DMKACO DMKCCH	DMKCKF DMKCKF DMKEIG	DMKSEV	DMKSIX						
TEXT THISRCW TICBLK	000070 000013	DMKIMG DMKCCD DMKCCW	DMKCCO	DMKCCS	DMKCCW						
TIMEDISP	000002 000297	DMKACO DMKCNS DMKCSW DMKLOJ DMKLOJ DMKNEA DMKQCO DMKSPR DMKTRQ DMKTRQ	DMKACR DMKCPI DMKCSX DMKIOQ DMKLOK DMKNES DMKRGA DMKSPS DMKUDU DMKVDB	DMKALG DMKCPJ DMKDIA DMKIOS DMKLOM DMKNET DMKRGB DMKSSS DMKUSQ DMKVDD	DMKAPI DMKCPN DMKDIB DMKIOT DMKNCH DMKNLD DMKRNH DMKSSV DMKVCB DMKVDE	DMKBLD DMKCPO DMKDIF DMKIUA DMKMCT DMKOPE DMKSCH DMKSTP DMKVCH DMKVDF	DMKCAO DMKCPP DMKDSP DMKIUE DMKMIA DMKPAG DMKSEL DMKSTR DMKVCP DMKVDT	DMKCFO DMKCPS DMKEXT DMKIUL DMKMID DMKPGM DMKSVD DMKSVD DMKVCS DMKVMC	DMKCFQ DMKCPU DMKFPS DMKJRL DMKMPO DMKPMA DMKSPK DMKSWA DMKVCT DMKWA I	DMKCFR DMKCPV DMKFRE DMKLOG DMKMSG DMKPRG DMKPRG DMKSPL DMKTCS DMKVCU	DMKCLK DMKCPY DMKGRI DMKLOH DMKMSW DMKQCN DMKSPM DMKTCT DMKVCW
TIMEPOP TIMER	000002 000055	DMKFRE DMKAPI DMKFPS	DMKCFY	DMKCMD DMKMCH	DMKCPI DMKMPO	DMKCPJ DMKPGS	DMKDDR DMKPMA	DMKDEF DMKPRG	DMKD I R DMKPTR	DMKDSP DMKQVM	DMKEXT DMKSCH
TIOCCH TMRDRP TNSCPIDN TNSDEVAD TNSKEVN TNSREC TNSSNS1 TNSSWS3 TNSVOLID TNS3480S TNS3800 TNS3809S TODATE	000017 000003 000004 000003 000024 000004 000001 000003	DMKSEL DMKCCH DMKIOJ DMKIOJ DMKIOJ DMKIOJ DMKIOJ DMKIOJ DMKIOJ DMKIOJ DMKIOJ DMKIOJ DMKIOJ	DMKSTR DMKEIG DMKTRQ DMKCKF	DMKSVD DMKSEV DMKCKM	DMKSWA DMKSIX DMKCVT	DMKTRA	DMKTRQ	DMKMID	DMKMNI	DMKSAD	DMKTOD
TODCLOCK TODSYNC TPOPFSLB TPOPRDXP	000007	DMKVME DMKDMQ DMKCLK DMKBSC DMKBSC									
TRACBEF TRACCEPT	000007	DMKACS DMKVCW	DMKCNS	DMKCPM	DMKIOS	DMKIOT	DMKVSJ				
TRACCURR	000114	DMKACS DMKIUA DMKSCH	DMKCNS DMKLOK DMKSTA	DMKCPM DMKMCC DMKSVC	DMKCPZ DMKMCH DMKSVD	DMKDSP DMKMHC DMKSWA	DMKEXT DMKMNT DMKTRT	DMKFRE DMKPMA DMKTRU	DMKFRT DMKPRG DMKVCV	DMKIOS DMKPTS DMKVCX	DMKIOT DMKRNH DMKVSJ
TRACEFLG TRACEND	000008 000040	DMKMCC DMKACS DMKIUA	DMKMCD DMKCNS DMKLOK	DMKMLA DMKCPM DMKMCH	DMKSTA DMKCPZ DMKMHC	DMKDSP DMKMNT	DMKEXT DMKPMA	DMKFRE DMKPRG	DMKFRT DMKPTS	DMKIOS DMKRNH	DMKIOT DMKSCH

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LABEL	COUNT	REFERENC	ES								
TRACEVCS	000002	DMKSTA DMKVCV	DMKSVC	DMKSVD	DMKSWA	DMKTRT	DMKVCV	DMKVCX	DMKVSJ		
TRACFLG1		DMKEXT	DMKFRE DMKSWA	DMKFRT	DMKIOT	DMKMCH	DMKPMA	DMKPRG	DMKPTS	DMKSCH	DMKSVC
TRACFLG2 TRACFLG3 TRACHAR TRACIUCV TRACK7	000008 000001	DMKACS DMKDSP DMKVCV DMKIUA DMKTAQ	DMKCNS DMKIUA	<b>ДМКСРМ</b> ДМКМНС	DMKDSP DMKVCS	DMKEXT DMKVCV	DMKIOS DMKVCW	DMKIOT DMKVCX	DMKLOK	DMKRNH	DMKVSJ
TRACPROC		DMKACS DMKIOT DMKRNH	DMKAPI DMKIUA DMKSCH	DMKCNS DMKLOK DMKSVC	DMKCPM DMKMCH DMKSVD	DMKCPZ DMKMHC DMKSWA	DMKDSP DMKMNT DMKVCV	DMKEXT DMKMON DMKVCX	DMKFRE DMKPMA DMKVSJ	DMKFRT DMKPRG	DMK10S DMKPTS
TRACPSAF TRACSTRT		DMKVCV DMKACS DMKIOS DMKPRG DMKVCV	DMKAPI DMKIOT DMKPTS DMKVCX	DMKCNS DMKIUA DMKRNH DMKVSJ	DMKCPM DMKLOK DMKSAD	DMKCPP DMKMCC DMKSCH	DMKCPZ DMKMCH DMKSTA	DMKDSP DMKMHC DMKSVC	DMKEXT DMKMNT DMKSVD	DMKFRE DMKMPO DMKSWA	DMKFRT DMKPMA DMKTRT
TRACSVCR		DMKACS DMKIUA DMKSVC	DMKCNS DMKLOK DMKSVD	DMKCPM DMKMCH DMKSWA	DMKCPZ DMKMHC DMKVCV	DMKDSP DMKMNT DMKVCX	DMKEXT DMKPMA DMKVSJ	DMKFRE DMKPRG	DMKFRT DMKPTS	DMKIOS DMKRNH	DMKIOT DMKSCH
TRACSVST TRACOA TRACOC TRACOD TRACO1	000006 000001 000001 000001 000002	DMKSAD DMKDSP DMKDSP DMKVSJ DMKEXT	DMKSTA								
TRAC02 TRAC03 TRAC04 TRAC05	000002 000003 000001 000002	DMKSVC DMKPMA DMKMCH DMKIOT	DMKSVD DMKPRG								
TRAC08 TRAC09 TRAC10 TRAC11 TRAC12 TRAC13	000003 000001 000001 000001 000001 000001 000005	DMKPTS DMKSCH DMKDSP DMKRNH DMKLOK DMKEXT	DMKSCH	DMKSWA							
TRAC17 TRAC67 TRAEDCHR TRAFPFLG TRAFUNCT TRAINSTR TRAINSTR TRAINSTR TRAIXBLK TRALGAID	000001 000001 000002 000001 000001	DMKMHC DMKFRE DMKVCV DMKVCV DMKVCV DMKVCV DMKVCV DMKVCV DMKVCV	DMKFRT								
TRALUCON TRAMODE TRAMSGLM	000001 000002 000001	DMKVCW DMKVCV DMKVCV				211/222			2001/2001	<b>DM</b> //D D D	
TRANMODE	000054	DMKCDS DMKMCH DMKTMR DMKMIA	DMKCFF DMKPEI DMKTRC DMKMON	DMKCFG DMKPMA DMKTRD	DMKCKW DMKPRG DMKVAT	DMKCPB DMKPRV DMKVER	DMKDAD DMKPRW DMKVFR	DMKDAS DMKPSA	DMKDAU DMKPTT	DMKDSP DMKSAD	DMKFPS DMKSVD
TRAPCP TRAPCR8 TRAPDATA TRAPOK	000014 000015 000028 000004	DMKPSA DMKDSP DMKCKF DMKTRT DMKTRP	DMKTRP DMKPRG DMKCPP DMKTRU DMKTRU	DMKTRT DMKSVD DMKCQC	DMKTRU DMKTRT DMKTRP	DMKTRU DMKTRT	DMKTRU	DMKTRX			
TRAPSTRT TRAPTT TRAPVT	000009 000014 000006	DMKTRP DMKCQC DMKPRG	DMKTRU DMKDSP DMKTRT	DMKTRP DMKTRU	DMKTRT	DMKTRU					

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LABEL	COUNT	REFEREN	CES		
TRAREPLY	000001	DMKVCV			
TRASEND1	000004	DMKVCV			
TRASEND2	000003	DMKVCV			
TRASEVER	000005	DMKVCW			
TRATIMER		DMKVCV			
TRATNTYP					
TRAUDITA	000004 000001	DMKVCV			
TRAUDIT2	000001	DMKVCV			
TRAUSER1	000001	DMKVCV			
TRAVCSET	000005	DMKVCS	DMKVCV	DMKVCW	DMKVCX
TRAVCSPA TRAVMADR	000001	DMKVCV DMKVCV			
TRAVMADR	000002 000002	DMKVCV			
TRAVSAPA		DMKVCV			
TRAVSAQS	000002	DMKVCV	DMKVCW		
TRAVSARM		DMKVCV	DMKVCW		
TRAVSARP		DMKVCR	DMKVCS	DMKVCV	
TRAVSASV TRAVSMCN			DMKVCW		
TRCCLCH	000002	DMKVSJ			
TRCCSW	000002	DMKVSJ			
TRCDROP	000002	DMKSCH			
TRCENTRY		DMKTEE	DMKTEF		
TRCEXT	000002 000001	DMKEXT DMKDSP			
TROFREE	000002	DMKFRE			
TRCFREEP	000002	DMKFRE			
TRCFRET	000002	DMKFRT			
TRCFRETP		DMKFRT			
TRCHALT	000002 000002	DMKVSJ DMKIUA			
TRCLOK	000002	DMKLOK			
TRCMCH	000002	DMKMCH			
TRCMSSF	000002	DMKMHC			
TRCNCP	000002 000020	DMKRNH DMKTEE	DMKTEF		
TRCOUTEN	000020	DMKTEE	DMKTEF		
TRCPARM	000011	DMKTEE	DMKTEF		
TRCPGM	000006	DMKPMA	DMKPRG		
TRCPTSRS		DMKPTS			
TRCRUN TRCSCH	000001	DMKDSP DMKSCH			
TRCSIGP	000002 000002	DMKEXT			
TRCSIOF	000001	DMKIOS			
TRCSMINT	000002	DMKIOT			
TRCSVC	000004	DMKSVC	DMKSVD		
TRCSWAPI TRCTCH	000002 000002	DMKSWA DMKIOS			
TRCUNBLK		DMKDSP			
TRCUNSTK		DMKDSP			
TRCVCS	000004	DMKVCV	DMKVCX		
TREXADD	000020	DMKPRG	DMKPRV		DMKVAU
TREXANSI	000006 000015	DMKPGS DMKSPM	DMKTRA DMKTRA	DMKTRC	DMKTRD DMKTRD
TREXBUFF	000015	DMKTRC	DMKTRD	DHAING	DEIKINU
TREXCCW	000006	DMKTRA	DMKTRD		
TREXCCWI	000007	DMKTRD	DMKVSI	DMKVSJ	

LABEL	COUNT	REFERENC	ES								
TREXCR9	000001	DMKMPO									
TREXCSW	000006	DMKTRA	DMKTRC	DMKVIO							
TREXCTL	000004	DMKTRA	brinkinko	Dillity i o							
TRFXCTI 1	000003	DMKTRC	DMKTRD								
TREXCTL1 TREXCTL2	000017	DMKSPM	DMKTRC	DMKTRD	DMKV10	DMKVSI	DMKVSJ				
TREXFLAG	000017	DMKDSP	DMKGRG	DMKPRV	DMKRGC	DMKTRC	DMKTRD	DMKVRS			
TREXFLAG TREXINST	000015	DMKSPM	DMKTRA	DMKTRC	DMKTRD						
TREXIN1	000013	DMKDSP	DMKPGS	DMKPRV	DMKSVD	DMKTRA	DMKTRC	DMKTRD	DMKVRS		
TREXIN2	000010	DMKDSP	DMKTRC	DMKTRD	DMKVRS						
TREXLCNT	000006	DMKTRC	DMKTRD								
TREXLOCK	000001	DMKCFM									
TREXMOR	000004	DMKGRG	DMKRGC	DMKTRD							
TREXNDSP	000008	DMKDSP	DMKPRV	DMKTRC	DMKTRD						
TREXNSI	000014	DMKPGS	DMKPRV	DMKTRC							
TREXPERA TREXPRNT	000001	DMKMPO	DMITDO	DMITTO							
IREXPRNI	000005	DMKTRA	DMKTRC DMKTRC	DMKTRD DMKTRD							
TREXRUNF	000006	DMKTRA DMKTRA	DMKTRC	DMKTRD							
TREXSVC1	000005	DMKTRC	DMKTRD	DMKVRS							
TREXSVC2		DMKTRC	DMKTRD	DMKVRS							
TREXT	000026	DMKCFM	DMKDSP	DMKGRG	DMKMPO	DMKPGS	DMKPRV	DMKRGC	DMKSPM	DMKSVD	DMKTRA
	000020	DMKTRC	DMKTRD	DMKVIO	DMKVRS	DMKVSI	DMKVSJ				
TREXTERM	000008	DMKCFM	DMKTRA	DMKTRC	DMKTRD						
TREXVAT	000006	DMKTRC	DMKTRD	DMKVRS							
TRLANCHR	000009	DMKCPP	DMKSCH	DMKWAI							
TRLCT	000022	DMKCPP	DMKSCH	DMKWAI							
TRLINQ	000002	DMKWA I									
TRLLABEL		DMKCPP	DMKDSP	DMKPXA	DMKPXB	DMKSCH	DMKWA I				
TRLLOCK	000024	DMKAPI	DMKDSP	DMKPXA	DMKPXB	DMKSCH	DMKWA I				
TRLSTL	000004	DMKPXA	DMKPXB	DMKWAI							
TRQBBPNT TRQBCHIO	000010	DMKEXT	DMKTRQ DMKGRD	DMKGRF	DMKGRG	DMKRGA	DMKRGB	DMKUSQ			
TROBCLIN	000020	DMKCFQ DMKGRD	DMKGRG	DMKRGC	DHKGKG	DHKNGA	DPIKINGD	DHKUJQ			
TROBODIN	000031	DMKCFM	DMKCFQ	DMKGRD	DMKGRE	DMKGRF	DMKGR I	DMKRGA	DMKRGB		
TRQBCPQ TRQBCRT	000061	DMKGRD	DMKGRE	DMKGRF	DMKGRG	DMKGRI	DMKRGA	DMKRGB	DMKVDS		
TRQBDEV	000005	DMKGRG	DMKGRI	DMKRGA	DMKRGB	brinton	Dimitori	51111105	5111150		
TRQBFLAG	000054	DMKGRD	DMKGRE	DMKGRF	DMKGRG	DMKGRH	DMKGR I	DMKQVM			
TRQBFLG2	000084	DMKCFM	DMKCFQ	DMKGRD	DMKGRE	DMKGRF	DMKGRG	DMKRGA	DMKRGB	DMKRGC	DMKUSQ
TRQBFLG3	000002	DMKGRF									
TRQBFPNT	000030	DMKCFP	DMKCPP	DMKDIB	DMKDIF	DMKDSP	DMKEXT	DMKPMA	DMKQVM	DMKSTP	DMKTMR
		DMKTRQ	DMKUSQ	D.4//0 50	D.44/0 5 D	DHINGELL	DM//OFM	DUIVODI	Duttono	DUVOOT	
TRQBIRA	000047	DMKBLD	DMKCFJ	DMKCFQ	DMKCFR	DMKCFU	DMKCFY	DMKCPJ	DMKCPO	DMKCQT	DMKDID
		DMKDRD	DMKDRE DMKMNL	DMKGRF DMKPGM	DMKIUJ DMKPGS	DMKIUS DMKQCP	DMKJRL DMKREI	DMKLOH DMKRGA	DMKMCH DMKRGB	DMKMHC DMKSSU	DMKMN I DMKSTP
		DMKMNJ DMKTOD	DMKVCV	DMKVDA	DMKVDR	DMKVDS	DMKVMC	DMKKGA	DMIKIGD	DMKSSU	DHKOTP
TRQBLINA	000020	DMKCFM	DMKGRF	DMKGRI	DMKGRT	DMKHPT	DMKQVM	DMKRGA	DMKRGC		
TROBLINE	000025	DMKGRD	DMKGRE	DMKGRF	DMKGRG	DMKGRH	DMKRGA	DMKRGB	DMKRGC		
TRQBLOGP		DMKGRF	Shatone	SHAORA	Shirono	Shinohin	Shinkory	Sinnob	51111100		
TROBLOK	000224	DMKBLD	DMKCDS	DMKCFJ	DMKCFM	DMKCFP	DMKCFQ	DMKCFR	DMKCFU	DMKCFY	DMKCPJ
		DMKCPO	DMKCPP	DMKCQT	DMKDIB	DMKDID	DMKDIF	DMKDRD	DMKDRE	DMKDSP	DMKENT
		DMKEXT	DMKFPS	DMKGRD	DMKGRE	DMKGRF	DMKGRG	DMKGRH	DMKGR I	DMKGRT	DMKHPT
		DMKIUJ	DMKIUS	DMKJRL	DMKLOH	DMKMCH	DMKMHC	DMKMID	DMKMN I	DMKMNJ	DMKMNL
		DMKMOO	DMKPGM	DMKPGS	DMKPMA	DMKQCP	DMKQVM	DMKREI	DMKRGA	DMKRGB	DMKRGC
		DMKSCH	DMKSSU	DMKSTP	DMKSVD	DMKTMR	DMKTOD	DMKTRQ	DMKUSQ	DMKVCV	DMKVDA
TD000440	000007				DMKVRR	DMKVRS					
TRQBPA1R			DMKRGB	DMKRGC							
TRQBPOLL TRQBQUE		DMKRGA DMKCFP	DMKEXT	DMKFPS	DMKQVM	DMKSCH	DMKSVD	DMKTMR	DMKTRQ	DMKVRR	DMKVRS
INQUQUE	000029	DRIVEL	DHILLAI	DIALIO	SULLOG VILL	Shitoon	DINOVD	DERVICEN			DIRANO

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LABEL	COUNT	REFERENC	ES								
TRQBSAVE TRQBSIZE	000016 000104	DMKCFQ DMKBLD DMKCPO DMKIUJ DMKMNJ DMKVCT	DMKCFR DMKCFG DMKCQT DMKIUS DMKQCP DMKVCV	DMKDID DMKCFJ DMKDIA DMKJRL DMKQVM DMKQVA	DMKMNI DMKCFM DMKDIB DMKLOH DMKREI DMKREI	DMKVDA DMKCFQ DMKDID DMKLOJ DMKRGA DMKVDS	DMKVDR DMKCFR DMKDIF DMKMCC DMKRGB	DMKCFS DMKGRI DMKMCD DMKSSU	DMKCFU DMKGRT DMKMCH DMKSTP	DMKCFY DMKHPT DMKMHC DMKTOD	DMKCPJ DMKIOQ DMKMNI DMKUSQ
TRQBTOD	000092	DMKVCT DMKCFJ DMKMCH DMKREI DMKVRR	DMKVCV DMKCFM DMKMHC DMKSCH	DMKVDA DMKCFQ DMKMNI DMKSSU	DMKVDR DMKCFR DMKMNJ DMKSTP	DMKVDS DMKCFU DMKMNL DMKTMR	DMKCPJ DMKMOO DMKTOD	DMKCPO DMKPGM DMKTRQ	DMKCQT DMKPGS DMKVDA	DMKD I D DMKPMA DMKVDR	DMKENT DMKQVM DMKVMC
TRQBUSER	000054	DMKBLD DMKDRD DMKMHC DMKSSU	DMKCFJ DMKDRE DMKMNI DMKSTP	DMKCFQ DMKDSP DMKMNJ DMKTOD	DMKCFR DMKEXT DMKMNL DMKTRQ	DMKCFU DMKGRF DMKPGM DMKVCV	DMKCFY DMKIUJ DMKPGS DMKVDA	DMKCPJ DMKIUS DMKQCP DMKVDR	DMKCPO DMKJRL DMKREI DMKVDS	DMKCQT DMKLOH DMKRGA DMKVMC	DMKDID DMKMCH DMKRGB
	000103	DMKCDS DMKCQT DMKJRL DMKPMA DMKTOD	DMKCFJ DMKDID DMKMCH DMKQCP DMKTRQ	DMKCFM DMKDRD DMKMHC DMKQVM DMKVCV	DMKCFP DMKDRE DMKMID DMKREI DMKVDA	DMKCFQ DMKDSP DMKMN I DMKRGA DMKVDR	DMKCFR DMKENT DMKMNJ DMKRGB DMKVDS	DMKCFU DMKEXT DMKMNL DMKSCH DMKVMC	DMKCPJ DMKGRF DMKMOO DMKSSU DMKVRR	DMKCPO DMKIUJ DMKPGM DMKSTP	DMKCPP DMKIUS DMKPGS DMKTMR
TRQCACHL TRQCACHP TRQCHN TRQINTEG	000003 000003 000006	DMKMNL DMKMNL DMKTRQ DMKGRD	DMKGRF	DMKRGB	DMKRGC						
TRQNAME TRQREGS TRQREGSD TRQREGSZ TRQREG0 TRQREG2	000002 000010 000010 000004 000006 000002	DMKRGA DMKDRD DMKCQT DMKDRD DMKCFR DMKCQT	DMKDRE DMKDRD DMKDRE DMKPGS	DMKIUJ DMKDRE DMKIUJ DMKVMC	DMKIUS DMKPGM DMKIUS	DMKPGM DMKPGS	DMKVMC				
TRIABLE TRUN TSEND TSKELOK TSKCCPD TSKFLAG3 TSKFLAG4 TSKAISC1 TSKMISC1 TSKMISC1 TSKSFB TSKSFB TSKSIZE TSKSIZEB TSKSIZEB TSKSIZEB TSKSIZEB TSKSTART TSKSYSID TSKTIME TTCODE TTENTRY	000001 000008 000004 000003 000002 000002 000008 000002 000008 000002 000007 000003 000006 000002 000001 000002 000001 000002 000006 000004 000002	DMKTTX DMKCMQ DMKCKV DMKCKV DMKCKV DMKCKV DMKCKV DMKCKV DMKCKV DMKCKV DMKCKV DMKCKV DMKCKV DMKCKV DMKCKV DMKCKV DMKCKV DMKCKV DMKCKV DMKCKV DMKCKV DMKCKV DMKCKV DMKCKV DMKCKV DMKCKV DMKCKV DMKCKV DMKCKV DMKCKV DMKCKV DMKCKV DMKCKV DMKCKV	DMKMNI DMKSCH DMKTRT DMKTRT DMKTRT	DMKMON							
TTSEGCNT TWOENTS TYPBSC	000013 000004 000081	DMKATS DMKCFQ DMKACO DMKCQG DMKCQC DMKLOM DMKUSQ	DMKBLD DMKVCN DMKBLD DMKCQP DMKGRT DMKNES DMKVCN	DMKCFF DMKVDR DMKCFM DMKCQQ DMKHVD DMKNLD DMKVDE	DMKCPP DMKCFT DMKCQS DMKHVE DMKOPE DMKVDS	DMKSTP DMKCFY DMKCQU DMKIOF DMKQCN DMKQRM	DMKCKD DMKCQY DMKIOS DMKQCO	DMKCKF DMKDIA DMKIOT DMKQCP	DMKCPM DMKDIB DMKJRL DMKQCQ	DMKCPO DMKEPS DMKLOG DMKQVM	DMKCPW DMKERM DMKLOH DMKRGA

LABEL	COUNT	REFERENC	ES								
TYPCTCA	000047	DMKCFQ DMKVDA	DMKCFR DMKVDR	DMKCPB DMKVDS	DMKCQG DMKVER	DMKDEF DMKV10	DMKD I B DMKVM I	DMKDIR DMKVSI	DMK10S DMKVSJ	DMKSCN	DMKVCA
TYPFBA	000009	DMKCPW	DMKDDR	DMKDEF	DMKLNK	DMKMNT	DMKVDS				
TYPIBM1	000009	DMKCFR DMKCQC DMKTRP	DMKDEF	DMKDIA	DMKDIF	DMKDIR	DMKNLD	DMKSCN			
TYPNUMAX	000005	DMKCQC	DMKTRP	DMKTRR							
TYPNUMIN	000002	DMKTRP	DMKTRR								
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		DMKCST	DMKCSU	DMKCSV	DMKCSW	DMKCSX	DMKCSY	DMKDEF	DMKDMP	DMKDRD	DMKHVF
		DMKNLE	DMKRSP	DMKSAD	DMKSCN	DMKSPL	DMKSSP	DMKURS	DMKVDD	DMKVSP	DMKVSQ
		DMKVST	DMKRSP DMKXAB	DMKCKV DMKCSV DMKSAD DMKXAD							•
TYPPUN	000079	DMKCST DMKNLE DMKVST DMKCKF	DMKCKH	DMKCQG	DMKCQR DMKCSY DMKVDS	DMKCSF	DMKCSO	DMKCSQ	DMKCSR	DMKCST	DMKCSU
		DMKCSV	DMKCSW	DMKCSX	DMKCSY	DMKDRD	DMKMNT	DMKRSE	DMKRSP	DMKSCN	DMKSEP
		DMKSPI	DMKSSP	DMKURS	DMKVDS	DMKVSP					
TYPRDR	000025	DMKACO	DMKCOL	DMKCSP	DMKCSQ	DMKCSR	DMKCST	DMKCSW	DMKCSX	DMKCSY	DMKDRD
	0000-22	DMKCSV DMKSPL DMKACO DMKHVF	DMKSCN DMKCPV	DMKSPK	DMKCSQ DMKSPL	DMKURS	DMKVMI	DMKVSI			
TYPSDLC	000007	DMKCCW	DMKCPV	DMKIOF	DMKSCN	DMKVDA					
TYPSRF	000001	DMKIOH									
TYPSS	000001	DMKCFQ									
TYPTFLE2	000005	DMKCFQ DMKDEF	DMKDIA	DMKDIF	DMKDIR	DMKSCN					
TYPTIMER	000007	DMKCQG	DMKDIA DMKDEF	DMKD I F DMKD I R	DMKVSP						
TYPTTY	000034	DMKBLD	DMKCCT	DMKCER	DMKCFT	DMKCFY	DMKCNS	DMKCPV	DMKCQU	DMKIOF	DMKIOS
		DMKNES	DMKQCQ	DMKVCN	DMKVCP	DMKVDA			•		
TYPUNDEF	000003	DMKCQG DMKBLD DMKNES DMKCNS	DMKQCQ DMKNES								
TYPUNDEF TYPUNSUP	000006	DMKCCO	DMKCCS	DMKCCW	DMKVMI						
TYP1050	000008	DMKCNS	DMKIOF	DMKQCN							
TYP1052	000013	DMKDEE	DMKDIR	DMKLOJ	DMKSPL	DMKVDR	DMKVDS	DMKVST			
TYP1053	000002	DMKDID									
TYP1050 TYP1052 TYP1053 TYP1403	000017	DMKDID DMKDEF	DMKDIR	DMKDMP	DMKIOF	DMKRSE	DMKTRT	DMKTRU	DMKVME		
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TYP1443 TYP2305	000008	DMKDEF DMKCCO DMKDEF DMKACR DMKCPW DMKDSB DMKPMA	DMKDIR	DMKIOF	DMKRSE DMKCCS DMKDAS						
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		DMKDSB	DMKCQG DMKHVE	DMKIOC	DMKIOE	DMKIOG	DMKIOJ	DMKLOS	DMKLOJ	DMKMNT	DMKPAG
		DMKPMA	DMKSAV	DMKSPK	DMKSPS	DMKUNT	DMKVCH	DMKVDA	DMKVDC	DMKVDD	DMKVDE
		DMKVDF DMKCCD DMKALO	DMKVDG DMKCQG DMKATS	DMKVDR DMKDDR DMKB10	DMKVDS	DMKVER	DMKZTD				
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TYP2314	000066	DMKALO	DMKATS	DMKBIO	DMKCCD	DMKCFG	DMKCFH	DMKCFS	DMKCKH	DMKCKN	DMKCKT
		DMKCKV	DMKCP.J	DMKCPP	DMKCQG	DMKDAS	DMKDDR	DMKDEF	DMKDGD	DMKDIR	DMKDMP
		DMKDSB	DWKHVD	DMKLNK	DMKNLD	DMKNLE	DMKPAG	DMKPMA	DMKSAV	DMKSEG	DMKSNC
		DMKDSB DMKSPK DMKDDR	DMKSPS DMKDEF	DMKSSP DMKDRD	DMKTCS	DMKVDG	DMKVER	DMKVSC	DMKZTD		
TYP2319	000007	DMKDDR	DMKDEF	DMKDRD	DMKDRE	DMKHVE					
TYP2319 TYP2401	000009	DMKDDR DMKDDR	DMKIOS	DMKIOT	DMKTAP	DMKTAQ	DMKVMI				
TYP2415	000005	DMKDDR	DMKTAP	DMKTAQ	DMKVMI						
TYP2420	000005	DMKDDR DMKDEF	DMKTAP	DMKTAQ DMK10F	DMKVMI						
TYP2501	800000	DMKDEF	DMKDIR	DMKIOF	DMKRSE	DMKVMI					
TYP2520P	000002	DMKRSE									
TYP2520R	000001	DMKIOF									
TYP2540P	000010	DMKACO	DMKDEF	DMKDIR	DMKMIA	DMKRSE					
TYP2540R	000013	DMKDEF	DMKDIR	DMKIOF	DMKRSE	DMKRST	DMKVMI				
TYP2700	000004	DMKCCT	DMKIOF	DMKNES	DMKSCN	· · · · · · · · · ·					
TYP2401 TYP2415 TYP2420 TYP2501 TYP2520R TYP2520R TYP2540R TYP2540R TYP2740 TYP2741 TYP2055	000014	DMKDEF DMKCCT DMKCFC	DMKCFM	DMKCNS	DMKIOF	DMKOPE					
	000001	DMKCCT									
TYP3066	000020	DMKCCT DMKBLD	DMKCPV	DMKGRD	DMKGRF	DMKIOF	DMKOPE	DMKOPR	DMKQCQ	DMKSSP	DMKVCN
		DMKVDA DMKDEF									
TYP3088	000002	DMKDEF									
TYP3138	000002	DMKDIR									
TYP3148	000002	DMKDIR									
TYP3158	000002	DMKDIR									

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LABEL	COUNT	REFERENC	ES								
TYP3203	000017	DMKCSB DMKVSR	DMKCSC DMKVSX	DMKDEF	DMKDIR	DMKIOJ	DMKIOS	DMKRSF	<b>ĎMKRSP</b>	DMKSEP	DMKVSP
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TYP3211	000048	DMKCSB DMKSEP	DMKČSC DMKSPL	DMKCSO DMKURS	DMKV3Q DMKDEF DMKVDR	DMKVST DMKDIR DMKVSP	DMKVS0 DMKIOF DMKVSQ	DMK10J DMKVSR	DMKIOS DMKVST	DMKRSE DMKVSX	DMKRSP
TYP3215 TYP3262	000007 000017	DMKCFQ DMKCSB DMKVSX	DMKD I R DMKCSC	DMKDEF	DMKDIR	DMKIOJ	DMKIOS	DMKRSF	DMKRSP	DMKSEP	DMKVSP
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TYP3278	000072	DMKCCW DMKGRG DMKSSP	DMKCFM DMKGRI DMKVCN	DMKCFT DMKHPS DMKVCP	DMKCFY DMKHVD DMKVCQ	DMKCKD DMKHVE DMKVCR	DMKCPV DMKIOT DMKVCU	DMKDIA DMKOPE DMKVDA	DMKD I B DMKOPR DMKVDS	DMKGRD DMKQCN	DMKGRF DMKQCQ
TYP3279 TYP3284 TYP3286	000002 000022 000005	DMKCFM DMKCKD DMKHPS	DMKCPV DMKHPT	DMKDID	DMKGRF	DMKGRG	DMKHVE	DMKIOT	DMKVCN	DMKVDA	
TYP3289E TYP3310		DMKCSB DMKALO DMKVER	DMKCSC DMKB10	DMKDEF DMKCPJ	DMKD I D DMKCPW	DMKD I R DMKCQG	DMK10J DMKDEF	DMKIOS	DMKRSF DMK10J	DMKVSP DMKMNT	DMKVSX DMKTDK
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TYP3340	000081	DMKUNT DMKALO DMKCPM DMKDSB DMKSAV	DMKVDA DMKBIO DMKCQG DMKGIO DMKSPK	DMKVDG DMKCCD DMKDAS DMKHVE DMKSPS	DMKVER DMKCCO DMKDDR DMKIOE DMKSSP	DMKVRR DMKCCS DMKDEF DMKIOG DMKTDK	DMKVSC DMKCFH DMKDGD DMKIOJ DMKUNT	DMKZTD DMKCFS DMKDIR DMKMNT DMKVDE	DMKCKH DMKDMP DMKMSW DMKVDG	DMKCKN DMKDRD DMKPAG DMKVER	DMKCPJ DMKDRE DMKPMA DMKVIO
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TYP3375	000100	DMKALO DMKCKN DMKDGD DMKIOS DMKSPK	DMKATS DMKCKP DMKDIR DMKMNT DMKSSP	DMKBIO DMKCKT DMKDMP DMKNLD DMKTCS	DMKCCD DMKCKV DMKDRD DMKNLE DMKTDK	DMKCCO DMKCPJ DMKDRE DMKPAG DMKUNT	DMKCCS DMKCPP DMKHVD DMKPGT DMKVDG	DMKCFG DMKCQG DMKHVE DMKPMA DMKVDH	DMKCFH DMKDAD DMKIOE DMKSAV DMKVER	DMKCFS DMKDDR DMK10G DMKSEG DMKVSC	DMKCKH DMKDEF DMKIOJ DMKSNC DMKZTD
TYP3380	000116	DMKALO DMKCKP DMKDEF DMKIOJ DMKSNC DMKVER	DMKATS DMKCKT DMKDGD DMKIOS DMKSPK DMKVSC	DMKBIO DMKCKV DMKDIR DMKMNT DMKSPS DMKZTD	DMKCCD DMKCPJ DMKDMP DMKNLD DMKSSP	DMKCCO DMKCPM DMKDRD DMKNLE DMKTCS	DMKCCS DMKCPP DMKDRE DMKPAG DMKTDK	DMKCFG DMKCPW DMKHVD DMKPGT DMKUNT	DMKCFH DMKCQG DMKHVE DMKPMA DMKVDE	DMKCKH DMKDAD DMKIOE DMKSAV DMKVDG	DMKCKN DMKDDR DMKIOG DMKSEG DMKVDH
TYP3410 TYP3411 TYP3420	000009	DMKCCO DMKDDR	DMKDDR	DMKIOE							
TYP3420 TYP3422 TYP3430 TYP3480	000012 000017 000019 000049	DMKCCO DMKCCO DMKCCO DMKACS	DMKDDR DMKDDR DMKDDR DMKCCO	DMKDMQ DMKDMQ DMKDMQ DMKCPM	DMKIOE DMKIOE DMKIOE DMKCPN	DMKIOF DMKIOF DMKIOF DMKCPS	DMKIOJ DMKIOJ DMKIOJ DMKCPW	DMKTAQ DMKMCC DMKMCC DMKDDR	DMKSPT DMKSPT DMKDMQ	DMKTAP DMKTAP DMKHVC	DMKTAQ DMKTAQ DMKIOE

LABEL	COUNT	REFERENC	ES								
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TYP3800	000102	DMKCCS DMKDID DMKSPL	DMKCKH DMKDIR DMKTCS	DMKCKS DMKIOE DMKTCT	DMKCKV DMKIOJ DMKURS	DMKCPO DMKIOT DMKVDR	DMKCPS DMKMNT DMKVDS	DMKCPW DMKRSE DMKVSP	DMKCQG DMKRSP DMKVSQ	DMKCSO DMKSCN DMKVSR	DMKDEF DMKSEP DMKVST
TYP38003	000129	DMKVSX DMKCCS DMKDEG DMKSEP	DMKWRM DMKCKH DMKDID DMKSPL	DMKCKS DMKDIR DMKTCS	DMKCKV DMKIOE DMKTCT	DMKCPO DMKIOJ DMKURS	DMKCPS DMKIOT DMKVDR	DMKCPW DMKMNT DMKVDS	DMKCQG DMKRSE DMKVSP	DMKCSO DMKRSP DMKVSQ	DMKDEF DMKSCN DMKVSR
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TYP4248	000052	DMKCPB DMKSPL	DMKCSB DMKVSP	DMKCSC	DMKDEF DMKVSR	DMKD1R DMKVST	DMKIOE DMKVSX	DMKIOF	DMKRSE	DMKRSP	DMKSEP
TYP5ACCW TYP8809	000034	DMKAPU DMKCFR DMKTAP	DMKCQI DMKCPS DMKTAQ	DMKRSP DMKDDR DMKVDD	DMKVSP DMKDMQ	DMKVSX DMKDSB	DMKIOE	DMKIOJ	DMKMCC	DMKMNT	DMKSPT
UC	000170	DMKACS DMKDAS DMKFMT DMKMON DMKSAD DMKVCN	DMKBSC DMKDAU DMKGRF DMKNLD DMKSAV DMKVDC	DMKCKD DMKDDR DMKHPS DMKNLE DMKSPS DMKVDE	DMKCKH DMKD I B DMKHPT DMKOPE DMKSSP DMKY I O	DMKCNS DMKDID DMKHVC DMKOPR DMKTAP DMKVMI	DMKCPI DMKDIR DMKIOE DMKRGA DMKTPE DMKVRR	DMKCPM DMKDMP DMKIOS DMKRGE DMKTRK DMKVRS	DMKCPZ DMKDMQ DMKIOT DMKRNH DMKUNT DMKVSI	DMKCQT DMKDSB DMKLD00E DMKRSE DMKVCA DMKVSP	DMKDAD DMKDSP DMKMNT DMKRSP DMKVCB DMKVSX
UCASE	000037		DMKCFM DMKNLE	DMKCFU DMKOPE	DMKCNS DMKRGC	DMKCNT DMKRNH	DMKCPJ DMKTOD	DMKEPS DMKVCR	DMKGRG DMKVCU	DMKGRT	DMKMSW
UCNTRL UCNTRLSZ UCSBBLOK UCSBFOLD UCSBNAME UCSBSIZE UCSBVER UCSCCWS UCSCCWOB UCSCCWO6	000001 000002 000002 000003 000011 000001 000001	DMKUDU DMKUDU DMKCSB DMKCSB DMKCSB DMKCSB DMKCSB DMKCSB DMKCSB	DMKCSC								
UCSCCW07 UCSFCBAD UCSFCBLD UCSFCBL1 UCSFFLD UCSLOAD UCSNAME	000004 000003 000005 000002 000001 000003	DMKCSB DMKCSB DMKCSB DMKCSB DMKCSB DMKCSB DMKCSB	DMKCSC								
UCSRDCCW UCSRDCC1 UCSREGS UCURPASS UDASDDEV UDASDDIR UDASDIPL UDASDMAC	000001 000001 000002 000002 000004 000003 000001	DMKCSB DMKCSC DMKCSB DMKUDU DMKUDU DMKUDU DMKUDU DMKUDU DMKUDU									

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LABEL	COUNT	REFERENCE	ES								
UDEVAD UDEVCNT UDEVCODE UDEVF UDIRAD UDIRF UDISPDEV UDISPMAC UE	000001 000001 000001 000002 000005 000005 000003 000002 000079	DMKUDU DMKUDU DMKUDU DMKUDU DMKUDU DMKUDU DMKUDU DMKUDU DMKBSC DMKHPS DMKSAD	DMKCNS DMKHVC DMKSAV	DMKCPB DMKIOH DMKSEP	DMKCSB DMKIOT DMKSPS	DMKDDR DMKLD00E DMKSSP	DMKD I R DMKMON DMKTAP	DMKDMQ DMKRGA DMKVCA	DMKFMT DMKRNH DMKVCN	DMKG10 DMKRSP DMKV10	DMKGRF DMKRST DMKVMI
UFLAGS UIPARMS UIPARMSZ UIPL UIPLOP	000001 000001	DMKVRS DMKUDU DMKUDU DMKUDU DMKUDU DMKUDU	DMKVSJ DMKVMG	DMKVSP	DMKVSX						
UIPLPRMS UIUCBLOK UIUCCHN UIUCDASD UIUCDISP UIUCGLBL UIUCCLAST UIUCMLIM UIUCPRTY UIUCRES UIUCRES UIUCREYK	000009 000002 000002 000004 000003 000002 000003 000004 000002 000002	DMKUDU DMKDIR DMKDIR DMKDIR DMKDIR DMKDIR DMKDIR DMKDIR DMKDIR DMKDIR	DMK I DR DMKUDR DMKUDR DMKUDR DMK I DR DMK I UC DMK I UC DMK I DR DMK I DR	DMKIUC	DMKUDR						
UMACAD UMACF UMD I SKAD UMD I SKMP UMD I SKMP UMD I SKWP UMCHG UNCHG UNEWPASS UNF I N UNLOAD UNOUPF	000010 000002 000001 00001 000001 000001 000001 000002 000001 000002 000001 000002 000005 000005 000006 000003	DMKDIR DMKDIR DMKDIR DMKUDU DMKUDU DMKUDU DMKUDU DMKUDU DMKUDU DMKUDU DMKSEL DMKUDU DMKSEL DMKUDU DMKUDU DMKUDU	DMKUDR DMKIDR DMKUDR	DMKIUC	DMKUDR						
UNREF UNSHRVM	000006 000002 000002 000005 000001 000002 000002 000002 000002 000003 000015 000003 000002	DMKSEL DMKCCW DMKCCW DMKUDU DMKUDU DMKUDU DMKUDU DMKUDU DMKUDU DMKUDU DMKUDU DMKUDU DMKUDU DMKUDU	DMKFRT								

LABEL	COUNT	REFERENCE	ES						
URPAGXIP	000001	DMKUDU							
URSBACK	000002	DMKRSP							
URSDEV	800000	DMKCQP	DMKCSO	DMKRSP	DMKRST	DMKURS			
URSFILE	000020	DMKCQP	DMKRSP	DMKRST	DMKURS				
URSFLUSH	000002	DMKRSP							
URSHELD	000002	DMKRSP							
URSOPER	000021	DMKCSO	DMKRSP	DMKRST	DMKURS				
URSPATH	000002	DMKCQP	DMKURS						
URSREAD	000002	DMKRST	DIMONO						
URSREP	000002	DMKRSP							
			DMI/HDC						
URSSTACK		DMKCQP	DMKURS						
URSSTART	000003	DMKCSO	DMKURS						
URSUSER	000004	DMKCSO	DMKURS						
USCRCPO	000001	DMKUDU		,					
USCRINA	000001	DMKUDU							
USCRINR	000001	DMKUDU							
USCRSTA	000001	DMKUDU							
USCRVMO	000001	DMKUDU							
USERCARD	000002	DMKACO	DMKCKF						
USERCL	000004	DMKMCC	DMKMNI	DMKMOO					
USPMSK	000003	DMKFRE							
USPSIZE	000005	DMKFRE	DMKFRT						
USPSIZE\$	000002	DMKFRE	DMKFRT						
USTORAGE		DMKUDU	51						
USVDASD	000003	DMKUDU							
UTESTMD	000020	DMKUDU							
UT3310	0000020	DMKCPW	DMKMNT						
	000002	DMKCPW	DMKMNT						
UT3370			DMKMNT						
UT3370M4		DMKCPW	DMKMINI					•	
UUSERID	000002	DMKUDU							
UVMBLOK	000002	DMKUDU							
UVPAGBUF	000005	DMKUDU							
UVPAGDIR		DMKUDU							
UWORK	000020	DMKUDU							
UWORK2	000003	DMKUDU							
UXIPLAD	000002	DMKUDU							
VACOK	000012	DMKACO	DMKCKF						
VACOVFR	000009	DMKACO	DMKCKF	DMKEXT	DMKMOO	DMKPRV	DMKTHI	DMKVFR	DMKVFS
VBFBLOK	000023	DMKVSP	DMKVSQ	DMKVSR	DMKVSV				
VBFBUF	000006	DMKVSP	DMKVSR						
VBFBUF1	000001	DMKVSR							
VBFBUF2	000002	DMKVSR							
VBFCCW1	000012	DMKVSP	DMKVSQ	DMKVSV					
VBFCOUNT		DMKVSP	DMKVSR						
VBFDATA	000023	DMKVSR	DMKVSV						
VBFDATLF	0000023	DMKVSQ	Dritte						
VBFDCACT		DMKVSQ							
		DMKVSQ							
VBFDCUSD		DMKVSQ							
VBFFLAG1									
VBFLGLFT		DMKVSQ	DMI/VCD						
VBFRADD	000004	DMKVSP	DMKVSR	DMIAVOV					
VBFRADD1		DMKVSQ	DMKVSR	DMKVSV					
VBFRADD2		DMKVSQ	DMKVSR	DMKVSV					
VBFRADD8		DMKVSR	DMINICH						
VBFTIC	000004	DMKVSQ	DMKVSV						
VBFVSQR0		DMKVSQ	DMKVSR						
VBFVSQR1		DMKVSR							
VBFVSQR2	000002	DMKVSR							

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VOOLADDO	000001	DHIKYON
VCONADDR	000006	DMKVCN
VCONANF	000011	DMKCFM
VCONANF2	800000	DMKCFQ
VCONBFSZ	000015	DMKCFQ
VCONBRK	000023	DMKCFT
		DMKVCU
VCONBUF	000031	DMKCFQ
VCONCAW	800000	DMKVCN
VCONCBRK	800000	DMKCFM
VCONCCW	000025	DMKVCN
VCONCCW2	000003	DMKQCO
VCONCNT	000010	DMKVCN
VCONCNT2	000004	DMKQCO
VCONCOMD	000040	DMKVCN
VCONCPRD	000006	DMKGRE
VCONCTL	000084	DMKACO
		DMKDEF
		DMKMSG
		DMKSND
		DMKVDS
VCONDIAG	000002	DMKCFM
VCONDWC	000022	DMKVCN
VCONEWA	000002	DMKVCN
VCONEWRT	000002	DMKVCN
VCONEXTN	000005	DMKDIA
VCONFLAG	000035	DMKVCN
VCONFLG2	000021	DMKCFU
VCONFSOP	000001	DMKVCN
VCONFSS	000034	DMKCFQ
VCONIDAP	000003	DMKVCN
VCONLED	000003	DMKVCN
VCONNCB	000017	DMKCFQ
VCONNICB	000021	DMKACO
V COMINI CD	000021	DMKSCN
VCONNTRM	000004	DMKVCN
VCONOBRK	000014	DMKCFT
YCONODAK	000014	DMKVCU
VCONOMSC	000005	DMKCEU

VCONRFLD 000003

VCONRIND 000010

VBFVSQR3 000004

VBFWORK

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VCONCCW	000025	DMKVCN
VCONCCW2 VCONCNT	000003	DMKQCO DMKVCN
VCONCNT2	000004	DMKQCO
VCONCOMD	000040	DMKVCN
VCONCPRD	000006	DMKGRE
VCONCTL	000084	DMKACO
		DMKDEF
		DMKMSG
		DMKSND
VCONDIAG	000002	DMKVDS DMKCFM
VCONDIAG	0000022	DMKVCN
VCONEWA	0000022	DMKVCN
VCONEWRT	000002	DMKVCN
VCONEXTN	000005	DMKDIA
VCONFLAG	000035	DMKVCN
VCONFLG2	000021	DMKCFU
VCONFSOP	000001	DMKVCN
VCONFSS	000034	DMKCFQ
VCONIDAP VCONLED	000003	DMKVCN
VCONLED	000003	DMKVCN DMKCFQ
VCONNICB	000021	DMKACO
100111100	000021	DMKSCN
VCONNTRM	000004	DMKVCN
VCONOBRK	000014	DMKCFT
		DMKVCU
VCONOMSG	000005	DMKCFU
VCONOPT	000066	DMKCFM
VCONPLF	000016	DMKRGA
VCONPLST	000016	DMKVCN DMKVCN
VCONPLSZ	000003	DMKVCN
VCONPPA1	000002	DMKVCN
VCONRBUE	000022	DMKALG
VCONRBYT	000003	DMKRGE
VCONRCNT	000009	DMKALG
VCONRD	000012	DMKGRE
VCONRDEV	000022	DMKACO
VCONDOCT	000011	DMKQCO
VCONRDSZ	000011	DMKALG DMKCFQ
VCONREME	000031	DMKCFQ
VCONREXW	000003	DMKGRD
VCONDELD	000003	DHILDOF

000001

DMKVSQ

DMKVSR

DMKCFQ

DMKGRD

DMKDIB

DMKCFY DMKVDS

DMKDIB

DMKCFT

DMKVCN

DMKVCN

DMKGRF

DMKALG

DMKDIA

DMKNEA

DMKTHI

DMKVCN

DMKGRD

DMKVCN

DMKVCN

DMKCFQ

DMKTRD

DMKCFY

DMKVDS

DMKMSG

DMKCFQ

DMKRGB DMKVCV

DMKCFQ DMKVCN

DMKGRG

DMKVCN DMKCFQ

DMKSCN DMKCFQ DMKCQG DMKCQG

DMKVCN

DMKVCN

DMKVCN

DMKRGE

DMKRGE

DMKCFT

DMKGRE

DMKDIF

DMKCQU

DMKDIF

DMKCQU

DMKCFM

DMKDIB

DMKQCN

DMKTRD

DMKVDR

DMKMSG

DMKVCV

DMKCKF

DMKVCN

DMKCQU

DMKQCN DMKCFT DMKRGC

DMKVDR

DMKCFT

DMKRGC

DMKCKF

DMKTHI

DMKCFT

DMKDIA

DMKDIA

DMKGRD

DMKRGB

DMKVCN

DMKGRE

DMKVCN

DMKGRD

DMKCFQ

DMKDIF

DMKQCO

DMKVCN

DMKQCN

DMKVDR

DMKCQG

DMKVDR

DMKGRE

DMKCQU

DMKVCN

DMKGRG

DMKSND

DMKCQG

DMKVCN

DMKGRG

DMKSCN

DMKSCN

DMKGRE

DMKVCS

DMKVDR

DMKGRF

DMKVCV

DMKGRE

DMKCFT

DMKEXT

DMKREI

DMKVCQ

DMKVCN

DMKDEF

DMKVDS

DMKGRF

DMKDIA

DMKVCS

DMKRGC

DMKVCN

DMKDEF

DMKVCU

DMKRGC

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DMKVCN

DMKGRG

DMKGRG

DMKVDR

DMKGRF

DMKCFU

DMKGRD

DMKRGA

DMKVCR

DMKDIA

DMKGRG

DMKEXT

DMKVCU

DMKSND

DMKVCU

DMKDIA

DMKVDR

DMKSND

DMKVDR

DMKVCV

DMKRGC

DMKCKF

DMKGRF

DMKRGC

DMKVCU

DMKHVE

DMKRGC

DMKGRE

DMKVDS

DMKVCU

DMKHVE

DMKVCU

DMKVDS

DMKVCQ

DMKCQG

DMKGRG

DMKRGE

DMKVCV

DMKQCN

DMKVCQ

DMKGRF

DMKVDR

DMKNEA

DMKVDR

DMKVCR

DMKCQU

DMKHVE

DMKSCN

DMKVDR

DMKQCO

DMKVCR

DMKGRG

DMKQCN

DMKRGA

DMKCFY

DMKGRE

DMKRGB

DMKVCS

DMKDIB

DMKRGA

DMKGRD

DMKVCV

DMKVCN

DMKDIB

DMKVDS

DMKVCN

DMKVDS

DMKVDR

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LABEL	COUNT	REFERENC	ES									
	000003	DMKRGA DMKVCN DMKVCN	DMKVCN	DMKVCR								
VCONRMAX VCONRMCT VCONRMOD	000004		DMKVCR									
VCONRMSZ VCONSCOL	000006	DMKDIA DMKGRE	DMKDIB	DMKD I F	DMKVDR							
VCONSCRN	000015	DMKGRE	DMKCFT	DMKCQU	DMKEXT	DMKGRD	DMKGRE	DMKGRF	DMKVCN			
VCONSHI VCONSIZE	000002 000003	DMKVDR DMKVCN	DMKVDS DMKVCV									
VCONSKIP VCONSPSS VCONSRM VCONWA	000010 000002 000010 000003	DMKGRE DMKGRE DMKGRE	DMKACA									
VCONWBFS VCONWBSZ	000004	DMKVCN DMKCFQ	DMKVCN	DMKVDR								
VCONWBUF	000013	DMKCFQ DMKVCN	DMKVCN	DMKVDR								
VCONWRRD VCONWRRM	000003											
VCONWRT VCONWSF	000003											
VCON3270		DMKCFM DMKRGB	DMKCFT DMKRGC	DMKCQU DMKVCN	DMKDIA	DMKEXT DMKVCU	DMKGRD DMKVCV	DMKGRE DMKVDS	DMKGRF	DMKGRG	DMKRGA	
VECAVAIL VECF	000028 000021	DMKAP I DMKAP I	DMKCPO DMKDSP	DMKMCH	DMKMHV DMKFPS	DMK PRG DMKMCH	DMKPRV DMKPRG	DMKVFC DMKPRV	DMKVFD DMKVFC	DMKVFE DMKVFR	DMKVFR	
VECINST	000006	DMKMOO DMKACO	DMKVFC DMKAP1	DMKCKF	DMKCPP	DMKMCH	DMKMOO	DMKPRG	DMKTHI	DMKVFC	DMKVFD	
VECSAOK	000011	DMKVFE DMKCPO	DMKVFR DMKDMP	DMKDSP	DMKPRG	DMKVFC	DMKVFD	DMKVFE	DMKVFR			
VECSTAT	000059	DMKACO DMKPRG	DMKAPI DMKPRV	DMKCKF DMKTH I	DMKCPO DMKVFC	DMKCPP DMKVFD	DMKDMP DMKVFE	DMKDSP DMKVFR	DMKMCH	DMKMHV	DMKMOO	
VECUSER	000046	DMKACO DMKPRV	DMKAPI DMKTHI		DMKCKF DMKVFD	DMKCPO DMKVFE	DMKDMP DMKVFR	DMKDSP DMKVFS	DMKFPS	DMKMCH	DMKPRG	
VECVARY VERLEN	000003 000001	DMKVFC DMKFCB		·								
VERNULL VERSP3	000001 000001	DMKWRM DMKWRM										
VFAULT	000020	DMKBIO DMKCPP	DMKDGD DMKTRP	DMKPRG DMKTRU	DMKPTR DMKVMG	DMKSWA	DMKVAT					
VHD	000003	DMKMPO DMKCCD	DMKQVM DMKCCF	DMKSPM	DMKCCS	DMKCCT	DMKCCW					
VIRFLAG	000037	DMKCCD	DMKCCO	DMKCCS	DMKCCT	DMKCCW	Diffeoti					
VIRTENAB	000001	DMKCCT	DMKCCO	DMKCCW	DMKCFQ	DMKCPW	DMKCQP	DMKDEF	DMKDEI	DMKDGD	DMKDID	
VIRTUAL	000056	DMKB10 DMKD1R	DMKDSB	DMKIOS	DMKIOT	DMKLNK	DMKLOJ	DMKMNT	DMKSSS	DMKVDA	DMKVSC	
VMABLOK VMACCOUN VMACOUNT	000002 000009	DMKATS DMKHVD DMKHVD DMKWA I	DMKCFF	DMKPGS	DMKVMA							
VMAFF VMAFPNT VMAIP VMAIP2	000071 000010 000013 000007	DMKATS DMKHVD DMKHVD	DMKCFF	DMKPGS	DMKVMA							
VMANAME VMASCCPD	000003	DMKATS DMKHVD	DMKCFF	DMKPGS								
VMASHRBK VMASIZE	000003	DMKCFF DMKATS	DMKVMA DMKCFF	DMKPGS								

<ul> <li>VMBLOK 001904</li> <li>DMKACG</li> <li>DMKACG</li> <li>DMKACG</li> <li>DMKACG</li> <li>DMKCGC</li> <li>DMKCGC</li> <li>DMKCGC</li> <li>DMKCFT</li> <li>DMKCFT</li> <li>DMKCFT</li> <li>DMKCFT</li> <li>DMKCFT</li> <li>DMKCFT</li> <li>DMKCFT</li> <li>DMKCFT</li> <li>DMKCGC</li> <li>DMKCGC</li> <li>DMKCFT</li> /ul>	LABEL	COUNT	REFERENC	ES							
VMČXŠTAT 000068 DMKHVC VMDEFSTK 000017 DMKWAI	VMBTRL VMCF VMCFREAD VMCFWAIT VMCLASSA VMCLASSA VMCLASSB VMCLASSD VMCLASSD VMCLASSD VMCLASSD VMCLASSD VMCLASSF VMCLNULL VMCMDLEV VMCONBUF VMCONBUF VMCONBUF VMCONSUF VMCONSUF VMCONSUF VMCONSUT VMCONSUT VMCONSUT VMCPSUT6	001904 000018 000018 000044 000044 000014 000005 000007 000003 000003 000003 000005 000007 000015 0000015 000004 0000015 000004 000003 000003 000003 000003 000003 000003 000003 000003 000003 000003 000003 000003 000003 000003 000003 000003 000003 000003 000003 000003 000004 000004 000004 000005 000004 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 000005 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00005 00005 00005 00005 00005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 0005 000000	DMKBIO DMKCFW DMKCFW DMKCFY DMKCFY DMKCFY DMKCFY DMKCFY DMKCFY DMKCFY DMKCFY DMKCFY DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT 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DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DMKFRT DM	DMKBLD DMKCFO DMKCFO DMKCKD DMKCKD DMKCKW DMKCRM DMKCQG DMKCQG DMKCQG DMKCQG DMKCQG DMKLOG DMKLOG DMKLOG DMKLOG DMKLOG DMKKICG DMKKPRG DMKVCG DMKVCB DMKVCP DMKVCP DMKVCP DMKVCP	DMKCAC DMKCCDM DMKCKF DMKCKF DMKCKF DMKCKF DMKCSB DMKCSB DMKCSB DMKCSB DMKCSB DMKCSB DMKCSB DMKCSB DMKCGRA DMKLOH DMKLOH DMKLOH DMKLOH DMKKPRV DMKKPRV DMKKPRV DMKKPRV DMKSPM DMKSPM DMKSTK DMKVCH DMKVCH DMKVCH DMKVCH DMKVCH DMKVCH DMKVCU	DMKCAO DMKCCAO DMKCCSQ DMKCCKH DMKCKB DMKCKI DMKCQI DMKCQI DMKCSC DMKCSC DMKDSF DMKDGF DMKCGC DMKIJ DMKIJ DMKIJ DMKIJ DMKNES DMKSPR DMKSPR DMKSPR DMKSPR DMKSPR DMKSPR DMKSPR DMKSPR DMKSPR DMKSPR DMKVCN DMKVCN DMKVCN DMKVCN DMKVCN DMKVCN DMKVCN DMKVCN DMKVCN	DMKCCD DMKCFC DMKCFR DMKCKM DMKCNT DMKCQP DMKCQP DMKCSF DMKCSF DMKCSF DMKDIA DMKENT DMKFRD DMKIUL DMKIUL DMKIID DMKNET DMKVFER DMKVFESA DMKSPS DMKSFS DMKVCP DMKVCP DMKVFG DMKVSP	DMKCCF DMKCFS DMKCFS DMKCPB DMKCPB DMKCPD DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKLOS DMKI DMKNLD DMKNLD DMKNLD DMKNEFT DMKSPET DMKSPET DMKSPC DMKVCQ DMKVCDC DMKVCQ DMKVCQ DMKVCQ DMKVCQ DMKVCQ DMKVCQ DMKVCQ DMKVCQ DMKVCQ DMKVCQ DMKVCQ DMKVCQ	DMKCCH DMKCFF DMKCFF DMKCCPI DMKCCPI DMKCCPI DMKCCPW DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKCCSP DMKKCSP DMKKORF DMKKNCC DMKKNCC DMKKSRM DMKKSRM DMKVCR DMKVCR DMKVCR DMKVCR DMKVCR DMKVCR DMKVCR	DMKCCO DMKCFG DMKCFG DMKCPJ DMKCPJ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ DMKCQQ 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DMKATS DMKCFJ DMKCFJ DMKCFW DMKCFW DMKCFW DMKCPN DMKCPN DMKCPN DMKCPU DMKCPU DMKCPU DMKCPU DMKCPU DMKCPU DMKSVD DMKSSU DMKSSU DMKSSU DMKSSU DMKVSU DMKVSD DMKVSD DMKVSD DMKVSU DMKVSU DMKVSU

LABEL	COUNT	REFERENC	ES								
VMDSTAT VMDVSTRT VMECEXT VMEPRIOR	000128	DMKWA I DMKHVC DMKHVC DMKWA I	DMKVCS	DMKVCU							
VMESTAT	000127	DMKHVC	DMKHVD	DMKIUS							
VMEXTCM	000101	DMKHVC	DMKHVD	DMKIUS							
VMEXWAIT		DMKHVC	DMKHVD	DMKVCS							
VMFSTAT	000102	DMKHVC									
VMFTRL	000030	DMKWAI									
VMGENIO	000031	DMKCFQ	DMKGRC	DMKGRD	DMKGRG	DMKLOJ	DMKQCN	DMKRGA	DMKRGB	DMKRGC	DMKSND
VMODDO	000165	DMKVCN	DMKVCR	DMKVCS							
VMGPRS VMGRFTAB	000165 000021	ÓMKHVC DMKVCU	DMKHVD								
VMIDLE	000021	DMKIUS									
VMIHIST	000012	DMKWAI									
VMINST	000151	DMKHVC									
VMIOWAIT	000041	DMKHVC									
VMIPLDEV		DMKHVD									
VMISTAT	000035	DMKIUS									
VMIUCV VMIUCVWT	000020	DMKIUS DMKIUS									
VMIUSTAT	000049	DMKIUS									
VMJSTAT	000017	DMKHVC									
VMKILL	000054	DMKVCP									
VMLDCTRS	000009	DMKHPU									
VMLKHIST		DMKWAI									
VMLOCK	000063	DMKWAI	DMKVCS	DMKVCW							
VMLOGOFF VMLOGON	000102 000077	DMKVCP DMKVCS	DMKVCU	DMKVCW							
VMMCODE	000016	DMKHVC	DRINGOO	DHINION							
VMMCPENV	000018	DMKVCU									
VMMHLITE	000011	DMKVCU									
VMMLEVEL	000073	DMKHVC	DMKVCU								
VMMLINED		DMKVCU	DMIAVOU								
VMMLVL2 VMMTEXT	000091 000019	DMKHVC DMKHVC	DMKVCU								
VMNOCPRD		DMKHVC									
VMNOECPS		DMKHVD									
VMNOFLU	000010	DMKIUS									
VMNPWOCL	000004	DMKHVC									
VMOSTAT	000244	DMKHVC	DMKURS	DMKVCP	DMKVCU	DMKWA I					
VMPATH VMPA2APL	000035	DMKWA I DMKHVD	DMKVCU								
VMPEND	000087	DMKWAI	DHKYCO								
VMPFUNC	000013	DMKVCU									
VMPROT	000037	DMKVCP	DMKVCU								
VMPSTAT	000139	DMKHVC	DMKHVD								
VMPSW	000543	DMKHPU	DMKHVC	DMKHVD	DMKIUS						
VMPSWDCT VMPXINT	000014 000046	DMKHVD DMKHPU	DMKVCU								
VMQSTAT	000048	DMKHVD	DMKVCU	DMKVCW							
VMRLPROC		DMKWAI	5111700	211111011							
VMRSTAT	000427	DMKHVC	DMKHVD	DMKIUS	DMKVCP	DMKVCS	DMKVCU	DMKVCW	DMKWA I		
VMRUN	000029	DMKWAI									
VMSEG	000519	DMKHPU	DMKHVC	DMKHVD	DMKSPK						
VMSHR VMSHRPRC	000029	DMKWA I DMKWA I									
VMSIZE	000075	DMKBIO	DMKBLD	DMKCCW	DMKCDB	DMKCDM	DMKCDS	DMKCFD	DMKCFF	DMKCFG	DMKCFH
1114 1 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4										·····	1 8

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LABEL	COUNT	REFERENC	ES								
		DMKCFP DMKIUL DMKVAT	DMKCFS DMKLOH DMKVBM	DMKCKM DMKMHC DMKVMD	DMKCKT DMKPGS DMKVME	DMKCPY DMKPRV DMKVSR	DMKDEG DMKPRW DMKWRM	DMKDSP DMKQVM DMKXAB	DMKFPS DMKSEG	DMKHVD DMKTRX	DMKIUE DMKUSQ
VMSLEEP	000018	DMKHVC	DHINYDH	DIANTIAL	DIRICHTE	Dring On	Dritter	DINOTE			
VMSPMFLG VMSTKCPU	000029	DMKHVC DMKWA I									
VMSTKLST	000023	DMKURS									
VMSTKO	000017	DMKURS									
VMSTOR VMSVSTAT	000050	DMKHVC DMKHVD									
VMSYSOP	000032	DMKURS									
VMTD1AG8 VMTERM	000004 000121	DMKHVC DMKHVD	DMKVCU								
VMTRCTL	000094	DMKHVC	DMKVCU								
VMTRMID	000036	DMKHVD									
VMTRPRV VMTTIME	000010 000027	DMKHVC DMKHVC									
VMUSER	000381	DMKHVD	DMKVCW								
VMVIRCF VMVPOREL	000021	DMKHVC DMKHVC									
VMVSPACE	000008	DMKHPU									
VMVTERM	000074	DMKVCS	DMKVCU								
VMV370R VMXUNSTK	000100	DMKHVC DMKWA I	DMKHVD								
VPMAAVAI	000002	DMKCFG	DMKPMA								
VRALOC VRSVUID	000003 000006	DMKBLD DMKCPI	DMKDEG DMKCPY	DMKLOH DMKLOH							
VSMPTR	000019	DMKCPP	DMKCQS	DMKCQT	DMKVCT	DMKVCV	DMKVCW	DMKVCX			
VSPBIGBF	000011	DMKVSP	DMKVSQ	DMKVSR	DHILLIOT	DMIAICN	DMIAVEL	DMI/VOV			
VSPBUFBK VSPBUFSZ	000043	DMKVSP DMKVSP	DMKVSQ DMKVSQ	DMKVSR DMKVST	DMKVST DMKVSW	DMKVSV DMKVSX	DMKVSW	DMKVSX			
VSPCAW	000012	DMKDRD	DMKVSP	DMKVSR	DMKVSW	DMKVSX					
VSPCCW VSPDCFOP	000213	DMKDRD DMKVSP	DMKVSP	DMKVSQ	DMKVSR	DMKVST	DMKVSV	DMKVSX			
VSPDPAGE	000031	DMKDRD	DMKVSP	DMKVSQ	DMKVSX						
VSPERR VSPFLAG1	000008	DMKVSP DMKVSP	DMKVSQ DMKVSQ	DMILLED							
VSPIDACT		DMKVSP	DMKVSQ	DMKVSR DMKVSX							
VSPIDAL VSPIDASW	000001	DMKVSR									
VSPIDASW VSPIDAW2	000009	DMKVSP DMKVSR	DMKVSR	DMKVSX							
VSPLCTL	000020	DMKCKF	DMKCSQ	DMKCSR	DMKDRD	DMKHVF	DMKSPL	DMKVSP	DMKVSQ	DMKVSR	DMKVST
VSPMISC	000009	DMKVSU DMKVSP	DMKVSV DMKVSQ	DMKVSW DMKVST	DMKVSX DMKVSW						
VSPMI SC2		DMKVSQ	DHKYJQ	DMKV31	DHKYJW						
VSPNEXT	000008	DMKVSQ	DMKVSX								
VSPRECNO VSPSFBLK	000002	DMKVSX DMKCKF	DMKCSQ	DMKCSR	DMKDRD	DMKHVF	DMKSPL	DMKVSP	DMKVSQ	DMKVST	DMKVSU
		DMKVSW	DMKVSX								
VSPSIZE VSPVPAGE	000009	DMKDRD DMKSPL	DMKSPL DMKVSP	DMKVSP DMKVSQ	DMKVSQ DMKVST	DMKVST DMKVSW	DMKVSW DMKVSX				
VSPVPG2	000004	DMKVSP	DMKVSQ	CHILLY ON	DUILACI	DUILAOM	DentyOA				
VSP5ACCW		DMKVSP	DMKVSQ	DMKVSR	DMIKOEM	DMIKOER	DMIKODI	DMUDDD			DMI/DCD
WAIT	000057	DMKAPI DMKEXT	DMKBLD DMKIOG	DMKCDS DMKIUA	DMKCFM DMKIUN	DMKCFP	DMKCPI DMKLOH	DMKDDR DMKMPO	DMKDMP DMKOVR	DMKDMQ DMKPMA	DMKDSP DMKQVM
		DMKSAD	DMKSVD	DMKTRA	DMKTRC	DMKTRR	DMKVBM	<b>.</b>			
WAITEND WAITSTRT	000008	DMKDSP DMKCLK	DMKEXT DMKDSP	DMKIOT	DMKMCH	DMKMPO	DMKWAI				
MALIOINI	000000	DRINGEN	UNINUUT								

LABEL	COUNT	REFERENC	ES								
WARNGRPT WCCALRM WCCO WCC3 WCKDNXTK WR WR I TBRK	000005 000001 000005	DMKCPJ DMKGRD DMKGRE DMKGRD DMKCCD DMKGRD DMKRNH	DMKGRF DMKCCO DMKRGD								
WRITE WRITEOP WRITEOT WRITREAT WRTREAD WRTSFLD WRTUKD WRTUPD WSF XCAPR	000025 000010 000004 000007 000001 000002 000002 000002 000003 000005 000004	DMKNRNH DMKCCW DMKRNH DMKVCN DMKVCN DMKVCN DMKCCD DMKCCD DMKCRE DMKEXT	DMKCQS DMKCCO DMKCCO DMKCRF DMKSCH	<b>ДМКСQY</b>	DMKDAU	DMKDGD	DMKDIR	DMKLDOOE	DMKLNK	DMKVCN	
XCDISP	000013	DMKDSP	DMKEXT	DMKIOS	DMKLOK	DMKPAG	DMKSCH	DMKSTK	DMKSVC	DMKVSJ	
XCMASK XCPEND	000012 000034	DMKAPI DMKCPP DMKVSJ	DMKDSP DMKDSP	DMKFRE DMKEXT	DMKIOT	DMKPMA DMKLOK	DMKMCT	DMKPAG	DMKSCH	DMKSTK	DMKSVC
XCRES XCWAK XFERSENS XFF	000002	DMKVSJ DMKEXT DMKEXT DMKCCS DMKCFY DMKAPI	DMKSCH DMKSCH DMKCCW								
XKEYASST XKEYMODE	000048	DMKCCW DMKDSP DMKSEL	DMKCPI DMKCDB DMKFPS DMKSPM	DMKDSP DMKCDM DMKLOJ DMKSTA	DMKPRW DMKCFH DMKMCH DMKSTR	DMKSPM DMKCFY DMKPRV DMKVAT	DMKCKM DMKPRW DMKVAU	DMKCPI DMKPSA DMKVME	DMKCPU DMKPTR	DMKCPY DMKPTS	DMKDGD DMKQVM
XMIGACT XMIGSWT XOBRCCW1 XOBRCCW2 XOBRCCW3 XOBRCCW4 XOBREXT	000001 000001 000003	DMKPGM DMKSTP DMKRSF DMKRSF DMKRSF DMKRSF DMKRSF	DMKSTP DMKSTR	:							
XOBRFCB XOBRFLAG XOBRMIS1 XOBRMIS2 XOBRRT1 XOBRRT2 XOBRRT3 XOBRRT3 XOBRRT5 XOBRRT6 XOBRSIZE XOBRSTAT	000002 000002 000006 000006 000006 000003 000007 000007 000007	DMKIOJ DMKRSF DMKRSF DMKRSF DMKRSF DMKRSF DMKRSF DMKRSF DMKRSF DMKRSF DMKRSF	DMKRSF DMK I OJ	DMKRSF							
XOBRT1	000006	DMKIOE	DMKIOJ	DMKRSF							
XOBRT2 XOBRT3 XOBR010 XOBR150 XOBR512	000001 000004 000003 000004 000008	DMKRSF DMKIOE DMKIOJ DMKIOJ DMKIOJ	DMKIOJ DMKRSF DMKRSF DMKRSF	DMKRSF							
XPAGNUM	000162	DMKAPT DMKCFS DMKDRE DMKIUC	DMKATS DMKCKV DMKFPS DMKIUP	DMKBIO DMKCPJ DMKFRT DMKMCH	DMKCCO DMKCPP DMKHPS DMKMHV	DMKCCW DMKCPS DMKHVC DMKMN I	DMKCDB DMKCQY DMKHVD DMKMPO	DMKCDM DMKCSC DMKHVE DMKNLD	DMKCDS DMKCSO DMKHVF DMKPE1	DMKCFG DMKDGD DMKISM DMKPER	DMKCFH DMKDRD DMKIUA DMKPMA

CADEE	00011		20							
XPGFULL XPPFULL	000006 000006	DMKPRV DMKSTA DMKVSR DMKPGM DMKPGM	DMKPRW DMKTCS DMKVSV DMKPGT DMKPGT	DMKPTR DMKTRX DMKWRM DMKSTP DMKSTP	DMKPTS DMKUNT DMKXAB	DMKQCN DMKVAT	DMKQCO DMKVAU	DMKRPA DMKVIO	DMKRSP DMKVMC	DMKSFB DMKVMD
XRIGHT16	000126	DMKCCD DMKDAS DMKGRI DMKQCN DMKSEV DMKVRS	DMKCCF DMKDAU DMKIUE DMKQCO DMKSIX DMKVSC	DMKCCO DMKDSP DMKIUL DMKQCQ DMKSSP	DMKCCS DMKERM DMKIUS DMKRGA DMKSTP	DMKCCW DMKFPS DMKLOH DMKRGB DMKTAP	DMKCDM DMKGRA DMKMSG DMKRGC DMKTRA	DMKCFF DMKGRC DMKOPR DMKRGD DMKTRK	DMKCFG DMKGRD DMKPEI DMKRGE DMKVCN	DMKCFS DMKGRE DMKPER DMKRNH DMKVCR
XRIGHT24 XSWFULL XTNDCR2	000027 000006 000023	DMKCCW DMKSTR DMKPGM DMKDSP	DMKCFP DMKSVD DMKPGT DMKEXT	DMKDSP DMKVCR DMKSTP DMKFRE	DMKHVD	DMKPEI	DMKPEL	DMKPEQ	DMKPER	DMKRGA
XTNDLOCK	000035	DMKDSP DMKRGC	DMKFRE DMKSCH	DMKFRT DMKSEL	DMKGRF DMKV10	DMK I OQ DMKWA I	DMKIOS	DMKLOK	DMKPAG	DMKPTR
X2048BND X40FFS	000024	DMKCCW DMKPSA DMKCDB	DMKCDB DMKTRD DMKCDM	DMKCDM DMKUNT DMKCFG	DMKCSW DMKVDS DMKCFT	DMKDAD DMKXAB DMKCPB	DMKDAS DMKCPS	DMKDRD DMKCPT	DMKDRE DMKDEF	DMKHVD DMKDSP
740113	000024	DMKPMA	DMKQVM	DMKSCH	DMKVCH	DMKVDE	DMKVFD	DMKVFE	DMKVMD	DMKVRR
Y0 Y2 Y4 ZERO ZEROES	000017 000017 000017 000017 000015 000650	DMKDMP DMKDMP DMKDMP DMKCMP DMKCFC DMKCCD DMKACO DMKACO DMKCAC	DMKDSP DMKDSP DMKDSP DMKCKF DMKCKF DMKMCH DMKACR DMKACR	DMKEXT DMKEXT DMKEXT DMKEXT DMKCKH DMKSSS DMKAPI DMKCCF	DMKIOT DMKIOT DMKIOT DMKIOT DMKFMT DMKAPU DMKCCH	DMKMCH DMKMCH DMKMCH DMKMCH DMKIOE DMKAPV DMKCCO	DMKMPO DMKMPO DMKMPO DMKNPO DMKIOF DMKAPX DMKACS	DMKPMA DMKPMA DMKPMA DMKPMA DMKIOG DMKATS DMKCCT	DMKPRG DMKPRG DMKPRG DMKPRG DMKIOJ DMKBIO DMKCCW	DMKQVM DMKQVM DMKQVM DMKQVM DMKIOS DMKBLD DMKCDB
		DMKCDS DMKCFT DMKCKV DMKCPV DMKCSO DMKDAU DMKFPS DMKIUA DMKIUA	DMKCFD DMKCFU DMKCLK DMKCPW DMKCSQ DMKDEF DMKHVC DMKIUC DMKIUC	DMKCFG DMKCFV DMKCNS DMKCPY DMKCSR DMKCGD DMKHVD DMKHVD DMKHUE DMKMCD	DMKCFH DMKCFW DMKCPB DMKCQC DMKCST DMKCGF DMKHVE DMKIUJ DMKKCH	DMKCFM DMKCFY DMKCPI DMKCQG DMKCSV DMKDIB DMKHVF DMKIUL DMKMCT	DMKCFO DMKCKD DMKCPN DMKCQH DMKCSW DMKDIF DMKIDR DMKIUP DMKIUP DMKMHC	DMKCFP DMKCKF DMKCPO DMKCQI DMKCSX DMKDMP DMKI0E DMKIUS DMKMIA	DMKCFQ DMKCKM DMKCPP DMKCQQ DMKCSY DMKDSB DMKIOJ DMKJRL DMKJRL DMKJID	DMKCFR DMKCKR DMKCPS DMKCQY DMKDAD DMKDAD DMKLNK DMKLNK DMKLNK
		DMKMNT DMKPEN DMKPSA DMKSF DMKSPT DMKTOD DMKUDU DMKVCR DMKVFD DMKVSJ	DMKMON DMKPER DMKPTR DMKSRM DMKSRM DMKUNT DMKVNT DMKVFE DMKVSP	DMKMOO DMKPET DMKQVM DMKSCH DMKSSS DMKURS DMKURS DMKVCT DMKVFR DMKVSR	DMKMSW DMKPGM DMKREI DMKSCO DMKSVC DMKUSQ DMKUSQ DMKVCV DMKVIO DMKVST	DMKNEA DMKREA DMKRET DMKSEP DMKSVD DMKTRK DMKVAT DMKVAT DMKVMC DMKVSV	DMKNET DMKRGA DMKRGA DMKSFB DMKTAP DMKTRP DMKVCA DMKVCA DMKVME DMKWRM	DMKNLD DMKPMA DMKRGB DMKSNC DMKTAQ DMKTRT DMKVCB DMKVCB DMKVMG DMKWRN	DMKOPE DMKPRG DMKRNH DMKSPL DMKTCS DMKTRU DMKVCH DMKVCR DMKVRS DMKXAD	DMKPEI DMKPRV DMKRPA DMKSPM DMKTCT DMKTCT DMKVCN DMKVCN DMKVSD DMKZTD

DMKSNC DMKVME

DMKDAD DMKGRG DMKPGS DMKSCH DMKVCX

DMKRGD

DMKPTT DMKHVF DMKMCD DMKVSP

DMKSVD DMKSVD DMKSVD DMKSVD DMKSVD

DMKBSC DMKCDM DMKCFS DMKCKS DMKCPU DMKCSC DMKCSC

DMKEXT

DMKEXT DMKIOT DMKLOH DMKLOH DMKPEL DMKPEL DMKPRW DMKRSE DMKSPS DMKUDP

DMKUDR DMKVCQ DMKVFC

DMKVSI

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LABEL

COUNT

REFERENCES

# **CP** Diagnostic Aids

This section contains the following information:

- Entry Points for CP Commands
- Function Codes for DIAGNOSE Instructions.

## **Entry Points for CP Commands**

The following is a list of CP commands and the modules that gain control to perform their functions:

Command	Entry Point(s)
ACNT	DMKCPVAC
ADSTOP	DMKCFDAD
ATTACH	(See footnote 1.)
ATTN	DMKCFJRQ
AUTOLOG	DMKALGON
BACKSPAC	DMKCSFBS
BEGIN	DMKCFJBE
CHANGE	DMKCSUCS DMKCSUCG
CLOSE	DMKCSQCL
COMMANDS	DMKCQSQC
COUPLE	DMKDIBCP
СР	DMKCFM
CPTRAP	DMKTRPST
DCP	DMKCDBDC
DEFINE	DMKDEFDS DMKDEFDG
DETACH	(See footnote 1.)
DIAL	DMKDIAL
DISABLE	DMKCPVDS
DISCONN	DMKUSODS
DISPLAY	DMKCDBDI
DMCP	DMKCDMDM
DRAIN	DMKCSODR
DUMP	DMKCDMDU
ECHO	DMKMSGEC
ENABLE	DMKCPVEN
EXTERNAL	DMKCPBEX
FLUSH	DMKCSFFL
FORCE	DMKUSOFL
FREE	DMKCSQFR
HALT	DMKCPSH
HOLD	DMKCSQH
INDICATE	(See footnote 1.)
IPL	DMKCFGIP
LINK	DMKLNKIN

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Command	Entry Point(s)
LOADBUF	DMKCSBLD
LOADVFCB	DMKCSBVL
LOCATE	DMKCFDLF
LOCK	DMKCPVLK
LOGOFF	DMKUSOLG
LOGON	DMKLOGON
MESSAGE	DMKMSGMS
MIGRATE	DMKPGMUS
MONITOR	DMKMCCCL
MSGNOH	DMKMSGNH
NETWORK	(See footnote 1.)
NOTREADY	DMKCPBNR
ORDER	DMKCSVOS DMKCSVOG
PER	DMKPEINT
PURGE	DMKCSVPS DMKCSVPG
QUERY	(See footnote 1.)
QVM	DMKQVMEP
READY	DMKCPBRY
REPEAT	DMKCSFRP
REQUEST	DMKCFJRQ
RESET	DMKCPBRS
REWIND	DMKCPBRW
SAVESYS	DMKCFHSV
SCREEN	DMKCFWEP
SEND	DMKSNDNH
SET	(See footnote 1.)
SHUTDOWN	DMKCPSSH
SLEEP	DMKCFJSL
SMSG	DMKMSGSM
SPACE	DMKCSFSP
SPMODE	DMKSPMEP
SPOOL	DMKCSPSP
SPTAPE	DMKSPTEP
START	DMKCSOST
STCP	DMKCDSCP
STORE	DMKCDSTO
SYSTEM	DMKCPBSR
TAG	DMKCSTAG

Command	Entry Point(s)
TERMINAL	DMKCFTRM
TRACE	DMKTRACE
TRANSFER	DMKCSVTS DMKCSVTG
UNLOCK	DMKCPVUL
VARY	DMKCPTNF
VMDUMP	DMKVMDEP
WARNING	DMKMSGWN
*	DMKCFM

¹DMKCFC uses a subcommand table in DMKCMD to find the entry points for the subcommands ATTACH, DETACH, INDICATE, NETWORK, QUERY, and SET. See the following chart.

Subcommand		Function Type	Entry Label in DMKCMD	Final Entry Label
АТТАСН АТТАСН	CHANNEL	R R	DMKCMDAT	DMKVDAAA DMKVDAAC
CACHE	ON OFF OWN QUERY	R R R R	DMKCMDCA	DMKCACON DMKCACOF DMKCAOWN DMKCACQY
DETACH DETACH	CHANNEL	R R	DMKCMDDE	DMKVDDDR DMKVDDDS
DETACH DETACH	CHANNEL	G G	DMKCMDDG	DMKVDDDG DMKVDDDC
INDICATE	DPA FAVORED I/O PAGING POSITION QUEUES USER LOAD USER	A, O A, O A A A A A A A, G G	DMKCMDIN DMKCMDIG	DMKTHIDP DMKTHIFA DMKTHIPA DMKTHIPA DMKTHIPO DMKTHIUS DMKTHIUS DMKTHIUG
NETWORK	SHUTDOWN ATTACH DETACH DISABLE DISPLAY DUMP ENABLE LOAD POLLDLAY QUERY VARY	O O,R O,R O,R O,R O,R O,R O,R O,R O,R	DMKCMDNO DMKCMDNR	DMKNESHD DMKNEAAH DMKNEADH DMKNESDS DMKNLEMP DMKNETEN DMKNLDR DMKNESPL DMKNESPL DMKNETQU DMKNETVA

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Subcommand		Function Entry Label Type in DMKCMD		Final Entry Label
QUERY	ALL DASD DUMP GRAF LINES MITIME STATUS STORAGE SYSTEM TAPES TDSK UR	R R R R R R R R R	DMKCMDQR	DMKCQPQA DMKCQPQD DMKCQPQG DMKCQPQL DMKCQPQL DMKCQSMI DMKCQTST DMKCQPQS DMKCQPQS DMKCQPQT DMKCQQQT DMKCQQQT
	VECTOR FILES HOLD PRINTER, PRT PUNCH, PCH	R S S S	DMKCMDQS	DMKCQPQU DMKVFCQV DMKCQHFS DMKCQRHD DMKCQHTS DMKCQHNS
	PONCH, PCH READER, RDR AFFINITY CPASSIST JOURNAL MINWS PAGING PRIORITY PSTOR QDROP RESERVE SASSIST SRM CPLEVEL CPUID CPLANG FILES LINKS LOGMSG NAMES PER PF PRINTER, PRT PROCESSR PUNCH, PCH READER, RDR SCREEN	S A,O A,O A,O A,O A,O A,O A,O A,O A,O A,O	DMKCMDQP	DMKCQHNS DMKCQHRS DMKCQRAF DMKCQYCA DMKJRLQU DMKCQSMW DMKCQRPG DMKCQRPG DMKCQRPR DMKCQSPS DMKCQSRE DMKCQYCA DMKCQYCA DMKCQYCA DMKCQYCA DMKCQYCA DMKCQYCA DMKCQYCA DMKCQYLM DMKCQYLM DMKCQYLM DMKCQYLM DMKCQYFF DMKCQYFF DMKCQYPF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQPAF DMKCQFAF
	SECUSER SET SPMODE S370E TERMINAL TIME CPTRAP USERS USERID VIRTUAL VMSAVE	G G G all G G F	DMKCMDQQ	DMKCQUSE DMKCQUST DMKCQYS9 DMKCQVS3 DMKCQUTE DMKCQVTI DMKCQCPT DMKCQYUS DMKCQYUI DMKCQYUI DMKCMDQV DMKCQYVS

Subcomma	n <b>d</b> °	Function Type	Entry Label in DMKCMD	Final Entry Label
QV ²	ALL CHANNELS CONSOLE DASD GRAF LINES STORAGE TAPES UR	G G G G G G G G	DMKCMDQV	DMKCQGQA DMKCQGQC DMKCQGQC DMKCQGQG DMKCQGQG DMKCQGQL DMKCQGQT DMKCQGQU
SET	AFFINITY CPASSIST FAVORED JOURNAL PRIORITY QDROP RESERVE SASSIST S370E	0 0 0 0 0 0 0 0 0	DMKCMDSO	DMKCFYAS DMKCFOSC DMKCSOSF DMKJRLSE DMKCFOSP DMKCFOSR DMKCFOSA DMKCFOS3
SET	DUMP LOGMSG MITIME	R R R	DMKCMDSR	DMKCFUDU DMKCFULO DMKCFUMI
SET	MINWS PAGING SRM	A A A	DMKCMDSA	DMKCFUMW DMKCFUPA DMKSRMSS
SET	MODE RECORD	C C	DMKCMDSC	DMKCFUMO DMKCFURE
SET	ACNT AFFINITY ASSIST AUTOPOLL CONCEAL CPCONIO CPUID ECMODE EMSG IMSG ISAM LINEDIT MIH MSG NOTRANS PAGEX RUN SMSG STBYPASS STMULTI SVCACCL TIMER VMCONIO VMSAVE WNG 370E		DMKCMDSG	DMKCFSAC DMKCFYAG DMKCFYSA DMKCFYSC DMKCFSCC DMKCFSCC DMKCFSEC DMKCFSIM DMKCFSIS DMKCFSIS DMKCFSIS DMKCFSNT DMKCFSNT DMKCFSNT DMKCFSNT DMKCFSSM DMKCFSSM DMKCFSSM DMKCFSSM DMKCFYSM DMKCFYSM DMKCFSVS DMKCFSVS DMKCFSVS DMKCFSVS DMKCFSVS

 2 QV - QUERY VIRTUAL

# **Function Codes for DIAGNOSE Instructions**

The following table indicates the DIAGNOSE codes used in VM/SP HPO and gives a brief explanation of their uses.

Function Code	Class	Function	DMKHVC Label	DMKHVD Label	DMKHVE Label	DMKHVF Label
000	any	Store extended identification code.		HVDSTIDX		
004	C,E	Examine data from real storage.		HVDCPC		
008	any	Execute VM/SP HPO CP commands.	HVCONFN			1
00C	any	Pseudo-timer facility.	HVCHRON			
010	any	Release virtual storage pages.	HVCPGRL			
014	any	Manipulate input spool files.		HVDSPRD		
018	any	Standard DASD I/O.	HVCDISK	1		1
01C	F	Clear error recording area.		HVDLRER		
020	any	General virtual I/O.	HVCFAKE			
024	any	Virtual device type inquiry.		HVDDTYP		
028	any	Dynamic CCW modification.	HVCDCPM	1		
02C	C,E,F	Get DASD address of error recording and number of cylinders allocated for error recording.			HVEEREP1	
030	C,E,F	Read a page of error recording data.			HVEEREP2	
034	C,E	Reads the system dump spool file.		HVDRSDF		
038	C,E	Reads the system symbol table.		HVDRDSYM		
03C	A,B,C	Dynamically updates the directory.		HVDDIRCT		
040	any	Clean up after virtual IPL by device.		HVDIPL		
044		Reserved for IBM use.	HVCEXIT			
048	any	Notify first level CP that this is a second level VM/370 or VM/SP system and this virtual machine has issued SVC 76.	HVCEXIT			
04C	any	Generate accounting records.		HVDACCT		
050	A,B,C	Saves 3704/3705 control program image.	1	HVD3705		
054	any	Enable or disable external interruptions.		HVDEXPA		
058	any	Virtual console interface for 3270.	HVCGRAF			
05C	any	Edit message according to EMSG settings.	HVCEMSG			
060	any	Provide virtual machine storage size.	HVCSTOR	1		
064	any	Load, find, or purge a named system.	HVCSYS			
068	any	Virtual Machine Communication Facility.	HVCVMCF			
06C	any	Low-address-protection interface for shadow table maintenance.	HVCSTBY			
070	any	Virtual machine accounting interface.		HNDVMAI		
074	A,B,C	Loads a 3800 printer named system into virtual storage.		HVD3800		
078	any	MSS communication.	HVCSSS			
07C	any	Virtual RDEVBLOK creation.		HVDVMPS		
080	any	Processes MSSFCALL instruction.	HVCMSSF			
084	В	Updates in-place a directory control statement in its online control block form.		HVDDUIP		
088		Reserved for IBM use.				
08C	any	Accesses certain device-dependent information.			HVEQRLY	
094	any	Dumps virtual storage.	T		HVEDUMP	

Function Code	1 Class	Function	DMKHVC Label	DMKHVD Label	DMKHVE Label	DMKHVF Label
098	any	Real I/O support for virtual machines.			HVEDRIO	
0A0	any	ACI security function.	HVCGRP			
0B0	any	Accesses diagnostic information saved for protected application facility users.			HVEPROT	
<b>0B</b> 4	any	Virtual printer external attribute buffer manipulation.			HVEXABD	
0B8	any	Spool file external attribute manipulation.			HVEXABS	
0BC	any	Opens a spool file for a spooled reader device.				HVFOSPID
0C8	any	Sets a language for CP.				HVFNLSST
0CC	Е	Saves a CP message repository for a language.				HVFNLSSA
0D0	any	3480 tape volume serial support.	HVCVOL			
0D4	В	Specifies an alternate user ID.				HVFALTID
0D8	D	Gets system spool information.				HVFSSI
0FC		Reserved for IBM use.				
100		Start of functions specified by a user.	HVCUSER			

### **Appendix A. Hardware Assist Commands**

### Hardware Assist Commands

Figure 37 illustrates the possible ways of running a virtual machine with various combinations of hardware assists and how the SET command affects their operation.

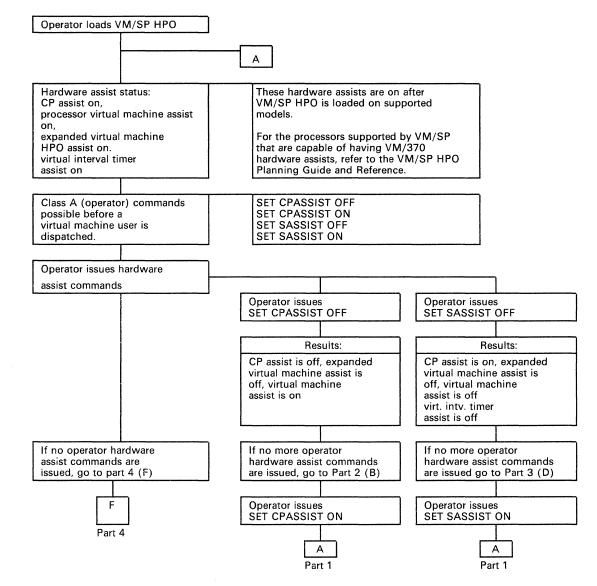


Figure 37 (Part 1 of 4). Hardware Assist Commands

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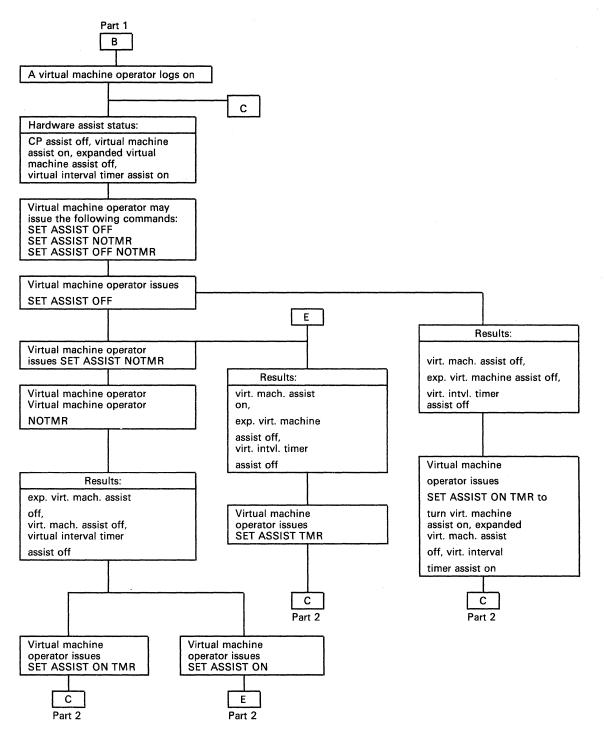
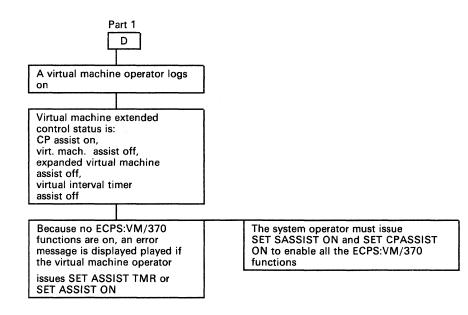
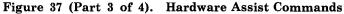


Figure 37 (Part 2 of 4). Hardware Assist Commands





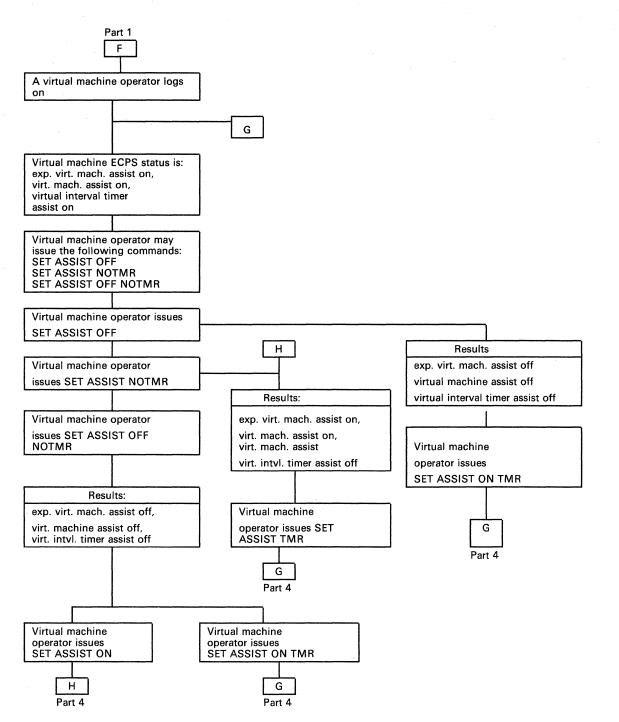


Figure 37 (Part 4 of 4). Hardware Assist Commands

### Assist Status according to ECPS Level

Assists are disabled or selectively enabled in different combinations, according to the ECPS level installed on your system. Use the following information as a guide:

- 1. On any ECPS level, the PTLB assist is disabled.
- 2. If the ECPS level is 17 or less, all of the CP assists are disabled.
- 3. If the ECPS level is greater than 22, all of the CP assists are disabled.
- 4. For ECPS levels 18 22, the following assists are disabled or enabled as shown below:

		PS Level 19	ECF 2	S Level	ECH 2	PS Level	ECPS Level 22
CP Assist	UP	AP/MP	UP	AP/MP	UP	AP/MP	
Get prime free*	D	D	D	D	D	D	Е
Get free storage*	Е	D	E	D	Е	D	E
Release free storage*	Е	D	E	D	Е	D	Е
Free CCW storage*	Е	D	E	D	Е	D	Е
Dispatcher 0	Е	Ε	E	Ε	Е	Ε	Е
Dispatcher 1&2*	Е	Е	E	Е	Е	Е	Е
Dispatcher 3&4*	D	D	D	D	D	D	Е
Dispatcher 5&6*	D	D	D	D	D	D	Е
SVC 8 (LINK)	Е	C	Е	С	E	С	Е
SVC 12 (RETURN)	Е	С	E	С	E	С	Е
Find virtual I/O control block	D	D	D	D	E	Е	Е
Find real I/O control block	E3D	4 E3D4	E3D	4 E3D4	E	Е	Е
Common CCW proc.	E	D	Е	D	E	D	Е
Diagnose 18 standard I/O*	D	D	D	D	D	D	Е

Legend:

- E Assist enabled (assist instruction left or inserted in code or appropriate mask bitin Control Register 6 set = 1)
- D Assist disabled (assist instruction in code replaced with no-op instructions or appropriate mask bit in Control Register set = <math>0)
- C Assist is crippled (assist instruction left in place but parameter list modified so that assist is effectively disabled)
- E3D4 Assist enabled if 3-bit mask shift used for RDEVBLOK indices (bit RDIDX in CPSTAT5 = 0)
  Assist disabled if 4-bit mask shift used for RDEVBLOK indices (bit RDIDX in CPSTAT5 = 1)
- * Assists marked with an asterisk are disabled if you generate your system with the FRET Trap logic.

Expanded VM Assist	18,		2	)	2		ECPS Level 22
SIO	D	D	D	D	E	Е	Е
ТСН	D	D	E	Е	E	Е	Е

Legend:

- E Assist enabled (assist instruction left or inserted in code or appropriate mask bitin Control Register 6 set = 1)
- D Assist disabled (assist instruction in code replaced with no-op instructions or appropriate mask bit in Control Register set = 0)

### Appendix B. VM/SP HPO MSS Support

### VM/SP HPO MSS Support

Following are annotated flow diagrams for the logic to support the IBM 3850 Mass Storage System.

### Log On a User Having a Minidisk on an Unmounted System Volume

### DMKLNK, CHK3330V

A required system volume is not mounted, try to get a 3330V mounted if the minidisk is a 3330.

### DMKSSSLN

Entry to mount an MSS system volume.

#### DMKSSS, FINDRDEV

Allocate a SYSVIRT real 3330V device. This may involve demounting a volume which is mounted but not in use. If there are none such volumes available, issue message DMKSSS080E and return with return code 8.

### DMKSSS, BLDCOMMT

Construct an MSSCOM, filling in the volume serial, device address selected, type of request (mount), and userid.

### DMKSSS, SETMNTFG

Build a CPEXBLOK for the return to DMKLNK after the MSC has processed the request. Chain it from field MSSTASK2. Build a CPEXBLOK for the return to DMKLNK after the mount is complete (pack change interruption received on the 3330V). Chain it from field MSSTASK1.

### DMKSSS, SCHMSSC

Put the MSSCOM in the queue, generate an attention interruption for the communication device if necessary, and exit to DMKDSP.

#### DMKSSS, HVC04ENT

Entry when DIAGNOSE code X'78', subcode 4 is received. OS/VS is ready to process an MSC request. Place the next MSSCOM in the virtual machine, and return to DMKHVC.

#### DMKSSS, HVC08ENT

Entry from DMKHVC when DIAGNOSE code X'78', subcode 8 is received. The MSC has processed the mount request.

### DMKSSS, RESETMQR

If there was an MSS error, write message DMKSSS083E and return to DMKLNK with return code 8.

### DMKSSS, MNTCOM

If there was not an MSS error, indicate that the MSSCOM is now waiting for the pack change interruption. Write message DMKSSS088I. Return to DMKLINK with return code 4.

### DMKLNK, MNTSETUP

Return from DMKSSS. Save the current workarea and control information. Return to caller.

### DMKDSB

Entry from DMKDAS on pack change interruption. If the device is a 3330V, look for an MSSCOM waiting for this volume serial. If one is found, stack a CPFXBLOK for entry to DMKSSSEN. Exit to DMKDSP.

### **DMKSSSEN**

Pick up the CPEXBLOK for DMKLNKSS and stack it.

#### DMKLNKSS

Complete the LINK processing for the minidisk.

### Log On a User Having a 3330V Dedicated As a 3330V

#### DMKLOG, CALLMSSA

Determine that a virtual 3330V is needed, save the UDEVBLOK, call DMKSSSL1.

#### DMKSSSL1

Go through device allocation, etc., to schedule a mount.

### DMKLOG, MSSMOUNT

If an MSS mount is in process (return code 4 from DMKSSS), proceed to get the next directory statement. Otherwise, find the RDEVBLOK for the device that DMKSSS allocated and continue the dedicate process.

### DMKLOGSS

Entry from DMKDSB and DMKSSSEN after mount.

#### DMKSCNRU

Get the RDEVBLOK.

### DMKVDSAT

Attach the virtual device.

### DMKLOG, TSTV333V

If the virtual device is a 3330V, set flag RDEV333V to indicate that there is no CP MSS CCW prefix.

### **DMKLOG, FREEUDEV**

If there is virtual I/O waiting, as indicated by a CPEXBLOK address in field MSSTASK3 of the MSSCOM used for the mount, stack the IOBLOK. Return to DMKDSP.

### **Process DIAGNOSE Code X'78'**

### DMKSSSHV

Entry from DMKHVC when DIAGNOSE code is X'78'.

### DMKSSS, HVC00ENT

The entry subcode was 0. Save the communication device address and the communicator VMBLOK address. Set PSAMSS indicating that the MSC is now available.

#### DMKSSS, HVC04ENT

The entry subcode was 4. If there is an MSSCOM in the queue to be processed, call DMKPTRAN to get the communicator's buffer address. Put the MSSCOM in the virtual machine buffer.

#### DMKSSS, HVC08ENT

The entry subcode was 8. The MSC has processed a request. If there was an error, write message DMKSSS088E, dequeue the MSSCOM, stack the return to the DMKSSS caller from MSSTASK2 with a return code 8, and return to DMKHVC. If there was no MSC error, stack the MSSTASK2 CPEXBLOK with a return code of 4, and return to DMKHVC.

### Generate the Channel Program Prefix for a 3330V

#### DMKCCW

Entry to generate a real channel program from a virtual machine channel program.

### DMKCCW, CCWINDSD

If the real device is a 3330V, set a flag indicating that the MSS channel program prefix is needed.

### DMKCCW, CCW02

If the prefix-needed flag is on and the virtual device is not a virtual 3330V, put the prefix in the RCWTASK.

### DMKCCW, DASDTBL AND DEDDTBL

These are tables of addresses of routines that are to get control to process specific CCW operation codes for DASD and dedicated devices. In each subroutine, a check is made to see if there is an unresolved MSS prefix. If so, it checks to see if the virtual channel program contains a SEEK. If so, it checks to see if the argument is used to generate the SEEK argument for the prefix. If not, the prefix CCW is set to SEEK to cylinder 0.

### Generate the Channel Program Prefix for CMS I/O to a 3330V

### DMKDGD

Entry to process I/O requests to DASD as initiated by the special DIAGNOSE code '78' interface from CMS.

#### DMKDGD, NOPRE

If the real device is a 3330V, set up the prefix in the RCWTASK.

### DMKDGD, CHKMOUNT

The VDEVBLOK for the virtual device could not be found. Check to see if there is an MSS mount in process for the required system volume. If so, build a CPEVBLOK for this request, put the address in the MSSTASK3 field of the MSSCOM, and exit to DMKDSP.

### **Process a Staging Adapter Cylinder Fault**

### DMKIOTIN

Entry when ending status is received from a device. Check to see if the CSW contains CE-DE with no error status.

### DMKIOT, TESTCYL

If the device type is a 3330V, see if the CE-DE is in the MSS prefix NOP CCW. If not, or if the device is dedicated as a virtual 3330V, stack the IOBLOK.

#### DMKSSS12

Set the IOBFLT flag, indicating that a cylinder fault is being resolved. Chain the IOBLOK from the REDEVFIOB field in the RDEVBLOK. Build a TRQBLOK to recognize missing attention interruptions and put it on the timer queue. Exit to the dispatcher.

### **Process an Attention Interrupt from a 3330V**

### DMKIOS, IOSUNSOL

Entry to process unsolicited I/O interrupts.

### DMKIOS, CALLMSSA

If the interrupt is an attention, the device is a 3330V, and it is not dedicated as a 3330V. Call DMKSSSI1 to restart I/O.

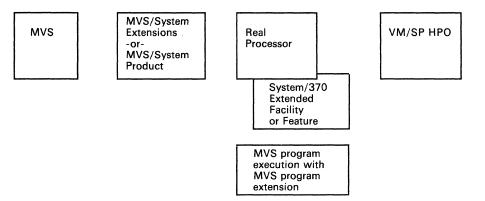
#### DMKSSSI1

Find each IOBLOK for this device that has the IOBFLT flag set. Find the associated timer queue element and remove it from the timer queue. Turn off IOBFLT so that the IOBLOK can be restarted when the device is available.

### **Appendix C. MVS Considerations**

MVS/System Extensions and MVS System Product support, available as a part of VM/SP HPO, allows an MVS system running in a virtual machine to utilize the enhancements available in the MVS/System Extensions Program Product (Program No. 5740-XE1) or the MVS System Product. Included in MVS/System Extensions and MVS/System Product support is the use of the System/370 Extended Facility of the 3031, 3032, 3033, and 3081 processors, or the System/370 Extended Feature of the Model 158 and 168 processors. For additional information on the System/370 Extended Facility or Feature, see the publication *IBM System/370 Extended Facility*, Order No. GA22-7072.

Figure 38 illustrates the relationship among MVS/System Extensions, MVS/System Product, VM/SP HPO, and the System/370 Extended Facility.



### Figure 38. Relationship among MVS/System Extensions or MVS/System Product, VM/SP HPO, and the System/370 Extended Facility

The system operator enables and disables MVS/System Extensions or MVS/System Product support for all virtual machines using the SET S370E ON/OFF command. The user enables and disables it for his own virtual machine via the SET 370E ON/OFF command.

VM/SP HPO determines the status of MVS/System Extensions or MVS/System Product support through two bits in DMKPSA: CP370EAV and CP370EON. CP370EAV indicates whether the feature is available (1 = yes, 0 = no). CP370EON indicates whether the feature, if available, may be used by virtual machines (1 = yes, 0 = no). When DMKCPI executes an MVS/System Extensions or MVS/System Product instruction, and the instructions does not cause a program check, DMKCPI sets the value of CP370EAV to one. CP370EAV may not be turned on or off after it is set. CP370EON is initialized to the same value as CP370EAV; it may be turned on or off via the SET S370E command. MVS/System Extensions and MVS/System Product support includes the following features:

- Low-address protection
- Common-segment facility
- Invalidate Page Table Entry (IPTE) instruction
- Test Protection (TPROT) instruction
- Virtual Machine extended-facility operations

### **Low-Address Protection**

Low-address protection prevents improper storing by instructions using logical storage addresses between 0 and 511. This facility is designed to prevent inadvertent program destruction of those storage locations that are used by the processors to fetch new PSWs during interruption processing. Low-address protection does not apply to the following:

- The storing of status by the processors (that is, old PSWs, logout data)
- Channel stores (for example, the CSW, LCL, or data)
- Diagnose instructions issued by the virtual machine

Bit 3 of control register 0 is defined as the low-address-protection bit, and is used to control whether or not stores using logical addresses between 0 and 511 are permitted. When this bit is zero, stores are permitted; when it is one, stores are not permitted. When an instruction attempts a store using an address between 0 and 511 and low-address protection applies, the contents of the storage area addressed by the instruction are not modified. The execution of the current instruction is terminated or suppressed, and a program interruption for a protection exception occurs.

Low-address protection is in force when all of the following conditions are met:

- 1. Bit 3 of the virtual machine's virtual control register 0 is set to one.
- 2. The VMF370E bit in the VMBLOK is on, indicating that MVS/System Extensions or MVS/System Product functions are enabled for this virtual machine.
- 3. The CP370EON bit in the PSA is on.
- 4. The virtual PSW indicates that EC mode is on.

The storage-protection routine, DMKPSASP, in DMKPSA also checks for violations against low-address protection (this routine is used for simulating privileged instructions, such as Store Then AND System Mask).

### **Common Segments**

The common-segment facility allows addressing segments to be classified as private or common. If bit 30 of the segment table entry for a given segment equals one, the segment is a common segment. Otherwise, it is private. A private segment table entry and the page table it designates may be used only in association with the segment table origin (STO) that designates the segment table in which the segment table entry resides. A common segment-table entry and the page table it designates may continue to be used for translating addresses even though a different STO is specified by changing control register 1.

DMKVAT ignores bit 30 of the virtual segment table entry when MVS/System Extensions or MVS/System Product support is enabled (that is, when the VMF370E bit in the VMBLOK and the CP370EON bit in the PSA are both on). If bit 30 equals one, and MVS/System Extensions or MVS/System Product support is not enabled, a translation-specification exception is reflected to the virtual machine.

In DMKVAT, tests are made at labels GIVE012 and LRAEXCP to determine whether the common-segment bit is the only "invalid" segment table entry bit on for a virtual machine with MVS/System Extensions or MVS/System Product support enabled. If it is, and the VMF370E bit in the VMBLOK equals one, and the CP370EON bit of the CPSTAT2 byte of the PSA equals one, then no translation-specification exception is generated. Execution returns to the in-line code after label DMKVATLA (for LRAEXCP) or label DMKVATRN for GIVE012).

### **Invalidate Page Table Entry (IPTE) Instruction**

Execution of the IPTE instruction causes the page table entry designated by the second operand (Ry) to be invalidated in the page table designated by the first operand (Rx), and the associated translation-lookaside buffer (TLB) entries in the system to be purged.

IPTE is an RRE-format instruction (four bytes long) with operation code X'B221'. The Rx field is in bits 24-27 of the instruction and the Ry field is in bits 28-31. The contents of register Rx have the format of a segment table entry with only the page table address used. The contents of register Ry have the format of a virtual address with only the page index used. Other fields of registers Rx and Ry are ignored. The translation format is contained in bits 8-12 of control register 0. The bit positions of register Ry that are selected as a page index depend on the segment and page size specified in control register 0.

Bits 8-12 of Control Register 0	Ry Bit Positions for Page Index
01000 ( $PS = 2K, SS = 64K$ )	16-20
10000 (4K,64K)	16-19
01010 (2K,1M)	12-20
10010 (4K,1M)	12-19

The page table address and page index are used, subject to the rules of dynamic address translation for page table lookup, to form a real address that designates a halfword page table entry. If the page size is 2K, bit 13 of the entry is set to one. Otherwise, the page size is 4K, so bit 12 of the entry is set to one. In any event, key protection applies to the access.

VM/SP HPO simulates an IPTE instruction by setting the proper bit in the virtual machine's page tables and then proceeding as if the instruction were a PTLB (that is, resetting the virtual machine's shadow tables and executing a PTLB with the proper synchronization procedures).

The secondary-operation decode routine for X'B2xx' instructions at label BEETWOS causes a branch to the routine DMKPRWIP in module DMKPRW. This routine simulates the IPTE instruction. DMKPRWIP computes the second-level page table (PGT) address, then calls TRANS21 to have the page table entry address translated to a real address. This address is checked for fetch and storage protection via calls to DMKPFAFP and DMKPSASP, respectively. Failing either protection check causes a branch to label PROTEXCP to reflect a protection exception to the virtual machine. After a successful protection check, DMKPRWIF sets the validity bit in the PGTE (PGTPVM) to one, which indicates that the entry is invalid.

If an IPTE instruction is executed by a virtual machine that does not have MVS/System Extensions or MVS/System Product support enabled, an operation exception is returned to the virtual machine.

### **Test Protection (TPROT) Instruction**

The TPROT instruction is an SSE-format instruction (six bytes long) with operation code X'E501'. The location specified by the first-operand address is tested for protection (including low-address protection) exceptions, using the key specified in bits 24-27 of the second-operand address. The first-operand address is subject to translation when dynamic address translation is on. Condition codes are set as follows:

- cc=0 Both fetching and storing are permitted.
- cc=1 Fetching is permitted, but storing is not.
- cc=2 Neither fetching nor storing is permitted.
- cc=3 Translation not available.

VM/SP HPO simulates the TPROT instruction in subroutine DMKPRWTP by comparing the key specified by bits 24-27 of the second-operand address to the key for the 2048-byte block specified by the swap table entry corresponding to the first-operand address. The virtual condition code is set to reflect the results of the comparison.

### Virtual Machine Extended-Facility Assist

The IBM System/370 Extended Facility includes four lock instructions, six tracing instructions, and the Fix Page and SVC Assist instructions, all of which are MVS-dependent instructions. All are SSE-format instructions (six bytes long) with operation code X'E5xx', where xx represents the secondary-operation code, code, which can take values between X'02' and X'0D'. These instructions are described in more detail in *IBM System/370 Extended Facility*, Order No. GA22-7072.

MVS/System Extensions and MVS/System Product support includes the virtual machine extended-facility assist for the 12 MVS-dependent operations. Control of this facility is handled by bits 1 and 29 of control register 6. Bit 1 is the virtual-machine problem-state bit (one indicates the problem state, and zero indicates the supervisor state), and bit 29 activates the virtual machine extended-facility assist for the MVS-dependent instructions. If bit 29 is one and bit 1 is zero, the MVS-dependent operations are executed, even when bit 15 of the real PSW is zero.

If bit 1 is one or bit 29 is zero, then all of the MVS-dependent operations cause program interruptions (privileged operation exceptions).

VM/SP HPO does not support the simulation of the MVS-dependent instructions; these instructions are executed by the hardware only.

Virtual machine extended-facility assist is activated in the following way:

When the VMF370E bit in the VMBLOK is one, bit 1 of real control register 6 is set to the same value as bit 15 (the problem state bit) in the virtual machine's PSW (VMPSW). Then the following tests are made:

- 1. Is the global MVS/System Extensions or MVS/System Product status bit equal to one (bit CP370EON in byte CPSTAT2 of the PSA)?
- 2. Is the extended-control-mode bit equal to one (bit 12 of VMPSW)?

If these tests are met, then:

- 1. Bit 29 of real control register 6 is set to one (this activates the virtual machine extended-facility assist).
- 2. Bit 3 of real control register 0 is set to the value in virtual control register 0 (this indicates whether low-address protection is enabled).

If these tests are not met, then:

- 1. Bit 29 of real control register 6 is set to zero.
- 2. Bit 3 of real control register 0 is set to zero.

These settings deactivate virtual machine extended-facility assist and low-address protection, respectively. In either case, processing continues normally.

### **Appendix D.** Access Verification Routines

The access verification routines provide the ability to install a security software package (such as RACF/VM) which will give you greater control over minidisk access, logon passwords, and movement of spool files.

Using the ACIGROUP directory control statement, you can identify userids as belonging to certain security groups. This allows you to restrict certain types of system activity to those who are present in the appropriate security groups. See the VM/SP HPO Service Routines and Program Logic for more information on the ACIGROUP control statement.

Three modules, DMKRPW, DMKRPI, and DMKRPD, contain the access verification routines. They are described below.

### **DMKRPW**

DMKRPW is called when either the LOGON or AUTOLOG command is issued. The command processor initializes ACIPARMS to pass the following information to DMKRPW:

- Function request code (X'10')
- Request groupname (if one exists)
- Request userid
- Logon passwords.

When a terminal issues the AUTOLOG command through an EXEC, the command processor initializes ACIPARMS to pass the following information to DMKRPW:

- Function request code (X'14')
- Request groupname (if one exists)
- Request userid
- Logon passwords.

Once a security package has been installed, the LOGON/AUTOLOG command processor recognizes the following return codes:

Return Code	Status
0	Request authorized
4	Request not verified
8	Request failed
12	User logged off
16	Terminal error

### DMKRPI

DMKRPI is called when the LOGOFF (FORCE), LINK, TRANSFER, SPOOL, or TAG command is issued. The appropriate command processor initializes ACIPARMS to pass information to DMKRPI. The information is different for each command.

ACIPARMS passes information to DMKRPI as listed below.

- For the LINK command:
  - Function request code (X'00')
  - Request groupname (if one exists)
  - Request userid
  - Target groupname (if one exists)
  - Target userid
  - Resource address
  - Access mode.
- For the SPOOL or TRANSFER command:
  - Function request code (X'04')
  - Request groupname (if one exists)
  - Request userid
  - Target groupname (if one exists)
  - Target userid
  - Resource address.
- For the TAG command:
  - Function request code (X'08')
  - Request groupname (if one exists)
  - Request userid
  - Target groupname (if one exists)
  - Target userid
  - Resource nodename.
- For the LOGOFF (FORCE) command:
  - Function request code (X'0C')
  - Request groupname (if one exists)
  - Request userid.

Once a security package has been installed, the LOGOFF (FORCE), LINK, TRANSFER, SPOOL, and TAG command processors recognize the following return codes:

Return Code	Status
0	Request authorized
4	Request not verified
8	Request failed, terminate command

**Entry Points Defined by DMKRPI for IUCV** 

DMKRPI defines entry points to support an IUCV interface. These entry points are:

DMKRPICN DMKRPISV DMKRPIIL DMKRPIQS DMKRPIRM

### DMKRPD

DMKRPD handles the DIAGNOSE code, X'A0'. Use DIAGNOSE X'A0' to retrieve a groupname for a given userid.

### **Entry Values**

The register specified as Ry contains the subcode X'00'. This requests DMKRPD to retrieve a groupname. The Rx register contains the address of a field consisting of 2 doublewords. The first doubleword is a userid. The second doubleword is empty. DMKRPD will return the groupname for the given userid to this empty field.

### **Exit Values**

If a groupname exists for the given userid, DMKRPD will return the groupname to the second half of the 2-doubleword field.

### **Condition Codes Set by DMKRPD**

DMKRPD sets the following condition codes:

Condition Code	Status
0	Request completed
1	Request failed



### Bibliography

### **Prerequisite Publications**

VM/SP HPO Release 5 Guide, Order No. SC23-0189

VM/SP HPO Operator's Guide, Order No. SC19-6225

VM/SP HPO CP for System Programming, Order No. SC19-6224

VM System Facilities for Programming, Order No. ST24-5288

VM/SP HPO CP Command Reference for General Users, Order No. SC19-6227

### **Corequisite Publications**

VM/SP Introduction, Order No. GC19-6222

VM/SP HPO Planning Guide and Reference, Order No. SC19-6223

VM/SP HPO Installation Guide, Order No. SC38-0107

VM/SP HPO System Messages Cross Reference, Order No. SC19-6226

VM/SP HPO OLTSEP and Error Recording Guide, Order No. SC19-6230

Virtual Machine Running Guest Operating Systems, Order No. GC19-6228

VM/SP HPO Data Areas and Control Block Logic- CP, Order No. LY20-0896

In addition, for EREP processing the following OS/VS Library manuals are required:

OS/VS, DOS/VSE, VM/370 Environmental Record Editing and Printing (EREP) Program Logic, Order No. GC28-0772

OS/VS, DOS/VSE, VM/370 Environmental Record Editing and Printing (EREP) Program Logic, Order No. SY28-0773

If the IBM 3850 Mass Storage System is attached, the following manuals are required:

OS/VS Message Library: Mass Storage System (MSS) Messages, Order No. GC38-1000

IBM 3850 Mass Storage System (MSS) Principles of Operation: Theory, Order No. GA32-0035

IBM 3850 Mass Storage System (MSS) Principles of Operation: Reference, Order No. GA32-0036

If the VTAM Communications Network Application (VM/VCNA) product is used, the following manual is a prerequisite:

IBM VM/VCNA Diagnostics, Order No. LY38-3033

For information on the Group Control System:

VM/SP Group Control System Guide, Order No. SC24-5249.

For information on VM/VTAM:

Network Program Products General Information, Order No. GC30-3350.

Network Program Products Samples: VM SNA, Order No. SC30-3309.

### **Supplementary Publications**

IBM System/360 Principles of Operation, Order No. GA22-6821

IBM System/370 Principles of Operation, Order No. GA22-7000

IBM 3270 Information Display System Components Description, Order No. GA27-2749

General Information Binary Synchronous Communications, Order No. GA27-3004

Input/Output Configuration Program User's Guide and Reference, Order No. GC28-1027

Field Engineering Programming Systems General Information Reference Summary, Order No. G229-2228. (

## VM/SP High Performance Option Library

To understand the interrelationships between the publications comprising the VM/SP High Performance Option library, see Figure 39.

### The VM/SP HPO Library

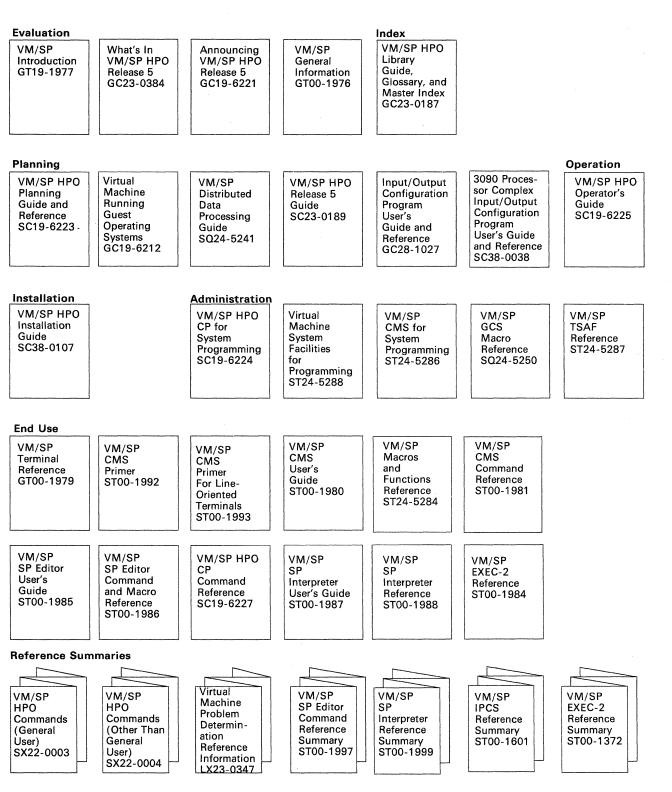


Figure 39 (Part 1 of 2). Library - Interrelationship of Publications

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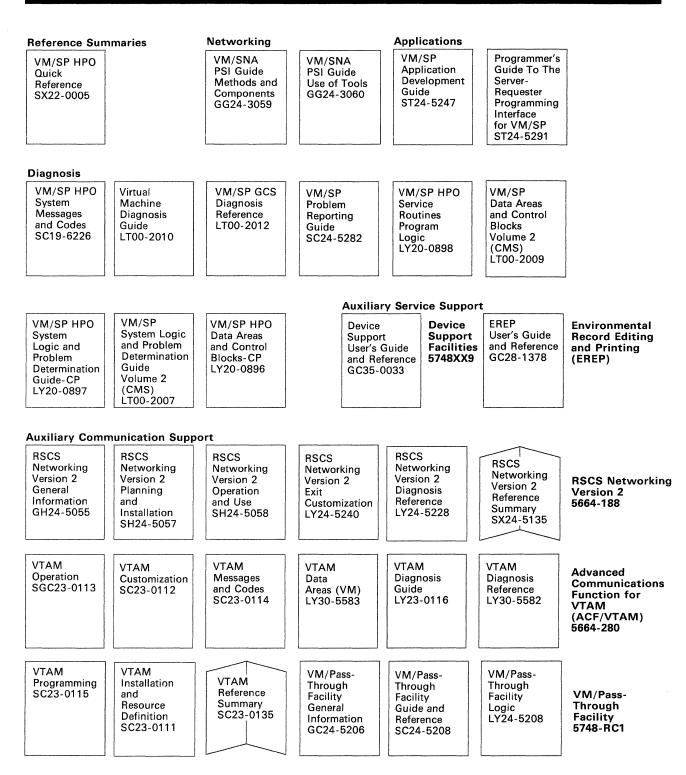


Figure 39 (Part 2 of 2). Library - Interrelationship of Publications

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Virtual Machine/ System Product High Performance Option

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