**Systems** 

# DOS/VS LIOCS Volume 3 DAM and ISAM Logic

Release 28



# First Edition (June, 1973)

This edition applies to Version 5 of the IBM Disk Operating System/Virtual Storage, DOS/VS, and to all subsequent versions and editions until otherwise indicated in new editions or Technical Newsletters. Changes are continually made to the information herein. Before using this publication in connection with the operation of IBM systems, consult the IBM System/360 and System/370 Bibliography, GA22-6822, and the IBM System/370 Advanced Function Bibliography, GC20-1763, for the editions that are applicable and current.

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....is the third in a series of four volumes providing detailed information about the IBM Disk Operating System / Virtual Storage (DOS/VS) Logical IOCS programs. The four volumes are:

- Volume 1: <u>General Information and</u> <u>Imperative Macros</u>, SY33-8559.
- Volume 2: SAM, SY33-8560.
- Volume 3: DAM and ISAM, SY33-8561.
- Volume 4: VSAM, SY33-8562.

This third volume is intended mainly for persons involved in program maintenance and for systems programmers who are altering the program design. Logic information is not necessary for the operation of the programs described.

General routines that apply to more than one access method or more than one file type are described in Volume 1. These routines include open/close, checkpoint/restart, and a number of transient routines. References to Volume 1 are made whenever required for a good understanding of the topics discussed.

This volume of the DOS/VS IOCS Logic Manuals consists of three parts:

- 1. LIOCS support for DAM files
- 2. LIOCS support for ISAM files
- 3. Charts.

Parts 1 and 2 supply descriptions of the declarative and imperative macros, DTF tables, and initialization and termination procedures for each of the file types described. Part 3 supplies the detailed flowcharts associated with the descriptions in the first two parts.

The appendixes in the back of the manual provide maintenance personnel with the service aids:

- 1. Label list
- 2. Message cross-reference list.

Effective use of this publication requires an understanding of IBM System/370 operation and the Disk Operating System / Virtual Storage (DOS/VS) Assembler Language and its associated macro definition language. Reference publications for this information are listed below.

# PREPRECUISITE PUBLICATIONS

- DCS/VS Data Management Guide, GC33-5372.
- DOS/VS Supervisor and I/O Macros, GC 33-5373.
- OS/VS and DOS/VS Assembler Language, GC33-4010.
- DOS/VS LIOCS Volume 1, General Information and Imperative Macros, SY33-8559.

# RELATED PUBLICATIONS

- DOS/VS LIOCS Volume 2, SAM, SY33-8560.
- DOS/VS LIOCS Volume 4, VSAM, SY33-8562.
- DOS/VS Supervisor Logic, SY33-8551.
- DOS/VS Messages, GC33-5379.

For the titles and abstracts of other related publications, refer to the <u>IBM</u> <u>System/360</u> and <u>System/370</u> Bibliography, GA22-6822.

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Chart MJ. \$\$BOIS10: ISAM Open,	Chart ND. \$\$BCRTV1: ISAM RETRVE Cpen,
Integrity Phase 2 (1 of 2)	Phase 1 (2 of 3)
Chart MK. \$\$EOIS10: ISAM Open,	Chart NE. \$\$BCRTV1: ISAM RETRVE Cpen,
Integrity Phase 2 (2 of 2)	Fhase 1 (3 of 3)
Chart NA. \$\$ECISCA: ISAM Close (1 of 2) 323	Chart NF. \$\$BCRTV2: ISAM RETRVE Cpen,
Chart NB. \$\$BCISOA: ISAM Close (2 of 2) 324	Fhase 2 (1 cf 2)
Chart NC. \$\$BORTV1: ISAM RETRVE Open,	Chart Ng. \$\$BORTV2: ISAM RETRVE Cpen,
Phase 1 (1 of 3)	Phase 2 (2 cf 2)

Direct Access (DA) files refer to files contained on DASD devices and processed by the Direct Access Method. Note that the term <u>Direct Access</u> applies to a method of processing DASD records and not to a type of file organization.

# DIRECT ACCESS METHOD

The Direct Access Method provides a flexible set of macro instructions for creating and maintaining a data file on a DASD device. This technique applies specifically to records organized in a random order, but it can also be used to process records sequentially. The macro language offered by this data management method permits the user to lcad, read, write, update, add, or replace records on a DASD file.

The Direct Access Method is an IOCS processing method specifically designed to utilize the capabilities of direct access storage devices. This method provides the following facilities:

- Processing of records organized in a random order.
- Processing, in physical sequence, of a file of records stored by record key.
- Utilizing track capacities.
- Two referencing methods:
  - Record ID (physical track and record address),
  - Record KEY (control field of the logical record).
- Multiple track searching beyond the specified track for resolving the key argument.

Providing a means of supplying the user with the Record Identifier (ID) of either the current record or the next record after a READ or a WRITE operation has been executed.

The Direct Access Method is subject to the following restrictions:

- Only unblocked records are processed.
- No work area and only  $\underline{one}$  I/C area can be specified for the file.
- The user must supply either a track reference or a record identifier for every record read or written by logical

DASD files processed by the Direct Access Method must be defined for lcgical IOCS by a DTFDA macro. If a DASD file is processed ty physical IOCS in a manner similar to the Direct Access Method, the file must be defined by a DTFPH macro.

# DTFDA MACRO

Whenever a file of DASD records is processed by the Direct Access Method, the logical file must be defined by a DTFDA macro. This macro generates a partial DTF table to describe the characteristics of the file for logical ICCs as shown in Figure 1. The DTF table is completed by the channel program builder subrcutine in the DA logic module. This subroutine tuilds, and inserts into the DTF table, the channel program CCWs needed to process the file. The number and specific nature of the CCWs varies with the imperative macros used with the file. Figure 12 summarizes the CCW chains needed to accomplish the function of a particular imperative macro.

DTF Assembly Label	Module DSECT Label	Bytes	Bits	Function
&Filename	IJICCB	0-15 (0-F)		Command Control Block (CCB).
! ! !	IJIMOD     	16   (10)       	1 2 3 4 5 6	1 = Trailer labels  Used by FREE macro  1 = COBCL Open/Ignore option  1 = Track hold option specified  1 = DTF relocated by OPENR  Not used  1 = SPNUNE  Used by CNTRL macro
1	<b> </b> 	  17-19  (11-13)		  Address of logic module. 
		20 (14)		DIF type for OPEN/CLOSE (X'22' = direct access files).
	IJISWI   	21 (15)	1   2   3   4   5   6	1 = Output; 0 = Input. 1 = Verify option specified. 1 = Search multiple track (SRCHM) specified. 1 = WRITE AFTER or WRITE RZERC macro used. 1 = IDLCC specified. 1 = Undefined; 0 = FIXUNB, VARUNB, or SPNUNB 1 = RELTYPE = DEC 1 = End of file.
1	   IJIFNM	22-28		Filename (DIF Name).
 	  IJIDVTP 	(16-1C)   29   (1D)		Device Type. X'00' = 2311, X'01' = 2314, 2319, X'02' = 2321, X'04' = 3330.
	IJIUNT 	  30-31  (1E-1F) 	  - 	Starting logical unit address of the first volume containing the data file. This value is supplied by the OPEN from EXTENT cards (can be initially zero).
	  IJIULB 	   32-35   (20-23)		Address of user's label routine.
	   IJIUXT 	(20-23)   36-39   (24-27)	İ	Address of user's routine for processing EXTENT    information.
	IJIRELPT   	40   (28) 		Pointer to relative address area:    &Filename.P - &Filename   2
	IJIERC	  41-43  (29-2B)	<b>1</b> 1 1	  Address of a 2-byte field in which IOCS can store  the error condition or status codes.
	  IJITST     	44-45   (2C-2D)     	İ	Macro code switch for internal use:   X'0000' = READ ID  X'0001' = READ KEY  X'0002' = WRITE ID  X'0003' = WRITE KEY  X'0004' = WRITE RZERO  X'0005' = WRITE AFTER
	  IJIBPT 	  46-47  (2E-2F)	 	  Pointer to channel program build area  (&Filename.E) minus 32.
	  IJICB2 	  48-63  (30-3F)	 	  Control seek CCB 

Figure 1. DTFDA table (1 of 6).

DTF Assembly	Module DSECT Label	Bytes	Bits	Function
&Filename.Z	IJICCW	64 <b>-7</b> 1		Control Seek CCW for overlap seek routine.
 	  IJIXMD 	(40-47) 72-75 (48-4B)	İ	
	IJIMSZ	76-77	İ	Maximum data length for FIXUNB or UNDEF records;
] 	IJISPT 	(4C-4D) 78 (4E)	,	BLKSIZE for VARUNB or SPNUNB records.    Pointer to READ ID string (Filename.0); X'00' if    no READ ID issued.
!   		79 (4F)		Pointer to READ KEY string (Filename.1); X'00' if   no READ KEY issued.
   		80 (50)		  Pointer to WRITE ID string (Filename.2); X'00' if   no WRITE ID issued.
 		81 (51)		Pointer to WRITE KEY string (Filename.3); X'00'    if no WRITE KEY issued.
 		82 <b>(</b> 52 <b>)</b>		Pointer to WRITE RZERO string (Filenaπe.4); X'00'   if no WRITE RZERO issued.
!   		83 (53)		Pointer to WRITE AFTER string (Filename.5); X'00'   if no WRITE AFTER issued.
; 	IJITRK	84-85		Track constant:    2311: H'0' if key length = 0,     H'20' if key length ≠ 0.
				20   11 key length + 0.    2314/2319: H'0' if key length = 0,     H'45' if key length + 0.
	<b>1</b>			3330: H'135' if key length = 0,
! 				H'191' if key length \( \tau \)    2321: H'0' if key length \( \tau \)     H'16' if key length \( \tau \)
1 	IJIRIC	86-87 (56-57)		10   11 key length + 0.    2311:   H'61'    2314/2319:   H'101'
 		7		3330: H'135'   2321: H'84'.
   	IJILAT /	88 (58)	-	  Not used
! !		(38)	2	1 = Non data transfer error.    Not used.
910705 2 Butto	E1200		4 5-6	1 = No room found
220		89		
 	<b> </b>	(59)	1	1 = Track overrun. 1 = End of cylinder.
<b>[</b>			3	1 = Data check when reading key or data.    1 = No record found.
[   			5	1 = End of file.   1 = End of volume.   Not used.
   	   IJILBTK 	90-95 (5A-5F)	) 	  Label track address, XBCCHH, where X is the    volume sequence number of the device on which the   label track is located.

This is the end of the common DTFDA table.

Figure 1. DTFDA table (2 of 6).

The following section is included if UNDEF, AFTER, or RZERO is specified.

DTF Assembly	Module  DSECT Label	  Bytes	Bits	Function
&Filename.L	IJILST	96-143 (60-8F)		Basic CCWs to build channel program
		144-183 (90-B7)		Basic CCWs for undefined length or formatting macros (see Figure 9).
   	IJIVIT 	184-185   (B8-B9)  		Instruction to give record length to user if record length is undefined. (NOPR 0 if no RECSIZE specified.)
 	IJIFRU 	186-187   (BA-BB) 		Instruction to get record length from user if record length is undefined. (NOPR 0 if no RECSIZE specified.)
&Filename.F	IJIFLD	188-192 (BC-C0)		Work area (used for R0 address - CCHH0).
&Filename.K	   IJICNT 	193-200 (C1-C8)		Work area (used for RO data field).
&Filename.C	IJICTS	201-208  (C9-D0)		Work area (included only for spanned or variable records for record count field).

The channel program builder strings are generated following the DTFDA table, and preceding the channel program building area. (See Figure 8 for the channel program builder string to be used for each macro.)

  &Filename.0	  Variablo 	Channel program builder string for READ ID macro.  If READ ID is not specified, the string is not generated.
&Filename.1		
&Filename.2		Channel program builder string for WRITE ID   macro. If WRITE ID is not specified, the string   is not generated.
&Filename.3		Channel program builder string for WRITE KEY   macro. If WRITE KEY is not specified, the string   is not generated.
&Filename.4 	  Variable   	
&Filename.5	  Variablo   	

Figure 1. DTFDA table (3 of 6).

The following section contains the channel program build areas and varies in size.

	ssembly bel	Module  DSECT Label	    Bytes	Bits	   	Function
  &File   C	ename.B こといじ	SILT HERE	0-7			CCW that is generated at program assembly and used by all channel programs.
1	į	1	Variable		  Area	to build:
1	İ				1.	Eight CCWs if AFTER is not specified.
	ļ		! !		2.	Eight CCWs if spanned or variable length records and AFTER=YES is specified.
! ! !	   		 		3.	Seven CCWs if undefined or fixed records and AFTER=YES is specified.
!			  Variable		  Area	tc build:
   	i	l 	 		1   1. 	Eight CCWs if AFTER is not specified and VERIFY=YES is specified.
 	   	! !	 		2.	Eight CCWs if spanned or variable length records and AFTER=YES and VERIFY=YES are specified.
       	   		     		3.	Five CCWs if undefined or fixed records and AFTER=YES and VERIFY=YES are specified.

The following section is added for spanned records only.

DTF Assembly Label	Module DSECT Label	Bytes	Bits	Function
		8 bytes		Count save area.
<b>!</b> !		8 bytes		SEEKADR save area.
<b>!</b> !		1 byte	0	1 = Relative addressing.
! !			1	1 = IJIGET switch on.
 			2	1 = Ignore hold switch on.
! !			3	Reserved for use by DAMODV.
! !			4	1 = New volume SEEKADR.
! 			5-7	Not used.
		1 byte		keserved.
		2 bytes		kecord size.
		12 bytes		Work area.
    	 	8 bytes	   	Control word save area.

Figure 1. DTFDA table (4 cf 6).

The following section is added to the DTFDA table if DSKXTNT (relative addressing) is specified.

DTF Assembly Label	Module DSECT Label	Bytes	Bits	Function
&Filename.P		3 bytes		3X'00' for padding.
&Filename.I		5 bytes		IDLOC record area (bucket used by mcdule).
&Filename.S	<b> </b>	8 bytes		SEEKADR in form:
   				M,B1,B2,C1,C2,H1,H2,R
		4 bytes		DC A(&SEEKADR)
		4 bytes		DC A(&IDLOC)
		8 bytes		Work area for REITYPE=DEC.
&Filename.X 	<b>!</b>	4 bytes		  Save area for CCHH portion of actual DASD  address.
! ! !		4 bytes		Alteration factor for C1 in SEEKADR (see bytes   112-119):
		4 bytes		2311: x'00000001' 2314, 2319: x'00000001' 3330: x'00001300' 2321: x'000003E8'  Alteration factor for C2 in SEEKADR (see bytes 112-119):  2311: x'00000000A' 2314, 2319: x'00000014' 3330: x'00000064'

Figure 1. DTFDA table (5 cf 6).

DTF Assembly	Module  DSECF Label	Bytes	Bits	Function
		4 bytes		Alteration factor for H1 in SEEKADk (see bytes   112-119):
] 				2311: X'CCCCCCC1'   2314, 2319: X'00000001'   3330: X'CCCCCCC1'   2321: X'00000014'
       		Variable  tc end  of DTF  table		DSKXTNT table composed of a variable number cf    8-byte entries containing extent information    in the following format:
! ! ! !				Bytes C-2 TTT2 -cumulative number of tracks in   the DSKXTNT table entries up to   and including the current entry.
]   				3 M -volume sequence number.
				4 B -bin number (0 for disk devices).
				Bytes 5-7 TTT1 -relative track number of lower limit of this entry.
   				A 2-byte end-of-table indicator containing    X'FFFF' follows the last entry in the DSKXTNT    table.

Numbers in parentheses are displacements in hexadecimal notation.

Figure 1. DTFDA table (6 cf 6).

Bytes	Bits	Function
0-15 (0-F)		ссв.
16 (10)		X'08' indicates DTF relocated by OPENR.
17-19 (11-13)		3x'00'
20   (14)		DTF type (X'23').
21 (15)		Option ccdes.
	4	<pre>1 = Output, 0 = Input. Not used. Not used. Not used. Not used. Not used. 1 = 2321 (Version 1/2 only). Not used.</pre>
22-28   (16-1C)	 	Filename.
29   (1D) 		Device type code: X'00' = 2311 X'01' = 2314, 2319 X'02' = 2321 X'04' = 3330.
30-31 (1E-1F)		Logical unit address of first volume containing the file.
32-35 (20-23)		Address cf user label routine.
36-39 (24-27)	   	Address of user routine to process EXTENT information.

Numbers in parentheses are displacements in hexadecimal notation.

Figure 2. DTFPH table for DAM files.

# DTFPH MACRO

Figure 2 illustrates the DTF table generated by the DTFPH macro when the parameters DEVICE=2311/2314/3330/2321 and MOUNTED=ALL are specified in the macro operand. The table contains the information to define a DASD file for processing by physical IOCS, in a manner similar to the Direct Access Method.

### REFERENCE METHODS AND ADDRESSING SYSTEMS

Each record read or written must be identified by providing the logical IOCS routines of the Direct Access Method with two references:

- Track reference location of the track within the pack or cell.
- Record number (ID), or Record Key (control information) - position of the record on the track.

The user can specify the track reference or record ID as either an actual physical DASD address or as an address relative to the start of the file. If relative addressing is used, the address provided by the user has been converted to either a 4-byte hexadecimal or a 10-byte decimal address. Actual physical addresses are supplied as 8-byte DASD addresses. Further details of the addressing systems are presented in the following discussion of reference methods.

#### TRACK REFERENCE

Before issuing a read or write instruction, the user must supply the proper track identification in the track reference field in main storage. (This field is identified by the SEEKADR= parameter specified in the DTFDA macro.) The track identification can be expressed in one of three formats depending on the addressing system used.

- Actual physical addressing the track identification is contained in the first seven bytes of the 8-byte track reference field (MBBCCHHR).
- Relative addressing (RELTYPE=HEX) the track identification is contained in the first three bytes of the 4-byte track reference field (TTTR).
- 3. Relative addressing (RELTYPE=DEC) the track identification is contained in the first eight zoned decimal bytes of the 10-byte track reference field (TTTTTTTRR).

The track reference selects the channel and unit on which the referenced track is found.

#### RECORD ID

Reference to a particular record can be made by supplying a specific number in the track reference field. This number (ID) refers to the consecutive position of the record on the given track; that is, the first data record on a track is number 1, the second is number 2, etc.

The form in which the record ID is supplied in the track reference field also depends on the addressing system used.

 Actual physical addressing - the record ID is the last byte (R-byte) in the 8-byte track reference field (MBBCCHHR).

- Relative addressing (RELTYPE=HEX) the record ID is the last byte (R-byte) in the 4-byte track reference field (TTTR).
- 3. Relative addressing (RELTYPE=DEC) the record ID is the last two zoned decimal tytes (RR) in the 10-byte track reference field (TTTTTTTRR).

When a READ or WRITE macro that searches for record ID is executed, logical IOCS refers to the track reference field to determine which record is requested by the program. The number in this field is compared with the corresponding field in the count areas of the DASD records.

When a READ ID macro is executed, IOCS searches the specified track for the particular record. If the record is found, the key area (if present and defined by the KEYLEN= parameter in the DTFDA macro) and the data area of the record are transferred into the main storage I/O area. If the corresponding record ID (R portion of the count area on the track) is not found, a no record found indicator is placed in the user's error status indicator. The WRITE ID operation is the same as the READ ID except a record is written instead of read.

#### RECORD KEY

If the DASD records include <u>key</u> areas, the records can be identified by the control information contained in the key. Whenever this method of referencing is used, the problem program must supply the key of the desired record to logical IOCS before a READ or WRITE macro is issued. When a READ or WRITE macro is executed, IOCS searches the track identified by the track reference field for the desired key. The search is confined to one track unless multiple track search is specified by the user. (See <u>Multiple Track Search</u>.)

If the desired key is not found on the track, IOCS posts a no record found indication in the user's error status indicator. When the desired key is found, IOCS reads the data area of the DASD record into main storage if a READ KEY macro was issued.

When a WRITE KEY macro is executed and the desired key is found, IOCS transfers the data in main storage to the data area of the DASD record. This replaces the information previously recorded in the data area.

When the record address supplied by the user in the track reference field (SEEKADR) is in relative address form, it must be converted to an actual DASD address (CCHHR) before it can be handled by the routines of the DA logic modules. The Seek Overlap subroutine in the logic module performs the conversion.

If the user wants to express the relative address as a 10-byte zcned decimal number (RELTYPE=DEC), the address is packed and converted to binary so that it takes the hexadecimal TTTR form before conversion to an actual address.

Conversion to an actual DASD address starts by comparing the TTT value given in the user-supplied relative address with the TTT2 value of each entry in the DSKXTNT (Refer to Figure 13 and to Relative Addressing under Initialization and Termination in this section of the manual.) The proper DSKXTNT entry is reached when the TTT2 value of the entry exceeds the TTT value in the address. The M and B2 values from the table entry are inserted into the seek address, MBECCHHR (B1 is always 0). The reconversion factor is calculated by subtracting the TTT1 value of the current extent entry from the TTT2 value of the previous entry. The reconversion factor is saved for reconversion of an actual address to a relative address if IDLOC is specified.

The user's TTT value is then divided, in turn, by the three device-dependent alteration factors; C1, C2, and H1 (refer to Figure 14). The quotient after each divide operation is placed in the respective position in the seek address. For example; the quotient (after the TTT value is divided by the C1 alteration factor), is inserted in the first C-byte of the seek address, MBBCCHHR. The remainder after each divide operation becomes the dividend for the next divide operation. The remainder after the final divide operation is the H2 value in the seek address, MBBCCHHR. The R-byte of the actual seek address is identical to the R-byte (or equivalent to the RR bytes if decimal relative addressing is used) in the TTTR relative address.

If a record ID is returned to the user in relative address form after a READ or WRITE macro instruction is executed (IDLOC specified), reconversion is accomplished by reversing the conversion process. Thus, the corresponding CCHH portions of the actual address are multiplied by the respective alteration factors and the reconversion factor is added to the result.

Again, the R-byte remains unmodified throughout the reconversion process. the decimal form of relative addressing is specified, the TTTR hexadecimal form is further converted to the 10-byte zoned decimal form TTTTTTTRR.

#### MULTIPLE TRACK SEARCH

The Direct Access READ KEY and WRITE KEY macro routines for processing DASD files normally search one track for the desired logical record. The user can specify a search of multiple tracks by including the CTFDA entry SRCHM (SeaRCH Multiple tracks) in the DTF. When SRCHM is specified, 10CS begins the search for a specified record key on the track specified in the track reference field. The search continues until one of two conditions occur:

- An equal compare occurs between the key argument (record key) in main stcrage and the key of the required record.
- The end of the specified cylinder is reached.

The search for multiple tracks continues through the cylinder, even though part of the cylinder may be assigned to a different logical file. This occurs with cr without relative addressing. IOCS provides the user with an end of cylinder indicator when the search reaches the end of a cylinder. This indicator is placed into the error/status byte by ICCS.

#### IDLOC

The parameter IDLOC= is provided (in both the DTFDA and DAMOD or DAMODV macros) if the user wants to identify records after each READ or WRITE operation is complete. If specified, IDLOC identifies a main storage location where ICCS supplies the address (either actual or relative) of a DASD record. If spanned records are being processed, the ID returned will be that cf the first segment of the record. address returned in location IDLCC after a particular macro depends on a variety of conditions. These conditions and the addresses returned are summarized in Figure 3.

When the problem program references a record by ID or KEY and does not specify the search multiple tracks (SRCHM) option, IOCS returns the ID of the next record under normal conditions. If the user is processing records sequentially on the

basis of the next ID, he can check the ID supplied by IOCS against his file limits to

determine when he has reached the end of his logical file.

Read/Write	SRCHM = YES				SRCHM 7	 	Seek address		
Function	Normal  No  I/O  reco  complete four		*End of cylinder	Normal I/O  ccmplete	No record found	*End  of  cylinder	EOF  record  read	not in   extent   area	
READ Filename,KEY	Same  record	Blank	Next record	Next record		Next  record	Dummy record	Dummy record	
WRITE Filename,KEY	Same record	Blank	Next record	Next record		Next record	Dummy record	Dummy record	
READ Filename,ID	Next record	Dummy record	Next record	Next reccrd		Next  record	Dummy record	Dummy record	
WRITE Filename,ID	Next record	Dummy record	Next record	Next reccrd	Dummy record	Next record	Dummy record	Dummy record	
WRITE Filename, AFTER	None	Dummy record	Dummy record	None		Dummy record	Dummy record	Dummy record	
WRITE Filename,RZERO	None	Dummy record	Dummy record	None 		Dummy record	Dummy  record	Dummy record	

|\*If an end-of-cylinder condition coincides with either a physical or a logical end of |volume, the ID supplied is that of the first record on the next volume. If this |condition occurs on the last volume, the ID supplied in IDLOC is equal to the maximum |number of tracks for the file. A dummy record is supplied when a physical end of |volume is reached if actual DASD addressing is used.

# Dummy record:

Actual addressing ------ 5 bytes (CCHHR) containing X'FFs Relative addressing (HEX) -- 4 bytes (TITR) containing X'FFs Relative addressing (DEC) -- 10 bytes containing decimal 9s

Figure 3. Record ID returned to IDLOC.

If the next record ID is returned to IDLCC, LIOCS searches for the ID of the next record on the specified cylinder. If an end of cylinder occurs before the next record is found, logical ICCS:

- Posts the end-of-cylinder bit in the error/status indicator, and
- Updates the address to head 0, record 1 of the next cylinder, and posts this updated address in IDLOC.

It is possible that there will be no record at this new address. In this case, logical IOCS posts a no-record-found in the error/status indicator. Two ways to avoid this possibility:

- Initialize the volume by writing a dummy record at the beginning of each cylinder.
- Add 1 to the record address and read or write again, and continue this process until logical IOCS finds the desired record.

CONTROL FIELD - SPANNED RECORDS

Figure 4 illustrates the format of the 8-byte control field associated with each spanned record. The first four bytes are called the <u>block descriptor</u> word and contain information supplied by LIOCS when

the record is written. The second four bytes are called the <u>segment descriptor</u> word and contain segment type information, the user supplied record length, and the <u>segment control</u> flag.

<u>Normal Segment</u>: The term normal segment refers to any segment of the kind described by the segment control flag.

<u>Null Segment</u>: The term null segment refers to a special 8-byte segment (control field only) that may be written by a WRITE AFTER macro when the file is being created. A null segment is written as the last record on a volume and indicates that the next logical record is written on a new volume. Spanned records do not span volumes; that is, the first portion of a logical record cannot exist on one volume and the remainder on another.

#### ERROR/STATUS INDICATOR

When processing records in a DASD environment, certain exceptional conditions must be handled within the program. Because the method used for handling these exceptional conditions depends on the application and operating environment, the logical IOCS routines of the Direct Access Method provide the user with exception indicators.

The user must specify a symbolic name for the address of a 2-byte field where IOCS places the exceptional condition codes. The symbolic name is written by the user in the DTFDA entry ERRBYTE. When needed, IOCS sets one or more of the bits in this error/status indicator for the conditions illustrated in Figure 5.

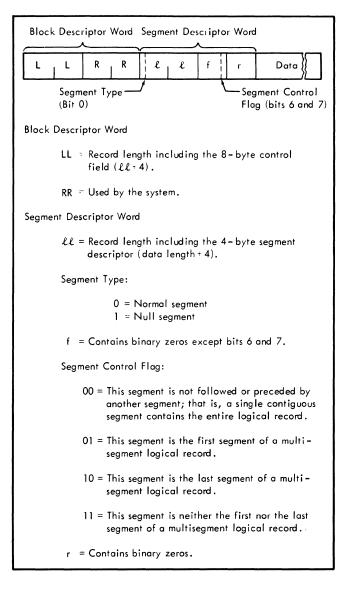


Figure 4. Spanned record control field.

Byte	Bit	Error/Status Indicator	Explanation
     0	0		Not used.
0     0     1     1     1	1	Wrong-length record	FIXUNB records: This bit is set on whenever the data length or key length of a record differs from the criginal record. If an updated record is shorter than the original record, the updated record is padded with binary zeros to the length of the criginal record. If the updated record is longer than the original record, the criginal record positions are filled and the rest of the updated record is truncated and lost.
   	   		UNDEF records: This bit is set on under the   following conditions:
			<ul> <li>When a READ is issued and the record is greater than the maximum data size (BLKSIZE   minus KEYLEN; or BLKSIZE minus the value of   KEYLEN plus eight, if AFTER is used), a wrong-length error condition is given and the value returned in the RECSIZE register   is that of the actual record length.</li> </ul>
			• When a WRITE ID or KEY is issued and the record to be written is greater than the maximum data size, a wrong-length error condition is given and the record written is equal to that of the maximum data length. If the DASD record is larger than the maximum data size, the remainder of the record is padded with binary zeros. The value in the RECSIZE register is set equal to that of the maximum data length.
			<ul> <li>When a WRITE AFTER is issued and the record to be written is greater than the maximum data size, a wrong-length error condition is given and the record written is truncated to the maximum data length. The value in the RECSIZE register is set equal to that of the maximum data length.</li> </ul>
	 		<u>VARUNB records</u> : This bit is set on under the   following conditions:
	       		<ul> <li>When a READ is issued and the LL (data   length + 8) count of the record read is greater than the maximum value specified by   the BLKSIZE= parameter in the DTFDA macrc.</li> </ul>
	         		• When a WRITE ID or KEY macro is issued and the LL count is greater than the value specified by the BLKSIZE parameter in the DTFDA macro. The record is written with the low-order bytes truncated.

Figure 5. Error/status indicator (1 of 4).

Byte	Bit	Error/Status Indicator	Explanation
			<ul> <li>When a WRITE AFTER macro is issued and the LL count is greater than the value specified by the BLKSIZE parameter in the DTFDA macro. The record is written with the low-order bytes truncated.</li> </ul>
	i !		SPNUNB records: This bit is set on under the following conditions:
	 		<ul> <li>When a READ macro is issued, the     wrong-length record error indicator is set     if the LL (data length + 8) count is larger       than the value specified by the BLKSIZE     parameter in the DTFDA macro. The number of       data bytes read into the I/O area is equal     to the value of BLKSIZE minus 8 bytes for     the control words.</li> </ul>
	   		<ul> <li>When a WRITE ID or KEY macro is issued, the wrcng-length record indicator is set if:</li> </ul>
	     		1. The LL ccunt for the record to be written exceeds the value specified by the ELKSIZE parameter in the DTFDA macro.
	!     		<ol> <li>The data length of the record to be written exceeds the data length of the original record.</li> </ol>
	!		In either of the above cases the record is written with the low-order bytes truncated.
	1 		The wrong-length record indicator is also set when the first segment encountered for the original record is not type 00 or 01. In this case the no-record-found (bit 4 in byte 1) indicator is also set on and no portion of the new record is written.
	! 		The wrong-length record indicator is set for multisegment records if any segment of the criginal record encountered after the first segment is not type 11 or 10. In this case the remainder of the new record is not written.
	 		<ul> <li>When a formatting WRITE AFTER macro is issued and the LL count for the record being written exceeds the value specified by the BLKSIZE parameter in the DTFDA macro. The record is written with the low-crder bytes truncated.</li> </ul>
	2         	Nondata Transfer Error      -  -  -	The record in error was neither read nor written.   If ERREXT is specified and this bit is cff (0),  transfer took place and the problem programmer    should check for other errors in the ERREYTE  field.

Figure 5. Error/status indicator (2 of 4).

Byte	Bit	Error/Status Indicator	Explanation
0	3		Not used.
0	4       	No-room-found	The no-rccm-fcund indication is applicable only when the formatting WRITE AFTER macro is used for a file. If the bit is set on, ICCS has determined that there is not enough rccm left to write the record; consequently, the record is not written.
0	5		Not used.
0	   6		Not used.
   0 	   7   	Record out of extent area	The relative address given is outside the extent   area of the file. No I/C activity has been   started and no other error indicators are set.
1	0	  Data check in ccunt area	This is an unrecoverable error.
   1 	   1 	  Track overrun   	The number of bytes on the track exceeds the theoretical capacity. (Will not occur when DOS macro instructions are used.)
   1         	   2           	  End-of-cylinder           	The end-of-cylinder indicator bit is set on when   SRCHM is specified for a READ or WRITE KEY and   the end-of-cylinder is reached before the record   is found (bit 4 of byte 1 is also turned on). If   IDLCC is also specified, certain conditions also   turn this bit on, possibly in conjunction with   the no-record-found indicator (bit 4 in byte 1) -   for further information see IDLCC.
1		  Data check when reading  key or data	This is an unrecoverable error.
1 1	   4   	  No-record-found   	
	 		If SRCHM=YES is specified, the end-of-cylinder indicator (bit 2 in byte 1) is also set on.
	1               		For SPNUNB records: the no-record-found indicator is also set on if, when the identified record is found, the control flag in the first segment encountered is not 00 or 01. In this case, the no-record-found indicator is set on in conjunction with the wrong-length-record indicator (kit 1 of byte 0).

Figure 5. Error/status indicator (3 of 4).

Byte	Bit	Error/Status Indicator	Explanation
	5	End-of-file	The end-of-file indication is applicable only when the record to be read has a data length of zero. The ID returned in IDLOC, if specified, is hexadecimal FFFFF. The bit is set only after all the data records have been processed. For example, in a file having n data records (record n+1 is the end-of-file record), the end-of-file indicator is set on when the user reads the n+1 record. This bit is also posted when an end of volume marker is detected. It is the user's responsibility to determine if this bit means true EOF or end of volume on a SAM file.
1   1                     	6 	End-of-volume	The end-of-volume indication is given in conjunction with the end-of-cylinder indicator (tit 2 of tyte 1). This bit is set on if the next record ID (CCHHR where CC = n+1, HH = 0, and R = 1) that is returned on an end-of-cylinder is higher than the volume address limit. The volume address limit is: cylinder 199, head 9, for a 2311; cylinder 199, head 19, for a 2314 or 2319; cylinder 403, head 18 for a 3330; and subcell 19, strip 5, cylinder 4, head 19, for a 2321. These limits allow for the reserved alternate track area.  If both end-of-cylinder and end-of-volume indicators are set on, the ID returned in IDLOC is FFFFF.
1	7		Not used.

Figure 5. Error/status indicator (4 of 4).

# CAPACITY RECORD (RZERO OR RO)

The Direct Access Method utilizes the first record on each track, RO, to monitor the amount of available space on the track. This record is unique in that it does not contain a key area even though keys may be specified for the data records of the file.

The Direct Access Method reads the data portion of the R0 record into the Filename.K lccation in the DTF table. The data portion has the following format:

- 5-bytes The identifier (CCHHR) of the last record written on the track.
- 2-bytes The number of unused bytes remaining on the track.
- 1-byte Flag for the Direct Access Method for the Operating System.

# WRITE RZERC MACRO

The WRITE Filename, RZERO macro is used to erase a specified track. To do this, the programmer must supply the track address in the track-reference field identified by the SEEKADR= parameter of the DTFDA macro. The system locates the track, restores the number-of-bytes-remaining information in the data field of the RO record to the maximum capacity of the track, and erases the remainder of the track after the RO record.

# FORMATTING MACRO

The formatting WRITE Filename, AFTER macro is used to write a record after the last current record on a specified track. To perform this function, the problem

programmer must supply, in the location specified by the SEEKADR= parameter of the DTFDA macro, the address of the track on which the new record is to be written. This form of the WRITE macro cannot return the ID of the new record in the IDIOC

When the formatting WRITE AFTER macro is used to write FIXUNB or UNDEF records on a file, the first eight bytes of the user's I/O area must be reserved for LICCS. Therefore, the blocksize (BLKSIZE) must be equal to:

# 8 + (KEYLEN, if specified) + DL

The ID of the new record can be found in the first five bytes of the I/O area after the write operation is complete because LIOCS uses the eight bytes that are reserved for the record count field with the following format:

- 5-byte track ID (CCHHR)
- 1-byte key length (KL)
- 2-byte data length (DL)

When the formatting WRITE AFTER macro is used to write VARUNB or SPNUNB records on a file, the first eight bytes of the user's I/O area contain the record control (Refer to Figure 4 for the information. format of the 8-byte control field.) Therefore, the blocksize (BLKSIZE) must be equal to:

#### Maximum DL + 8

The ID of the new record can be found in the DTF table at location Filename.C after the write operation is complete. This area of the DTF table is generated specifically for VARUNB and SPNUNB records and is used for the count field of the new record. It has the following format:

- 5-byte track ID (CCHHR)
- 1-byte key length (KL)
- 2-byte data length (DL)
  - \* For VARUNB and SPNUNB records, DL includes the 8-byte control field.

#### DAM LOGIC MODULE MACROS

IBM supplies two macros to generate independent logic modules needed to process records under the Direct Access Method. These macros are:

- DAMOD for fixed-length unblocked and undefined records.
- DAMODV for variable-length unblocked and spanned unblocked records.

The modules generated by these macros include routines for the basic imperative macros READ and WRITE, which allow the user to read, write, update, add, or replace records in the file.

#### DIRECT ACCESS MODULES

Under the Direct Access Method an individual module, either DAMCD cr DAMCDV (depending on the record format specified), provides the logic to support all the imperative macros used to process the file. In each case, the CNTRI, FREE, and WAITF macros have individual entries into the required module; that is, the logic for executing these macro functions is tailored to the specific macro. Cn the other hand, the input/cutput macros (READ and WRITE, with their variations) have a common entry to the respective module and a common logic in the module for performing their functions.

When the user issues a READ or WRITE macro instruction for a file, program control transfers to a logical ICCS  $\,$ routine, which builds the proper channel program to accomplish the command. The IOCS routine issues an execute channel program that causes the I/O request to start. IOCS then returns control to the problem program. A WAITF macro instruction must be issued by the user before the next READ or WRITE for the file. The WAITF macro routines test the status of the channel to ensure that the operation is complete. If the channel is busy, the routine waits for I/C completion. The WAITF macro routines supply indications of exceptional conditions to the problem program in the error/status indicator. the completion of the I/C operation, control returns to the problem program.

# DAMOD: Input/Output Macros Chart AA

Objective: To read or write a fixed-length unblocked or undefined record on a direct access file.

Entry: From any Input/Output macro used with the Direct Access Method.

Exit: To the problem program via linkage register 14.

Method: Each of the six Input/Output macros has a unique expansion that results in a branch to a different entry point in the module. The entry point is at one of a series of exclusive OR instructions. The exclusive OR instructions cause a unique bit structure to be set up in a one-byte macro switch in the DTFDA table. From this macro switch, the module determines which macro has been issued.

After the macro switch has been set, a test is made for undefined records or an end-of-file condition. If neither, the data length is set to the maximum length. If end of file, the data length is set to zero. If undefined and a read operation, the data length is set to the maximum. For a WRITE AFTER, WRITE KEY, or WRITE ID instruction, this routine gets the data length from the user, and determines whether it is greater than the maximum length. If so, it is set to maximum and the wrong-length record bit is set on in the DTF table. The CCW data areas are then updated, and a branch and link is made to the seek overlap routine to position the device for subsequent processing. If track hold has been specified, and entry to the seek overlap routine was not from the CNTRL or FREE macros, an SVC 35 is performed to hold the track.

Next, this routine branches to the channel program builder to build the CCW chain for the macro that is being processed (refer to Figure 12). A test is then made for a WRITE AFTER or WRITE RZERC macro being processed. If neither of these, this routine issues the SVC 0 to perform a read or write operation. Control then returns to the problem program.

If the macro is a formatting macro (WRITE AFTER or WRITE RZERO), additional processing is necessary. If the macro is WRITE AFTER, RO is read and the capacity of the track is checked. If the space remaining on the track is not large enough for the record, the no-room-found bit is set on in the DTF and control returns to the problem program.

If the track capacity is large enough, the routine calculates the space remaining on the track after the record is written and stores it in the RO write area. The channel program builder then builds a CCW chain to WRITE AFTER, updates the previous record ID by 1 in the RO write area, and tests for end of file. If end of file, the key and data length fields in the count field are set to zero. If not end of file, the key and data lengths of the record are inserted in the count field. An SVC 0 is then issued to write out the record. If track hold has been specified, an SVC 36 is

issued to free the held track, and control returns to the problem program.

If the macro issued is a WRITE RZERO, CCWs are mcdified, and a new RO record is written. If track hold has been specified, the module issues an SVC 36 to free the track. Control then returns to the problem program.

#### DAMOD: WAITF Macro Charts AB-AE

Cbjective: To ensure that the transfer of a record has been completed, to supply the ID of a record to the user, if IDLCC is specified, and to post error conditions in the error/status indicator, if necessary.

Entry: From the WAITF macro.

Exit: To the problem program.

Method: After saving the user's registers, this routine first issues an SVC 7 WAIT macro to ensure that the previous I/O operation is complete. The second error byte from the CCB is placed in the error/status indicator in the DTF table.

If IDLOC is specified, IOCS surplies the user with the ID of a record after each READ or WRITE is completed (see Figure 25). If IDLOC is specified, a test is made for the type of macro issued. If a READ KEY cr WRITE KEY macro, the routine determines if the search multiple track option (SRCHM) has been specified. If so, the ID returned to the user is the ID of current record transferred.

If a READ or WRITE KEY macro has been issued without a search multiple track option, or a READ or WRITE ID macro has been issued, the ID returned to the user is the ID of the next record location, unless an end-of-cylinder condition is encountered. In this case, the ID returned is that of the first record of the next cylinder. If an end-of-volume condition is detected while updating the cylinder address, the end-of-volume bit is set in the error status indicator in the DTF table, and a dummy record is returned in IDLOC.

After the module determines the contents of IDLOC, the error/status bytes are set in accordance with the conditions posted to the CCB by physical IOCS, and returned to the user. Then, if record length is undefined and a READ macro has been issued, the record length is calculated and returned to the user. This routine then restores the user's registers, resets the macro switch in the DTF table, and returns

control to the problem program via linkage register 14.

#### DAMCD: CNTRL Macro Chart AF

Objective: To perform nondata operations on a file. For a disk device, a seek operation is executed. For a 2321, either a seek operation or a restore operation is executed.

Entry: From the CNTRL macro.

Exit: To the problem program.

Method: This routine saves the user's registers, and then branches and links to the seek-overlap routine, which performs the nondata operation (seek for a disk device, seek or restore for a 2321). the operation has been completed, the user's registers are restored, and control returns to the problem program via linkage register 14.

# DAMOD: FREE Macro Chart AF

Objective: To release a protected (held) track on a direct access storage device.

Entry: From a FREE macro expansion in the problem program.

Exit: To the problem program.

Method: After storing the user's registers, the FREE routine branches to the seek-overlap subroutine. The subroutine determines the seek address of the held track from the seek CCW in the channel program build area. The mcdule (M) number from the seek address calculates the symbolic unit address which is then inserted into the control-seek CCB. 36 is issued to free the held track. After completing the subroutine, the FREE routine restores the user's registers and returns control to the problem program.

# DAMCDV: Input/Output Macros Charts AK-AN

Objective: To read or write a variable-length unblocked cr a spanned unblocked record on a Direct Access file.

Entry: From any Input/Output macro used with the Direct Access Method.

Exit: To the problem program via linkage register 14.

Methcd: Each of the six Input/Cutput macros has a unique expansion that results in a branch to a different entry point in the module. The entry point is to one of a series of exclusive CR instructions, which cause a unique bit pattern to be set up in a 1-byte macro switch in the DTFDA table. The module determines which macro has been issued by testing this switch.

READ Macro - VARUNB Records: The procedure followed for both the READ ID and the READ KEY macros is exactly the same. The only difference between the two macros is in the CCW chain built by the channel program builder subroutine, IJISBID. Refer to Figure 12, Chart I for READ ID; Chart J for READ KEY.

The byte count in the basic read data CCW (Figure 9) is set equal to the length specified by the user in the BLKSIZE= parameter for the DTFDA macrc. The IJISOVP subrcutine is then entered to execute a controlled seek to the proper track. Next, the channel program builder subroutine is used to build the required channel program. The channel program is executed to read the record into the I/O area and control is returned to the problem program.

READ Macro - SPNUNB Records: The procedure followed for both the READ ID and the KEAD KEY macros is exactly the same. The only difference between the two macros is in the CCW chain built by the channel program builder subroutine, IJISBID. Refer to Figure 12, Chart I for READ ID; Chart J for READ KEY.

The byte count in the basic read data CCW (Figure 9) is set equal to the length specified by the user in the BLKSIZE= parameter for the DTFDA macrc. The IJ1SOVP is then entered to execute a controlled seek to the proper track. Next, the channel program builder subroutine is used to build the required channel program, which is then executed to start the read operation. If the segment descriptor for the record read indicates that it is a null segment or that the segment contains the entire logical record (segment type 00), control is returned to the problem program.

If the record read is segment type 01 (the first segment of a multisegment record - at this point, segment types 10 or 11 would be in error) which indicates that the rest of the logical record continues on another track, the CCW chain is modified and the seek address is updated to the next track. One of the modifications made to the READ ID CCW chain is the substituting cf a TIC\*+8 CCW for the RDKD CCW when

KEYLEN is specified. This is done because the record key is associated only with the first segment of a multisegment record.

The last eight bytes of the last portion of the record read into the I/C area are temporarily stored in the DTF table to allow the control words (block descriptor and segment descriptor) of the next segment to be read in along with the data (see Figure 6); these bytes are later restored after the control word information for the next segment is processed. The mcdified channel program is reexecuted to read the next segment into the I/O area and its length is added to the combined length of the previous segments. The combined total length is then compared to the BLKSIZE specified by the user. Should the combined length exceed the BLKSIZE, a wrong-length-record (WLR) indicator is set. If the segment just read is type 11 neither the first nor the last segment of a multisegment record) the procedure described in this paragraph is repeated.

When the last segment (type 10) is read, the combined length of all the record segments is posted to the segment

descriptor word in the I/C area and control is returned to the problem program.

WRITE Macro - VARUNB Records: The procedure followed for both the WRITE ID and the WRITE KEY macros is exactly the same. The only difference between the two macros is in the CCW chain built by the channel program builder subroutine, IJISELD. Refer to Figure 12, Chart K for WRITE ID and VERIFY, Chart L for WRITE ID and NO VERIFY; Chart M for WRITE KEY and VERIFY, and Chart N for WRITE KEY and VERIFY.

The logical record length (11) is obtained from the user's segment descriptor word in the I/O area. The length specified for the record plus four bytes for the block descriptor word is then tested to see if it is greater than the maximum block length specified in the BIKSIZE parameter of the DTFDA macro. If it is not greater than the BLKSIZE value, the byte count in the basic read data CCW (RDD CCW - Figure 9) is set equal to the specified 11 + 4 (that is, IL) value. If, on the other hand, the LL value is greater than the BLKSIZE value, the record capacity

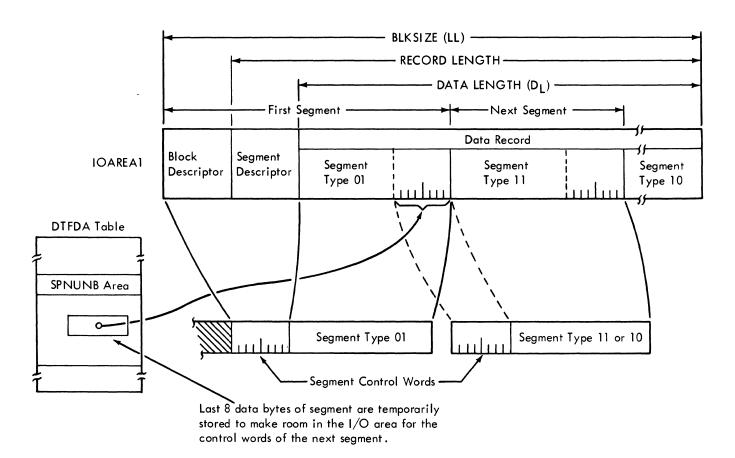


Figure 6. Multisegment spanned record.

register(IJICPR) and the RDD CCW byte count are set equal to the BLKSIZE value and the wrong-length-record (WLR) indicator is set. This causes truncation of the record when it is written.

The IJISOVP subroutine is entered to execute a controlled seek to the propertrack. Next, the channel program builder subroutine is used to build the required channel program, which is then executed to write (and verify, if so specified) the record. Control is then returned to the problem program. If the track hold option has been specified, the track on which the record written resides is freed before control is returned.

WRITE Macro - SPNUNB Records: The procedure followed for both the WRITE ID and the WRITE KEY macros is exactly the same. The only difference between the two macros is in the CCW chain built by the channel program builder subroutine, IJISBLD. Refer to Figure 12, Chart K for WRITE ID and VERIFY, Chart L for WRITE ID and NO VERIFY; Chart M for WRITE KEY and VERIFY, and to Chart N for WRITE KEY and NC VERIFY.

The IJISOVP subroutine is entered to execute a controlled seek to the proper track. Next, the channel program builder subroutine is used to build the first portion of the WRITE macro channel program. It is at this point that spanned record handling differs markedly from the handling of records of other formats.

The first portion of the WRITE macro channel program (refer to Figure 12, Charts K or L for WRITE ID; Charts M or N for WRITE KEY) is actually a CCW chain to read the eight bytes of control information contained in the existing DASD record. This read operation is necessary because, before a spanned record can be written, the arrangement of the record being replaced must be determined. That is, it must be known if the existing record is contained in a single DASD segment (type 00) or in multiple DASD segments and, if in multiple segments, the lengths of the individual segments. Thus, for each segment of a multisegment spanned record, it is necessary to execute a read and a write operation.

If the segment control flag in the segment descriptor of the existing record is type 00, the record to be written is handled in a manner similar to a normal variable-length record. That is, the channel program builder subrcutine is entered to build the write CCW chain, the channel program is executed to write the new record, and control is returned to the problem program.

If the segment control flag in the DASD segment read is type 01 (the first segment cf a multisegment record), the channel program builder subroutine is entered to build the write CCW chain. The CCW chain is then mcdified according to the various options specified for the type of macro being used. Next, the length of the current DASD segment is determined from the control words obtained by the read operation and compared to the user-specified length of the record to be written (LL). If the record length is less than the length of the current segment, the byte count in the write data (WRD) CCW is changed to the length of the record (if VERIFY is specified, the byte count in the verify read data CCW is likewise changed). Otherwise, the CCW byte count remains equal to the length of the segment that can be accommodated on the track; that is, the length of the current segment. The channel program is then executed to write the segment.

After the first segment of the record is written, the seek address is updated to the next track and a similar procedure is followed for the next segment(s) of the record. During the procedure for writing segments after the first segment, the last eight data bytes of the preceding segment in the I/C are temporarily stored in the DTF table to allow the control words of the subsequent segment to be read into the I/O area (see Figure 6). The segment length obtained from the control words is used to set the byte count in the WRD CCW for all type 11 segments. Each time a segment is written, its length is added to the combined lengths of the previously written segments, and the total is subtracted from the user-specified record length. The result of this calculation is the number of bytes in the record that remain to be written. When the last segment (type 10) is written, this remainder is used to determine if the new record is larger cr smaller than the original record. If it is larger, a WLR indicator is set and the truncated remainder of the record is written; if smaller, the byte count in the WRD CCW is reduced to the value necessary to write the remainder of the record.

Because each segment of a multisegment spanned record is handled as an individual physical DASD record, if the VERIFY option is specified, each segment is verified after it is written and before the next segment is read. Therefore, if VERIFY is used, three I/O operations are required for each segment: read, write, and read.

WRITE AFTER Macro - VARUNB Records: The byte count of the basic read data CCW (Figure 9) is set equal to the block length (LL) of the record to be written, and the

IJISOVP subroutine is entered to execute a controlled seek to the track specified by the user. The channel program builder subroutine, IJISBLD, is then used to build the first portion (read RZERC) of the channel program for the WRITE AFTER macro (refer to Figure 12, Chart 0).

Next, the ID (CCHHR) of the R0 record on the specified track is set up in the DTF table, at location Filename.F, and the channel program is executed to read the 8-byte data field of R0 into the DTF table at location Filename.K. The data field of the R0 record contains the following information:

- Bytes 0-4: The CCHHR of the last record currently written on the track.
- Bytes 5-6: The number of unused bytes currently remaining on the track.
- Byte 7: Not used by DCS/VS.

Using the information contained in bytes 5 and 6 of the RO data field, a test is made to determine if sufficient room exists on the track to write the new record. If enough room is not available, the no-room-found indicator is set in the DTF table and control is returned to the problem program.

If there is enough room on the track for the new record, the DASD space that remains after the new record is written is calculated to update the RO record. Next, the channel program builder subroutine is used to build the rest of the WRITE AFTER channel program, which includes the CCWs needed to write the updated RO record and the new record (and verify both, if so specified).

The channel program is then executed and control is returned to the problem program. If the track hold option has been specified, the track is freed before control is returned.

WRITE AFTER Macro - SPNUNB Records: The procedure followed for the WRITE AFTER macro for spanned records is the same as that followed for variable-length records up to the point of testing to determine if there is sufficient room on the specified track for the new record. For spanned records, the test is first made to determine if a minimum length (KEYLEN + 9) segment can be written in the space remaining on the track. If not, the no-room-found indicator is set in the DTF table and control is returned to the problem program. If the minimum length segment can fit, a second test determines

if the entire record can be written on the track. If it can, the record is written in the manner described for variable-length records.

If the entire record will not fit in the space remaining on the specified track, the length of the portion that can fit is calculated and subtracted from the user-specified length of the record. The seek address is then updated to the next track.

The RO record for the next track is read and checked for full availability; that is, if the track is not empty, a no-room-found indicator is set and control is returned to the problem program. The data field of the  ${\tt R0}$  record is tested to determine if all the remaining bytes of the record (plus eight tytes for control words) can be contained on the new track. If not, the length of the largest single record that fits on a track is subtracted from the number of record bytes remaining to be written, and the seek address is once again updated. This process is repeated until the point is reached where the entire logical record can te accommodated. If the track hold option has been specified, a hold is placed on all the tracks checked.

The channel program builder subroutine is then used to build the second portion of the WRITE AFTER channel program, and the first segment of the record is written on the specified track. If KEYLEN is specified, the key is written with the first segment. The rest of the record is then written in as many segments as necessary, along with the RO records for each of the tracks involved. If the track hold option has been specified, the tracks are individually freed after the respective segment is written.

If, during the checking of the series of tracks needed to write the record, the updated seek address indicates a change to a new volume, the RO records of all the tracks between the user-specified track and the first track on the new volume are rewritten with their respective data fields indicating no space available. Checking is reinitiated on the new volume and, when it is established that sufficient room is available on the new volume, the first segment (and, if specified, the record key) is written on the first track. The rest of the record is written on subsequent tracks in the normal manner.

WRITE RZERC Macro - VARUNB or SPNUNB Records: The IJISCVP and IJISBID subrcutines are entered in sequence to execute a controlled seek to the specified track and build the channel program. The ID for the RO record (CCHHO) on the

specified track is set up in locations Filename.F and Filename.K in the DTF table. The number of bytes remaining on the track is set equal to the full track capacity and inserted into bytes 5 and 6 cf the RO data field (Filename.K). The channel program is then executed to erase the track and write the updated RO record, after which control is returned to the problem program.

# DAMCDV: CNTRL Macro Chart AF

Objective: To perform nondata operations
on a file. For a disk device, a seek operation is executed. For a 2321, either a seek operation or a restore operation is executed.

Entry: From the CNTRL macrc.

Exit: To the problem program.

Method: This routine saves the user's registers, and then branches and links to the seek-overlap routine, which performs the nondata operation (seek for a disk device; seek or restore for a 2321). the operation has been completed, the user's registers are restored, and control returns to the problem program via linkage register 14.

#### DAMCDV: FREE Macro Chart AF

Objective: To release a protected (held) track on a direct access storage device.

Entry: From a FREE macro expansion in the problem program.

Exit: To the problem program.

Method: After storing the user's registers, the FREE routine branches to the seek-overlap subroutine. The subroutine determines the seek address of the held track from the seek CCW in the channel program build area. The module (M) number from the seek address calculates the symbolic unit address which is then inserted into the control-seek CCB. An SVC 36 is issued to free the held track. completing the subroutine, the FREE routine restores the user's registers and returns control to the problem program.

#### DAMOEV: WAITF Macro Charts BA-BC

Chjective: To ensure that the transfer of a record has been completed, to supply the ID of a record to the user, if IDLOC is specified, and to post error conditions in the error/status indicator, if necessary.

Entry: From the WAITF macro.

Exit: To the problem program.

Method: After saving the user's registers, this routine first issues an SVC 7 WAIT macro to ensure that the previous I/O operation is complete. The second error byte from the CCB is placed in the error/status indicator in the DTF table.

If IDLOC is specified, IOCS supplies the user with the ID of a record after each READ or WRITE is completed (see Figure 1). If IDLOC is specified, a test is made for the type of macro issued. If a READ KEY or WRITE KEY macro, the routine determines if the search multiple track option (SRCHM) has been specified. If sc, the ID returned to the user is the ID of the current record transferred.

If a READ or WRITE KEY macro has been issued without a search multiple track cption, or a READ or WRITE ID macro has teen issued, the ID returned to the user is the ID of the next record location, unless an end-of-cylinder condition is encountered. In this case, the ID returned is that of the first record of the next If an end-of-volume condition is cylinder. detected while updating the cylinder address, the end-of-volume bit is set in the error status indicator in the DTF table, and a dummy record is returned in IDLOC.

After the module determines the contents of IDLOC, the error/status bytes are set in accordance with the conditions posted to the CCE by physical IOCS, and returned to the user. Then, if record length is undefined and a READ macro has been issued, the record length is calculated and returned to the user. This routine then restores the user's registers, resets the macro switch in the DTF table, and returns control to the problem program via linkage register 14.

# DAMOD and DAMODV: Channel Program Builder Subroutine Chart AJ

Chjective: To construct a channel program in accordance with the processing macro issued in the problem program.

Note: Figure 12 provides a summary of the channel programs built to process DASD records by the Direct Access Method.

Entry: From a direct access logic module (either DAMOD or DAMODV) via a branch and link instruction.

Exit: To the calling routine.

Method: To perform direct access processing, many different channel programs, varying in length from 5 to 17 CCWs, are needed in DOS/VS (refer to Figure 10). The many CCWs required can be built from 11 basic CCWs by modifying command codes and/or flag bytes. Of these 11 CCWs, 5 are required for initial file loading. The other 6 are needed for normal file maintenance processing. TIC CCWs are built directly from storage addresses.

For each channel program that is built, a string of descriptor bytes are generated in the DTF table at program assembly time. The content of the string depends on the imperative macro issued by the problem program to access the file. There is one descriptor byte for each CCW in the channel program. This descriptor byte is divided into three subfields, which rerform the functions illustrated by Figure 7.

[			<b></b>										]
   Bit	-	0	1 2	3	4	5	6	7			)   		
Field		Αļ	В				С						
i i													i
Field	A:	R	efe:	ren	C	es	tl	ne	cc	nna	and	cod	đe.
   Field   	Field B: A relative pointer to select one of the 11 basic CCWs (see Figure 9).												
   Field   	C:	C	urti ode yte	, a	nd	<b>1</b> n	noc	lif	Eie				

Figure 7. DAM descriptor byte.

Because the first CCW in any Disk Operating System channel program must be a seek command, the seek CCW is generated at program assembly time as the first CCW in the CCW build area, and is never mcdified. As each channel program is requested, the channel program builder subroutine is called to build the remainder of the CCW chain.

Before entering this subroutine, the logic module uses the macro switch to determine the address of the string of descriptor bytes for the macro issued (refer to Figure 8). After pointers are set to the current descriptor byte and the CCW build area, the subroutine isolates the relative pointer to the basic CCW needed (see Figure 9) and tests to determine if the CCW is to be a Transfer In Channel (TIC). Figure 9 shows the Basic CCWs used to build channel programs.

If fields A and C of the descriptor byte are zero, the CCW is to be a TIC. Field B determines the address of the CCW to which control is to be transferred. This address and the TIC command code are stored in the TIC CCW (see Figure 10). If the end of the descriptor string has not been reached, the subroutine returns to build the next CCW; ctherwise, control returns to the calling routine.

If the CCW is not a TIC, Field B determines which of the basic CCWs is moved to the build area. Fields A and C of the descriptor byte are tested to see which fields in the CCW, if any, are to be modified (see Figure 10). A test is then made for the end of the descriptor string. If the end has not been reached, the routine returns to build the next CCW in the chain; otherwise, control returns to the calling routine.

Масго	Option	FIXUNB	UNDEF	VARUNB	SPNUNB		
READ ID	No options KEYLEN IDLOC KEYLEN,IDLOC	DC X'871814' DC X'87182C' DC X'8718979E' DC X'8718AF9E'	DC X'C718BF14' DC X'C718BF2C' DC X'C719BF129C' DC X'C718BF2A9C'	DC X'871816' DC X'87182A16' DC X'8718129E' DC X'87182A129E'	DC X'871816' DC X'87182A16' DC X'8718129E' DC X'87182A129E'		
READ KEY	No options SRCHM IDLOC SRCHM,IDLOC	DC X'8F1814' DC X'A718881814' DC X'8F18979E' DC X'A7189A881014'	DC X'BF8F1014' DC X'A718B8881014' DC X'BF8F10129C' DC X'A718B8881014'	DC X'A7188F1816' DC X'A718881816' DC X'A7188F18129E' DC X'A7189A881016'	DC X'A7180A1816' DC X'A7188A1816' DC X'A7180A18129E' DC X'A7189A8A1016'		
WRITE ID (No VERIFY)*	No options KEYLEN IDLOC KEYLEN, IDLOC	DC X'871895' DC X'8718AD' DC X'8718919E' DC X'8718A99E'	DC X'871895' DC X'8718AD' DC X'8718939C' DC X'8718AB9C'	DC X'871895' DC X'8718AB95' DC X'8718919E' DC X'8718AB919E'	DC X'871814871895' DC X'8718148718AB95' DC X'8718148718919E' DC X'8718148718AB919E'		
WRITE ID (VERIFY)	No options KEYLEN IDLOC KEYLEN,IDLOC	DC X'871891871815' DC X'8718A987182D' DC X'8718918718119E' DC X'8718A98718299E'	DC X'871893871815' DC X'8718AB87182D' DC X'8718938718139C' DC X'8718AB87182B9C'	DC X'871891871815' DC X'8718AB9187182B15' DC X'87184B918718119E' DC X'8718AB9187182B119E'	DC X'871814871891871815' DC X'8718148718AB9187182B15' DC X'8718148718918718119E' DC X'8718148718AB9187182B119E'		
WRITE KEY (No VERIFY)	No options SRCHM IDLOC SRCHM, IDLOC	DC X'8F1895' DC X'A718881895' DC X'8F18919E' DC X'A7189A881095'	DC X'8F1895' DC X'A718881895' DC X'8F18939C' DC X'A71888881095'	DC X'A7188F1895' DC X'A718881895' DC X'A7188F18919E' DC X'A7189A881095'	DC X'A7180A18140A1895' DC X'A7189A8A10148A1895' DC X'A7180A18140A18919E' DC X'A7189A8A1014BA1895'		
WRITE KEY (VERIFY)	No options SRCHM IDLOC SRCHM,IDLOC	DC X'8F18918F1815' DC X'A7188818918F1815' DC X'8F18918F18119E' DC X'A7189A8810918F1815'	DC X'8F18938F1815' DC X'A7188818938F1815' DC X'8F18938F18139C' DC X'A718B88810938F1815'	DC X'A7188F18910A1815' DC X'A7188818910A1815' DC X'A7188F18910A18119E' DC X'A7189A8810910A1815'	DC X'A7180A18140A18910A1815' DC X'A7189A8A10148A18910A1815' DC X'A7180A18140A18910A18119E' DC X'A7189A8A10148A18910A1815'		
Macros WRITE	AFTER and WRITE	RZERO use the same strings. If	AFTER is not specified in the D	TFDA macro, the strings are no	t generated.		
If AFTER is spe	If AFTER is specified, these strings are generated for all record formats:						
DC X'C718D752C718B5' WRITE RZERO DC X'C71834' READ RZERO							
And one of the	e following strings:						
No VERIFY DC X'		DC X'C718B18718CD'		No options DC X'C718B18: KEYLEN DC X'C718B18:			
VERIFY		DC X'C718B18718C9C7183187	'184D'	1 .	718CB91C7183187184B15' 718CBAB91C7183187184B2B15'		

One string for each macro to be used is generated, dependent upon the options specified in the DTFDA macro.

Figure 8. DAM channel program builder strings.

<sup>\*</sup>Indicates the operation used in the example given of the Channel Program Builder.

Field B	Basic CCW	Function
0000	X'31', &SEEKADR+3, X'40', 5	Search identifier equal using the address specified in the user's track – reference field.
	X'31', &Filename . S+3, X'40', 5	If relative addressing is used.
0001	X'29',KEYARG,X'40',Key length	Search key equal for key specified by user in KEYARG field.
0010	X'06', &IOAREA+16, X'40', Data length	Read data into I/O area (FIXUNB and UNDEF records).
	X'06', &1OAREA, X'40', BLK SIZE	Read data into I/O area (VARUNB and SPNUNB records).
0011	X'12',&IDLOC,X'40',5	Read count (CCHHR) into IDLOC .
	X'12', &Filename .I, X'40', 5	Read count (CCHHR) into work area in DTF table if relative addressing is used.
0100	X'39',&SEEKADR+3,X'40',4	Search home address equal using the address specified in the user's track – reference field.
	X'39',&Filename.S,X'40',4	If relative addressing is used.
0101	X'0E',&IOAREA+8,X'40',Key and Data length	Read key and data into I/O area (FIXUNB and UNDEF records).
	X'0E, &KEYARG, X'C0', Key length	Read key into user's KEYARG field (VARUNB and SPNUNB records).
0110	X'06', &Filename.K, X'40', 8	Read RO data into work area in DTF table.
0111	X'12', &Filename .K, X'40', 8	Read RO count into work area in DTF table .
1000	X'31', &Filename .F, X'40', 5	Search identifier equal using the address specified in the 5 – byte work area in the DTF table.
1001	X'1E',&IOAREA,X'40',Count, Key, and Data length	Read count, key, and data into I/O area (FIXUNB and UNDEF records).
	X'1E',&Filename.C,X'C0',8	Read count (CCHHRKLDLDL) into work area in DTF table (SPNUNB records).
1010	X'11', &Filename. B+32, X'40', Length of the largest single record that fits on a track.	Control erase of track.

Figure 9. Basic CCWs for DAM channel program builder.

Field A		Fiel	d B			Field C		Meaning
1	Z	Z	И	Z	1	1	1	Basic CCW not modified.
1	Z	Z	Z	Z	0	0	0	Modify command code to multiple-track operation .
Ì	Z	Z	Z	Ν	0	0	1	Modify command code in write operation.
1	Z	Z	Z	Z	0	1	0	Modify command code to multi-track, set SLI flag on .
1	Z	Z	Z	Z	0	1	1	Modify command code to write, set SLI flag on.
1	Z	Z	Z	Z	1	0	0	Modify command code to multi-track, set CC flag off.
1	Z	Z	Z	Z	1	0	1	Modify command code to write, set CC flag off.
1	Z	Z	Z	Z	1	1	0	Modify command code to multi-track, set CC flag off, SLI flag on .
0	Z	Z	Z	N	0	0	1	Set SKIP flag on in CCW.
0	Z	Z	Z	Ν	0	1	0	Set SLI flag on in CCW.
0	Z	N	Z	Ν	0	1	1	Set SLI and SKIP flag on in CCW.
0	Z	Ν	Z	Ν	1	0	0	Set CC flag off in CCW.
0	Z	Z	Z	Z	1	0	1	Set CC flag off, SKIP flag on in CCW.
0	Z	Z	Z	7	1	1	0	Set CC flag off, SLI flag on in CCW.
0	Z	Z	N	Z	1	1	1	Set CC flag off, SLI and SKIP flag on in CCW.
0	0	0	0	0	0	0	0	TIC to *- 32
0	0	0	0	1	0	0	0	TIC to *- 24
0	0	0	1	0	0	0	0	TIC to 16
0	0	0	1	1	0	0	0	TIC to *- 8
0	0	1	0	0	0	0	0	TIC to *- 0
0	0	1	0	1	0	0	0	TIC to *+ 8
0	0	1	1	0	0	0	0	TIC to * + 16
0	0	1	1	1	0	0	0	TIC to ~+ 24
0	1	0	0	0	0	0	0	TIC to * + 32

1 2 3 Bit 6 7

NOTE: NNNN = bits 1 - 4 of the descriptor byte and is one of the 11 bit combinations shown in Figure 7 under the column heading Field B. This field contains the relative pointer to the basic CCW (Figure 7)

CC - Command Chaining
SLI - Suppress Length Indicator
SKIP - Suppress Transfer of Information to storage.

Figure 10. DAM channel program descriptor bytes.

Descriptor Byte	CCW Built	Meaning
	X'07', &SEEKADR+1, X'00', 6	Seek to the address specified in the user's track reference field.
X'87'	X'31', &SEEKADR+3, X'40', 5	Search identifier equal the address specified in the user's track reference field.
X'18'	X'08', Pointer to *-8	TIC to *-8.
X'93'	X'05', &IOAREA+16,X'60', Data length	Write the data portion of the record from the IOAREA.
X'9C'	X'12', &IDLOC, X'00', 5	Read the count field into IDLOC.
	I .	I

Figure 11. Example of DAM channel program for a WRITE ID macro.

The following discussion describes how the DAM channel program builder constructs a channel program for the given example.

Example: Write an undefined record referenced by ID in the location specified by the user's track-reference field, and return the corresponding track record identifier (CCHHR) in IDLOC (option).

Figure 11 illustrates the CCWs needed for the complete channel program to accomplish this operation. In all, five CCWs are required. The first CCW (seek) is generated at assembly time and the remaining four CCWs are built using the string of descriptor bytes included as part of the DTF table for the WRITE ID macro. Refer to Figure 8. The descriptor string for the WRITE ID macro is:

## X'8718939C'

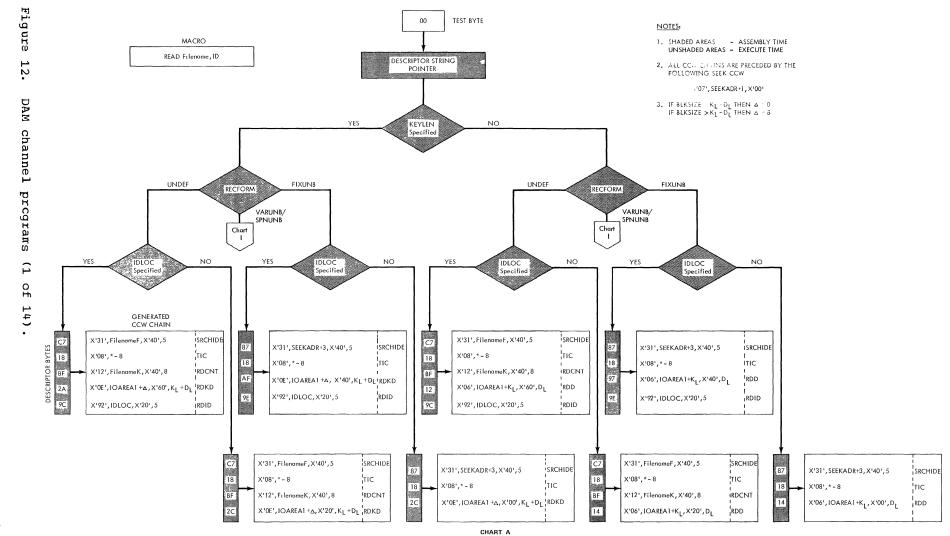
Except for the Seek CCW that is generated for any channel program at assembly time and never modified, each pair cf hexadecimal characters (descriptor byte) corresponds to one CCW. Thus, X'87' corresponds to the CCW to Search Identifier Equal as illustrated in part 1 cf the explanation that follows.

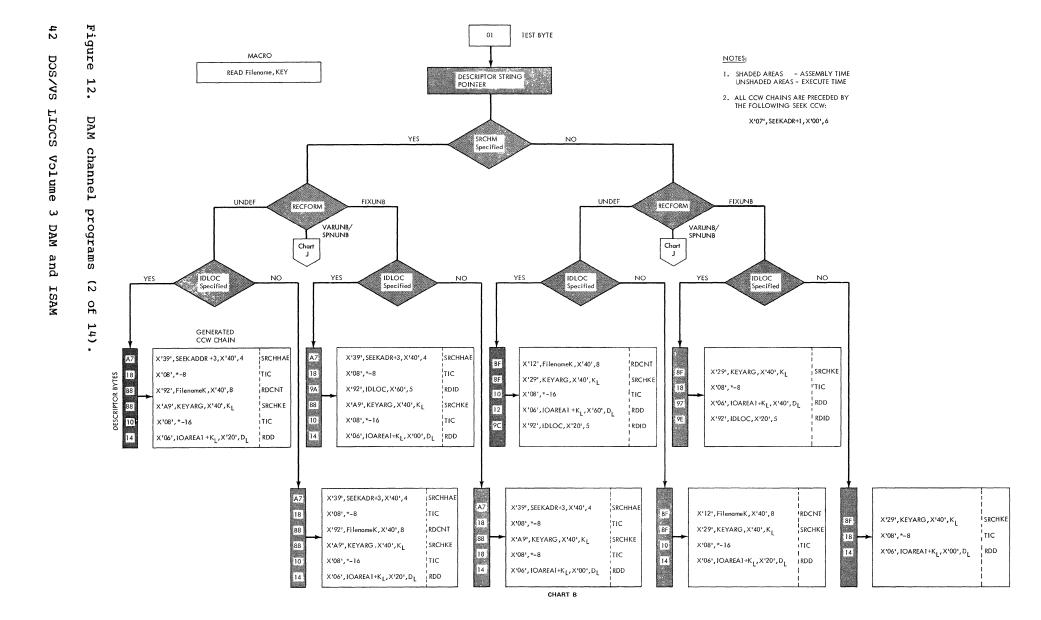
The CCW chain is generated from the descriptor string in this order:

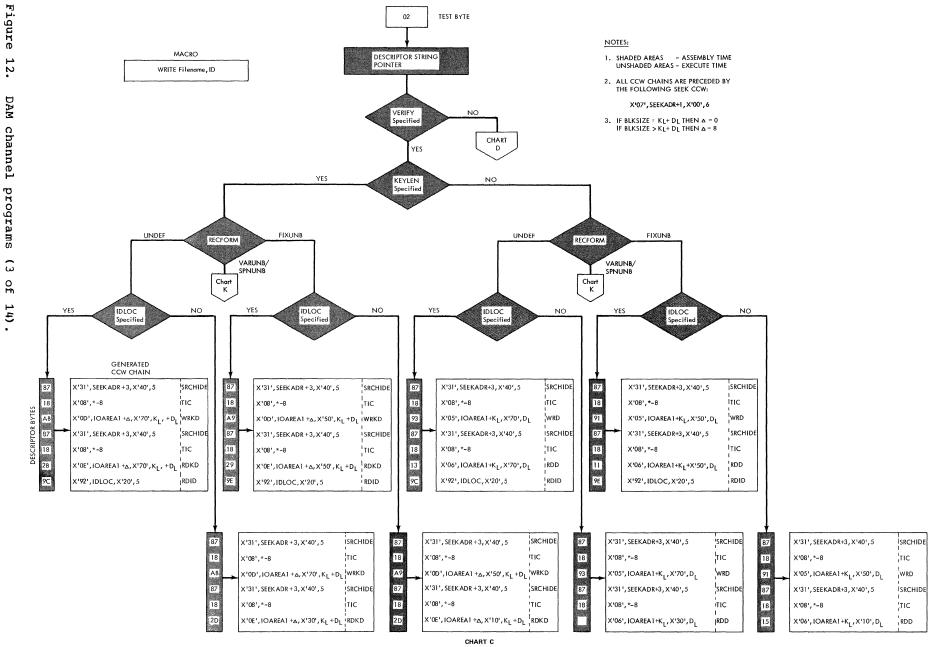
1. X'87' (10000111): Figure 9 illustrates that the CCW for a descriptor byte with a B-field = 0000 is a Search Identifier Equal CCW. Figure 10 further illustrates that a descriptor byte with an A-field = 1 and a C-field = 111 performs no modification of the basic

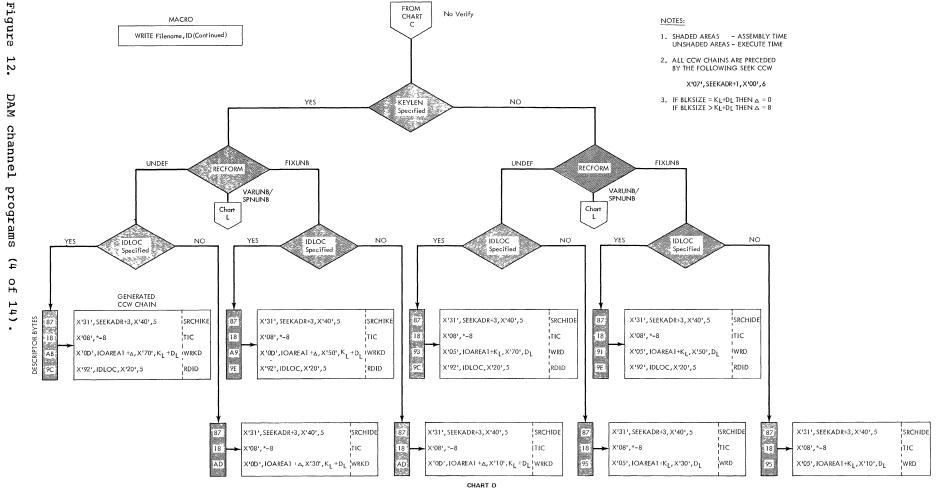
CCW. Therefore, the second CCW (the first being the Seek CCW) in the channel program CCW chain is an unmodified Search Identifier Equal CCW, X'31', &SEEKADR+3, X'40', 5 (refer to Figure 11).

- 2. X'18' (00011000): Because both the A and C fields are all zeros (a characteristic of a descriptor byte used to generate a TIC CCW), the second descriptor byte in the string generates a TIC CCW for the third CCW in the channel program. Figure 10 illustrates that a descriptor byte of this kind with a B-field = 0011 supplies the CCW, TIC to \* 8 (refer to Figure 11 for generated CCW).
- 3. X'93' (10010011): The B-field = 0010 in this descriptor byte indicates that the next CCW in the channel program chain will be the third basic CCW (refer to Figure 9). Because the A-field = 1 and the C-field = 011, Figure 10 shows that the command code is modified to a WRITE and that the SLI (Suppress Length Indicator) bit is turned on.
- 4. X'9C' (10011100): The B-field = 0011 in the last descriptor byte indicates that the last CCW in the chain will be the fourth basic CCW in Figure 9, Read Count into IDLOC. A descriptor byte with an A-field = 1 and a C-field = 100 indicates that the command code is modified for a multitrack operation and that the command chaining bit is turned off to signify the end of the channel program (Figure 10).









MACRO

WRITE Filename, KEY

03

DESCRIPTOR STRING

VERIFY Specified

YES

POINTER

TEST BYTE

NO

CHART

NOTES

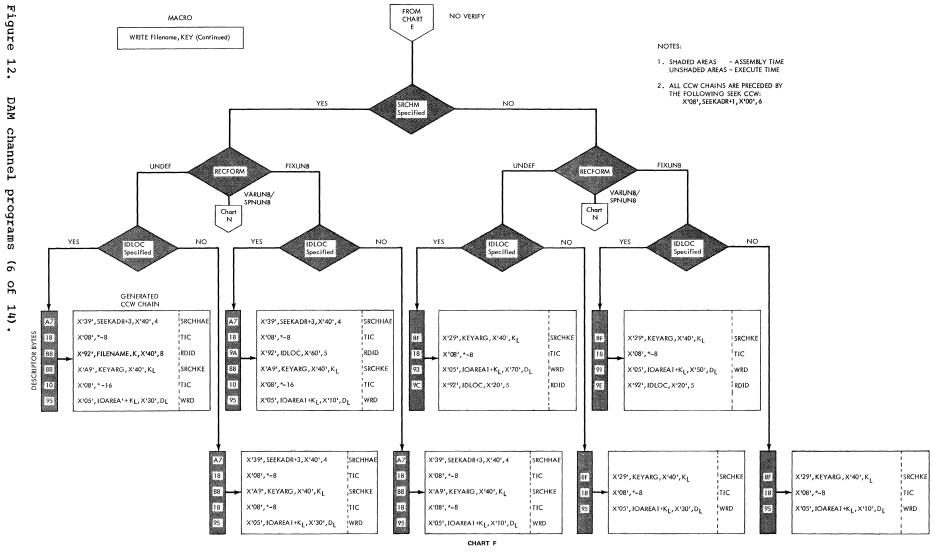
1. SHADED AREAS - ASSEMBLY TIME UNSHADED AREAS - EXECUTE TIME

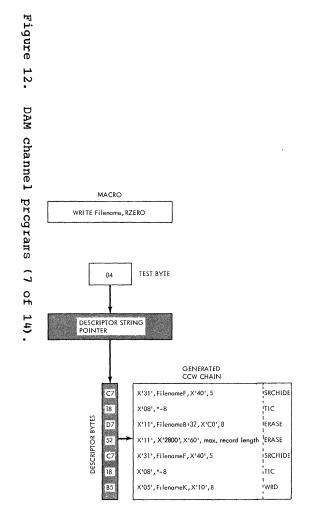
2. ALL CCW CHAINS ARE PRECEDED BY THE FOLLOWING SEEK CCW

X'07', SEEKADR+1, X'00', 6

iles

Access





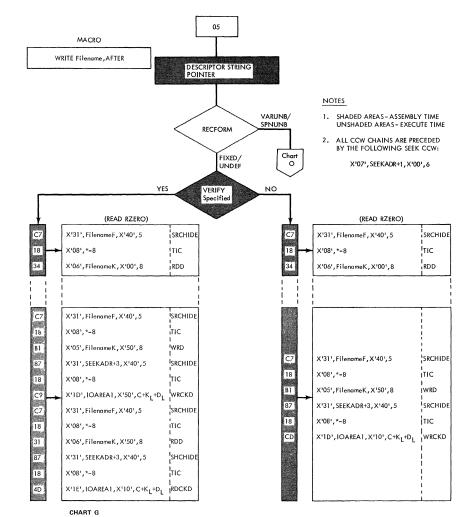
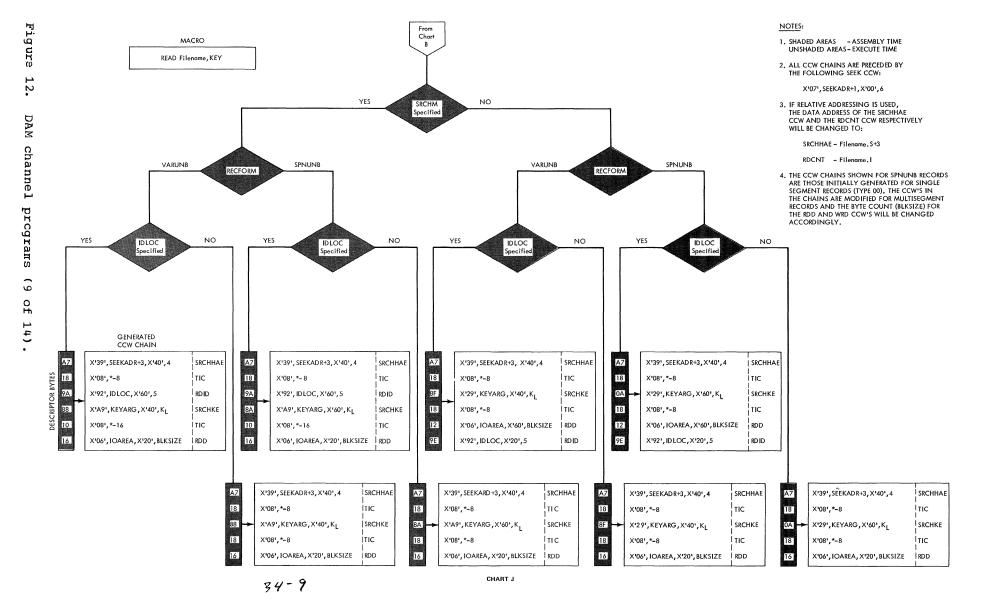
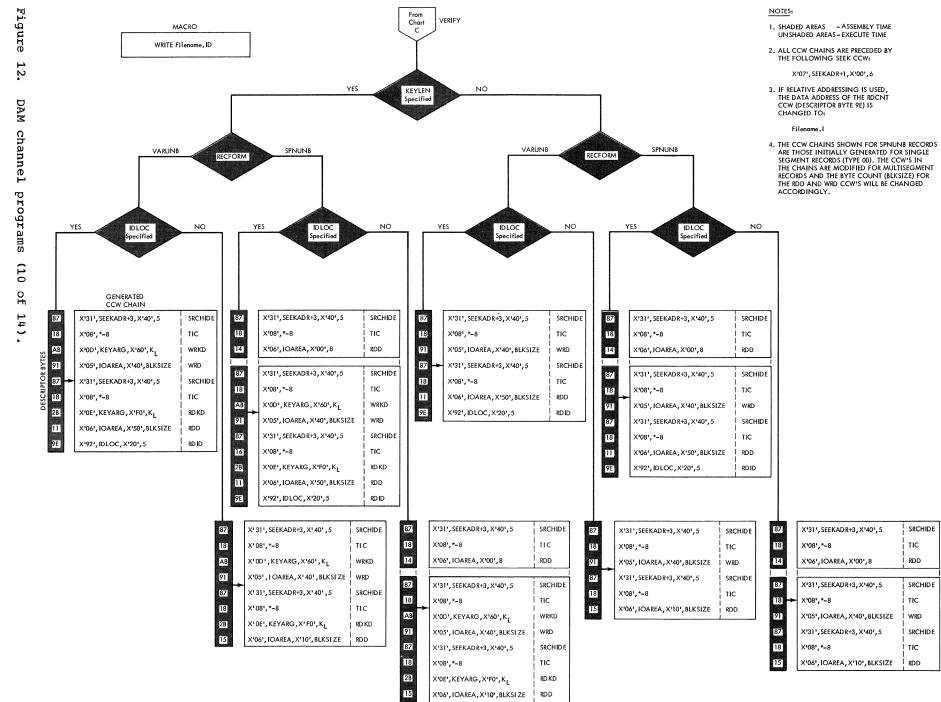


CHART I





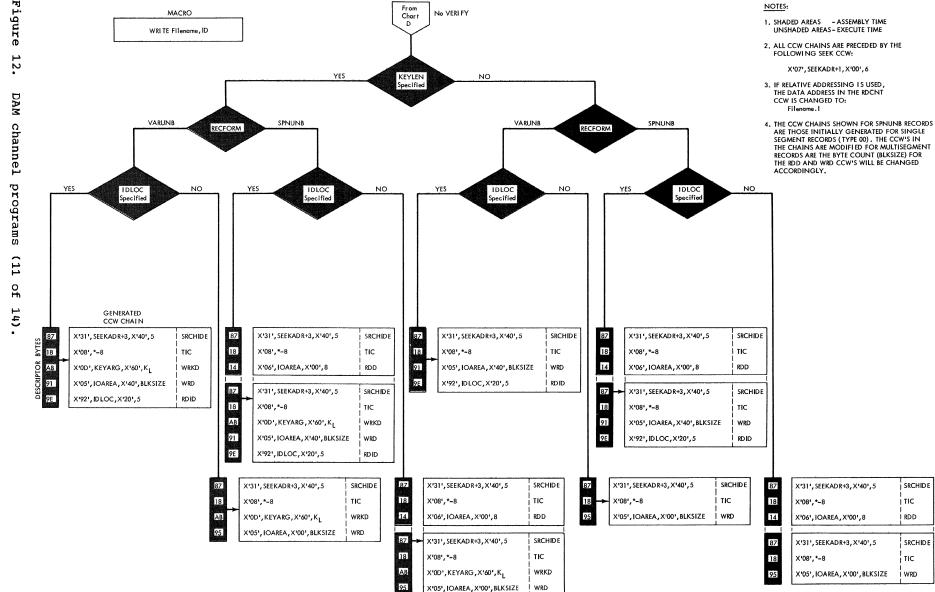


CHART L

X'06',1OAREA,X'10',BLKSIZE

CHART M

RDD

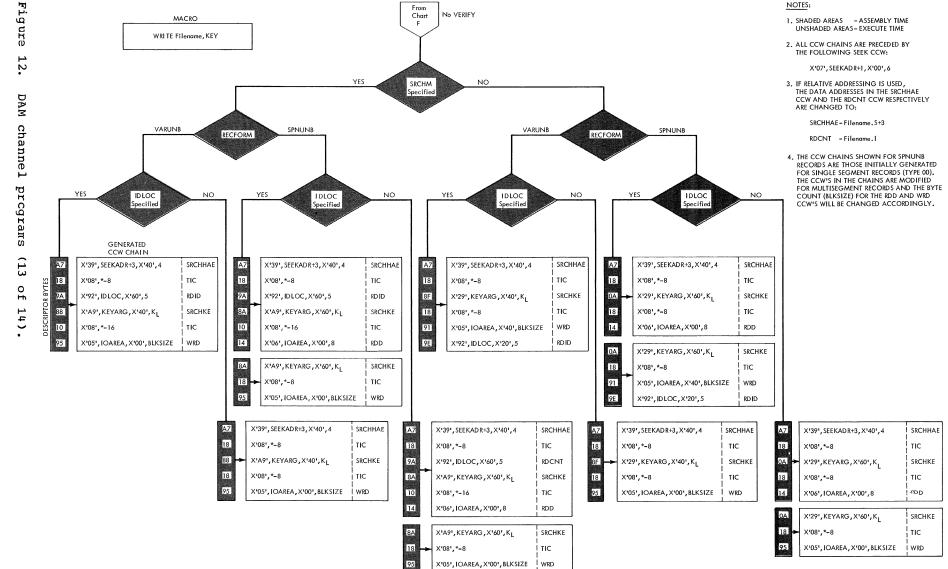


CHART N

15

X'06', IOAREA, X'10', BLKSI ZE

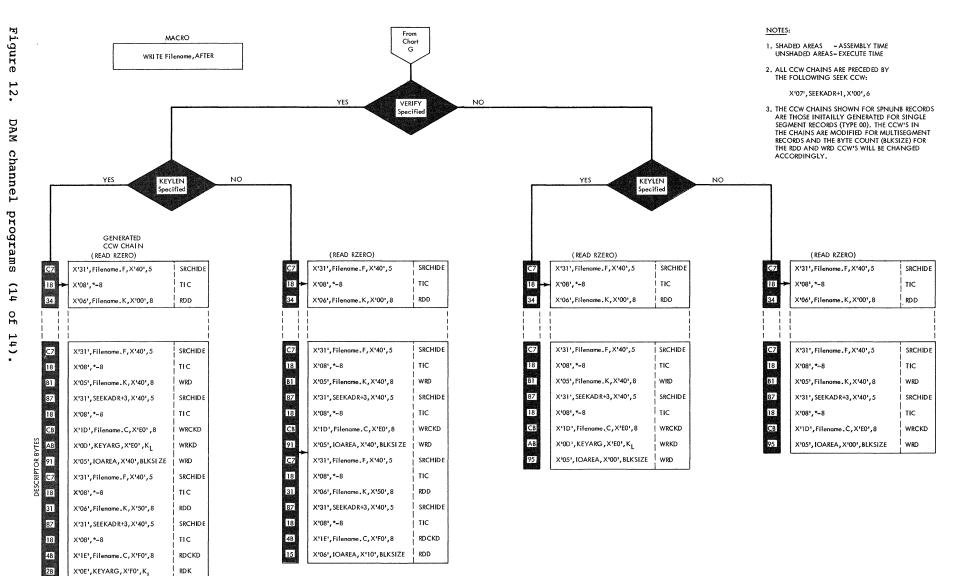


CHART O

# INITIALIZATION AND TERMINATION

When a DASD file is processed by the Direct Access Method, all extents specified by the user must be opened before any data is transferred.

The DAM Open logical transients make all the extents for the file available for use by the problem program. To accomplish this, the open routines check and create standard DASD labels or, in the case of nonstandard labels, pass control to the user for label processing.

To open a file, the open routines use label information supplied by the user in job control statements and stored on the SYSRES label information cylinder (refer to DCS/VS LIOCS Volume 1, SY33-8559). This information is used either to check or create the actual file labels in the Volume cell containing the file. Refer to <a href="DCS/VS">DCS/VS</a> LIOCS Volume 1, SY33-8559, for details of SYSRES label information and the standard DASD file labels processed by DOS/VS logical IOCS.

Close is required for DASD files processed by the Direct Access Method only when user standard trailer labels are specified.

### DAM OPEN CHART 01

For input files, the volume and Format 1 labels are checked against the SYSRES label information supplied by the user's // DLBL job control card. User labels are then processed, providing LABADER=address has been specified in the DTFDA macro defining the file. Finally, EXTENT information is passed to the user for checking and/or processing.

For output files, extents are checked to ensure that they do not overlap the VTOC or other extents. Labels are created and written in the VTOC, and user labels are processed, if required.

# Relative Addressing

When relative addressing is specified for a file, the open routines convert extent information supplied as actual physical DASD addresses into a relative addressing format. The converted extent information is stored at the end of the DTF table, in a table (DSKXTNT) at location &Filename.P+48. The 12 bytes preceding the DSKXTNT table

contain device-dependent alteration factors (4 bytes each) used to convert the extent limit addresses. The format of the DSKXTNT table and the location of the alteration factors is illustrated in Figure 13. The alteration factors are summarized in Figure

	Actual C1,C2,H1,H2 ad	Alteration factor for C1			
	Alteration factor for C	Al	teration factor for H1		
First extent	X <sub>1</sub> =V <sub>1</sub> -L <sub>1</sub> +1			L <sub>1</sub>	
Second extent	$X_2 = X_1 + (V_2 - L_2 + 1)$			L <sub>2</sub>	
Third extent	$X_3 = X_2 + (V_3 - L_3 + 1)$			L <sub>3</sub>	
Last Extent	$X_n = X_{n-1} + (V_n - L_n + 1)$			L <sub>n</sub>	
End of table	X'FF' X'FF'				
	TIT2	$\mathcal{L}$		TTT1	

TTT1, L = relative track number of the extent lower limit; that is, the number of tracks from cylinder 0, track 0 to the lower limit of the corre – sponding extent. (3 bytes)

TTT2 = cumulative total tracks in current extent plus previous extents in the table. (3 bytes)

B2 = second byte of bin number (BB), 0 for a disk device. (1 byte)

M = symbolic unit number, incremented by 1 for each new symbolic unit. (1 byte)

V = number of tracks from cylinder 0, track 0 to the upper limit of corresponding extent.

Figure 13. DSKXTNT table for relative addressing

Factor	2311	2314/ 2319	3330	2321
C1	1	1	4864	1000
C 2	10	20	19	100
H1	1	1	1	20

Figure 14. Alteration factors for relative addressing

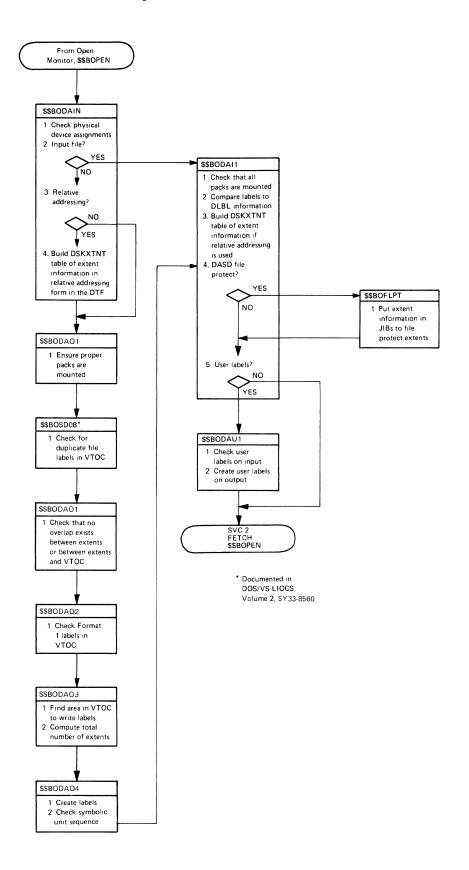
An actual physical extent address is converted to a relative address in the following manner. Each of the four bytes (CCHH) of the actual address are handled separately and are referred to as C1, C2, H1, and H2. Starting with C1, the first three bytes of the actual address are multiplied, one at a time, by the respective device-dependent alteration factor (refer to Figure 14). The result of

each multiply operation is added into an accumulating register. To complete the conversion, H2 is added to the accumulated result. If the conversion is performed on the lower limit address of the extent, the value obtained is the L (or TTT1) value and is stored in the DSKXTNT table (refer to Figure 13).

If the conversion is performed for the upper limit address of the extent, the

converted value is increased by 1 and TTT1 is subtracted from the result. The value obtained from this calculation is the total number of tracks included in the extent. The total number of tracks in the extent is then added to the total number of tracks of all previous entries to obtain the TTT2 value for the current extent entry in the DSKXTNT table (refer to Figure 13).

Chart 01. DAM Open



# \$\$BODAIN: DA Open Input/Output Charts BG-BJ

Objective: To ensure that the correct device is assigned for each extent, and to build a table (DSKXTNT) of extent values if relative addressing is used.

Entry: From the Open Monitor, \$\$BCPEN1.

## Exits:

- To the TES Processor, \$\$BCPEN, if the COBOL Open/Ignore option is specified.
- To \$\$BODAI1 if an input file.
- To \$\$BODAO1 if an output file.
- To \$\$BOMSG1 if messages are needed.

<u>Method</u>: This phase determines if the correct device has been assigned to the file. If the correct device has not been assigned, the message writer phase, \$\$BOMSG1, is fetched to print message 4683I on SYSLOG. If the correct device is assigned, a test is made for relative addressing.

If the file is an input file with relative addressing, the proper alteration factors for the device are inserted in the DTF table and phase \$\$BODAI1 is fetched. If the file is an input file with actual addressing, phase \$\$BODAI1 is fetched directly.

For output files with relative addressing, phase \$\$BODAIN, in addition to storing the proper alteration factors, converts the actual extent limits of each extent to a relative address. The relative address form of each extent is stored in sequence in the DTF table. (This extent information in relative addressing form is referred to as the DSKXTNT table and is located in the DTF table at location &Filename.P+48.) When the extent information is complete, phase \$\$BCDAO1 is fetched.

For output files with actual addressing, phase \$\$BODA01 is fetched after the device assignment of each extent is checked.

#### \$\$BODAI1: DA Open Input Charts BK-BM

Objective: To ensure that all packs are mounted, that the symbolic units are specified in sequence, and that the symbolic unit (and bin, if 2321) agrees with the user supplied DLBI information.

Entry: From \$\$BODAIN, or from a message
writer phase.

## Exits:

- Tc \$\$BCDSMW to write data security message.
- To the TES Processor, \$\$BOPEN, if no user options are specified.
- Tc \$\$BODAU1 to process user's options.
- To \$\$BOFLPT if DASD file protect is specified.
- To \$\$BCMSG1 if a message is required.

Method: Phase \$\$BODAI1 reads the VoL1 label and checks the volume serial number in the label against the volume serial number given in the extent to ensure that the correct pack is mounted. The phase then reads the Format 1 label for the file and checks the symbolic unit specified for each extent for proper sequence.

If the data security indicator in the format 1 label is ON, and the data security message has not been issued for the file, exit is made to \$\$BODSMW to print the data security message.

If relative addressing is specified, phase \$\$BCDAI1 converts every set of extent limits in the Format 1 label, and the Format 3 label(s) as required, from actual (CCHH) addresses to a relative address form. The extent information in relative form is placed in a table (DSKXINT) within the DTF table at location &Filename.P+48.

When all extents have been checked, a test is made for file protected extents. If file protect is specified, phase \$\$POFLPT is fetched to build JIBs for the protected extents.

Phase \$\$BODAI1 returns control either to the TES Processor (\$\$BOPEN), or to phase \$\$BODAU1 if processing of user labels or extent information is required.

# \$\$BODAO1: DA Open Output, Phase 1 Charts EN-BC

Objective: To check the volume serial number and determine if the DLBL and EXTENT serial numbers are equal, and to ensure that no extent specified by the user overlaps on the VTCC or on another extent for the same file.

Entry: Frcm \$\$BODAIN.

# Exits:

- To \$\$BODAO2 if job goes to normal completion.
- To \$\$BOSDO8 to check for duplicate DLBL in VTOC.
- To \$\$BOMSG1 if messages are to be put out.

Method: This phase reads the volume label for the symbolic unit (and bin, if the device is a 2321). It checks to see that the correct volume is available by comparing the volume serial number with the serial number specified on the first extent. If they are not equal, message 4755A is initialized and \$\$BOMSG1 is fetched to write the message on SYSLOG.

If the serial numbers are equal, the Format 4 label is read. Each extent for the current symbolic unit is checked to see that it does not overlap on the VTCC. If an overlap is present, message 4741A is initialized and written out on SYSIOG by \$\$BCMSG1. Upon reentry to this phase, the current extent is deleted at the user's request.

The next extent is checked to see if it has the same symbolic unit as the previous extent. If it does not, the routine exits to \$\$BOSDO8 to check for a duplicate DIBL in the VTOC. If the next extent has the same symbolic unit, the phase checks to see if the first extent has two tracks. If it does not, message 4766A is initialized and written on SYSLOG by \$\$BOMSG1. Upon reentry to this phase, the first extent is deleted at the user's request.

If the first extent has two tracks, a check determines if extents cverlar. overlap exists, message 4740A is initialized and written on SYSLOG. Upon reentry to this phase, the extent causing the overlap is deleted at the user's request. All remaining extents are moved down the length of one extent so that the upper limit of the extent is decreased by the length of one extent.

When all extents on the file have been checked in this manner, this phase exits to \$\$BODAO2.

## \$\$BODAO2: DA Open Output, Phase 2 Charts CA-CD

Objective: To ensure that no extent for the incoming file overlaps on any extent for any file cataloged in the VTCC.

Entry: From \$\$BODA01.

#### Exits:

- Tc \$\$BODAO3 to create labels.
- To \$\$BOMSG1 if messages are to be printed.

Method: This phase reads the VOL1 label to get the starting address (CCHHR) of the VIOC. If the VOL1 label is not found,  $\pi$ essage 4706I is initialized, and phase \$\$BOMSG1 is fetched to write the message on SYSLOG.

The Format 4 label, or VTCC definition label, is read to get the upper limit address (CCHH) of the VTCC. If the Format 4 lakel is not found, message 4704I is initialized, and phase \$\$BCMSG1 is fetched to write the message.

If the Format 4 label is found, the routine reads DASD labels (ccunt, key, and data) from the VTOC, one at a time, until a Format 1 label is found or the last record in the VTOC is read. If a no-record-found condition results before the end of the VIOC is reached, message 4709I is initialized and written on SYSLOG by phase \$\$BOMSG1.

If the end of the VTOC is reached without finding a Format 1 label, the routine determines the address of the extent for the next file on a different symbolic unit, and returns to process the extents for that file. If an extent on a different symbolic unit cannot be found, and all extents have been scanned, this phase fetches phase \$\$BCDAC3 to create labels.

When a Format 1 label is found, the extents for the new file are checked fcr overlap on the extents in the Format 1 lakel and any other labels chained to it. If overlap is found, the Format 1 label is reread, and the expiration date is checked to see if the file has expired. If the file has not expired, message 4744A is initialized and phase \$\$BCMSG1 is fetched to write the message on SYSLOG. At this point, the operator sets indicators either to delete the file, or delete the extent. Upon reentry to this phase, the indicators determine the action to be taken, and that action is executed.

If the file has expired, or the operator requests that the file be deleted, it is done by writing zeros over all the labels in the chain. When the file is deleted, the phase returns to read the next Format 1 lakel from the VTCC and continue processing.

When overlap occurs on an unexpired data secured file, \$\$BOMSG1 is fetched to issue message 4798I OVLAP UNEXPRD SECRD FILE. If overlap on an expired secured file occurs, message 4797I OVLAP EXPIRED SECRD FILE is issued. In both cases, the job is canceled.

When all the extents for the incoming file have been processed, this phase exits to \$\$BODAO3 to create labels.

# \$\$BCDAO3: DA Open Output, Phase 3 Charts CE-CF

Objective: To find an open area in the VTOC in which to write labels. To set the DADSM bit on in the Format 4 label and compute the total number of extents.

Entry: From \$\$BODAO2.

# Exits:

- To \$\$BODAO4 to create Format 1 and Format 3 labels.
- To \$\$BOMSG1 if messages are to be printed.

Method: This phase first counts the number of extents on the symbolic unit and saves the total number to be inserted in the label later. A test determines if the total number exceeded the limit. If it did, an exit is made to phase \$\$BOMSG1 to issue a message to that effect.

If the limit has not been exceeded, the VOL1 label is read to get the starting address of the VTOC. The Fcrmat 4 label, or VTOC definition label, is then read and the DADSM bit is set on in the Format 4 label. This phase then writes and verifies the Format 4 label.

The VTOC is then searched to find the first open area in which to write a label. This first open area contains the Format 1 label, or in case of more than three extents, the Format 3 label. If a Format 3 label is to be created, the VTCC is searched again to find a second open area for the Format 1 label.

When the necessary number of open areas have been found, this phase exits to phase \$\$BODAO4 to create the labels.

\$\$EOCAO4: DA Open Output, Phase 4 Charts CG-CJ

Cbjective: To create Format 1 and Format 3 lakels, and write them in the VTOC. To ensure that symbolic units are in sequence.

Entry: From \$\$BODA03.

## Exits:

- To \$\$BCDA03 if additional extents are to be processed.
- To \$\$BOPEN if there are no user routines.
- To \$\$BODAU1 if there are user labels.
- Tc \$\$BCMSG1 if messages are to be put out.
- To \$\$BOFLPT if DASD file-protect is present.

Method: This phase tests for user labels. If there are any, a user label extent is created from the first prime data extent for the symbolic unit, and the first track becomes the user label track. If the limits in the prime data extent specify more than one track, the lower limit is updated by one track to form a new lower limit for the prime data extent.

Fields specified in the DLBL are used to create the Format 1 label. The extents are then inserted into the Format 1 label from the extent storage area. If more than three extents on the same symbolic unit are specified, the Format 1 label is written cut and the Format 3 label is created. The remaining extents are inserted into the Format 3 label.

When a different symbolic unit or the end of the extent storage area is detected, the label currently being created is written out. If the last symbolic unit has not been processed, the DIBL volume sequence number is updated by 1. The next symbolic unit is processed. If the symbolic unit has changed, the new symbolic unit must be in sequence. If not, phase \$\$BOMSG1 is fetched to issue a message to that effect.

To process the next symbolic unit, this phase exits to phase \$\$BCDAO3 to find another open area for the next label.

# \$\$BODAU1: DA Open Input, Output Charts

Objective: To check user header labels for an input file if the user elects the user label option. To create user header labels for an output file, to be written by the open routine on a DASD device. To pass extents to the user from the lakels if requested to do so by the user.

## Entry:

- From \$\$BODAI1 if an input file.
- From \$\$BODAO4 if an output file.

## Exits:

- To \$\$BOMSG1 if messages are to be printed.
- To \$\$BOPEN at completion of CPEN routine.

Method: The VOL1 label is read to get the starting address of the VTOC. The VTOC is then searched to find the correct Format 1 label. If the label cannot be found, a message is written by phase \$\$BCMSG1 to that effect.

When the correct label has been found, a test is made for user labels. If there are none, the routine branches to pass extents if requested by the user.

Then a test determines if the current label is a user label. If it is nct, the routine branches to pass extents to the user. If the current label is a user label, a test determines if the device is a 2321. If it is, the maximum number of header labels for input or output files must be modified because only five user labels are possible with a 2321 file. Eight user labels may be used with a disk file.

When the maximum constants have been modified (if necessary), this phase determines whether the file is an input or output file. If the file is an input file, the user header labels are read by this phase and checked to ensure that they are correct user header labels. The data portion is then passed to the user if the labels are correct. If the labels are not correct, a message is written by \$\$BOMSG1 to that effect. The reading of lakels continues until the maximum number of labels have been read (and rewritten if the user has updated the label), or the user signals that he does not want to read any more labels.

If the file is an output file, this phase creates the count field, including the ID, key length, and data length and the key field, (UHL1 for the first user label to UHL5 or UHL8 for the maximum number of user labels). It also initializes the first four bytes of the data field which correspond to the key field. This phase then exits to the user in order to create the last 76 bytes of the data field. Upon return from the user, the lakel is written ky this phase.

The creating and writing of labels continues until the maximum number of labels has been written or the user signals that he does not want to create any more lakels. When the last label has been written, two file marks are created and written, the first of which is a header label with a data length of zero, and the second of which is a trailer label with a key of UTIC and a data length of zero. When these file marks have been written, the routine branches to pass extents.

A test determines whether the user wants extents passed. If not, this phase proceeds to process the next symbolic unit. If the user does want extents rassed, the extent information is passed via a fourteen tyte area illustrated by Figure 15.

Pyte 0	Extent Type
Byte 1	Extent Sequence Number
Eytes 2 tc 5	Extent Lower Limit
Bytes 6 to 9	Extent Upper Limit
Eytes 10, 11	Symbolic Unit
Byte 12	Original Bin Number (O for disk)
Byte 13	Present Bin Number (0 for disk)

Figure 15. Format of extent information to

Extents are passed until the end of the extent area in the label is detected. Another test determines whether a pointer to a Format 3 label is present. If sc, the Format 3 label is read and the extents in that lakel are passed to the user. continues until there are no more extents within a label to be passed to the user.

The phase then finds the next symbolic unit to process those lakels and extents. When all the incoming extents in the extent storage area in main storage have been

processed, this phase exits to the TES processor, \$\$BOPEN.

# \$\$BCDACL: DA Close, Input/Output Charts CM-CP

Objective: To read or write standard user trailer labels, and to test for track hold.

Entry: From the Close Monitor or from a
message writer phase.

<u>Exit</u>: To the Close Monitor, \$\$BCLOSE; to \$\$BCMSG1 if a message is required; or to \$\$BOSDC2 to free any tracks.

Method: For input files, phase \$\$BODACL initializes the search CCW with a key argument of the first standard user trailer label (UTLO). The label is read and control is passed to the user's latel routine. Processing of standard user trailer labels continues until either the

maximum number of trailer labels are read (5 for 2321, 8 for disk devices), or a file mark (a UTI with a data length of 0) is read. Control then returns to the Close Monitor.

For cutput files, phase \$\$BCDACL initializes the search CCW with a key argument of UTLO, the end-of-file mark written after the last UHI. When the UTLO lakel is found, control passes to the user's label routine. Control returns to \$\$BODACL to write the standard user trailer lakel on the user's lakel track. The first standard user trailer label written is identified by UTLO and is written over the end-cf-file mark previously identified by UTLO. A new end-of-file mark (a standard user trailer label with a data length of 0) is written and the Close Monitor is fetched after all standard user trailer labels are processed. The maximum number of standard user trailer labels permitted (excluding the end-of-file mark), is 5 for a 2321 and 8 for a disk device.

The indexed sequential access method (ISAM) permits processing DASD records in both random and/or sequential order by control information. For random processing, the user supplies the control information (record key) of the desired record to ISAM, and then issues READ or WRITE macro instructions to transfer the specified record. For sequential processing, the user specifies the first record to be processed, and then issues GET and/or PUT macro instructions to retrieve records in sequential order by record key. Variations in macro instructions permit:

- A logical file of records to be loaded onto DASD (created).
- Individual records to be read from, added to, or updated in the file.

#### RECCRD TYPES

Logical records in an ISAM-crganized file must be fixed-length records either blocked or unblocked. Each physical record in the file must contain a key area. If the records are blocked, the record key (control information) of the highest (last)

logical record in the block is stored in the key area of the block.

#### STCRAGE AREAS

#### I/O Areas

An I/O area must be specified for each ISAM file to be processed in a problem program. This I/O area must be defined to contain sufficient space for the data area. If unblocked records are to be retrieved sequentially or records are to be loaded cr added, space for a key field is required. Space for the count area must be provided when the file is being loaded or additions to the file are being made. Space for a sequence-link field is required when additions are to be made to the file cr when records are retrieved from a file. The sequence-link field is used for overflow records (see Add Records to a

Figure 16 shows the ISAM I/C area requirements.

Function	  Count		Кеу	Sequence Link	Data
Load - Unblccked Records	8	Кеу	Length		Record Length
Load - Blocked Kecords	8	Key	Length		Record Length X Blocking Factor
Add - Unblocked Records	8	Кеу	Length	10	Record Length
Add - Blocked Records	8	Key	Length		
'	8	кеу	Length	10	Record Length
Random Retrieve - Unblocked Records	-	   		10	Record Length
Sequential Retrieve - Unblocked Records	-	Key	Length	10	Record Length
Random or Sequential Retrieve - Blocked Records	<b>-</b>	       			Record Length (Including  Keys) X Blocking Factcr  cr*
	-			10	Record Length

\* Whichever is larger.

Figure 16. ISAM I/O area requirements (in bytes).

LCAD: To create or extend a disk file of blocked or unblocked records. This area must be defined with enough capacity for an 8-byte count field, a control information field (key area), and the data record(s).

ADD, UNBLOCKED RECORDS: The output area for adding unblocked records to an ISAM organized file must be defined with enough capacity for an 8-byte count field, a control information field (key area), and a data record area. The data record area must have space for a 10-byte sequence-link field that is used in conjunction with overflow records (see Add Records to a File). The sequence-link field is required when a record is written on an cverflow track. ISAM determines the correct sequence link and stores this information at the beginning of the data section of the I/O area. When the sequence-link field is not used, the ten unused bytes fall at the end of the data section and are ignored. Figure 17 shows the format of the 10-byte sequence-link field.

ADD, BLOCKED RECORDS: The cutput area for adding blocked records to an ISFMS organized file must contain enough space for an 8-byte count field, a control information field (key area) and a data section large enough to contain the block of logical records. The minimum size for the data section is one logical record plus 10-bytes to be used for a sequence-link field when required.

SEQUENTIAL RETRIEVE, UNBLOCKED RECCRDS: The input area for reading unblocked records must contain sufficient capacity

for a key area and a data area. The data area must include enough space for the logical record plus 10-bytes for the sequence-link field of overflow records. If a record does not have a sequence-link field, the extra 10-bytes in the I/O area fall at the end of the data section and are ignored by the program.

RANDOM RETRIEVE, UNBLOCKED RECORDS: The input area for reading unblocked records must contain space for a data area. The data area must include enough space for the logical record plus 10-bytes for the sequence-link field of an overflow record. If a record does not have a sequence-link field, the extra 10-bytes in the I/O area fall at the end of the data section and are ignored by the program.

RETRIEVE, BLOCKED RECORDS: The input area for reading blocked records must contain space for a data area. The data area must te large enough to contain a full block of records. The minimum size of the data area is one logical record plus 10-bytes for the sequence-link field used with overflow records.

When blocked or unblocked records are to be retrieved and processed directly in the I/O area, a register must be specified. This register is used for indexing, to point to the beginning of each lcgical record when it is needed for processing.

The fo	ormat of the sequence – link fie	ld of an overflow record or the in	ndex-level po	inter is MBBCCHHRFP:		
	2311/2314/2319/3	330 Disk		2321 Data Cell		
M = E	xtent Sequence Number		M = Extent Sequence Number			
BB = 0	0		BB = Cell Number			
CC =	Cylinder Number		сс	= Subcell and Strip Number		
HH =	Head (Track) Number		нн	= Cylinder and Head Number		
R = Re	cord Number		R -	Record Number		
F = (c-	cccciii) Entry Type and Index	Level. See Note 1.	F =	(ccccciii) Entry Type and Index Level. See Note 1.		
P = Po	inter type. See Note 2.		<b>P</b> =	Pointer type. See Note 2.		
Note 1: F=	cccciii					
Ent	ry Type (ccccc)	Index Level (iii	;)	DASD Address Information		
00000 - Normal	Entry (Unshared Track)	000 – Track-Index 001 – Cylinder Index 010 – Master Index		R = 0		
00001 - Normal	Entry (Shared Track)	000 – Track Index		R = N (Points to First Data Record on the Track)		
00010 - Overflo	ow Entry (End)	000 – Track Index or Sequence – Link Field		R = 255		
00011 - Overflo	ow Entry (Chained)	000 – Track Index or Sequence – Link Field		R = N (Actual Record Address)		
00100 - Dummy Entry (End)		000 – Track Index 001 – Cylinder Index 010 – Master Index		M through R = 0		
00101 - Dummy	Entry (Chained)	001 - Cylinder Index 010 - Master Index		M through H Points to First Track on Next Cylinder, R = 0		
00110 - Inactiv	e Entry	000 – Track Index 001 – Cylinder Index 010 – Master Index		M through R = 0		
Note 2:						
P = Seek Op-	Code					
Seek Op - Code		Meaning		Index Level		
07	Entry Points to Cylinder Ind	ex Track on Different Strip (2321	Data Cell)	Master Index		
18	Entry Points to Cylinder Ind	ex Track on Same Cylinder		Master Index		
ОВ	Entry Points to Cylinder Ind	ex Track on Different Cylinder		Master Index		
07	Entry Points to Track Index			Cylinder Index		
18	Normal Entry (Shared or Unshared)			Track Index		
07	Overflow Entry (End)			Track Index or Sequence - Link Field		
07 Overflow Entry (Chained)			Track Index or Sequence - Link Field			
07	Dummy Entry (End)			Master, Cylinder and Track Indexes		
07	Dummy Entry (Chained)			Master and Cylinder Indexes		
07	Inactive Entry			Master, Cylinder and Track Indexes		

Figure 17. Format of sequence-link field/index level pointer.

## Work Areas

When a work area is specified on input, ISAM moves each record from the I/O area to the work area. The problem program can then process the record in the work area. When a work area is specified on output, ISAM moves the record from the work area to the I/O area in preparation for transferring the record to DASD stcrage. If a work area is specified, an I/C register is not required. Figure 18 shows the ISAM work area requirements.

	Unblocked Records	Blocked Reccrds				
Load	KL+DL or 10*	DL or 10*				
  Add   	KL+DL or 10*	DL or (KL+10)*				
  Random  Retrieve	DL	DL				
  Sequential    Retrie <b>v</b> e	KL+DL	DL				
Where:	b					
   K = Key   D = Data   L = Length 						
* Whichever is larger.						

ISAM work area requirements (in Figure 18. bytes).

## OVERFLOW AREAS

The location of the overflow area(s) for a logical file may be specified by the user. The overflow areas may be built by one of three methods:

Overflow areas for records may be located on each cylinder within the prime data area that is specified by a job control extent card for the data file. In this case, the user must specify the number of tracks to be reserved for overflow on each cylinder occupied by the file. The cverflow records that occur within a particular cylinder are written in the cylinder overflow area for that cylinder.

The number of tracks to be reserved for each cylinder cverflcw area must be specified in the DTFIS entry CYLOFL when a file of records is to be loaded

and when records are to be added to an crganized file.

- An independent overflow area may be specified for storing all cverflow records for the logical file. In this case, a job control EXTENT card must be included when the program is executed to specify the area of the volume to be used for the overflow area. This area may be on the same volume with the data records, or on a different volume that is on-line. However, it must be contained within one volume. (It must te the same kind of device as that containing the prime data area.)
- 3. Cylinder overflow areas (method 1) and an independent overflow area (method 2) both may be used. In this case, overflow records are placed first in the cylinder overflow areas within the data file. When any cylinder cverflcw area becomes filled, the additional overflow records from that cylinder are written in the independent overflow area. The specifications required for both methods 1 and 2 must be included for this combined method of handling cverflcws.

All records placed in the overflow area will be in the unblocked format and will have a sequence-link field prefixed to each record. There must always be one prime data track available (for a DASD record that has a data length of 0) when additions are being made to the last track in the prime data area containing records. The format of the overflow area upper limits (MEECCHHR) is shown in Figure 25.

## INDEXES

As ISAM loads the records, it creates a set cf two or three indexes to be used to control the processing and location of the data records. Two indexes, the track index and the cylinder index, are always built for each file. The third, a master index, is built only when specified by the user. A master index should be specified only for large files. As a guide line, if a cylinder index occupies less than five tracks, it is usually faster to search only the cylinder index (followed by a search on the track index) than to search a master index, alsc.

Indexes are developed as a series of entries, each including the address of a DASD track and the highest (last) record key on that track or cylinder. Each entry is a separate DASD record composed of a key area and a data area. The key area

contains the highest key on the track or cylinder, and its length (number of bytes) is the same as the key-area length specified by the user for the data records. The data area of each index record is 10 bytes in size and contains the physical address of the logical record or of another index. Figure 17 shows the format of the 10-byte index level pointer (index data area).

# Track Index (TI)

The lowest level index for logical file is the track index. This index has two important functions.

- Point to the correct track in the cylinder that contains the specified
- Provide direct linkage to the record overflow areas.

Each track index is built on the cylinder that it is indexing. The track index is located on the first track of each cylinder. The index can occupy a partial track, a full track, or more than one track. If the track index does not fill a track and if the remaining portion is large enough to hold any prime data records, then prime data records are stored on the remaining portion of the track.

The track index can contain the following types of entries:

- Normal Entry Unshared
- Normal Entry Shared
- Cverflcw Entry Chained
- Cverflow Entry End
- Dummy Entry End
- Inactive Entry

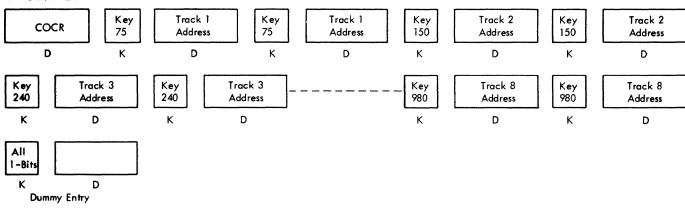
When first created, the track index is formatted with two entries for each track used on the cylinder. These two entries are the normal entry and the cverflow entry. Each entry is a DASD record containing a key area and a data area. Figure 19 is an example cf a track index built for the prime data area of a logical file utilizing eight tracks on a cylinder.

The normal entry is the first of the two entries. After a track is loaded with records for a file, this entry has in its data area the address of the track referenced by the entry. The key of the last record on the track is maintained in the key area of the normal entry. The key area is changed each time a record is added to the track, so that it always reflects the key of the last record on the track. (See Add Records to a File.)

When the first track containing prime data records is shared with the last cr cnly track of the track index, the data area of the track index normal entry is ncdified to indicate a shared entry.

The cverflow entry is used both in the track index and in the sequence-link field cf an everflow record. See the section Add

## TRACK INDEX



K = Key Area D = Data Area

COCR = Cylinder Overflow Control Record (R0)

Figure 19. Schematic example of a track index.

Records to a File for a description of the overflow entry in the sequence-link field. The overflow entry is required for handling overflow chaining when additional records are inserted into the file. Before a record is added to a track, the track index overflow entry for that track is similar to the normal entry in that they both contain the key of the last record on the track and the address of the track. Note, at this point the last record on the track is the last record placed on the track when the file was originally loaded. When cverflow records occur, the data area of the overflow entry is changed to reflect the address of the lowest record in the overflow chain. An overflow chain is developed for each track. The key area of the overflow entry is not changed, but always contains the key of the highest record, because records added to a track always have keys lower than the highest key originally lcaded onto the track. The technique used to add records is explained in the section Add Records to a File.

The two types of overflow entries in the track index are overflow chained entries and overflow end entries (see Figure 17). The data field of the track index cverflow entry is initially set to indicate an overflow end entry. If an cverflow chain is later built, the overflow end entry indicates the last overflow record in the chain. A overflow chained entry is built to indicate an overflow chain exists. The data field of a overflow chained entry contains a pointer to the lowest record in the overflow chain.

The last entry on a track index is always a dummy end entry. The dummy end entry indicates the end of the track index and indicates that any following records are logical file data records.

The key area of the dummy record is the same length as the user's key length and contains bytes of all one-bits. The data field is the same length as the normal entries but is a null field.

Inactive track index entries are built during the load operation. For a 2311 DASD device type, inactive entries are written for the unused portion of the prime data extent. For all other DASD device types,

inactive entries are written only for the unused portion of the last cylinder containing prime data records. The key area of inactive entry contains bytes of all one-bits and is the same length as the user's key length. The data field is the same length as the normal entry. See Figure 17 for the format of the track index data area entries.

When the cylinder overflow crticn is specified by the user, record zero (track descriptor record) of track zero in the track index is used as a Cylinder Overflow Control Record (COCR). This entry is set up in the data area of record zero (R0). The address of the last overflow record on the cylinder and the number of tracks remaining in the cylinder overflow area are maintained by ISAM in this record. The format of the COCR is HHR00T00, where HHR = Address of last overflow record on cylinder. T = Number of tracks remaining in the cylinder overflow area. The COCR format is shown in Figure 20.

# Cylinder Index (CI)

The cylinder index is present for all ISAM-organized files. It is an intermediate level index used to point to the correct track index.

The cylinder index can contain the following types of entries:

- Normal Entry
- Dummy Entry Chained
- Dummy Entry End
- Inactive Entry

A cylinder index is built by ISAM to contain one index entry for each cylinder in the prime data area of the file. This entry contains the highest record key associated (in the cylinder cr a corresponding overflow area) with the cylinder, and the address of the track index for that cylinder. Figure 21 is an example of a cylinder index built for a file requiring nine cylinders.

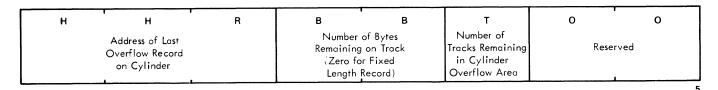
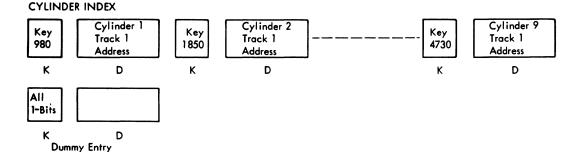


Figure 20. Cylinder overflow control record (COCR).



K = Key Area D = Data Area

Figure 21. Schematic example of a cylinder index.

The cylinder index can be located wherever the user chooses except on one of the cylinders that contains data records for the file. It must be on a separate cylinder or it can be placed on a separate volume that will be on-line whenever the logical file is processed. The cylinder index can also be located on one or more successive cylinders. When more than one cylinder is required, the last entry on each cylinder is a dummy chained entry that points to the first track of the next cylinder. However, the cylinder index cannot be continued from one volume to another. A job control EXTENT card must be used to specify the correct location for this index.

The last entry in the cylinder index is a dummy end entry. The key of the dummy entry is the same length as the user's key length and contains bytes of all one-bits. The data field is of the same length as the normal entries, but is a null field.

Inactive cylinder index entries have the same format as the track index inactive entries. They are written to provide for future expansion of the file and for OS/VS1 and OS/VS2 compatibility. An inactive cylinder index entry is written for each track in the cylinder containing track index inactive entries. See Figure

17 for the format of the cylinder index data area entries.

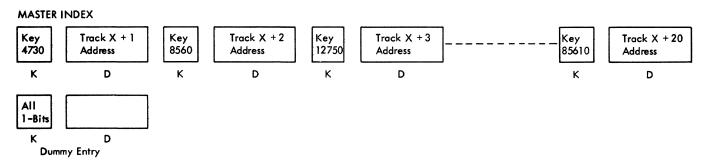
## Master Index (MI)

The master index is the highest level index for a logical file built by ISAM . This index is crticnal; and if required, must be specified by the user in the DIFIS entry MSTIND.

The master index can contain the following types of entries:

- Normal Entry
- Dummy Entry Chained
- Dummy Entry End
- Inactive Entry

The master index must immediately precede the cylinder index on a volume, and it may be located on one cr more successive cylinders. Whenever it is continued from one cylinder to another, the last index entry on the first cylinder contains a linkage field that points to the first track of the next cylinder. This type of entry is a dummy chained entry.



K = Key Area D = Data Area

Figure 22. Schematic example of a master index.

index may not be continued from one volume to another. It must be completely contained within one volume. The last track assigned to the master index area must be contiguous to the first track of the cylinder index area. A job control EXTENT card must be used to specify the correct location. Like the cylinder index, it can be located on the same volume with the data records or on a different volume that will be on-line whenever the records are processed.

The entries in this index point to each track of the cylinder index. Each entry contains the highest record key on the cylinder index track and the address of that track. For example, if a master index is located on track x and a cylinder index is located on tracks x+1 through x+20, the master index might contain the entries shown in Figure 22.

The last entry on the master index is a dummy end entry. The key of the dummy end entry is the same length as the user's key length and contains bytes of all one-bits. The data field is of the same length as the normal entries, but is a null field.

Inactive master index entries have the same format as the track index inactive entries. They are written to provide for future expansion of the file and for OS/VS compatibility. An inactive entry is written for each track of the cylinder index containing inactive entries. See Figure 17 for the format of the master index data area entries.

# FUNCTIONS PERFORMED BY ISAM

ISAM performs the following four basic functions as specified in the DTFIS entry, ICRCUT:

- LOAD. To build a logical file on DASD or to extend a file keyond the highest record presently in an organized file.
- ADD. To insert new records into an crganized file.
- RETRVE. To retrieve records from a file for either random or sequential processing and/or updating.
- ADDRTR. Both to insert new records into a file (ADD) and to retrieve records for processing and/cr updating (RTR).

## LOAD OR EXTEND A DASD FILE

Data records to be loaded onto a DASD file must be scrted into sequence by record key, before being presented to the ISAM load routines.

The data records are written by ISAM onto a DASD track in an area of the file (called the prime data area) specified by the user. The position of each logical record is a function of the record key used in the presort operation. That is, each record is written one after the other onto the prime data area of the logical file. The user must specify one extent for the prime data area on one pack. If a file is to be loaded onto more than one pack, the prime data area must continue from the last track of one pack to the first track of another pack. Extents must be adjacent. The starting and ending limits of the prime data area are specified by the user in job control EXTENT cards. In addition, all packs to be used for a multipack file must te on-line throughout the load creration.

## ADD RECORDS TO A FILE

After a logical file has been organized on DASD, it may subsequently become necessary to add records to the file. These records may contain keys that are above the highest key presently in the file and, thus, constitute an extension of the file. may also contain keys that fall between or below keys already in the file and therefore require insertion in the proper sequence in the organized file.

If all records to be added have keys that are higher than the highest key in the organized file, the upper limit of the prime data area of the file can be adjusted (if necessary) by the specification in a job control EXTENT card, and the new records can be added by prescrting them and loading them into the file. No overflow area is required. The file is merely extended further on the volume. However, new records can be batched with the normal additions and added to the end of the file.

If records must be inserted among those already organized, an overflow area is required. ISAM uses the overflow area to permit the insertion of records without necessitating a complete recrganization of the established file. The fast random and sequential retrieval of records is maintained by inserting references to the overflow chains in the track indexes, and by using a chaining technique in the overflow records. For chaining, a sequence-link field is prefixed to the user's data record in the cverflcw area. The sequence-link field enables ISAM to follow a chain of sequential records in a search for a particular record. This 10-byte sequence-link field has two types of entries: an overflow chained entry and an overflow end entry (see Figure 17).

The overflow chained entry contains the address of the record in the overflow area that has the next-higher key. The overflow end entry indicates the end of the chain. All records in the overflow area are unblocked, regardless of the specification (in DTFIS RECFORM) for the data records in the logical file.

To add a record by insertion, ISAM searches the established indexes first to determine on which track the record must be inserted. After the proper track index entries are located, the point of insertion can then be determined. The keys of the last records on the tracks in the originally organized file determine the track where an inserted record belongs. A record is always inserted on the track where:

- 1. The last key is higher than the insertion, and
- The last key of the preceding track is lower than the insertion.

After the proper track is determined, ISAM searches the individual records on the track or cverflow area (if necessary) to find where the record belongs in key crder. This results in either of two conditions:

The record falls between two records presently on the track. ISAM adds the record by inserting it in the proper sequence and shifting each succeeding record one record location higher on the track, until the end record is forced off the track. ISAM transfers the end record to the overflow area, and prefixes the record (data area) with a sequence-link field. The first time a record is inserted on a track, the sequence-link of the overflow record indicates that this is the highest record associated with the track. Thereafter, the sequence-link field cf each overflow records points to the next-higher record for that track.

ISAM also updates the track index to reflect this change. The normal entry for the track has the key field changed to indicate the new last record located on the track. The overflow entry for the track has the track address (in the data area) changed to point to the address of the overflow record.

The record falls between the last record presently on the track and the last record originally on the track. Thus, it belongs in the overflow area. ISAM writes the record in the cverflcw area following the last record previously written. ISAM searches through the chain of records associated with the corresponding track for this record and identifies the sequential position the record should take. Then the sequence-link fields of the new record, and of the record preceding it by sequential key, are adjusted to point to the proper records.

## RANDOM RECCRD RETRIEVAL

Random retrieval from an indexed-sequential file is performed by the READ macro instruction. In response to the READ instruction, ISAM searches the indexes to locate the track containing the desired record and then searches the track for the record. The block containing the record is read and the record is made available for processing. For both blocked and unblocked files, only the data portion of the record is read; the key field is not read.

After record processing has been completed, a WRITE macro instruction can be issued to write the record back in its original location. To allow overlap of input and output operations with processing, READ and WRITE dc nct wait for completion of the operations, but return control to the problem program. The WAITF macro instruction is used at the point in the program where processing must be held up until the I/O operation is complete.

## SEQUENTIAL RECORD RETRIEVAL

Sequential retrieval from an indexed-sequential file begins at a location or record specified in a SETL macro instruction. Input blocks are read and each record is presented in sequence in response to the GET macro instruction. When necessary, ISAM reads those records from the overflow area that were displaced from the prime data area by added records. The track index overflow entry is used to indicate when this is necessary. The key field of unblocked records is read along with the data field. With blocked records, however, the key of the block (repeated in the last record of the block) is not read.

After record processing has been completed, a PUT can be issued to write the record back into its original location. If the file is blocked, the entire block is written back after either all records in the block have been processed and a GET is issued for the first record in the next block or an ESETL macro instruction is issued. The PUT macro instruction does not have to be issued for records that have not been changed; a series of GETs can be issued with no intervening PUT. The entire block is written back into the file if, and only if, a PUT is issued for any record in the block.

Cnce a SETL macro instruction has been issued, GET and PUT are the only I/O operations that can be performed before issuing an ESETL macro instruction. For example, if a WRITE is to be issued to add a record to a file that is being processed sequentially, it must be preceded by an ESETI. After adding the new record, the SETL macro instruction can be reissued, specifying the last record processed as the new starting point.

# DTFIS MACRO

Before an indexed sequential file can be processed, it must be defined by the DTFIS declarative macro. Some cf the fields within the DTFIS table generated from this macro instruction are not determined cr filled in until the file is opened during execution of the program. Many cf the fields in the table are retained with the file in the DASD format-2 label.

In addition to the parameters that describe the file to be processed, the DTFIS macro instruction includes certain parameters identical to those in the ISMOD macro instruction.

The following five DTF tables are generated according to function. They are:

DTFIS LOAD (Figure 23)

DTFIS ADD (Figure 24)

DTFIS RETRVE, RANDOM (Figure 27)

DTFIS RETRVE, SECNTI (Figure 28)

DTFIS ADDRTR (Figure 29)

In addition, the DTF tables for ADD, RETRVE, and ADDRTR are divided into the three parts that appear in the assembly listing. The first part of the DTF table is common to the ADD, RETRVE and ADDRTR functions. The rest of the table is variable and is generated according to the options specified in the DTFIS detail entries.

For a description of the DTFIS header entry and detail entries see the DOS/VS Supervisor and I/O Macros, GC33-5373.

Ncte: The DTFIS may be altered when used by any of the compilers. For further information, refer to the Programmer's Guide for the appropriate compiler.

DTF Assembly Label	Module  DSECT Label	B <b>yte</b> s	    Bits	Function
&Filename   	IJHKCCB	0-15 (0-F) 16 (10)	2   3   4   5   6	Command Control Block (CCB).    Not used.  1 = COBOI open ignore option.  Not used.  1 = DTF table address constants relocated by CPENR.  Not used.  1 = Data set security.  1 = Wrong blocksize error during file extension.   Address of logic module.
     	   	(11-13)   20   (14) 	 	  File type for OPEN/CLOSE (X'24' = ICAD).     
 	IJHKOPCO   	21 (15)	   	Option byte.
\ 			0	1 = 2321 (Version 1/2 only).  Not used.  1 = Cylinder overflow option.  Not used.  1 = Elccked records (used by previous versions).   = Verify.  1 = Indexes on 2321 (Version 1/2 only).  1 = 2 I/O areas present.
 	1   	22-28   (16-1C)	   	File name.
       	IJHKPDEV       	29 (1D)	• • • • •	Prime data device type indicator.   X'00' = 2311   X'C1' = 2314/2319   X'02' = 2321   X'04' = 3330.
&Filename.C    KCCOD    -   (1E)	   0   1   2   3   4   4   5   6	Status byte.		

Figure 23. DTFIS LOAD table (1 cf 5).

DTF Assembly Label	Module  DSECT Label	Bytes	Bits	Function
	IJHKHNDV       	31 (1F)		High level index device type indicator. x'C0' = 2311 x'01' = 2314/2319 x'02' = 2321 x'04' = 3330.
# 	       	32 (20)	       	Relative position of the DSKXTN (logical unit, cell number) table (in words). This value is the length of the DTF table divided by 4.
 		33-34   (21-22)	! ! !	  First prime data track in cylinder (HH). 
	1	35   <b>(</b> 23)	<b>[</b>	First prime data record in cylinder (R).
	1	36-37 (24-25)	 	Last prime data track in cylinder (HH).
Ì !		38 (26)	<b>i</b> 	High record on master index/cylinder index  track (R).
	IJHKNRPD	   39   (27)	 	High record on prime data track (R).
	1	40 (28)	 	High record on overflow track (R).
 	   IJHKNRSH 	41   (29)	   	  High record on last track index track in  cylinder (whether shared or unshared).
1	IJHKNRTI   	42 (2A)		High record on track index track other than    last in cylinder. If only one track index  track in cylinder, it is equal to Byte 41.
	IJHKFLAG	43 (2E)	   	  Condition Code. 
!    -  -  -	 		0 1 2 3	1 = WLR checks requested (for extension).  1 = First record in file.  1 = Prime data extent full.  1 = Master index/cylinder index extent too   small.
     	 		4     5   6-7	<pre> 1 = Prime data upper limit has been   increased (for extension).  1 = Extension.  Not used.</pre>
1		44-50   (2C-32)	 	Prime data lower limit (MBECCHH).
	! !	51-57   (33-39)   58-64		Cylinder index lower limit (MBECCHH).    Master index lower limit (MEECCHH).
	   	(3A-40)   65   (41)		  Number of index levels (1 if cylinder index,  2 if master index).

Figure 23. DTFIS LOAD table (2 cf 5).

DTF Assembly Label	Module  DSECT Label	Bytes	Bits	Function
&Filename.H	IJHKLPDR	66-73 (42-49)	!   	Address of last prime data record
	IJHKLGIN     	74-75   (4A-4B)   76-77   (4C-4D)		Logical record length.    Key length.
	   ijhkekln     	78-79 (4E-4F) 80-81 (50-51)		Block length (logical record length times   number of records).   Overflow record length (logical record   length +10).
	IJHKNRCD	   82-83   (52-53)	1 1	  Blocking factor (number of logical records).  
	i I	84-85 (54-55)		Index entry length (key length +10).
		86-87   (56-57)   88-89   (58-59)   90-91   (5A-5E)   92-93   (5C-5D)   94-95   (5E-5F)		Prime data record length (key length + physical record length).  Overflow record length with key (key length   + logical record length + 10).  Prime data record format length (key length   + physical record length + 8).  Coverflow record format length (key length + logical record length + 18).  Key location (in blocked records).
This is the elis variable a macro instruc	nd is generate			ne format of the remainder of the table the parameters specified in the DTFIS
&Filename.S	   IJHKSBKT 	   96-103  (60-67)		  Seek/Search address area (MBBCCHHR).
&Filename.P	   IJHKLGCT     	  104-105  (68-69)  106-107  (6A-6B)		Logical record counter (for blocking).     Number of bytes for high level index. 
	   IJHKPRCT             	  108-111  (6C-6F)  112  (70) 	3-5	Prime data record counter (logical records).    Status indicators.   Not used.   1= File closed.   Not used.   1 = Last prime data track full.   1 = Last block full.
	   IJHKLTIR 	  113-117  (71-75)		  Last track index normal entry address  (CCHHR).
	   IJHKLCIR 	  118-122  (76-7A)	   	  Last cylinder index entry address (CCHHR).
	   IJHKLMIR 	  123-127  (78-7F)	1	Last master index entry address (CCHHR).

Figure 23. DTFIS LOAD table (3 cf 5).

DTF Assembly Label	Module  DSECT Label	Bytes	    Bits	Function
&Filename.B			†   	CCW build area. See description of SETFL macro, phase 1 - \$\$BSETFI.
	<b>.</b>   	128-135 (80-87)	1	Seek CCW.
	i I	136-143 (88-8F)	į Į	Search ID equal CCW.
	   	144 <b>-</b> 151  (90-97)		TIC CCW.   
	IJHKRDWR	152-159 (98-9F)	İ	Read/Write CCW.
		160-167  (A0-A7)	1	Search ID equal CCW.
	<b>!</b> 	168-175  (A8-AF)  176-183	 	TIC CCW.   
	   	(B0-B7)		
&Filename.M	IJHKADCN 	184-187 (B8-BB)		Address of IOAREAL.
	 	188-191  (BC-BF)  192-195  (C0-C3)		Address cf data in WORKL. (FIXBIK = address   of WORKL; FIXUNB = address cf WORKL + key).    Address cf key in WCRKI. (FIXEIK = address    of WCRKL + KEYLOC - 1; FIXUNB = address cf    WORKL.)
	I   IJHKBPCS 	  196-199  (C4-C7)		  Blcck positicn indicator (address of logical   record in ICAREAL).
	   IJHKMIXT       	200 (C8)		Master index, extension indicator.   Not used.   1 = Extending file, 0 = Creating file.   Not used.   1 = Master index being used, 0 = Nc master
	 	201-204   <b>(</b> C9-CC <b>)</b>   205-208		index being used.  Cylinder index upper limit (CCHH).      Master index upper limit (CCHH).
	     IJHKPDUL	(CD-D0)     209-215		 
		(D1-D7)  216-222  (D8-DE)		extension) (MBBCCHH).   Prime data new upper limit (for extension)   (MBBCCHH).
•	   IJHKLTM1 	223 (DF)	 	  Last prime data track in cylinder - 1.   
	   IJHKKLM1 	  224-225  (E0-E1)		  Key length - 1. 
	   IJHKLLM1 	226-227   (E2-E3)		Logical record length - 1.
	IJHKTIDR 	228-229  (E4-E5)		Address cf track index dummy record (HR).
   	IJHKBFCR   	230-231  (E6-E7)	<u> </u>	Address of record before first prime data record in cylinder (HR).

Figure 23. DTFIS LOAD table (4 cf 5).

DTF Assembly Label	Module  DSECT Label	Bytes	  Bits	Function
	IJHKNRCM	232 (E8)		Number of records on master index/cylinder   index track - 1.
	IJHKCMCT           	233-236 (E9-EC)	         	Master index/cylinder index DASD address   control field (CCHH).   2311 = X'CCC70009'   2314/2319 = X'00C70013'   2321 = X'13090413'   3330 = X'01FF0012'
	IJHKPDCT       	237-239 (ED-EF)		Prime data address control field (CCH).  2311 = X'CCC700'  2314/2319 = X'00C700'  2321 = X'130904'  3330 = X'01FF00'
	   IJHKPDBG     	240-242 (F0-F2)		Prime data beginning of volume (CCH).  2311 = X'CC0100'  2314/2319 = X'000100'  2321 = X'CC0001'  3330 = X'000100'
	IJHKPDEN       	243-245 (F3-F5)		Prime data end of volume (CCH).    2311 = X'CCC700'    2314/2319 = X'00C700'    2321 = X'130504'    3330 = X'019300'
	 	246-247 (F6-F7)		Used for alignment.
&Filename.E           	IJHKXTBL   	248-251 <sup>1</sup> (F8-FB)  256-259 <sup>2</sup> (100-103)  260-263 (104-107)  264-267 (108-10B)		First entry in DSKXTN table (logical unit,

<sup>1</sup>Each entry in the DSKXTN table is four bytes long. The minimum number of entries is two. There is one entry per extent.

|2Location of the end-of-table indicator depends on length of DSKXTN table.

Figure 23. DTFIS LOAD table (5 cf 5).

DTF Assembly Label	Module  DSECT Label	Bytes	Bits	Function
&Filename                         	IJHCCCB	0-15 (0-F) 16 (10) 17-19 (11-13) 20 (14)	2   3   4     5   6	CCB.    Not used.  1 = COBOL cpen ignore option.  1 = Track hold specified.  1 = DTF table address constants relocated by OPENR.  Not used.  1 = Data set security.  1 = Wrong blocksize error during addition to file.  Logic module address.
 	IJHCOPT                   	21 (15)           22-28   (16-1C)	1   2   3   4   5   6-7	Option byte.    1 = 2321 (Version 1/2 only).  1 = Prime data in core.  1 = Cylinder overflow.  1 = Cylinder index in core.  1 = Blocked records.  1 = Verify.  Not used.  DTF file name.
	IJHCPDEV	29   (ID) 	         	Prime data device type indicator.   X'00' = 2311   X'C1' = 2314/2319   X'02' = 2321   X'C4' = 3330.
&Filename.C                 	IJHCSTEY	30 (1E)	1   2   3   4   5	Status byte.
         	IJHCHNDV	31 (1F)		Highest level index device type.   X'00' = 2311   X'C1' = 2314/2319   X'02' = 2321   X'C4' = 3330.

Figure 24. DTFIS ADD table, part 1 (1 of 4).

DFT Assembler    Label	Module DSECT Label	Bytes	  Bits	Function
	IJHCPNT	32 (20) 33-35 (21-23) 36-37 (24-25)		Relative position of the DSKXTN (logical unit, cell number) table (in words). This value is the length of the DTF table divided by 4.  First prime data record in cylinder (HHR).  Last prime data track in cylinder (HH).
 	<b>1</b>	38 (26)		  High record number on master  index/cylinder index track (R).
 	   IJHCPDH   	39 (27) 40 (28)	       	  High record number on prime data track (R).      High record number on overflow track (R).
] 	IJHCSTH	41 (29)	   	  High record number on shared track (R). 
 	I IJHCTIH	42 (2A)	 	  High record number on track index (TI)  track (R).
	IJHCRTR   	43 (2B) 44-50 (2C-32)	1   2   3   4   5	Retrieval byte.    1 = WORKR area specified.  1 = WORKS area specified.  Overflow switch.  1 = Read.  Not used.  1 = Cutput.  1 = Write key.  1 = PUT macro issued.  Prime data lower limit (MBECCHH).
] 	IJHCCIS	51-57 (33-39)		Cylinder index lower limit (MBBCCHH).
! 	IJHCMIS	58-64 (3A-40)	!   	
	IJHCILN	65 (41)		Index level number, WAITF indicator.   X'02' = Master index.   X'40' = WAITF seek check bit   X'80' = From WAITF routine.
  &Filename.H 	IJHCCLPA	66-73 (42-49)	    -	  Last prime data record address (MEECCHHR). 
 	   IJHCRESZ 	74-75 (4A-4B)		  Logical record length (RECSIZE). 
1 	IJHCKYSZ	76-77 (4C-4D)		  Key length (KEYLEN). 
! 	   IJHCBLSZ 	78-79 (4E-4F)		  Elcck size (logical record length times  number of records).
   	IJHCRL10	80-81 (50-51)	 	Overflow record length (logical record   length + 10).

Figure 24. DTFIS ADD table, part 1 (2 of 4).

DFT Assembler   Label	Module  DSECT Label	   Bytes	  Bits	Function
	IJHCBFAC	82-83 (52-53) 84-85 (54-55)		Blocking factor (number of logical records  in block (NRECDS)).  Index entry length (key length + 10).
	I IJHCABCD	86-87 (56-57)		Prime data record length (key length plus
 		88-89   <b>(</b> 58-59)	!   	Overflow record length plus key (key length   + logical record length + 10).
	IJHCCMAX 	90-91 (5A-5B) 92-93 (5C-5D)		Prime data record format length (key length   + tlock size + 8).  Overflow record format length (key length +   logical record length + 18).
	IJHCKYLC	94-95 (5E-5F)	   	Key location (KEYLOC) for blocked records.
		96-97   (60-61)   98-99   (62-63)	     	Constant = 5.    Constant = 10.
	IJHCATB2	100-101	 	  Displacement of Part 2 of the DTFIS table  frcm start of Part 1.
	IJHCATE3	102-103	   	  Displacement of Part 3 of the DTFIS table  frcm start cf Part 1.
  &Filename.S   	   IJHCSADR 	104-113	    -	  Seek/search address area (MEBCCHHRFP). 
  &Filename.W 	IJHCBKCT	114-123 (72-7E)	   	  Random/sequential retrieval work area. 
  &Filename.P 	   IJHACPRC 	124-127 (7C-7F)	 	Prime data record count.
	IJHACSTI   	128 (80)	     0-1   2   3-5   6   7	Status indicators.    Not used.  1 = File Clcsed.  Not used.  1 = Last prime data track full.  1 = Block complete.
	IJHACLTA	  129-133  (81-85)		  Last track index normal entry address  (CCHHR).
	   IJHACLCA 	  134-138  (86-8A)	 	  Last cylinder index entry address (CCHHR). 
 	   IJHACLMA 	  139-143  (8B-8F)	   	  Last master index entry address (CCHHR). 
 	   IJHACLOA 	  144-151  (90-97)	     	  Last independent overflow record address  (MBBCCHHR).

Figure 24. DTFIS ADD table, part 1 (3 of 4).

DFT Assembler Label	Module  DSECT Label	     Bytes	    Bits	Function
&Filename.I	IJHACOTC	152-153 (98-99)	   	Number of independent overflow tracks.
  &Filename.A 	   IJHACOFC	  154-155  (9A-9B)	1   	  Number of full cylinder overflow areas.
&Filename.O	   IJHACORC 	  156-157  (9C-9D)	   	Overflow record count.
!   	   IJHACOLL 	  158-164  (9E-A4)	     	Independent overflow area lower limit
 	   IJHACOUP 	  165-171  (A5-AB)	     	  Independent cverflow area upper limit  (MBBCCHH).
!   	IJHAHRAA 	  172-175  (AC-AF)	1 	A(&Filename.D) - Address of work area for cylinder overflow control record (COCR).
! 	     	  176-179  (B0-B3)		A(&Filename.D+8) - Address of work area for   the current track index normal entry count field.
 		  180-183  (B4-B7)		A(&Filename.C+16) - Address of work area for current track index overflcw entry ccuntfield.
] [ ]	! !	  184-187  (B8-EB)	   	  A(&Filename.C+24) - Address of work area for  current prime data record count field.
!   	; <b>[</b> ]	  188-191  (BC-BF)	!   	A(&Filename.D+32) - Address of work area for current overflow record count field.
! ! !	   	192-195 (C0-C3)		A(&Filename.D+40) - Address of work area for track index normal entry data field.
 	IJHADLNK	196-199  (C4-C7)		A(&Filename.D+50) - Address of work area for current overflow record linkage field.
•   	IJHAARAD	200-203 (C8-CB)		A(&ICAREAL) - Address of IOAREAL, the I/C   area used for adding records to a file.
	IJHACUSE 	204-207 (CC-CF)		A(&WORKL) - Address of WCRKI, work area containing user data records to be added to the file.
i   	IJHADKEY	208-211 (D0-D3)		A(&Filename.K) - Address of the ADD key area.
 	 	212-215 (D4-D7)		A(&ICAREAL+8) - Address of key position in IOAREAL.
   	IJHAKLN8	216-219 (D8-DB)	   	  A(&IOAREAL+8+&KEYLEN) - Address cf data  positicn in IOAREAL.

Figure 24. DTFIS ADD table, part 1 (4 of 4).

DTF Assembly Label	Module  DSECT Label	Bytes	Bits	Function
&Filename.2	IJHCASAD   	0-3 (0-3) 4 (4) 5-7 (5-7)	•	A(&Filename.S+3) - Address cf the  seek/search address area+3.   1 = Seek check indicated.  Not used.   1 = Over/under seek has occurred.   1 = An error has been found, but a seek  check is indicated.  A(&Filename.W) - Address of  random/sequential retrieval work area.
The following is specified.	information i	is generate	ed if	the cylinder index in core option
	IJHCORST       	8-11 (8-B) 12-13 (C-D) 14-21 (E-15) 22-26 (16-1A)		A(&INDAREA) - Starting address of main   storage area specified for cylinder index.  AL2(&INDSIZE) - Number of tytes in main   storage available for cylinder index.   Next cylinder index entry to be read   (MEECCHHR).   Last cylinder index entry (CCHHR).
	IJHCORBT         	27 (1B)	0   1   2   3	Core index byte.   Core index byte.   Core index byte.   Core index byte.   Core index time through B-transient,   S\$BINDEX.   Core index reached.   Core index skip option specified.   Core index in-core option and read   Core index.   Core index index.   Core index
† 	   IJHCORKY 	28-31 (1C-1F)		Pointer to key (stored by module).
				the prime data in core add aligned on a double word boundary.
	IJHPSIZE 	32-33 (20-21)	   	Size of IOAREAL.
!   	IJHPMAX	34-35 (22 <b>-</b> 23)	}   	  Maximum number of prime data reccrds in main  stcrage.
	   IJHPDSP1 	   36-39   (24-2 <b>7)</b>	 	  Address cf write CCW's. 
 	   IJHPDSP2 	40-43 (28-2B)	   	  Address of read CCW's. 
! 	   IJHPSW 	   44   (2C)	   	  Switch byte.   
 	 	   45-47   (2D-2F)		1 = EOF.  Not used.  For boundary alignment. 

Figure 24. DTFIS ADD table, part 2.

DTF Assembly Label	Module  DSECT Label	T     Bytes	    Bits	Function
&Filename.B           	       IJHCCCW   	0-7   (0-7)   8-127   (8-7F)	         	CCW X'07', &Filename.S+1, X'40', 6 - Long seek CCW with command chaining. Channel program build area. See Figures 17-38 for a description of the channel program builder.
&Filename.D	   IJHACOCR 	128-135 (80-87)	1 1 1	Cylinder overflow control record (COCR).
! !	   IJHACTNA 	  136-143  (88-8F)		  Current track index normal entry ccunt field  address.
 	   IJHACTCA 	  144-151  (90-97)	 	  Current track index overflow entry count  field address.
]   	   IJHACRID 	  152-159  (98-9F)	    -	  Current prime data record ccunt field  address.
! !	   IJHACFID 	  160-167  (A0-A7)	 	  Current cverflow record count field address. 
<b>!</b> <b>!</b>	   IJHACTIN 	  168-177  (A8-B1)	   	  Track index normal entry data field. 
i   	   IJHACLNK 	  178-187  (B2-BB)	<u> </u> 	  Current cverflow record sequence link field. 
<b>!</b>	   IJHACTIA 	  188-197  (BC-C5)	   	  Current track index overflcw entry data  field.
 	Ï	  198  (C6)  199-201  (C7-C9)		X'01' - Add to EOF.  X'02' - Add to independent overflow area.  Overflow control bytes (CCH).
	   IJHAOCOH     	202-203   (CA-CB)   204-211   (CC-D3)		High HR on overflow track. See Figure 10.     Volume upper limit for prime data records   (MEECCHHR). See Figure 11.
       	   ijhaicom   	  212-217  (D4-D9) 	1 1 1	CLC 0(&KEYLEN,13),0(6) - Unblocked CLC 0(&KEYLEN,13),&KEYLOC-1(6) - Blocked Utility CLC for key.
	   ijhaisky   	218-223 (DA-DF)		MVC 0(&KEYLEN,13),0(12) - Unblocked   MVC 0(&KEYLEN,13),&KEYLOC-1(12) - Blocked   Utility MVC for key.
  &Filename.E     	         	  224-2271  (E0-E3)  232-2352  (E8-EB)		  First entry in DSKXTN table (logical unit,  cell number).  4X'FF' - End of DSKXTN table. 
  &Filename.K   	   	236+   (EC-end)	   	Key area for ADD only. Number of bytes depends on key length, KEYIEN.

1Each entry in the DSKXTN table is four bytes long. The minimum number of entries is two. There is one entry per extent.
| Location of the end-of-table indicator depends on length of DSKXTN table.

Figure 24. DTFIS ADD table, part 3.

2311	2321	2314/2319	3330
M = Extent sequence number	M = Extent sequence number	M = Extent sequence number	M = Extent sequence number
BB = 00	BB = Cell number	BE = 00	BB = 0
C = 0	C = 19	C = 0	CC = 403
C = 199	C = 5	C = 199	
H = 0	H = 4	H = 0	H = 0
H = 9 - CYLOFL (number of tracks reserved for cylinder overflow	H = 19 - CYLOFL (number of tracks reserved for cylinder overflow	H = 19 - CYLOFL (number of tracks reserved) for cylinder cverflcw	<pre>H = 18 - CYLCFL     (number of     tracks reserved     for cylinder     overflow</pre>
R = Number of records that fit on overflow track.	R = Number of records that fit on overflow track.	R = Number of records that fit on overflow track.	R = Number of records that fit on overflow track.

Figure 25. Overflow area upper limits (MBBCCHHR).

2311/2314/2319/3330	2321
M = Extent sequence number	M = Extent sequence number
BB = 00	BB = Cell number
CC = 199 for 2311/2314/2319	C = 19
= 403 for 3330	C = 5
Н = 0	H = 4
   H = Last prime data track   in cylinder	H = Last prime data track in cylinder
R = Last record on current track.	R = Last record on current track.

Figure 26. End of volume limits for prime data area (MBBCCHHR).

DTF Assembly Label	Module  DSECT Label	Bytes	    Bits	Function
&Filename               	IJHCCCB	0-15   (0-F)   16   (10)	1   2   3	Command Control Block (CCB).   Not used.  1 = GET issued.  1 = CCBOL open ignore option.  1 = HOID option specified.  1 = DTF table address constants relocated by OPENR.   Not used.
 	 	17-19 (11-13) 20 (14)	 	Address cf lcgic module. 
	I IJHCOPT	21 (15)	0   1   2   3   4   5	Option byte.    1 = 2321 (Version 1/2 only).  1 = Prime data in core.  1 = Cylinder overflow option.  1 = Cylinder index in core option.  1 = Blocked records.  1 = Verify.  Not used.
 	    -	22-28 (16-1c)		File name (DTF name).
	IJHCPDDV         	29   (1D)   	       	Prime data device type.   x'00' = 2311   x'01' = 2314/2319   x'02' = 2321   x'04' = 3330.
&Filename.C               	IJHCSTBY	30 (1E)	0   1   2   3   4   5	Status byte.
 	   IJHCHNDV       	   31   (1F) 	 	High level index device type.   X'C0' = 2311   X'01' = 2314/2319   X'C2' = 2321   X'04' = 3330.
 	   IJHCPNT       	32 (20)	         	Relative position of the DSKXTN (logical   unit, cell number) table (in words). This   value is the length of the DTF table divided   ty 4.

Figure 27. DTFIS RETRVE, RANDOM table, part 1 (1 cf 3).

DFT Assembler	Module  DSECT Label	Bytes	    Bits	Function
		33-35 (21-23)		First prime data record in cylinder (HHR).
 		36-37 (24-25) 38		Last prime data track in cylinder (HH).      High record number on master index/cylinder
<u> </u>	IJHCPDH	(26) 39 (27)		index track (R). High record number on prime data track (R).
 		40 (28)		High record number on overflow track (R).
 	IJHCSTH	41 (29)		High record number on shared track (R).
] 	ІЈНСТІН	42 (2A)		  High record number on track index track (R). 
1 	IJHCRTR	43 (2B)		  Retrieval byte. 
		44-50 (2C-32)	2   3   4   5   6	1 = WORKR specified.  1 = WORKS specified.  Overflow switch.  1 = Read key.  Not used.  1 = Output.  1 = Write key.  1 = PUI macro issued.  Prime data lower limit (MBECCHH).
j 1 1	IJHCCIS	51-57 (33-39)		  Cylinder index lower limit (MBECCHH). 
 	   IJHCMIS   	58-64 (3A-40)		  Master index lower limit (MBBCCHH).
	IJHCILN   	65 (41)	1   2   3   4–5	Index level number, WAITF, and track hold   indicators.   1 = From WAITF routine.   1 = Seek check from WAITF.   1 = Index track held.   1 = Data track held.   Not used.   1 = Master index.   1 = Cylinder index.
! !	IJHCCLPA	66-73 (42-49)		  Last prime data record address (MBECCHHR). 
 	   IJHCRESZ 	74-75 (4A-4B)		
 	   IJHCKYSZ 	76-77 (4C-4D)	   	  Key length. 
! !	   IJHCBLSZ 	78-79 (4E-4F)	! !	  Elcck size (logical record length times  number of records).
 	   IJHCRL10 	80-81 (50-51)	    -	Overflow record length (logical record length + 10).

Figure 27. DTFIS RETRVE, RANDCM table, part 1 (2 of 3).

DFT Assembler	Module DSECT Label	Bytes	  Bits	Function
	IJHCBFAC	82-83 (52-53) 84-85 (54-55)	†       	Blocking factor.
	IJHCABCD	86-87 (56-57) 88-89 (58-59)		Prime data record length (key length +   physical record length). Overflow record length with key (key length   togical record length + 10).
 	IJHCCMAX	90-91 (5A-5B)	! ! !	Prime data record format length (key length    + physical record length + 8).
! ! !	1	92-93 (5C-5D)	!   	Overflow record format length (key length +   logical record length + 18).
 	IJHCKYLC   	94-95 (5E-5F) 96-97 (60-61) 98-99 (62-63)		Key location (blocked records).
	   IJHCATE2 	  100-101  (64-65)		
! !	   IJHCATB3 	  102-103  (66-67)	   	
&Filename.S	IJHCSADR   	  104-113  (68-71)		Seek/search address area (MEBCCHHRFF).   
&Filename.W	IJHCBCKT 	114-123  (72-7B)		Randcm/sequential retrieval work area.

Figure 27. DTFIS RETRVE, RANDOM table, part 1 (3 cf 3).

DTF Assembly Label	Module  DSECT Label	Bytes	  Bits	Function
  &Filename.2 	IJHCASAD 	0-3 (0-3)	†   	Address of seek/search address area + 3.
<b> </b> 	<b>!</b> !	(4)	1 <b>-</b> 5	1 = Seek check indicated.  Not used.
   	 		7	1 = An errcr has been found, but a seek
Î 1	1	5 <b>-7</b>   (5 <b>-7</b> )		Address of random/sequential retrieval work   area.
Î <b>I</b>	IJHSIOAR 	8-11 (8-B)	1	Address of IOAREAS.
 	IJHCRARA   	12-15   (C-F)	 	Address cf ICAREAR.   
 	IJHCRKEY 	16 <b>-</b> 19   <b>(1</b> 0-13 <b>)</b>	1	Address of KEYARG.
 	IJHCRWCR	20-23   <b>(</b> 14-1 <b>7)</b>		Address of WORKR.
 	IJHSDB1 	24-27   (18-1B)	1	Current sequential I/O area address.
 	IJHSLICR 	28-31   (1C-1F)		4-byte NO-OP instruction.
 	IJHSLMIT 	32   (20)		X'00' = Nc verify, X'40' = Verify.
] !		33	!	X'08' = Unblocked, X'00' = Blocked.
   	 	34   (22)   35 <b>-</b> 39	 	R = First prime data record on shared track.  
,   	     IJHSINIT	(23-27) 40-41	! !	(CCHHR).  H'0' = Elccked records.
İ		(28-29)		H'2' = Overflow record.  H'8' = Unblccked records.
i I		42 (2A)	İ	X'C7' = 2311, 2314, or 2319; X'09' = 2321;  X'93' = 3330.
<u> </u> 		43-47 (2B-2F)	İ	Initial values for sequential retrieval.
&Filename.H	IJHSCADR	   48-55   (30-3 <b>7)</b>	1	  Current DASD address for sequential    (MBBCCHHR).
   	IJHSCOVF	(30-37)   56-63   (38-3F)		(MBBCCHHR).  Current cverflow DASD address for sequential    (MBBCCHHR).
	IJHSRCNT	64-65 (40-41)		Sequential record counter.
i !	IJHSTICU	66-67 (42-43)	İ	Current track index entry for sequential   (HR).
  &Filename.T	 	   68-69   (44-45)	1	  Number of records tagged for deletion.
! !	   IJHRREGS 	70-71 (46-47)		  Load IOREG for random retrieval. 
  &Filename.G	   IJHRIDSV 	   72-79   (48-4F)	1	  DASD address save area (MBBCCHHR).   
<u> </u> 	IJHRADSV	80-83 (50-53)		Record pointer within I/O area for write operation.

Figure 27. DIFIS RETRVE, RANDOM table, part 2 (1 of 2).

DTF Assembly Label	Module  DSECT Label	Bytes	  Bits	Function
&Filename.R	IJHROVCN	84-87 (54-57)		Nonfirst overflow record count.
The following specified.	information i	is generate	ed wher	n the cylinder index in ccre option is
	IJHCORST	88-91 (58-5B)		A(&INDAREA) - Starting address of main    storage area specified for cylinder index.
 	 	92-93 (5C-5D)	     	AL2(&INDSIZE) - Number of tytes in main    storage available for cylinder index.
   		94-101   (5E-65)	     	Next cylinder index entry to be read     (MEECCHHR). (Initialized by \$\$BINDEX to   cylinder index starting address.)
	   IJHCORET   	102-106 (66-6A) 107 (6B)	   0   1   2	Last cylinder index entry.    Core index byte.    1 = First time through transient.    1 = End of index reached.    1 = Index skip option.    Not used.
   	   IJHCORKY 	108-111 (6C-6F)		Pointer to key (stored by the mcdule).
 	   	112-143 (70-8F)		Reserved.

Figure 27. DIFIS RETRVE, RANDOM table, part 2 (2 cf 2).

DTF Assembly Label	Module  DSECT Label	Bytes	  Bits	Function
&Filename.B		0-7 (0-7)		X'C7',&Filename.S+1,X'40',6 - Long seek CCW with command chaining.
	   ijhcccw 	8-63 (8-3F)	 	
  &Filename.E     	 	64-671 (40-43) 72-752 (48-48)	         	First entry in DSKXTN table (logical unit,    cell number).    4X'FF' End cf DSKXTN table.

The length of one entry is the four bytes shown here. The minimum number of entries is 2. There is one entry per extent.

2The location of the end-of-table indicator depends on length of DSKXIN table.

Figure 27. DTFIS RETRVE, RANDOM table, part 3.

DTF Assembly Label	Module  DSECT Label	   Bytes	  Bits	Function
& Filename                       	IJHCCCB	0-15   (0-F)   16   (10)     17-19   (11-13)   20   (14)	0   1   2   3   4   5   6	Command Control Block (CCB).    Not used.  1 = GET issued.  1 = COBOL open ignore option.  1 = Track hold specified.  1 = DTF table address constants relocated by OPENR.  1 = EOF on sequential retrieve.  1 = Data set security.  1 = Different blocksize in format 1 label than in DTFIS.  Address of logic module.   File type for OPEN/CLOSE (X'26' = RETRVE).
	   IJHCOPT                     	21 (15)                 22-28   (16-10)	1   2   3   4   5   6	Option byte.   1 = 2321 (Version 1/2 only).   1 = Prime data in core.   1 = Cylinder overflow option.   1 = Cylinder index in core option.   1 = Blocked records.   1 = Verify.   1 = IOAREAS just used, 0 = ICAREA2 just used.   1 = 2 I/C areas present.   File name (CTF name).
! ! ! !	   IJHCPDEV       	29   (1D) 	 	Prime data device type.   X'00' = 2311   X'C1' = 2314/2319   X'02' = 2321   X'C4' = 3330.
& Filename.C                 	   IJHCSTBY                 	30   (1E)           	0   1   2   3   4   5	Status byte.   1 = Uncorrectable DASD error (except WLR error).   1 = WLR error.   1 = EOF (sequential).   1 = No record found.   1 = Illegal ID specified.   1 = Durlicate record sensed.   1 = Overflow area full.   1 = Record retrieved from overflow area.   1 = Record retrieved fr
         	IJHCHNDV         	31   (1F) 	           	High level index device type.   X'CO' = 2311   X'01' = 2314/2319   X'C2' = 2321   X'04' = 3330.

Figure 28. DTFIS RETRVE, SECNTI table, part 1 (1 of 3).

DFT Assembler Label	Module  DSECT Label	Bytes	Bits	Function
     	IJHCPNT 	32 (20)		Relative position of the DSKXTN (lcgical   unit, cell number) table (in words). This   value is the length of the DTF table divided   by 4.
 	 	33-35 (21-23) 36-37		First prime data record in cylinder (HHR).   
 		(24-25) 38 (26)		High record number on master index/cylinder    index track (R).
]   	I IJHCPDH	39 (2 <b>7)</b> 40		  High record number on prime data track (R).      High record number on overflow track (R).
   		(28)		
 	IJHCSTH	41 (29)		High record number on shared track (R).
	IJHCTIH	42 (2A)		High record number on track index track (R).
	IJHCRTR   	43 (2B)	0 1 2 3 4 4	Retrieval byte.   1 = WORKR specified.  1 = WORKS specified.  Overflow switch.  1 = Read key.  1 = First record being processed (after issuing SETI macro).  1 = Cutput.  1 = Write key.
   	 	44-50 (2C-32)		1 = PUT macrc issued.  Prime data lower limit (MBECCHH).
	IJHCCIS 	51-57 (33-39)		Cylinder index lower limit (MBBCCHH).
 	IJHCMIS	58-64 (3A-40)		  Master index lower limit (MBBCCHd). 
	IJHCILN       	65 (41)		Index level number, WAITF indicator.   X'02' = Master index.   X'40' = WAITF seek check bit.   X'80' = From WAITF routine.
[ [	IJHCCLPA	66-73 (42-49)		Last prime data record address (MBECCHHR).
 	IJHCRESZ	74-75 (4A-4B)	 	  Logical record length. 
1 	   IJHCKYSZ 	76-77 (4C-4D)		  Key length. 
 	IJHCBLSZ	78-79 (4E-4F)	 	Blcck size (logical record length times    number of records).
   	   IJHCRL10 	80-81 (50-51)	   	Overflow record length (logical record

Figure 28. DTFIS RETRVE, SEQNTL table, part 1 (2 of 3).

DFT Assembler   Label	Module  DSECT Label	Bytes	  Bits	Function
	IJHCBFAC	82-83 (52-53) 84-85 (54-55)	†       	Blocking factor.   
! 	IJHCABCD   	86-87 (56-57) 88-89 (58-59)	 	Prime data record length (key length +    physical record length).    Overflow record length with key (key length    + lcgical record length + 10).
	IJHCCMAX	90-91 (5A-5B) 92-93 (5C-5D)		Prime data record format length (key length   + physical record length + 8).    Overflow record format length (key length +    logical record length + 18).
	IJHCKYLC     	94-95 (5E-5F) 96-97 (60-61) 98-99 (62-63)		Key location (blocked records).
 	   IJHCATB2 	100-101 (64-65)		  Displacement of Part 2 of the DTFIS table      frcm Part 1.
1	   IJHCATB3 	102-103 (66-6 <b>7</b> )	!     	Displacement of Part 3 of the DTFIS table
&Filename.S	   IJHCSADR 	104-113 (68-71)	!   	Seek/search address area (MBBCCHHRFP).
  &Filename.W 	IJHCBCKT	114-123 (72-7B)	   	Random/sequential retrieval work area.

Figure 28. DTFIS RETRVE, SEQNTI table, part 1 (3 of 3).

DTF Assembly Label	Module  DSECT Label	Bytes	    Bits	Function
&Filename.2         	IJHCASAD           	0-3 (0-3) 4 (4)		Address of seek/search address area + 3.     1 = Seek check indicated.     Not used.   1 = Over/under seek has occurred.   1 = An error has been found, but a seek   check is indicated.
 	 	5-7   (5-7) 	 	Address of random/sequential retrieval work   area.
 	IJHSIOAR   	8-11 (8-B)	 	Address of ICAREAS.
  -	IJHCRARA 	12-15 (C-F)	   	Address of IOAREA2.
 	IJHCRKEY   	16-19 (10-13)	[   	Address of KEYARG.
!   	IJHCRWCR	20-23 (14-1 <b>7)</b>	 	Address of WORKR.
1	IJHSDB1 	24-27 (18-1B)		Current sequential I/C area address.
;   	IJHSLIOR	28-31 (1C-1F)	     	L ICREG, *-4 - Load IOREG.
	IJHSLMIT           	32 (20) 33 (21) 34 (22) 35-39 (23-27)		X'CC' = No verify, X'4C' = Verify.   X'C8' = Unblocked records, X'00' = Blocked   records.   R = First prime data record on shared track.   Upper limit for sequential retrieval   (CCHHR).
!   	   ijhsinit   	40-41   (28-29) 	     	H'0' = Blocked records,  H'2' = Overflow record,  H'8' = Unblocked records.  X'C7' = 2311, 2314, or 2319; X'09' = 2321;
 	   	(2A) 43-47 (2B-2F)	Ì	X'FF' = 3330.  Initial values for sequential (CCHHR).
&Filename.H	IJHSCADR 	48-55 (30-3 <b>7)</b>		Current DASD address for sequential    retrieval (MBBCCHHR).
!   	   IJHSCOVF 	56-63 (38-3F)	!   	Current overflow DASD address (MEECCHHR).
1   	   IJHSRCNT 	64-65 (40-41)		Sequential record counter.
     	   IJHSTICU   	66-67 (42-43)	   	Current track index entry (HR).

Figure 28. DTFIS RETRVE, SECNTI table, part 2.

DTF Assembly Label	Module  DSECT Label	Bytes	Bits	Function
&Filename.B		0-7 (0-7)		X'07',&Filename.S+1, X'40',6 - Long seek CCW  with command chaining.
	IJHCCCW   	8-63 (8-3F)		Area to build CCW-string. See Figures 49-56   for a description of the channel program    builder for sequential retrieval.
&Filename.E		64-671 (40-43) 72-752 (48-4B)		First entry in DSKXTN table (lcgical unit,  cell number).   4X'FF' - End of DSKXTN table.

The length of one entry is the four tytes shown here. The minimum number of entries is 2. There is one entry per extent.

2The location of the end-of-table indicator depends on length of DSKXTN table.

Figure 28. DTFIS RETRVE, SEQNTL table, part 3.

DTF Assembly Label	Module  DSECT Label	Bytes	    Bits	Function
&Filename                     	IJHCCCB	0-15 (0-F) 16 (10) 17-19 (11-13) 20 (14)	1   2   3   4   5   5   6	CCB.    Not used.  1 = GET issued.  1 = COBOI open ignore option.  1 = Track hold option specified.  1 = DTF table address constants relocated by CPENR.  EOF switch.  1 = Data set security.  1 = Wrong blocksize error during addition to file.  Logic module address.   File type for OPEN/CIOSE (X'27' = ADDRTR).
	IJHCOPT  I I I I I I I I I I I I I I I I I I	21 (15)	1   2   3   4   5   6   7	Option byte.    1 = 2321 (Version 1/2 only).   1 = Prime data in core.   1 = Cylinder overflow.   1 = Cylinder index in core.   1 = Plocked records.   1 = Verify.   1 = IOAREAS just used, 0 = IOAREA2 just used.   1 = 2 I/O areas present.   DTF file name.
 	IJHCPDDV     	29 (1D)		Prime data device type indicator.  X'C0' = 2311  X'01' = 2314/2319  X'C2' = 2321  X'04' = 3330.
&Filename.C               	IJHCSTBY   	30 (1E)	0   1   2   3   4   5	Status byte.     1 = Uncorrectable DASD error (except WLR).   1 = WLR error.   1 = EOF (sequential).   1 = No record found.   1 = Illegal ID specified.   1 = Duplicate record sensed.   1 = Cverflow area full.   1 = Record retrieved from overflow area.   1 = Record retrieved from c
 	IJHCHNDV       	31 (1F)	       	Highest level index device type.   X'00' = 2311   X'01' = 2314/2319   X'02' = 2321   X'C4' = 3330.

Figure 29. DTFIS ADDRTR table, part 1 (1 of 4).

DFT Assembler Label	Module   DSECT Label	Bytes	  Bits	Function	
	IJHCPNT	32 (20) 33-35 (21-23) 36-37	<b>+</b>               	Relative position of the DSKXTN (lcgical   unit, cell number) table (in words). This   value is the length of the DTF table divided   by 4.   First prime data record in cylinder (HHR).   Last prime data track in cylinder (HH).	
		(24-25) 38 (26)		High record number on master index/cylinder   index track (R).	
! ! !	IJHCPDH	39 (27) 40	     	  High record number on prime data track (R).      High record number on overflow track (R).	
 	IJHCSTH	(28) 41	  -	  High record number on shared track (R).	
! 	   IJHCTIH   	(29) 42 (2A)	   	  High record number on track index (TI)	
	IJHCRTR	43 (2B)	)   0   1   2   3   4     5	3	Retrieval byte.
;		44-50	0   7 	1 = PUI macro issued.  Prime data lower limit (MBBCCHH).	
<b>!</b> !	   IJHCCIS 	(2C-32) 51-57 (33-39)	   	  Cylinder index lower limit (MBBCCHH).	
<u> </u>	IJHCMIS	58-64 (3A-40)	İ	Master index lower limit (MBBCCHH).	
	IJHCILN       	65 (41)	   0   1   2   3   4-5   6	Index level number, WAITF, and track hold   indicators.   1 = From WAITF routine   1 = Seek check from WAITF.   1 = Index track held.   1 = Data track held.   1 = Data track held.   Not used.   0 = Cylinder index;   1 = Master Index.   Not used.	
&Filename.H 	IJHCCLPA 	66-73 (42-49)	 	Last prime data record address (MEECCHHR).   	
1   	IJHCRESZ 	74-75 (4A-4B)	!   	Logical record length (RECSIZE).	
 	   IJHCKYSZ 	76-77 (4C-4D)	 	  Key length (KEYLEN). 	
 	   IJHCBLSZ 	   78-79   (4E-4F)		Block size (logical record length times   number of records).	
   	   IJHCRL10 	   80-81   (50-51)	   	Overflow record length (logical record   length +10).	

Figure 29. DTFIS ADDRTR table, part 1 (2 of 4).

DFT Assembler Label	Module  DSECT Label	     Bytes	Bits	Function
	IJHCBFAC	82-83 (52-53) 84-85 (54-55)		Elccking factor (number of logical records   in block (NRECDS)).   Index entry length (key length +10).
 	IJHCABCD 	86-87 (56-57) 88-89 (58-59)		Prime data record length (key length plus
   	   IJHCCMAX     	90-91   (5A-5B)   92-93   (5C-5D)		  Prime data record format length (key length    + block size + 8).
         	IJHCKYLC         	   94-95   (5E-5F)   96-97   (60-61)   98-99   (62-63)	   	
 	   IJHCATB2 	  100-101  (64-65)	 	  Displacement of Part 2 of the DTFIS table    from start of Part 1.
 	   IJHCATE3 	  102-103  (66-67)	     	Displacement of Part 3 of the DTFIS table    frcm start cf Part 1.
  &Filename.S   	   IJHCSADR 	  104-113  (68-71)		  Seek/search address area.   
  &Filename.W 	IJHCEKCT	114-123 (72-7B)	i   	Random/sequential retrieval work area.   
  &Filename.P   	   IJHACPRC 	124-127  (7C-7F)		Prime data record count.
	IJHACSTI   	128 (80) 	0-4 5 6	Status indicators.     Not used.     1 = File closed.     1 = Last prime data track full.     1 = Block complete.
	   IJHACLTA 	  129-133  (81-85)		  Last track index normal entry address    (CCHHR).
! 	   IJHACLCA 	  134-138  (86-8A)	 	  Last cylinder index entry address (CCHHR).   
   	   IJHACLMA 	  139-143  (8B-8F)	 	  Last master index entry address (CCHHR).   
]   	   IJHACLCA 	  144-151  (90-97)	1     	  Last independent overflow record address    (MBBCCHHR).
  &Filename.I 	   IJHACOTC 	  152-153   <b>(</b> 98-99)	   	  Number of independent overflow tracks.     
  &Filename.A   	   IJHACOFC   	  154-155  (9A-9B) 	     	Number of full cylinder overflow areas.   

Figure 29. DTFIS ADDRTR table, part 1 (3 of 4).

DFT Assembler Label	Module  DSECT Label	   Bytes	  Bits	Function
&Filename.C	IJHACORC	156-157  (9C-9D)	   	Overflow record count.
   	IJHACOLL	  158-164  (9E-A4)	     	Independent cverflow area lower limit (MBBCCHH).
 	IJHACOUP	  165-171  (A5-AB)	   	Independent overflow area upper limit
		  172-175  (AC-AF)  176-179  (B0-B3)    180-183  (B4-B7)    184-187  (B8-BB)  188-191		A(&Filename.D) - Address of work area for   cylinder overflow control record (CCCR).   A(&Filename.D+8) - Address of work area for   the current track index normal entry count   field.   A(&Filename.D+16) - Address of work area for   current track index overflow entry count   field.   A(&Filename.D+24) - Address of work area for   current prime data record count field.   A(&Filename.D+32) - Address of work area for   A(&Filename.D+32) - Address of work area for
		(BC-BF)  192-195  (C0-C3)		current overflow record count field.  A(&Filename.C+40) - Address of work area for track index normal entry data field.
   	IJHADLNK	  196-199  (C4-C7)	   	A(&Filename.D+50) - Address of work area for current overflow record sequence-link field.
	   IJH <b>A</b> ARAD 	  200-203  (C8-CB)	    -	A(&ICAREAL) - Address of IOAREAL, the I/C   area used for adding records to a file.
   	IJHACUSE 	   204-207   (CC-CF) 	 	A(&WORKL) - Address of WCRKI, work area  containing user data records to be added to    the file.
 	IJHADKEY 	  208-211  (D0-D3)  212-215  (D4-D7)		A(&Filename.K) - Address of the ADD key area    A(&IOAREAL+8) - Address of key position in  IOAREAL.
<u> </u>	   ijhakln8 	216-219  (D8-DB)	     	A(&ICAREAL+8+&KEYLEN) - Address of data   position in IOAREAL.

Figure 29. DTFIS ADDRTR table, part 1 (4 of 4).

DTF Assembly   Label	Module  DSECT Label	Bytes	Bits	Function
&Filename.2	IJHCASAD	0-3 (0-3) 4 (4) 5-7 (5-7)	1-5   6	A(&Filename.S+3) - Address of the    seek/search address area+3.     1 = Seek check indicated.
 	   IJHSIOAR	8-11 (8-B)		Address of IOAREAS, I/O area used for   sequential retrieval.
 	   IJHCRARA 	12-15 (C-F)	   	  Address of ICAREAR, I/C area used for random   retrieval or address of IOAREA2 (if    specified) for sequential retrieval.
 	IJHCRKEY	16-19 (10-13)	 	Address of KEYARG, field containing      user-supplied key used for random READ/WRITE   operations and sequential retrieval    initiated by key.
	   IJHCRWCR   	20-23 (14-17)	{   	Address of WORKR, work area used for random    retrieval.
	IJHSDB1	24-27 (18-1B)	   	  Current sequential I/C area address.   
f   	IJHSLIOR 	28-31 (1C-1F)		1. L IOREG,*-4 - Load I/O register for     sequential or    2. 4-byte NO-OP instruction for random.
	IJHSLMIT         	32 (20) 33 (21) 34 (22) 35-39 (23-27)		X'00' = No Verify; X'40' = Verify.  X'00' = Elocked; X'08' = Unblocked.  R = First prime data record on shared track.   Limits for sequential (CCHHR).
	IJHSINIT       	40-41 (28-29) 42 (2A) 43-47 (2E-2F)	i	H'C' = Blocked records.  H'2' = Overflow record.  H'E' = Unblocked records.  X'C7' = 2311, 2314, or 2319; X'09' = 2321;    X'FF' = 333C.  Initial values for sequential.
  &Filename.H 	IJHSCADR	48-55 (30-37)	   	  Current sequential DASD address (MEECCHHR).   
[ [	   IJHSCOVF 	56-63 (38-3F)	! ! !	  Current cverflow DASD address (MBBCCHHR).   
! ! !	IJHSRCNT	64-65 (40-41)		  Sequential record count.
 	   IJHSTICU 	66-67 (42-43)	   	  Current track index entry for sequential    (HR).

Figure 29. DTFIS ADDRTR table, part 2 (1 of 2).

DTF Assembly	Module		r - <del></del> 7			
	DSECT Label	Bytes	Bits	Function		
&Filename.T		68-69 (44-45)		Number of records tagged for deletion.		
	IJHRREGS	70-71 (46-47)		LR &IOREG,0 for random (or 2-byte NO-OP for sequential).		
&Filename.G	IJHRIDSV	72-79 (48-4F)		DASD address save area for random retrieval (MEECCHHR).		
	IJHRADSV	80-83 (50-53)		Record pointer within I/O area for write (for random retrieval).		
&Filename.R	IJHROVCN	84-87 (54-57)		Non first overflow record count.		
The following specified. By				the cylinder index in core cpticn is ed.		
	IJHCORST	92-95 (5C-5F) 96-97 (60-61) 98-105 (62-69) 106-110 (6A-6E)		A(&INDAREA) - Starting address of main storage area specified for cylinder index. AL2(&INDSIZE) - Number of bytes in main storage available for cylinder index. Next cylinder index entry to be read (MEPCCHHR).  Last cylinder index entry (CCHHR).		
	IJHCORBT	111 (6F)	0 1 2 3	Core index byte.  1 = First time through B-transient,  \$\$EINDEX.  1 = End of cylinder index reached.  1 = Index skip option specified.  1 = Suppress index in-core option and read cylinder index.  Not used.		
	IJHCORKY	112-115 (70-73)	 	Pointer to key (stored by mcdule).		
specified. The lindex in core	The following information is generated if the prime data in core add function is specified. This information is aligned on a double word boundary. If both cylinder index in core and prime data in core add functions are specified, the following information is found in bytes 120-135 (78-87).					
	IJHPSIZE	112-113 (70-71)		Size of IOAREAL.		
	   IJHPMAX 	114-115 (72-73)	   	Maximum number of prime data records in main storage.		
 	   IJHPDSP1 	116-119 (74-77)	   	  Address of write CCW's. 		
!   	   IJHPDSP2 	  120-123   (78-7 <sub>B</sub> )	    -	  Address of read CCW's. 		
	   IJHPSW 	124   (7C)	•	  Switch byte.  1 = FOF.		
   	 	  125-127   (7D-7F)	1-7   	Not used.  For boundary alignment. 		

Figure 29. DTFIS ADDRTR table, part 2 (2 of 2).

IDUE Accombly	r	T	Ţ	<b>T</b>
DTF Assembly     Label 	Module  DSECT Label 	Bytes	  Bits 	Function
&Filename.B     	   IJHCCCW 	0-7 (0-7) 8-63 (8-3F)		X'C7', &Filename.S+1, X'40', 6 - Lcng seek  CCW with command chaining.  Channel program build area. See Figures  56-91 for a description of the channel  program builder.
  -  &Filename.D    -	     IJHACOCR 	64-127   (40-7F)  128-135  (80-87)		Channel program build area for add function   only.   Cylinder overflow control record (CCCR).
	IJHACTNA 	136-143 (88-8F)		Current track index normal entry ccunt field.
: [ 	   IJHACTCA 	  144-151  (90-97)		Current track index overflow entry count
! ! !	   IJHACRID 	  152-159  (98-9F)		  Current prime data record count field.
	   IJHACFID 	  160-167  (A0-A7)	   	  Current cverflow record count field. 
 	   IJHACTIN 	  168-177  (A8-B1)	 	  Track index normal entry data field. 
! !	   IJHACLNK 	  178-187  (B2-BB)	 	  Current cverflow record sequence-link field.  
 	   IJHACTIA 	  188-197  (BC-C5)	   	  Current track index overflcw entry data  field.
 	   IJHAGATE   	  198  (C6)  199-201  (C7-C9)		X'01' - Add to FOF.  X'02' - Add to independent overflow area.  Overflow control bytes (CCH).
!   	 	  202-203  (CA-CB)  204-211  (CC-D3)	       	
] 	   IJHAICOM   	  212-217  (D4-D9) 	     	CLC 0(&KEYLEN,13),0(6) - Unblocked   CLC 0(&KEYLEN,13),&KEYLOC-1(6) - Blocked   Utility CLC for key.
1   	   IJHAISKY 	  218-223  (DA-DF)	! ! !	  MVC 0(&KEYLEN,13),0(12) - Unblocked  MVC 0(&KEYLEN,13),&KEYLOC-1(12) - Elccked  Utility MVC for key.
  &Filename.E     		224-2271   (E0-E3)   232-2352   (E8-EB)		First entry in DSKXTN table (lcgical unit,  cell number).   4X'FF' - End of DSKXTN table.
  &Filename.K     	!   	  236+  (EC-end)	     	Key area for add only. Number of bytes  depends on key length, KEYLEN.

1-Each entry in the DSKXTN table is four bytes lcng. The minimum number of entries is two. There is one entry per extent.
| 2-Location of the end-of-table indicator depends on length of DSKXTN table.

Figure 29. DIFIS ADDRTR Table, Part 3

## ISMCD MACRO

The ISMOD (Indexed-Sequential Mcdule) macro instruction must be included for each logic module required to support each DTFIS macro in a particular problem program. The logic modules are described by an ISMOD header entry and a series of parameter entries. See <a href="DOS/VS Supervisor and 1/0 Macros">DOS/VS Supervisor and 1/0 Macros</a>, GC33-5373, for an explanation of the parameters.

The following imperative macros use the logic in the ISMOD:

ESETL
GET
PUT
READ
WAITF
WRITE, KEY
WRITE, NEWKEY

The logic for the other imperative macros used by ISAM (ENDFL, ESETI, SETFL and SETL) is found in various B-transient routines. Flowcharts for ISFMS are in alphabetical order by macro within function. The functions appear in the following sequence:

LOAD
ADD
RETRVE, RANDOM
RETRVE, SEQNTL
ADDRTR

This section does not discuss each of these macros separately but, instead, presents them in the context of a particular function.

REENTRANT MODULE: A reentrant module is a logic module that can be asynchronously used, or shared, by more than one file. ISMCD is made reentrant by inclusion of the RDONLY=YES parameter in the ISMCD macro instruction. The RDONLY (read-only) parameter assures, regardless of the processing requirements of any file(s) using the module, that the generated logic module is never modified in any way. This feature is implemented through the establishment of unique (one for each task using the module) save areas external to the logic module. Each save area must be 72 bytes and doubleword aligned. The save area for ISMCD contains general registers 2-14, the last overflow record address, the new overflow record address, and a work area for the channel program builder. A task must provide the address of its unique save area in register 13 before an imperative macro is issued to the file and a logic module is entered by the task.

ERROR OPTION EXTENSIONS: When ERREXT is not specified and an unrecoverable I/O error occurs, ISFMS indicates this error in Filename.C and returns to the problem program. Control is returned to ISFMS only by issuing another macro instruction.

When ERREXT is specified and an unrecoverable I/O error occurs, bit 0 cf Filename.C is set on. Also, byte 2, bit 2 cf the CCB in the DTF is set on when data transfer has not occurred. The problem program error processing routine should determine if data transfer occurred by checking the data transfer bit (byte 2, bit 2) in the DTF. Information concerning the record being read or written and the operation being performed at the time of the error can be found in the 18-byte parameter list pointed to by register 1. See Figure 30 for a description of this parameter list.

,		
EYTES	EITS	CONTENIS
0-3		DTF address.
4-7		Main stcrage address cf the record in error.
8 <b>-1</b> 5		DASE address of record in error (MEBCCHHR), where M is the extent sequence number and R is the record number. R can be inaccurate if a read error occurred during a read of the highest level index.
16		Record identification:
	0	Data.
	1	Track index.
1 	2	Cylinder index.
	3	Master index.
		Type of operation:
	4	Not used.
	5	Not used.
; [	6	Write.
ļ	7	Read.
17		Command code of failing     CCW.

Figure 30. ERREXT parameter list.

After checking for errors and taking corrective action if necessary, the problem program error processing routine can return to ISAM via the ERET macro. The ERET IGNORE or ERET SKIP macro returns to ISAM to ignore the error condition and process the record. The ERET RETRY returns to ISAM to make another attempt to read or write the record.

Note: The ERREXT coding is not designed to handle non-recoverable errors that are posted in Filenaπe.C. Examples of non-recoverable errors are No Record Found, Prime Data Area Full, Master Index Full, etc.

DOUBLE BUFFERING: Double buffering is meaningful only when creating the file or sequentially retrieving from the file. If IOAREA2=YES is specified as an ISMOD macro parameter, and the presence of two I/O areas is indicated in the DTF table, overlapping of I/O with processing is provided for the load create and sequential retrieve functions.

ISAM MACRO INSTRUCTIONS TO ICAD OR EXTEND A DASD FILE

The function of originally loading a file of presorted records onto DASD, and the function of extending the file by adding new presorted records beyond the previous high record, are the same. Both are considered a LOAD operation (specified by the DTFIS entry IOROUT), and they both use the same macro instructions in the problem program. However, the type field in the DLBL card must specify ISC for load creation and ISE for load extension.

The areas of the volumes used for the file are specified by job control EXTENT cards. The areas are: the prime data area where the data records are written, a cylinder index area where the user wants ISAM to build the cylinder index, and a master index area if a master index is to be built (specified by the DTFIS entry MSTIND).

During the load operation, ISAM builds the track, cylinder, and master (if specified) indexes.

Three different macro instructions are always required in the problem program to load original or extension records into the logical file on DASD.

The SETFL (set file load mode) macro instruction causes ISAM to set up the file so that the load or extension function can be performed. When loading a file, SETFL

preformats the last track of each track index; but when extending the file, SETFL preformats only the last track of the last track index plus each new track index for the extension of the file. This allows prime data on a shared track to be referenced even though no track index entries exist on the shared track.

This macro must be issued whenever the file is to be loaded or extended.

When a WRITE macro instruction with the parameter NEWKEY is issued in the problem program between a SETFI instruction and an ENDFL instruction (the third macro required for loading), it causes ISAM to load a record onto DASD.

Before issuing the WRITE instruction, the problem program must store the key and data portions of the record in a work area (specified by DTFIS WORKL). The ISAM routines construct the I/C area by moving the data record to the data area, moving the key to the key area, and building the count field. When the I/O area has been filled, ISAM transfers the records to DASD storage and then constructs the count field for the next record. The WAITF macro should not be used when loading or extending an ISAM file.

Before records are transferred, ISAM performs both a sequence check (to ensure that the records are in order by key) and a duplicate-record check.

After each WRITE is issued, ISAM makes the ID of that record or block available to the problem program. The ID is located in an 8-byte field labeled Filename.H.

As records are loaded on DASD, ISAM writes track index entries each time a track is filled, writes a cylinder index entry each time a cylinder is filled, and writes a master index entry (if DTFIS MSTIND is specified) each time a track of the cylinder index is filled.

The ENDFL macro performs an operation (similar to a CLOSE) for the file that has been lcaded. It writes the last block of data records, if necessary, and then writes an end-cf-file record after the last data record. It writes any irdex entries that are needed. It also writes inactive track index entries for the unused portion of the prime data extent for the 2311 device types. For 2314/2319/3330/2321 device types, only the remaining portion of the last cylinder containing prime data records has inactive track index entries.

When extending or adding to a file, the user is responsible for checking byte 16,

bit 7 of the DTF to determine whether the correct blocksize has been specified.

ISAM MACRO INSTRUCTIONS FOR ADDING RECORDS TO A FILE

After a file has been organized on DASD, new records can be added to the file. Each record is inserted in the proper place sequentially by key. This function is provided by specifying ADD or ADDRTR in the DTFIS entry IOROUT.

The file can contain either blocked or unblocked records, as specified by the DTFIS entry RECFORM. When the file contains blocked records, the user must provide ISAM with the location of the key field that is provided through the DTFIS entry KEYLOC. The records to be inserted are written one record at a time. The records must contain a key field in the same location as the records already in the file. Whenever the addition of records is to follow sequential retrieval (ADDRTR), the macro instruction ESETI must be issued before a record is added.

Two macro instructions, WRITE NEWKEY and WAITF are used in the problem program for adding records to a file.

Before the WRITE macro is issued for unblocked records, the program must store the record (key and data) to be added into a work area specified in the DTFIS entry WORKL. For blocked records, the program must store only the data (the key is assumed to be a part of the data). Before any records are transferred, ISFMS checks for duplicate record keys. If no duplication is found, ISAM inserts the record in the file.

To insert a record into a file, ISAM performs an index search at the highest level index. This search determines if the record to be inserted can be placed within the file, or if it is higher than the last record on the file.

If the record can be inserted within the file, searching of the master index (if available), the cylinder index, and the track index determines the appropriate location to insert the record.

For an entry to an unblccked file, an equal/high search is performed in the prime data area of the track. When a record on the track is found that is equal to or higher than the record to be inserted, the record is read from the track and placed in main storage (in the I/O area). The two records are compared to see if a duplicate

record is found. If a duplicate record is found, that information is posted to the user in the DTF table at Filename.C. If no duplicate is found, the appropriate record (in the user's work area) is written directly on the track. The record (just displaced from the track) in the I/O area is moved by ISAM to the user's work area. The next record on the track is read into the I/O area.

Then, the record in the work area is written on the track. Succeeding records are shifted until the last record on the track is set up as an overflow record. If the ADD I/C area (ICAREAI) is increased to permit the reading or writing of more than one record on DASD at a time, an equal/high search is performed in the prime data area of the track. When a record on the track is found that is equal to or higher than the record to be inserted, as many records as can fit into the I/O area specified in the DTFIS operand IOAREAL are read from the track and placed in main storage (in the I/O area).

The record to be added is compared to existing records in the I/C area. If a duplicate key is found, the condition is posted to the user in the DTF table Filename.C. If no dup icate is found, the records are shifted in main storage, leaving the record with the highest key remaining in the user's work area. The other records are rewritten directly onto the track. Any remaining record(s) on the track are then read into the I/C area. The process continues until the last record on the track is set up as an overflow record.

This last record is then written into the appropriate overflow area, and the appropriate track index entries are updated. This is the cylinder overflow area, if CYLOFL has been specified for this file and the area has not been filled.

If the cylinder overflow area is filled, or if only an independent cverflow area has been specified by a job control EXTENT card, the end record is transferred to the independent overflow area. If an independent overflow area has not been specified (or is filled) and the cylinder overflow area is filled, there is no room available to store the overflow record. ISAM posts this condition in the DTF table at Filename.C.

In all cases, before any records are written, ISAM determines if  $rcc\pi$  is available.

For an entry to a blocked file, the work area, WORKL, is required in the DTFIS entries. Each record to be added must contain a key field in the same location as

the records already in the file. The high-order position of this key field, relative to the leftmost position of the logical record, must be specified to ISAM by the user. The DTFIS entry KEYLCC is used for this specification.

When the WRITE macro is issued in the problem program, ISAM first locates the correct track by referring to the necessary master (if available), cylinder, and track indexes. Then, a search on the key areas of the DASD records on the track is made to locate the desired block of records. The block of records (or as many as will fit into the I/C area if IOAREAL has been increased for reading and writing more than one record on DASD at a time) is read into the I/O area. ISAM then examines the key field within each logical record to find the exact position in which to insert the new record and to check for duplication of records. If duplication of keys exists, the condition is posted in Filename.C. the key of the record to be inserted (contained in the work area WORKL) is low, it is exchanged with the record presently in the block.

This procedure continues with each succeeding record in the block until the last record is moved into the work area. ISFMs then updates the key area of the DASD record to reflect the highest key in the If the IOAREAL has been increased, succeeding blocks in the I/O area are also updated. The block (or blocks) is then written back onto DASD. The remaining blocks on the track are similarly processed until the last logical record on the track is moved into the work area. This record is then set up as an overflow record with the proper sequence-link field and moved to the overflow area. The indexes are updated and ISAM returns to the problem program for the next record to be added. If the overflow area is filled, the information is posted in Filename.C.

If the proper track for a record is an overflow track (determined by the track index), ISAM searches the overflow chain and checks for duplication. If no duplication is found, ISAM writes the record, preceded by a sequence-link field in the data area of the DASD record, and adjusts the appropriate linkages to maintain sequential order by key. ISAM writes the new record in either the cylinder overflow area or the independent overflow area. If these areas are filled, the user is notified by a bit in Filename.C.

If the new record is higher than all records presently in the file (end-of-file), ISAM checks to determine if the last track containing data records is

filled. If it is not, the new record is added, replacing the end-cf-file record. The end-cf-file record is written in the next record location on the track, or on the next available prime data track. Another track must be available within the file limits. If the end-of-file record is the first record on any track, the new record is written in the appropriate overflow area. After each new record is inserted in its proper location, ISAM adjusts all indexes that are affected by the addition.

The WAITF macro instruction is issued to ensure that the transfer of a record has been completed.

This instruction must be issued before the problem program attempts to process an input record or build another output record for the file concerned. The program does not regain control until the previous transfer of data is complete.

ISAM MACRO INSTRUCTIONS FOR RANDOM RETRIEVAL

When a file has been organized by ISAM, records can be retrieved in random order for processing and/or updating. Retrieval must be specified in the DTFIS entry ICROUT (ICRCUT=RETRVE or IOROUT=ADDRTR). Random processing must be specified in the DTFIS entry TYPEFLE=RANDOM.

Because random reference to the file is by record key, the problem program must supply the key of the desired record to ISAM. To do this, the key must be stored in the main storage key field specified by the DTFIS entry KEYARG. The specified key designates both the record to be retrieved and the record to be written back into the file in an updating operation. Records added to the file between the READ and the WRITE macro for a particular record to be updated can result in a lost record and a duplicate key.

Three macro instructions (READ-KEY, WRITE-KEY and WAITF) are available for use in the problem program for retrieving and updating records randomly.

The READ KEY instruction used in conjunction with WAITF macro instruction causes ISAM to retrieve the specified record from the file.

To locate the record, ISAM searches the indexes to determine the track on which the record is stored, and then searches the track for the specific record. When the record is found, ISAM transfers it to the

I/O area specified by the DTFIS entry ICAREAR. The ISAM routines also move the record to the specified work area if the DTFIS entry WORKR is included in the file definition.

When records are blocked, ISAM transfers the block that contains the specified record to the I/O area. It makes the individual record available for processing either in the I/O area or the work area (if specified). For processing in the I/O area, ISAM supplies the address of the record in the register specified by DTFIS ICREG. The ID of the record can be referenced by using Filename.G.

The WRITE instruction with the parameter KEY is used in conjunction with the WAITF macro instruction for random updating. It causes ISAM to transfer the specified record from main storage to DASD storage.

ISAM rewrites the record retrieved by the previous read instruction for the same file. The record is updated from the work area, if one is specified; otherwise, from the I/O area. The key need not be specified again ahead of the WRITE instruction.

The WAITF macro instruction is issued to ensure that the transfer of a record has been completed. This instruction must be issued before the problem program attempts to process an input record or build another output record for the file concerned. The program does not regain control until the previous transfer of data is complete.

The WAITF instruction posts any exceptional information in the DTFIS table at Filename.C.

ISAM MACRO INSTRUCTIONS FOR SEQUENTIAL RETRIEVAL

When a file has been organized by ISAM, records can be retrieved in sequential order by key for processing and/cr updating. The DTFIS entry ICRCUT=RETRVE must be specified. Sequential processing must be specified in the DTFIS entry TYPEFLE=SEONIL.

Although records are retrieved in order by key, sequential retrieval can start at a record in the file identified either by key or by the ID (identifier in the count field) of a record in the prime data area. Cr, sequential retrieval can start at the beginning of the logical file. The user specifies, in SETL, the type of reference he will use in the problem program. Whenever the starting reference is by key and the file contains blocked records (RECFORM=FIXBLK), the user must also provide ISAM with the position of the key field within the records. This is specified in the DTFIS entry KEYLOC. To search for a record, ISAM first locates the correct block by the key in the key area of the DASD record. (The key area contains the key of the highest record in the block.) Then, ISAM examines the key field within each record in the block to find the specified record.

Four macro instructions (SETL, GET, PUT and ESETL) are available for use in the problem program for retrieving and updating records sequentially.

The SETI (set limits) macro instruction initiates the mode for sequential retrieval and initializes the ISAM routines to begin retrieval at the specified starting address. It requires two parameters. The first operand (Filename) specifies the name of the file (specified in the DTFIS header entry) from which records are to retrieved.

The second operand specifies where processing is to begin. If the user is processing by the record ID, the operand Idname or (r) specifies the symbolic name of the main-storage field in which the user supplies the starting (or lowest) reference for ISAM use. The symbolic field contains information as shown in Figure 31. If processing is to begin with a key supplied by the user, the second operand is KEY. The key is to be supplied by the user in the field specified by the DTFIS entry KEYARG. If the specified key is not present in the file, an indication will be given at Filename.C.

The second operand BCF specifies that retrieval is to start at the beginning of the logical file.

Selected groups of records within a file containing identical characters cr data in the first locations of each key can be processed by specifying GKEY in the second cperand. The GKEY specification allows processing to begin at the first record (cr key) within the desired group. The user must supply a key that will identify the significant (high order) bytes of the required group of keys. The remainder (or insignificant bytes) of the key must be padded with blanks, binary zeros, or bytes lower in collating sequence than any of the insignificant bytes in the first key of the group to be processed. The problem program must determine when the generic group is completed. Otherwise, ISAM continues through the remainder of the group.

Byte	Identifier	Contents	Information
0	М	2-245	Extent sequence number of the volume in which the starting record is located.
1-2	B <b>,</b> B	0,0 (for disk)   0, 0-9 (for   data cell)	Always zero for disk. Cell number for data cell.
3-4	с,с	0,1-199 (fcr 2311/2314/2319) 0-403 (fcr 3330) 0-19,0-9 (for 2321)	Cylinder number for disk. Sub-cell and strip for data cell.  Note: The last four strips on each cell are reserved for alternate tracks.
5- 6       	н,н	0,0-9 (for 2311) 0,0-19 (for 2314/2319) 0,0-18 (for 3330) 0-4,0-19 (for 2321)	Head position for 2311, 2314, 2319, and 3330   disks. Cylinder and head for 2321 data cell.
7	R	1-254	keccrd location.

Figure 31. Pointer to first record to be processed by sequential retrieval.

This method also allows starting at a key equal-to or greater-than the one specified in the DTFIS entry KEYARG without getting an error indication in Filename.C.

The GET macro instruction causes ISAM to retrieve the next record in sequence from the file. It can be written in either of two forms, depending on where the record is to be processed.

The first form is used if records are to be processed in the I/O area (specified by DTFIS IOAREAS). It requires only one parameter, which is the name of the file from which the record is to be retrieved. ISFMS transfers the record from this file to the I/O area, and the record is available for the execution of the next instruction in the problem program. The key is located at the beginning of IOAREAS and the register (IOREG) points to the data. If blocked records are specified, ISAM makes each record available by supplying its address in the register specified by the DTFIS entry ICREG. The key is contained in the record.

The second form of the GFT instruction is used if records are to be processed in a work area (DTFIS specifies WORKS). It requires two parameters both of which can be specified as symbols or in register notation. The first is the name of the file, and the second is the name of the

work area. When register notation is used, workname should not be preloaded into register 1. The record is available for the execution of the next program instruction.

If blocked records are specified in the file definition, each GET that transfers a block of records to main storage will, if necessary, also write the preceding block back into the file in its previous block location. GET writes the preceding block if a PUT instruction has been issued for at least one of the records in the block. nc PUT instructions have been issued, updating is not required for this block, and GET does not cause the block to be rewritten. Whenever an unblocked record is retrieved from the prime data area, ISAM supplies the ID of that record in the field addressed by Filename.H. If blocked records are specified, ISAM supplies the ID of the block. The PUT macro instruction is used for sequential updating of a file, and causes ISAM to transfer records to the file in sequential order. PUT returns a record that was obtained by a GET. It can be written in either of two forms, depending on where records are processed.

The first form is used if records are processed in the I/O area (specified by DTFIS IOAREAS). It requires only the name of the file to which the records are to be transferred.

The second form of the PUT instruction is used if records are processed in a work area. It requires two parameters, both of which can be specified either as a symbol or in register notation. The first is the name of the file, and the second is the name of the work area. When register notation is used, workname should not be loaded into register 1. The work area name may be the same as that specified in the preceding GET for this file, but this is not required. ISAM moves the record from the work area specified in the PUT instruction to the I/O area specified for the file in the DTFIS entry IOAREAS.

When unblccked records are specified, each PUT writes a record back onto the file in the same location from which it was retrieved by the preceding GET for this file. Thus, each PUT updates the last record that was retrieved from the file. If some records do not require updating, a series of GET instructions can be issued without intervening PUT instructions. Therefore, it is not necessary to rewrite unchanged records.

When blocked records are specified, PUT instructions do not transfer records to the file. Instead, each PUT indicates that the block is to be written after all the records in the block have been processed. When processing for the block is complete and a GET is issued to read the next block into main storage, that GET also writes the completed block back into the file in its previous location. If a PUT instruction is not issued for any record in the block, GET does not write the completed block. The ESETL macro instruction writes the last block processed, if necessary, before the end-of-file. The ESETL (end set limit) macro instruction ends the sequential mode initiated by the SETL macro. If blocked records are specified, ESETI writes the last block back if a PUT was issued.

Note: If ADDRTR and/or RANSEQ are specified in the same DTF, ESETL should be issued before issuing a READ or WRITE. Another SETL can be issued to restart sequential retrieval.

## <u>ISAM LOAD: ENDFL Macro, Phase 1 - \$\$BENDFL Charts DA-DE</u>

Objective: To validate IOAREAL address limits and DTFIS table limits. To reset error indicators in DTF table. To pad key field and write partially filled block if present. To write EOF record. To write track index (TI) entries, cylinder index (CI) entry, and master index (MI) entry if needed. To compute number of bytes used in

the highest level index used. To write track index inactive entries if needed.

Entry: From the ENDFI macro expansion.

Exits: To the second phase of the ENDFL
macro, \$\$BENDFF.

Method: This phase first validates the address limits of IOARFAL and the DTFIS table via an SVC 26. It then resets error indicators in the DTF table for prime data area full, duplicate record, and sequence error. It checks for a partially filled blocked record. If one is present, it pads the key field with all 1's and writes the partially filled block.

A series of tests is made to determine the location of the last prime data record written. If the record was not the last record on the track, the last track full indicator is set off. If the record was the last record on the track, the address in the ID field of IOAREAL is modified. If space is available in the prime data area, the EOF record is written. If enough space is not available for the ECF record, this condition is posted at Filename.C in the DTF table.

A test is made to determine if the last prime data track was full. If not, this routine writes the track index normal entry and the track index overflow entry. It also writes the cylinder index normal entry, and (if the master index is being used) the master index normal entry. If the last prime data track was full, and the last track index record number was not 0, this routine writes a cylinder index normal entry and a master index normal entry, if the master index is being used.

If the last track index record number was zero, but the track was not 0, a cylinder index normal entry is written. If the last track index track was 0, and the cylinder index record is not the last record on the track, a master index normal entry is written (if the master index is being used). Otherwise, pointers are set to the lower limit of the highest index level being used.

The routine then computes the total number of bytes used in the normal entries of the highest level index being used.

When the total number of bytes in the highest level index has been determined, this phase formats the track index inactive entries and then tests to determine if there are more track index records on the cylinder. If so, track index inactive entries are written until there are no more records on the cylinder. A test is made for the device type. If the device is a

2311, this routine continues to write track index inactive entries until the end of the prime data extent is reached, keeping a count of the number of cylinders containing track index inactive entries. If the device is a 2321, 2314/2319, or 3330, this phase does not format any more track index inactive entries.

When there are no more track index inactive entries to be written, the address of the DTF is saved for the next phase, and this phase exits to phase \$\$BENDFF.

ISAM\_LCAD: ENDFL Macro, Phase 2 - \$\$BENDFF Charts DC-DD

Objective: To write cylinder and master index inactive entries for any unused cylinders. To write cylinder index and master index dummy end entries. To write cylinder index and master index dummy chained entries.

Entry: From the first phase of the ENDFL macro, \$\$BENDFL.

Exits: To the problem program via an SVC 11.

Method: This transient routine first formats the cylinder index inactive entry. Using the count of the number of cylinders containing track index inactive entries, as determined by \$\$BENDFL, this routine writes cylinder index inactive entries for the unused cylinders. While the inactive entries are being written, a count is kept of the number of tracks containing cylinder index inactive entries. After the last cylinder index inactive entry is written, a cylinder index dummy end entry is written.

The routine then tests to determine if the master index is being used. If it is, master index inactive entries are written using the count of cylinder index tracks containing inactive entries. Cne master index inactive entry is written for each track of cylinder index inactive entries. After the last master index inactive entry is written, this routine writes a master index dummy end entry.

At the end of each master index or cylinder index cylinder, there is a master index or cylinder index dummy chained entry that points to the next master index or cylinder index cylinder. In other words, the last record on the last track of a cylinder of cylinder index records points to record 0 on track 0 of the following cylinder if it is also in the cylinder index extent. After the dummy end entries have been written, this routine writes

dummy chained entries, if any, for the cylinder index, and for the master index, if it is being used.

When the dummy chained entries have been written, this phase exits to the problem program via an SVC 11.

<u> ISAM LOAD: SETFL Macro, Phase 1 - \$\$BSETFL</u> Charts DE-DF

Chjective: To validate DTFIS table limits and IOAREAL limits. To test for prime data cn data cell and disk devices. To determine the number of cylinders in the prime data extent, the maximum number of cylinder index entries in the cylinder index extent, and to check if the cylinder index extent is too small. To check if the master index extent (if present) is too small. To build the basic CCW string for use by the LOAD module and ENDFL transients. To move last prime data, track index, cylinder index, and master index record addresses to the DTF table.

Entry: From the SETFL macro expansion.

 $\underline{\mathtt{Exits}}$ : To the \$\$BSETFF for normal exit.  $\underline{\mathtt{Tc}}$  problem program (via SVC 11) if cylinder index or master index extents are too

Method: This B-transient first validates the address limits of ICAREAL and the DTFIS takle. It then tests whether the rrimedata is on a data cell or disk device, and moves the address limits to the prime data control field of the DTF table.

This phase then calculates the number of prime data extents minus one. With this information, the total number of strips is calculated (if the device is a 2321). Four strips per extent are then decreased from the total number to allow for alternate tracks. The total number of cylinders minus cne is then calculated.

This phase next calculates the number of active records in the cylinder index, and compares this number with the total number of prime data cylinders minus 1. If the number of cylinders is greater than or equal to the number of cylinder index records, the cylinder index extent is too small and flags are set to indicate this condition.

A test is then made to determine whether the master index is being used. If it is, the total number of cylinder index records referenced by the master index is used to determine the number of cylinder index cylinders referenced by the master index.

One dummy record per cylinder index cylinder is subtracted from the total number of cylinder index records. The result of this subtraction (number of cylinder index records referenced by the master index) is compared to the total number of prime data cylinders minus one. If the number of prime data cylinders is greater than or equal to the number of cylinder index records, the master index is too small, and flags are set to indicate this condition. If the master index is not being used, this check is bypassed.

The phase then checks to determine if either the cylinder index or master index extent is too small. If so, this phase returns to the problem program via an SVC 11. If the extents are large enough, the record number of the track index dummy record is calculated, and the logical transient proceeds to build the CCW string shown in Figure 32.

When the CCW string has been completed, the seek/search address is set up. The lower limit address of the prime data area is moved to the seek/search address area, and a test is made to see if the file is to be extended. If so, the extension indicator in the DTF table is set on, and the address of the last prime data record is saved for \$\$BSETFF. LTIRA (last track index record address) is initialized to the address of the last track index overflow entry. A test is made to see whether the upper limit address of the prime data extent has been increased. If sc, an indicator is set for \$\$BSETFF, and the old prime data upper limit is moved to the seek/search address area. The upper limit address is initialized with the new prime data upper limit.

A test is then made to see if the last prime data track is full. If it is not

full, the last two track index entries must be rewritten during the lcad operation (since the highest key on the prime data track increases when new records are written). Therefore, the last track index record number (LTIRA) is decreased to point to the track index entries for the previous track. The last cylinder index record number (LCIRA) and the last master index record number (LMIRA) are also decreased by one. (LMIRA is only decreased if the master index is being used.) \$\$BSETFF is then fetched.

If the last prime data track is full, and it is not the end of the cylinder, the last prime data record address (IPDRA) is increased to record zero on the next track.

A test is made to see whether the last track index entry address is the last record on the track index track. If sc, the last track index entry address (LTIRA) is set to record zero on the next track. Then the last cylinder index record address (LCIRA) and the last master index record address (IMIRA) are decreased by one, and \$\$BSETFF is fetched.

If the last prime data track is full, and the cylinder is full, the last prime data entry address (LPDRA) and the last track index entry address (LTIRA) are updated to the start of the next cylinder of the prime data extent. Since the last prime data cylinder is full, the last cylinder index record address (LCIRA) is not changed. If the last cylinder index track is not full, the last master index record address (LMIRA) is decreased by one. If the last cylinder index track is full, LMIRA is not changed.

When all processing has been completed, this phase exits to phase \$\$BSETFF to initialize the CCW chain and I/C areas.

CCW Built	Function
X'07', Address of Prime Data Lower Limit, Command Chaining, 6.	Long seek.
X'31', Address of Prime Data Lower Limit, Command Chaining, 5.	Search identifier equal for the beginning of the prime data area.
X'08', Pointer to *-8, Command Chaining,	TIC to *-8.
X'1D', Address of IOAREAL, Suppress     Length Indicator, 16384. 	Write count, key and data in prime data area. If the verify option is specified, the command chaining bit is set on in the flag field.
If the verify option is specified, the above.	e following CCWs are built in addition to those
X'31', Address of Prime Data Lower Limit, Command Chaining, 5.	Search identifier equal.
X'08', Pointer to *-8, Command Chaining,	TIC to *-8.
X'1E', 0, Suppress Length Indicator   and Data Transfer, 1 	Read count, key and data to verify prime data record written. No data is transferred to main storage.

Figure 32. CCW chain built by \$\$BSETFL to write prime data records.

## ISAM LOAD: SETFL Macro, Phase 2 - \$\$BSETFF Chart DG

<u>Objective</u>: To initialize the CCW chain and I/O areas required to write the last track index track in each cylinder of the prime data area and the cylinder overflow control record (COCR), if the cylinder cverflow option has been specified in the DTFIS table.

Entry: From the first phase of the SETFL macro, \$\$BSETFL.

Exit: To the third phase of the SETFL
macro, \$\$BSETFG.

<u>Method</u>: This phase first gets the key length from the DTFIS table and stores it in the count fields associated with the write count, key and data CCW's. These CCW's are built to write up to 41 track index records with one EXCP.

A track index dummy record is built in the user's IOAREAL and the number of cylinder overflow tracks in each cylinder is calculated and saved in the CCCR data field. This phase then determines the correct Write Count, Key and Data CCW to write the track index dummy record. When the correct CCW is found, the address of IOAREAL is moved to its data address field.

A test is made to determine if the cylinder overflow option has been specified. If it has, Record Zero (R0) in the track index contains a Cylinder Cverflow Control Record (COCR). The COCR is found in the data area of R0. The COCR contains the address of the last cverflow record on the cylinder and the number of tracks remaining in the cylinder cverflow area. This phase initializes the COCR with the address of the first cylinder cverflow track and the number of cylinder overflow tracks.

It tests to determine if the track index records to be formatted are cn track 0 cf each cylinder. If so, the seek command code is changed to a NO-OP, the flag bits are set to indicate command chairing, the file protect indicator is reset and \$\$BSETFG is fetched for execution. All writing is done on track 0 with one EXCP. If the track index records are not on track C, a test for DASD file protect is made. If the DASE file protect feature is not present, flag bits of the seek CCW are set to indicate command chaining, the file protect indicator is reset and \$\$BSETFG is fetched for execution. All writing is done with one EXCP. If the file protect feature is present, the file protect indicator is left on and \$\$BSETFG is fetched. All writing is done with two EXCPs. If the cylinder cverflow option has not been

specified, the file protect indicator is reset. All writing will be done with one EXCP.

ISAM LCAD: SETFL Macro, Phase 3 - \$\$BSETFG
Chart DH

<u>Objective</u>: To format last track index track in each prime data cylinder. To write Cylinder Overflow Control Record (COCR) data if the cylinder cverflow option has been specified.

Entry: From the second phase of the SETFL
macro, \$\$BSETFF.

Exit: To problem program via an SVC 11 or to phase 4 of the SETFL macro, \$\$BSETFH, via an SVC 2.

Method: This logical transient phase first checks to determine whether the file has been extended, but the prime data area upper limit has remained the same. If so, no formatting is required, and a branch is taken to read the last prime data record. If the file is being created or being extended with increased upper limit, this phase formats the last track index track in each prime data cylinder (1 track per EXCP through use of an extended CCW chain), and writes the CCCR data for each cylinder if the cylinder overflow option is specified.

When the last track index tracks are formatted, this phase tests to determine whether the file is being extended or created. If the file is being created, the seek/search address is set up, the count field of the IOAREAL is initialized with the address, key length and data length of the last prime data record., The CCB is initialized with the address of the CCW chain.

If the file is being created, the block position address is set with the current logical record address. The logical record counter is saved in the DTF table, the CCB is initialized with the address of the CCW chain, and this phase exits to the problem program via an SVC 11.

If the file is being extended, \$\$BSETFH is fetched via an SVC 2.

<u>ISAM LOAD: SETFL Macro, Phase 4 - \$\$BSETFH Chart DJ</u>

Objective: For extension of file, to read the last prime data record so that keys may be compared by the ISMOD macro.

Entry: From the third phase of the SETFL
macro, \$\$ESETFG.

Exit: To problem via an SVC 11.

Method: For extension of file, this phase reads the last prime data record (the address was saved by the first phase, \$\$BSETFL). This provides keys for a comparison in the load operation. If the records are blocked and the last block was not filled by a previous load operation, this phase finds the padded record and sets the block position address to load the next prime data record at the location of the padded record. If the records are blocked and the last block is full, this phase reads the last cylinder index entry to chtain the highest key of the load file and sets the block position address to load the next prime data record at the location of the data in IOAREAL.

## ISAM\_LOAD: Write\_Macro, NEWKEY Charts\_DK-DM

Objective: To ensure that keys are in ascending sequence. To write prime data record in correct location. To write track index entries, cylinder index entry and master index entry, if necessary.

Entry: From the WRITE, NEWKEY macro
expansion.

Exit: To problem program via return
register 14.

Method: This routine first tests switches in the DTF table to determine if the prime data area is full or if the cylinder/master index is too small. If either condition exists, this routine exits to the problem program via linkage register 14. A test is then made to determine if IOAREA2=YES is specified as an ISMCD macro parameter cption. If IOAREA2 is specified and the presence of two I/O areas is indicated in the DTF table to allow overlapping of I/O with processing while creating the file, this routine gets the addresses of ICAREAL and IOAREA2 and determines if the ENDFL macrc was issued. If it has been issued, a wait for I/O completion and a test fcr ERREXT=YES are made. If ERREXT is specified, additional error conditions can be returned to the problem program, thus giving the user greater flexibility in attempting to continue processing.

If IOAREA2 is not specified or if the ENDFL macro was not issued, a test is made to determine if the current record is the first record in the file. If it is the first record in the file, a test for

IOAREA2=YES is made. If ICAREA2 is specified and there are two I/O areas, the traffic bit in the CCB is turned on and the ICAREA2 address constant is relocated. If IOAREA2 is not specified or if the current record is not the first record in the file, the current key is moved to the I/O area and a test is made to determine if the previous key is lower than the current key. If the previous key is not lower, a test for duplicate keys is made. If the keys are equal, a duplicate record indicator is set at Filename.C. If the current key is lower than the previous key, an out-of-sequence indicator is set at Filename.C. Control then returns to the problem program.

If the previous key is lower than the current key, the current key and data are moved to the I/O area, the prime data record count and logical record count are updated by 1 and a test is made to determine if this is the first logical record in the block. If it is the first logical record, the record number in the count field of the I/O area is updated. If IOAREA2 is specified, a test for a full block is made. If the block is not full, the logical record count and the block position address are saved and control returns to the problem program.

If the block is full, the logical record count is reset to 0 and a test for IDAREA2=YES is made. If ICAREA2 is specified, the addresses of the twc I/O areas are interchanged and saved in the DTF The I/O area data address is saved as the block position address, and a test for ERREXT=YES is made. If ERREXT is specified, the record type in the parameter list is set to indicate data. A test for IOAREA2=YES is made. If ICAREA2 is specified and there are two I/O areas present, the prime data record ID is updated again and a prime data record from the second I/O area is written. If IOAREA2 is not specified, a prime data record from IOAREAL is written.

<u>Note</u>: The preceding process works for both blocked and unblocked records.

A check is then made to determine if the data record was written on a shared track. If it was written on a shared track, and it was not the last record of a shared track, control returns to the problem program. If the record was the last on a shared track, a test for IOAREA2=YES is made. If IOAREA2 is specified, a wait for I/C completion is made. The track index normal entry is then initialized to indicate a shared track index entry.

If the record was not written on a shared track and it was not the last record

cn the prime data track, control returns to the problem program. If the end of the prime data has been reached, a test for IOAREA2=YES is made. If IOAREA2 is specified, a wait for I/C completion is made and a test for ERREXT=YES is made. If ERREXT is specified, the record type in the parameter list is set to indicate track index. The track index normal and overflow entries are written and the last track index record address is saved.

Tests are then made for end of cylinder. If the last prime data record written was not the next-to-last or last record on the cylinder, the current prime data track number is updated by 1 and control returns to the problem program. If the record was the next-tc-last record in the cylinder, a test is made for the end of the prime data extent. If the end of the extent has not teen reached, the prime data track number is increased and control returns to the problem program. If it is the end of the extent, the end-of-extent indicators in the DTF table are set and control returns to the problem program.

If the record was the last prime data record on the cylinder, a test for ERREXT=YES is made. If ERREXT is specified, the record type in the parameter list is set to indicate cylinder index. A cylinder index entry is then written and the last cylinder record address is updated. If a master index is being used, a master index entry is written if:

- The cylinder index entry is the next-tc-last track in the cylinder.
- 2. The cylinder index is the last record on the track. Before the master index entry is written, a test for ERREXT=YES is made. If ERREXT is specified, the record type in the parameter list is set to indicate master index.

Next, this routine tests for the end of the prime data volume. If end of volume has been reached, the extent sequence number is updated by 1 and the seek/search address is modified to the beginning address of the volume. If it is not the end of volume, the address in the seek/search area is updated, and the last track index record address and the address in the count field of the I/O area are modified. (This modification also occurs for end of prime data volume.) This routine then exits to the problem program via return register 14.

### ISAM ADD: WAITF Macro Charts FA-ED

<u>Cbjective</u>: To add a record to an indexed sequential file, adjusting the indexes and other records as necessary.

Entry: From the WAITF macro expansion.

<u>Exit</u>: To the problem program via linkage register 14.

Method: This routine first tests for ERREXT=YES. If ERREXT is specified, additional error conditions can be returned to the problem program, thus giving the user greater flexibility in attempting to continue processing. After waiting for the completion of the I/O operation, this routine determines the type of add function to be performed. The three types of add functions are:

- Normal add to the prime data area
- Add to the cverflow area
- EOF add

Normal Add to the Prime Data Area: If a normal add to the prime data area is required, this routine determines if the record is to be added to the last prime data track. If it is and the last prime data track is full, the overflow record address is calculated, and EXCP is issued to search and read the prime data track to determine the point of insertion and a wait for I/O completion is made. Figures 33-54 give a description of the channel program builder for the ADD function. If the addition is not on the last prime data track, the overflow record address is calculated and the prime data track is searched to determine the point of insertion for the record to be added to the file. When an equal/high key is found during the search, the count and data fields of that location are read into a save area in the DTF table and ICAREAL respectively.

A test is made to determine if the prime data in core option has been specified as an ISMOD macro parameter. If it has been specified, as many records as can fit into the I/O area specified in the DTFIS operand IOAREAL are read from the prime data track into main storage. The key of the record to be added is compared to the keys of the existing records in the I/O area. If a duplicate key is found, the condition is indicated to the user in the DTF table entry labeled Filename.C. If no duplicate key is found, the records are shifted in main storage leaving the record with the highest key remaining in the user's work

area, WORKL. The other records are rewritten directly onto the track. Any remaining records on the track are then read into the I/O area. The process continues until the last record on the track is set up as an overflow record. When the last prime data record on the track has been rewritten, the new overflow record is written in the overflow area, the track index normal and overflow entries and the COCR are written and control returns to the problem program.

If the prime data in core option has not keen specified as an ISMOD macro parameter, a test for blocked records is made. If the file is unblocked, the record previously found on the search key equal/high is reread to get the key field. If it is a ouplicate key, a switch is set on in the CTFIS table indicating a duplicate key has teen sensed, and a return to the problem program is made. If there are no duplicate keys, the user's key and data are written from the work area, WORKL, onto the DASD file. The record in the I/O area, IOAREAL, replaces the user's record in the work area. The next record on the track replaces the one in the I/O area. This process is repeated until the end of track is reached.

If the end-of-file (ECF) record is read during the process of shifting the records over one record position, this rcutine writes the last record over the ECF record, and then writes a new EOF record (Figures 35, 43, 44).

If the file contains blocked records, this routine reads the block of records (cr as many as fit in the I/C area if IOAREAL has been increased for reading and writing more than one record at a time) into IOAREAL. The key field within each logical record is analyzed to determine the correct position in which to insert the new record. If there is duplication of keys, a switch is set on in the DTFIS table and control returns to the problem program.

If the key of the record to be inserted (contained in WORKI) is low, it is exchanged with the record presently in the block. This procedure continues with each succeeding record in the block until the last record is moved into the work area. The key field of the DASD record is then updated to reflect the highest key in the block. If the size of IOAREAL has been increased, succeeding blocks in the I/O area are also updated. The block (or blccks) is then written tack onto DASD. The remaining blocks on the track are similarly processed until the last logical record on the track is moved into WORKL. This record is then set up as an overflow record with the correct sequence-link field added and written in the overflow area. The sequence-link field for the new overflow record is taken from the track index overflow entry. The indexes are updated and control returns to the problem program for the next record to be added. If the overflow area is full, this information is indicated to the user in the DTF table entry labeled Filename.C.

The track index normal entry key field is updated to the key of the new last record, the track index overflow entry data field is updated to the address of the new overflow entry (that entry has the lowest key for the overflow for that track) and the COCR is updated. These records are written on the DASD file before control returns to the problem program.

If the last block in the prime data area is padded, the last record to be shifted is included in that block. If the ECF record is read during the process of shifting the records one record position, the last record is written as a new block and a new EOF record is written before returning control to the problem program.

Add to the Cverflow Area: This routine computes the new overflow record address and reads the cverflow chain to get the address of the record with the next highest This address is stored in the sequence-link field of the new record. new overflow record is then written in either the cylinder overflow area cr independent overflow area. If these areas are full, this condition is indicated to the user in the DTFIS table entry labeled Filename.C. Each time an overflow record is added to the independent overflow area, an EOF record is written to maintain the integrity of the indexed sequential file (Figure 45). The next overflow record followed by an EOF record overlays the previous EOF record.

If the new cverflow record has the lowest key in the overflow chain, its address is used to build a new track index overflow entry. The new overflow entry is then written on the DASD file (Figure 42) and control returns to the problem program. If a cylinder overflow condition occurs, the updated COCR (cylinder cverflow control record) is written on DASD before control is returned to the problem program (Figure 39).

If the new overflow record does not have the lowest key, the sequence-link field of the record with the next lower key is updated to contain the address of the new overflow record. This overflow record is then rewritten on DASD and the COCR is

updated. Control returns to the problem program.

<u>FCF Add</u>: This routine first determines if the last prime data track is full. If the last prime data track is not full, the new record is inserted on it. If the file is blocked, the block is read and the new record is inserted.

If the file is not blocked or if it is blocked and the last block is full, a new last prime data record address is stored and the new record is written at that address. A new EOF record is then written (Figure 35).

If the last prime data track is full, the new record is inserted in the overflow area. The new overflow record address is computed and the record is written in the overflow area.

If an overflow chain is present, the next lower record in the chain is found and the address of the new record is moved to the sequence-link field of the next lower record.

If no overflow chain is present, the address of the new overflow record is moved to the track index overflow entry. The track index overflow entry is then written with the new high key. The master index (if present) and the cylinder index are updated with the new high key. A test for the cylinder index in core option is then made. If it has not been specified, control is returned to the problem program. If the cylinder index in core option has teen specified, the new key is inserted into the appropriate index in core entry before returning control to the problem program.

ISAM ACC: WRITE Macro, NEWKEY Charts EE+EF

<u>Chjective</u>: To perform the necessary initialization to add a record to a file.

Entry: From the WRITE, NEWKEY macro
expansion.

<u>Exit</u>: To the problem program via linkage register 14.

<u>Method</u>: After initializing the pointers to the three parts of the DTFIS table, this routine gets the starting address of the highest level index, builds a CCW chain to search the highest level index (Figure 33) executes the channel program and tests for ERREXT=YES. If ERREXT is specified, additional error conditions can be returned to the problem program, thus giving the

user greater flexibility in attempting to continue processing. The channel program is executed and a wait for I/O completion is made. The routine then tests the F code of the index level pointer to determine if the next search is of the cylinder or track index. The F code refers to the index level just searched. If it was the master index, the next search is on the cylinder index. See Figure 17 for a description of the F code.

If the F code indicates a dummy chained entry, the search of the master, cylinder or track index continues. If the index level pointer did not indicate a dummy chained entry, a test for an inactive or dummy end entry is made. If an inactive or dummy end entry is indicated, the EOF add indicator is set on in the DTFIS table, a CCW chain is built to read the last track index entries (Figure 46), the channel program to bypass the last of the track index entries is executed, a wait for the I/O operation to be completed is made, and control returns to the problem program. Processing continues with the record following the last key.

If an inactive or dummy end entry is not indicated, a test for the presence of a master index is made. If the master index is not present, indicating the cylinder index was just searched, a search of the track index is performed, and a return to the problem program is made.

If the master index is present, a test is made to determine if the cylinder index in core option was specified as an ISMCD macro parameter. If it was not specified, an EXCP is issued to search the cylinder index, followed by a wait for I/O completion, an EXCP to search the track index, a wait for I/O completion, and a return to the problem program. If the cylinder index in core option was specified, a search of the track index is performed, and a return to the problem program is made. When HOLD=YES is specified in the DTF, any held data tracks and index tracks are freed before control returns to the problem program.

CCW Builder  Control Code <sup>1</sup>	CCW Built	Function
7961 	X'69', &KEYARG, CC and SLI, Key Length	Search key equal or high the master/cylinder index. Key supplied by user in the DTFIS table.
   0C6B 	X'08', Pointer to *+16, CC and SLI, 5	TIC to *+16.
D17B     	X'1A', <sup>2</sup> &Filename.D+8, CC, SLI and SKIP, 5	Read home address into work area for the current track index normal entry count field in the DTFIS table.
516C	X'92', <sup>2</sup> &Filename.D+8, CC and SLI, 10	Read count (multiple track) into work area for the current track index normal entry count field in the DTFIS table.
7961     	X'69', &KEYARG, CC and SLI, Key Length	Search key equal or high the master/cylinder index. Key supplied by user in the DTFIS table.
046B	X'08', Pointer to *-16, CC and SLI, 5	TIC to *-16.
150C	X'06', <sup>2</sup> &Filename.D+40, 00, 10	Read data (next 10-byte index level pcinter) into work area for track index normal entry data field in DTFIS table.

1-2 See notes 1 and 2 in Figure 54.

Figure 33. Channel program kuilder for ADD -- CCW chain built to search master cylinder index.

CCW Builder	CCW Built	Function
8C4B	X'31', <sup>2</sup> &Filenaπe.S+3, CC, 5	Search identifier equal the track index using the pointer (CCHHR) in the common seek/search area.
066B	X'08', Pointer to *-8,CC and SLI, 5	TIC to *-8.
106C	X'06', 2&Filename.D, CC and SLI, 10	Read data (COCR record) into the cylinder cverflow control record (COCR) area.
516C   	X'92', <sup>2</sup> &Filename.D+8, CC and SLI, 10	Read count (multiple track) into work area for the current track index normal entry count field in the DTFIS table.
7941   	X'69', &KEYARG, CC, Key Length	Search key equal or high the track index. Key supplied by user in the DTFIS table.
046B 	X'08', Pointer to *-16, CC and SLI, 5	TIC to *-16.
156C       	X'06', 2&Filename.D+40, CC and SLI, 10	Read data (next 10-byte pcinter to prime data record) into work area for track index normal entry data field in DTFIS table.
526C   	X'92', <sup>2</sup> &Filenaπe.D+16, CC and SLI, 10	Read count (multiple track) into work area for current track index overflow entry count field in DTFIS table.
1D0C	X'06', <sup>2</sup> &Filename.W, 00   10	Read data (10-byte overflcw entry) into randcm/sequential retrieval work area.

<sup>1-2</sup> See notes 1 and 2 in Figure 54.

Figure 34. Channel program builder for ADD -- CCW chain built to search master cylinder index.

CCW Builder Control Code1	CCW Built	Function
8C4B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal for the last prime data record address using pointer, CCHHR, in common seek/search area in DTFIS table.
066B	   X'08', Pointer to *-8, CC   and SLI, 5	TIC to *-8.
342C 	X'10', <sup>2</sup> &Filename.D+32,   SLI, 10 	Write count, key and data of ECF record     located in current overflow record count     field in DTFIS table.

<sup>1-2</sup> See notes 1 and 2 in Figure 54.

Figure 35. Channel program builder for ADD -- CCW chain built to write new EOF record.

CCW Builder    Control Code1	CCW Built	Function
8C4B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal the prime data track using pointer, CCHHR, in common seek/search area in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
436C	X'12', 2&Filename.D+24, CC and SLI, 10	Read count field for current prime data record.
7941 	X'69', &KEYARG, CC, Key Length	Search key equal or high the prime data track. Key supplied by user in DTFIS table.
046B	X'08', Pointer to *-16, CC and SLI, 5	TIC to *-16.
1802	X'06', Address of ICAREAL+8 +KEYLEN, 00, Blcck Size	Read data (prime data block) into IOAREAL+8+Key Length.

 $<sup>^{1-2}</sup>$  See notes 1 and 2 in Figure 54.

Figure 36. Channel program builder for ADD -- CCW chain built to find prime data record.

[		<b> </b>
CCW Builder  Control Code <sup>1</sup>	CCW Built	Function
8C4B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal the track index using pointer, CCHHR, in common seek/search address area in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
B06C	X'05', <sup>2</sup> &Filename.D, CC and SLI, 10	Rewrite COCR located in cylinder overflow     control record work area in DTFIS table.
E14B	X'B1', <sup>2</sup> &Filenaπe.D+8, CC, 5	Search identifier equal (multiple track)
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
2A45     	x'0D', Address of   IOAREAL+8, CC,   Key Length + 10	
E24B	X'B1', <sup>2</sup> &Filename.D+16, CC, 5	Search identifier equal (multiple track)    for the pointer, CCHHR, in the cverflcw    entry count field.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
BDCC	X'05', <sup>2</sup> &Filename.W, CC and DC, 10	  Rewrite overflow entry lccated in    randcm/sequential retrieval work area.
824B   	x'31', <sup>2</sup> &Filename.D+16, CC, 5	  Search identifier equal for the pointer,    CCHHR, in the overflow entry count field.
066B 	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
1D3C	X'06', <sup>2</sup> &Filename.W, SLI and   SKIP, 10	Read data to verify record just written.    Information is not transferred to main    storage.

<sup>1-2</sup> See notes 1 and 2 in Figure 54.

Figure 37. Channel program builder for ADD -- CCW chain built to rewrite track index entry.

CCW Builder    Control Code <sup>1</sup>	CCW Built	Function
8C4B	X'13', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal for RO using pointer, CCHHR, in common seek/search areal in DTFIS table.
066B 	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
B06C	X'05', <sup>2</sup> &Filename.D, CC and SLI, 10	Write data (updated COCR) from the cylinder overflow control record (COCR) area in the DTFIS table.
E14B	X'B1', <sup>2</sup> &Filename.D+8, CC, 5	Search identifier equal (multiple track) the track index using the pointer, CCHHR, in the work area for the current track index normal entry count field.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
BDCC	X'05', 2&Filename.W, CC and DC, 10	Write data (track index overflow entry)   from the random/sequential retrieval wcrk   area.
814B     	x'31', 2&Filename.D+8, CC, 5	Search identifier equal the track index using the pointer, CCHHR, in the work area for the current track index normal entry count field.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
1D3C     	X'06', <sup>2</sup> &Filename.W, SII   and SKIP, 10	Read data to verify record just written.   Information is not transferred to main storage.

<sup>1-2</sup> See nctes 1 and 2 in Figure 54.

Figure 38. Channel program builder for ADD -- CCW chain built to write track index entry.

CCW Builder    Control Code1	CCW Built	Function
8C4B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal for RO using pointer, CCHHR, in common seek/search areal in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
B02C	X'05', <sup>2</sup> &Filename.D, SLI, 10	Write data (updated CCCR) from the cylinder overflow control record area in the DTFIS table.

 $<sup>^{1-2}</sup>$  See notes 1 and 2 in Figure 54.

Figure 39. Channel program builder for ADD -- CCW chain built to write COCR.

CCW Builder Control Code1	CCW Built	Function
8C4B	X'31', <sup>2</sup> &Filenaπe.S+3, CC, 5	Search identifier equal using pointer, CCHHR, in common seek/search area in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
AA07	X'0E', Address of ICAREAL+8, 00, Key Length + Record Length + 10	Read key and data of previously low overflow record into ICAREAL+8.

<sup>1-2</sup> See notes 1 and 2 in Figure 54.

Figure 40. Channel program builder for ADD -- CCW chain built to read previous overflow record.

CCW Builder  Control Code1	CCW Built	Function
8C4B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal using pointer, CCHHR, in common seek/search area in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
2A47	X'OD', Address of ICAREAL+8, CC, Key Length + Record Length + 10	Write key and data of previously low overflow record located at IOAREAL+8.
8C4B   	x'31', 2&Filename.S+3, CC, 5	Search identifier equal using pointer, CCHHR, in common seek/search area in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
AA37	X'0E', Address cf IOAREAL+8, SLI and SKIP, Key Length + Record Length + 10	Read key and data to verify record just written. Information is not transferred to main storage.

<sup>1-2</sup> See notes 1 and 2 in Figure 54.

Figure 41. Channel program builder for ADD -- CCW chain built to write previous overflow record.

CCW Builder Control Code <sup>1</sup>	CCW Built	Function
8C4B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal for last overflow record address using pointer, CCHHR, in common seek/search area in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	IIC to *-8.
37C9	X'1D', Address of ICAREAL, CC and DC, Key Length + Record Length + 18	Write count, key and data of new overflow record located at IOAREAL.
8C4B   	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal for last overflow record address using pointer, CCHHR, in common seek/search area in DTFIS table.
066В 	X'08' Pointer to *-8, CC and SLI, 5	TIC to *-8.
   C739 	X'1E', Address cf IOAREAL, SLI and SKIP, Key Length + Record Length + 18	Read count, key and data to verify record just written. Information is not transferred to main storage.

<sup>1-2</sup> See notes 1 and 2 in Figure 54.

Figure 42. Channel program builder for ADD -- CCW chain built to write new cverflow record.

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CCW Builder    Control Code <sup>1</sup>	CCW Built	Function
8C4B	x'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal for present ECF record address minus 1 using pcinter, CCHHR, in common seek/search area in DTFIS table.
   066B 	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
37C8     	X'1D', Address of ICAREAL, CC and DC, Key Length + Block Size + 8	Write count key and data of new record to be added located at ICAREAL.
8C4B   	x'31', 2&Filename.S+3, CC, 5	Search identifier equal for present ECF record address minus 1 using pcinter CCHHR, in common seek/search area in DTFIS table.
066B 	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
C738	X'1E', Address of ICAREAL,   SLI and SKIP, Key Length +   Block Size + 8	Read ccunt, key and data to verify record just written. Information is not transferred to main storage.

<sup>1-2</sup> See notes 1 and 2 in Figure 54.

Figure 43. Channel program builder for ADD -- CCW chain built to write over ECF record (blocked records).

CCW Builder    Control Code1	CCW Built	Function
8C4B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal for present EOF record address minus 1 using pcinter, CCHHR, in common seek/search area in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
37C8	X'1D', Address of ICAREAL, CC and DC, Key Length + Block Size + 8	Write count, key and data of new record to be added, located at ICAREAL.
8C4B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal for present ECF record address minus 1 using printer, CCHHR, in common seek/search area in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
C738     	X'1E', Address of ICAREAL,   SLI and SKIP, Key Length +   Block Size + 8	Read ccunt, key and data to verify record just written. Information is not transferred to main storage.

<sup>1-2</sup> See nctes 1 and 2 in Figure 54.

Figure 44. Channel program builder for ADD -- CCW chain built to write over ECF record (unblocked records).

CCW Builder  Control Code1	CCW Built	Function
8C4B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal for present EOF   record address minus one using pointer,   CCHHR, in common seek/search area in DTFIS  table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
37AC	X'1D', Address of IOAREAL, DC and SLI, 10	Write count, key and data of ECF record   located at IOAREAL.

<sup>1-2</sup> See notes 1 and 2 in Figure 54.

Figure 45. Channel program builder for ADD -- CCW chain built to write ECF in independent overflow area.

CCW Builder    Control Code1	CCW Built	Function
8C4B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal the track index using the pointer (CCHHR) in the ccmmcn seek/search area.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
106C	X'06', <sup>2</sup> &Filename.D, CC and SLI, 10	Read data (COCR record) into the cylinder cverflow control record (COCR) area.
E14B	X'B1', <sup>2</sup> &Filenaπe.D+8, CC, 5	Search identifier equal (multiple track) the track index for the last normal entry using information in the work area for the current track index normal entry count field.
066B 	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
154C	C'06', 2&Filename.D+40, CC, 10	Read data (last track index normal entry) into work area for track index normal entry data field.
526C     	X'92', <sup>2</sup> &Filename.D+16, CC and SLI, 10	Read count (multiple track) of last track index overflow entry into work area for the current track index overflow entry count field.
1D0C	X'06', Filename.W, 00, 10	Read data (last track index cverflcw entry) into random/sequential retrieval work area.

<sup>1-2</sup> See notes 1 and 2 in Figure 54.

Figure 46. Channel program builder for ADD -- CCW chain built to read last track index entry.

CCW Builder   Control Code1	CCW Built	Function
8C4B	x'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal the overflow chain using the pointer (CCHHR) in the ccππcn seek/search area.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
AA07	X'0E', Address cf IOAREAL+8,   00, Key Length + Record   Length + 10	Read key and data of overflow record into ICAREAL+8.

<sup>1-2</sup> See notes 1 and 2 in Figure 54.

Figure 47. Channel program builder for ADD -- CCW chain built to read overflow record.

CCW Builder    Control Code1	CCW Built	Function	1
8C4B	x'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal for last prime data record address using pointer, CCHHR, in common seek/search area in DTFIS table.	i   
066B	X'08', Pointer to *-8, CC and SL1, 5	TIC to *-8.	
1BO2	X'06', Address cf IOAREAL+ 8+KEYLEN, 00, Block Size	Read block into ICAREAL + 8 + KEYLEN.	

<sup>1-2</sup> See notes 1 and 2 in Figure 54.

Figure 48. Channel program builder for ADD -- CCW chain built to read last prime data record.

CCW Builder    Control Code1	CCW Built	Function
8C4B   	X'31', <sup>2</sup> &Filename.S+3, CC, 3	Search identifier equal for last prime data record address using pointer, CCHHR, in common seek/search area in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
2AC6   	X'OD', Address cf IOAREAL+8, CC and DC, Key Length + Block Size	Write key and data of prime data block   located at IOAREAL+8.
8C4B   	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal for last prime   data record address using pointer, CCHHR,   in common seek/search area in DTFIS table.
   066B 	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
AA36   	X'0E', Address of ICAREAL+8, SLI and SKIP, Key Length + Block Size	Read key and data to verify record just   written. Information is not transferred   to main storage.

<sup>1-2</sup> See notes 1 and 2 in Figure 54.

Figure 49. Channel program builder for ADD -- CCW chain built to write block of prime data records and verify.

CCW Builder  Control Code <sup>1</sup>	P .	Function
8C4B	X'31', <sup>2</sup> &Filenaπe.S+3, CC, 5	Search identifier equal for last track index address using pointer, CCHHR, in common seek/search area in DTFIS table.
066B	X'08', Pointer to8, CC and SLI, 5	TIC to *-8.
2A45	X'OD', Address of ICAREAL+8, CC, Key Length + 10	Write key and data of track index   normal entry located at ICAREAL+8.
E24B	X'B1', <sup>2</sup> &Filename.D+16, CC, 5	Search identifier equal (multiple track) the track index for the last over flow entry using the count for the current track index overflow entry.
066B	C'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
2845 	X'0D', Address of WORKL, CC, Key Length + 10	Write key and data of track index cverflcw entry located at WORKI.
824B   	X'31', 2&Filename.D+16, CC, 5	Search identifier equal the track index   for the last overflow entry using the   count for the current track index overflow   entry.
066B 	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
A835   	X'OE', Address of WCRKL,   SLI and SKIP, Key Length + 10	Read key and data to verify record    just written. Information is not  transferred to main storage.

<sup>1-2</sup> See nctes 1 and 2 in Figure 54.

Figure 50. Channel program builder for ADD -- CCW chain built to write track index entry.

CCW Builder    Control Code1	CCW Built	Function
8C4B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal the master/cylinder index using the pointer, CCHHR, in the common seek/search area in the LTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
150C	X'06', 2&Filename.D+40, 00, 10	Read data (index entry) into work area for track index normal entry data field.

<sup>1-2</sup> See notes 1 and 2 in Figure 54.

Figure 51. Channel program builder for ADD -- CCW chain built to read index entry.

CCW Builder    Control Code1	CCW Built	Function
8C4B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal the master/cylinder index using pointer, CCHHR, in common seek/search area in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
2A45	X'OD', Address of ICAREAL+8, CC, Key Length + 10	Write key and data of master/cylinder index entry located at ICAREAL+8.
8C4B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal the master/cylinder index using pointer, CCHHR, in common seek/search area in DTFIS table.
AA35	X'OE', Address of ICAREAL+8,   SLI and SKIP, Key Length   + 10	Read key and data to verify record just written. No information is transferred to main storage.

<sup>1-2</sup> See notes 1 and 2 in Figure 54.

Figure 52. Channel program builder for ADD -- CCW chain built to write index entry.

CCW Builder  Control Code1	CCW Built	Function
8C4B   	X'31', <sup>2</sup> &Filenaπe.S+3, CC, 5	Search identifier equal the track index using the rointer, CCHHR, in the common seek/search area in the DTFIS table.
066B 	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
B06C	X'05', <sup>2</sup> &Filename.D, CC and SLI, 10	Write data (COCR) from the cylinder overflow control record work area in DTFIS table.
E24B	X'B1', 2&Filename.D+16, CC, 5	Search identifier equal (multiple track) the track index using the pointer, CCHHR, in the work area for current track index overflow entry count field.
066B 	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
AA35     	X'OE', Address of ICAREAL+8,   SLI and SKIP, Key Length + 10	Read key and data to verify record just written. Information is not transferred to main storage.

<sup>1-2</sup> See nctes 1 and 2 in Figure 54.

Figure 53. Channel program builder for ADD -- CCW chain built to write track index cverflow entry.

#### Note 1:

The first character of the control code references an creration code at IJHCSTRI.

The second character of the control code references a data area at IJHAHRAA.

The third character of the control code references the following information:

Control Character	CCW Flag Field	<u>Meaning</u>
l I 0	x'00'	End of CCW Chain
2	X * 20 *	SLI (Suppress Length Indicator)
3	x'30'	SLI and SKIP (Suppress Data Transfer)
4	x 40 40	CC (Command Chaining)
6	x'60'	CC and SLI
j 7	x'70'	CC, SLI, and SKIP
A	X'A0'	SLI and DC (Data Chaining)
C	X'C0'	CC and DC

The fourth character of the control code references a byte count (length) field at

### Note 2:

&Filename = DTF name supplied by user.

 $\delta$ Filename.X = X is suffix supplied by DTFIS fcr unique DTF labels.

Figure 54. Channel program builder for ADD -- notes.

### <u>\$\$BINDEX\_Read\_Cylinder\_Index\_Intc\_Storage</u> <u>Charts\_FA-FB</u>

<u>Objective</u>: To read all or part of the cylinder index into main storage.

Entry: From the indexed-sequential logic
module (ISMOD).

Exit: To the problem program.

Method: This phase determines the number of cylinder index entries that can be read into main storage at one time. Each cylinder index entry consists of a key area and a data area. The key area contains the highest key associated with the cylinder, and its length is the same as that specified for logical data records in the DTFIS entry KEYLEN. The data area is ten bytes long and contains the pointer to the track index for that cylinder. See Figure 17 for the format of this ten-byte pointer. When this phase reads the cylinder index entry into main storage, only six bytes of the 10-byte pointer are retained. The last four bytes of the pointer to the track index are the same for all entries in the cylinder index. Therefore, only the first

six kytes of the pointer are required for processing.

If it is the first time through this E-transient phase, the key of the first core index entry is set to 0. If it is not the first time through this phase, the key cf highest entry minus 1 that was previously read into main storage is moved to the key area of the first core index entry. A test is made to determine if the index skip option was specified in the DTF entry. If the index skip option was specified, any cylinder index entries preceding the one needed to process a given key are not read into main storage. In crder to skip the cylinder index entries preceding the one needed to process a given key, a CCE to read the cylinder index is built along with a string of CCWs. Figure 55 gives a description of the CCW string.

This transient then executes the channel program and determines if the address of the first cylinder index entry read is the address of the required entry (SKEH, TIC, NO-OP). If it is, there are no cylinder index entries to be skipped and the cylinder index is then read into main storage from that point. If the addresses

are not the same (RDID, SKEH, TIC, RD), a check is made to determine if this is a dummy chained entry.

If it is a dummy chained entry (indicating the end of the cylinder), its address points to the first track of the next cylinder containing the cylinder index. This phase subtracts 1 from the record number of the dummy chained entry to get the preceding cylinder index entry, moves the chain address to the next cylinder index entry to be read (in the DTFIS table), and reads the cylinder index into main storage starting with the entry preceding the dummy chained entry.

If it is not a dummy chained entry, a test is made to determine if the required entry is the first record on the first track of the cylinder. If it is, this phase sets up to read the cylinder index into main storage starting with entry preceding the dummy chained entry for the previous cylinder. If the required entry is not the first record on the first track of the cylinder, a test is made to determine if the record number of the cylinder index entry is 1.

If it is 1, the track number is decreased by 1 and the record number is updated to the maximum record number for the cylinder index track. The cylinder index is read into main storage starting with the last record on the preceding track. If the required cylinder index

record number is greater than 1, the record number is decreased by 1, and the cylinder index is read into main storage starting with the preceding entry on the track. Each time a cylinder index entry is read, the number of available index entries in main storage is decreased by 1.

If the index skip option was not specified in the DTF, this phase decreases the number available core index entries by 2. These two core index entries contain dummy entries. The first dummy entry at the beginning of the cylinder index storage area contains either a key of all zeros (if this is first time the cylinder index has been read into main storage) or it contains the key of the last cylinder index entry read into main storage. The second dummy entry is located at the end of cylinder index storage area and has a key of all

Before a part or all cf the cylinder index is read into main storage, a test is made to determine how many cylinder index records can fit in the area available. A CCB and a CCW chain are built to perform the actual read operation. Figure 55 gives a description of the CCW chain. The channel program is executed and the number of core index entries is decreased by the number of records read. The cylinder index is read into main storage until either the end of the cylinder index is reached or there are no more core index entry positions.

CCW Built	Function
X'07', Address of cylinder index entry,  Command Chaining, 6	Long seek.
X'31', Address of cylinder index entry,  Command Chaining, 6	Search identifier equal (SIDE) the    cylinder index.
X'08', Pointer to *-8, -,	TIC to *-8.
X'69', &KEYARG, Command Chaining and  Suppress Length Indicator, KEYLEN  (Key Length).	Search key equal or high (SKEH) the    cylinder index. Key supplied by user in    the DIF table.
X'08', Pointer to *+16, -,	TIC to *+16.
X'03', -, Suppress Length Indicator, 1.	NO-CP.
X'92', IDOFHIT, Command Chaining, 8.	  Read count (multiple-track) (RIDM) into    IDOFHIT, 8-byte area for record found on    SIDE.
X'69', &KEYARG, Command Chaining and  Suppress Length Indicator, KEYLEN  (Key Length).	Search key equal or high (SKEH) the    cylinder index.   
X'08', Pointer to *-16, -,	TIC to *-16.
X'06', POINTER, End of Chain, 10.	Read data portion of cylinder index entry   (RD) into PCINTER, 10-byte area for   pointer to track index.

Figure 55. CCW chain built by \$\$BINDEX to skip cylinder index entries preceding the one to process a given key.

CCW Built	Function
X'07', Address of cylinder index entry,  Command Chaining, 6.	Long Seek.
X'31', Address of cylinder index entry,  Command Chaining, 6.	  Search identifier equal (SIDE) the  cylinder index.
X'08', Pointer to *-8, -,	TIC to *-8.
X'OE', Address of cylinder index entry in   main storage (multiple of key length + 6),   Command Chaining, Key Length + 10	

Figure 56. CCW chain built by \$\$BINDEX to read the cylinder index into storage.

ISAM RETRVE, RANDOM: READ Macro, KEY Chart FC

<u>Objective</u>: To perform the random retrieval function for an indexed sequential file by searching the indexes to determine the track on which the desired record is stored.

Entry: From the REAL, KEY macro expansion.

Exit: To the problem program via linkage
register 14.

Method: This routine first initializes pointers and status bits in the DTFIS table. It then constructs the CCW chain to search the master or cylinder index (Figure 57). It determines the highest level index (master or cylinder) being used, and tests for ERREXT=YES. If ERREXT is specified, additional error conditions can be returned to the problem program, thus giving the user greater flexibility in attempting to continue processing. This routine then searches the highest level index to get the address of the next index to be searched.

A test of the F code from the index level pointer is then made to determine if the next search is of the track index (Figure 17). The F code refers to the index level just searched. If the master index was just searched, the next search is on the cylinder index. If the next search is not on the track index, the routine gets the index entry type and determines the routine to process that type.

If the entry type is a normal entry, the routine returns to search the next index. If the entry type is a dummy end entry or an inactive entry, the routine branches to an error routine to set a no-record-found flag in the DTF table and to return to the problem program. If the entry is a dummy chained entry, the routine returns to search the index using the address supplied by the 10-byte index level pointer.

When the next search is found to be on the track index, a test is made to determine if the track index takes up one track or more. If the track index does not require a full track, the routine builds a new CCW chain to search the track index (Figure 58). This routine then issues the EXCP and SVC7 (WAIT) to search the track index. If the over/under seek routine is not needed, it returns to the problem program.

# ISAM RETRVE, RANDOM: WAITF Macrc Charts FD-FG

Cbjective: To ensure that the last EXCP issued has been completed and that the condition is normal. If the operation is a read, to locate the specified record and complete the transfer of data to the I/O area specified by the DTFIS entry IOAREAR, and to the specified work area if the DTFIS entry WORKR is included in the file definition. If the operation is a write, the objective is to return control to the problem program.

Entry: From the WAITF macro expansion.

Exit: To the problem program via linkage
register 14.

Method: This routine first tests for ERREXT=YES. If ERREXT is specified, additional error conditions can be returned to the problem program, thus giving the user greater flexibility in attempting to continue processing. After initializing pointers to the DTFIS table, this routine tests the status byte in the DTF table, to determine if the condition so far is normal. If an abnormal condition exists, control returns to the problem program.

If the condition is normal, the routine issues a WAIT to determine if the EXCP issued by the READ or WRITE routines has teen completed, and also tests for errors. Then, if the operation is a WRITE, this routine returns control to the problem program.

If the operation is a READ, this routine must complete the read operation by moving the data to the I/C area. If first moves the address of the track in which the desired record is stored to the seek/search area, and initializes pointers to KEYARG and the I/C area. It also gets the relative key location and key length.

The routine then gets the index entry type (F code) from the search address and determines the routine to process that type. If the entry is a normal entry on an unshared track, a new CCW chain is built to find the record in the prime data area. (Figure 59). If blocked records are specified, the CCW command ccde is mcdified to search high or equal. An EXCF and WAIT are issued to find the record and read the block into the I/C area. If records are unblocked, the record is moved into WORKR, if specified, and control returns to the problem program. If records are blocked, this routine tests to determine if KEYARG is less than the key in the first logical record. If it is, the record has not been found, and the corresponding bit is set on

in the DTF table. Otherwise, the corresponding key is found within the block and the routine moves the block to WORKR, if specified. Control then returns to the problem program.

For a normal entry on a shared track, the routine decreases the record number in the search address by 1, and builds a new CCW chain to find records on a shared track (Figure 60). Processing continues as in the routine to process a normal entry on an unshared track.

If the entry is an overflow end entry or an overflow chained entry, this routine first constructs a CCW chain to search the overflow chain (Figure 61). An EXCP and WAIT are issued to locate the record in the overflow chain. A test is made to determine if the desired record has been found. If it has not been found, the routine tests for an overflow end entry. If it is an overflow end entry, the no-record-found bit is set on in the DTF table. If it is not an overflow end entry, the sequence link field is inserted in the seek/search address, and the overflow chain is searched again.

If the record has been found, overflow bits are set on in the DTF table, and the first nonoverflow record count is increased by 1. The logical record is moved to WCRKR, if specified. Control returns to the problem program via register 14.

If the entry is a dummy end entry or an inactive entry, the routine sets a no-record-found bit on in the DTF table, and returns control to the problem program.

## ISAM RETRVE, RANDOM: WRITE Macrc, KEY Chart FH

<u>Objective</u>: To perform the random retrieval output function for an indexed sequential file.

Entry: From WRITE, KEY macro expansion.

Exit: To the problem program via register 14.

<u>Method</u>: This routine first sets the write <u>lit</u> on in the DTFIS table. It then tests for an uncorrectable DASD error, wrong length record error, or no record found error. If any of these errors exist, the no-record-found bit is set on in the DTF table, and control returns to the problem program.

If there are no errors, the status byte in the DTF table is reset, and printers to the DTF table are initialized. This routine then gets the count field of the record as saved by the READ routine, the address of WORKR, and the address of the logical record within the I/C area. The record, or block of records, is moved to the I/O area from WORKR, if specified. The CCW chain to write records is then built (Figure 62).

If the entry to be written is not an everflow entry, the byte count field in the write and verify CCW's is modified to the block length from the DTF table. This routine then issues the EXCP to write the record, and returns control to the problem program without issuing a WAIT. The WAIT function is left to the WAITF macro, which must be issued before the user can continue processing.

ISAM RETRVE, RANDOM: FREE Macro Chart FK

Objective: To free a held track if the track hold option has been specified.

Entry: From the FREE macro expansion.

Exit: To the problem program via linkage
register 14.

<u>Method</u>: This routine determines whether the track hold option has been specified in the DTF. If so, both the held data track and the applicable held index track are released. All tracks are released by SVC 36. Control then returns to the problem program.

CCW Builder    Control Code <sup>1</sup>	CCW Built	Function
7461   	X'69', &KEYARG, CC and SLI, Key Length	Search key equal or high the master/cylinder index. Key supplied by user in the DTFIS table.
0C6B	X'08', Pointer to *+16, CC and SLI, 5	TIC to *+16.
D17B	X'1A', <sup>2</sup> &Filename.W, CC, SLI, and SKIP, 5	Read home address into randcm/sequential retrieval work area in DTFIS table.
536C	X'92', &IOAREAR, CC and SLI, 10	Read count (multiple track) intc IOAREAR.
7461 	X'69', KEYARG, CC and SLI, Key Length	Search key equal or high the master/cylinder index. Key supplied by user in DTFIS table.
046B	X'08', Pointer to *-16, CC and SLI, 5	TIC tc *-16.
110C	X'06', <sup>2</sup> &Filename.W, 00, 10	Read data (10-byte index level pointer)    into random/sequential retrieval area in    DTFIS table.

 $<sup>^{1-2}</sup>$  See notes 1 and 2 in Figure 63.

Figure 57. Channel program builder for random retrieval -- CCW chain built to search master cylinder index.

CCW Builder Control Code <sup>1</sup>	CCW Built	Function
D36B	X'1A', &IOAREAR, CC and SLI, 5	Read home address into IOAREAR.
9461	X'E9', &KEYARG, CC and SLI, Key Length	Search key equal or high (multiple track) track index. Key supplied by user in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
110C	X'06', <sup>2</sup> &Filename.w, 00, 10	Read data (10-byte index level rcinter)  intc randcm/sequential retrieval area in    DTFIS table.

 $<sup>^{1-2}</sup>$  See notes 1 and 2 in Figure 63.

Figure 58. Channel program builder for random retrieval -- CCW chain built to search track index.

CCW Builder    Control Code1	CCW Built	Function
7461 	X'69', &KEYARG, CC and SLI, Key Length	Search key equal or high in prime data  area. Key supplied by user in DTFIS  table.
0C4B	X'08', Pointer to *+16, CC, 5	TIC to *+16.
D1 <b>7</b> B	X'1A', <sup>2</sup> &Filename.W, CC,   SLI, and SKIP, 5	Read home address into random/sequential   retrieval work area in DTFIS table.
406C	X'12', <sup>2</sup> &Filename.S+3, CC and SLI, 10	Read count into common seek/search area in DTFIS table.
6461   or 7461 	X'29' or X'69', &KEYARG, CC and SLI, Key Length	If records are unblocked, search key equal the prime data area. If records are blocked, search key equal or high in prime data area. Key supplied by user in TTFIS table.
044B	X'08', Pointer tc *-16, CC, 5	TlC to *-16.
1302	X'06', &IOAREAR, 00, Block Length	Read data (block) containing starting record into IOAREAR.

<sup>1-2</sup> See notes 1 and 2 in Figure 63.

Figure 59. Channel program builder for random retrieval -- CCW chain built to find record in prime data area (unshared track).

CCW Builder Control Code <sup>1</sup>	CCW Built	Function
804B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal the prime data area using the pointer (CCHHR) in the connon seek/search area in the DTFIS table.
064B	X'08', Pointer to *-8, CC, 5	TIC to *-8.
406C	X'12', <sup>2</sup> &Filename.S+3, CC and SLI, 10	Read count into common seek/search area in DTFIS table.
6461 or 7461	X'29' or X'69', &KEYARG, CC and SLI, Key Length	If records are unblocked, search key equal the prime data area. If records are blocked, search key high or equal the prime data area. Key supplied by user in CTFIS table.
044B	X'08', Pointer to *-16, CC, 5	TIC to *-16.
1302	X'06', &IOAREAR, 00, Block Length	Read data (block) containing record into IOAREAR.

<sup>1-2</sup> See notes 1 and 2 in Figure 63.

Figure 60. Channel program builder for random retrieval -- CCW chain built to find record in prime data area (shared track).

CCW Builder  Control Code <sup>1</sup>	CCW Built	Function
804B	X'31', <sup>2</sup> &Filenaπe.S+3, CC, 5	Search identifier equal the cverflow chain using the pointer (CCHHR) in the common seek/search area in the DTFIS table.
064B	X'08', Pointer to *-8, CC, 5	TIC to *-8.
6461	X'29', &KEYARG, CC and SLI, Key Length	Search key equal the cverflow chain. Key supplied by user in DTFIS table.
116C	X'06', <sup>2</sup> &Filename.w, SLI, 10	Read data (10-byte sequence link field)   into random/sequential retrieval area in   DTFIS table. This CCW is executed when   the required overflow record is not found   in the overflow chain.
1303	X'06', &IOAREAR, 00, Record Length + 10	Read data (sequence link field rlus

<sup>1-2</sup> See nctes 1 and 2 in Figure 63.

Figure 61. Channel program builder for random retrieval -- CCW chain built to find record in cverflcw chain.

CCW Builder	CCW Built	Function
804B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal prime data area    using pcinter (CCHHR) in common    seek/search area in DTFIS table.
064B	X'08', Pointer to *-8, CC, 5	TIC to *-8.
ВЗСЗ	X'05', &IOAREAR, CC, Record Length + 10	Write data from IOAREAR.
804B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal the prime data    area, using pointer (CCHHR) in common    seek/search area in DTFIS table.
064B	X'08', Pointer to *-8, CC, 5	TIC to *-8.
1333   	X'06', &IOAREAR, SLI and SKIP, Record Length + 10	Read data to verify record just  written. Information is not transferred    to main storage.

<sup>1-2</sup> See notes 1 and 2 in Figure 63.

Figure 62. Channel program builder for random retrieval -- CCW chain built to write record.

### Note 1:

The first character of the control code references an operation code at IJHCSTRI.

The second character of the control code references a data area at IJHCASAD.

The third character of the control code references the following information:

Control Character	CCW_Flag_Field	<u>Meaning</u>
0	x • 00 •	End of CCW chain
<u> </u>	X'20'	SLI (Suppress Length Indicator)
3	x'30'	SLI and SKIP (Suppress Data Transfer)
j 4	X 40 4	CC (Command Chaining)
6	x'60'	CC and SLI
j 7	x'70'	CC and SII and SKIF
C	X'C0'	CC and DC (Data Chaining)

The fourth character of the control code references a byte count (length) field at IJHCRESZ.

Note 2:

|&Filename = DTF name supplied by user.

&Filename.X = X is suffix supplied by DTFIS for unique DTF labels.

Figure 63. Channel program builder for random retrieval -- notes.

### ISAM RETRVE, SEONTL: ESETL Macro Chart GA

ISAM RETRVE, SEQNTL: GET Macrc
Charts GB-GE

<u>Objective</u>: To write the last record if necessary, and reset the status byte in the DTFIS table.

Entry: From the ESETL macro expansion.

Exit: To the problem program via linkage
register 14.

Method: After initializing pointers to the DTFIS table, this routine sets the status byte in the DTFIS table to 0. A test is then made to determine if the PUT issued bit is on in the retrieval byte of the DTF table. If it is on, the last block of records is written. A test for ICAREA2=YES is then made. If IOAREA2 is specified as an ISMOD macro parameter option to allow overlapping of I/O with processing, a bit is set in the DTF table to indicate the first record is being processed and a wait for I/O completion is made. If HCID=YES is specified, an SVC 36 releases any held tracks. Control then returns to the problem program.

<u>Objective</u>: To perform the sequential retrieval input function for an indexed-sequential file.

Entry: From the GET macrc expansion.

<u>Exit</u>: To the problem program via linkage register 14.

Method: This routine initializes pointers to the DTFIS table, and then tests for IOAREA2=YES. If ICAREA2 is not specified as an ISMCD macro parameter cpticn, a test is made to determine if the last record read was in the overflow area. If the last record read was in the overflow area, the contents of the sequence-link field is moved to the seek/search area and a test is made to determine if the end of the cverflow chain has been reached. If the last record read was in the cverflcw area and HOLD=YES has been specified, the track is released, then the overflow record is read and addresses are saved. If HOLD=YES is specified, the index track and data track are held during update procedure. The index track is then released. record is then moved to WCRKS, if specified, and control returns to the problem program.

If the end of the chain has been reached, the current DASD address is updated to the next track and the next record is read. The record is moved to the work area if specified, addresses are saved, and control returns to the problem program.

If the last record read was not in the overflow area, the routine determines if all records in the block have been processed. If all the records in the block have not been processed, the I/O area pointer is updated to the next logical record. If that record is not a padding record, it is moved to the work area, if specified, addresses are saved, and control returns to the problem program. If it is a padding record, the EOF indicator is set in the DTFIS table, and control returns to the problem program.

If all records in the block have been processed, a check is made to determine if the PUT macro has been issued. If it has, the record is written. A test is made to determine if the prime data device type is a 2321. If it is a 2321 data cell, the upper limit for the strip is initialized, and then modified if the last record was on the next-to-last subcell.

A test is made to determine if the end of the track has been reached. If it has been reached, the track index is searched to find the next track index entry. The current track index record number in the DTF table is then updated, and a test is made to determine if there is any overflow record indicated in the track index entry just read. If the track index entry indicates an overflow record is present, that overflow record is read, and moved to the work area if specified. Control returns to the problem program.

If there is no overflow record indicated in the track index entry, the current address is updated by 1, and the record is read and moved into the work area, if specified. Control returns to the problem program.

If IOAREA2 has been specified to allow overlapping of I/O with processing, a test is made to determine if the last record read was in the overflow area. If the last record read was in the overflow area, a test is made to determine if the record being processed by the user is an overflow record. If it is an overflow record, a wait for I/O completion is made, the next available I/O area address is obtained, the current record address is saved, and a test is made to determine if the first record is being processed.

If the first record is being processed, the overflow record is read, its address is saved in the DTF table and the record is moved to the work area, if specified. Control then returns to the problem program.

If the first record is not being processed, the address of the next overflow record is moved to the seek/search address and a test is made to determine if the end of the overflow chain has been reached. If the end has not been reached, the overflow record is read, and addresses are saved. If HOLD=YES is specified, an SVC 36 releases any held tracks. Control then returns to the problem program.

If the end of the overflow chain has keen reached, the current disk address is updated to the next track and the next record is read. The record is moved to the work area if specified, addresses are saved, and control returns to the problem program.

If the last record was not an overflow record and the current record is not an overflow record, this routine determines if all the records in the block have been processed. If all records in the block have not been processed, the I/O area pointer is updated to the next logical record and the record number is updated by 1. If the next logical record is not a padding record, it is noved to the work area, if specified, addresses are saved and control returns to the problem program. If it is a padding record, the ECF indicator is set in the DTF table and control returns to the problem program.

When all the records in the block have keen processed, a check is made to determine if the PUT macro has been issued. If the PUT macro has been issued, the record is written. A test is made to determine if the prime data device type is a 2321. If it is a 2321, the upper limit for the strip is initialized to 9 and then  $\pi$ cdified to  $\bar{5}$  if the last record was on the next-to-last subcell. A test for the presence of two I/C areas is then made. the presence of two I/O areas is indicated in the DTF table, a test is made to determine if the first record is being processed. If the first record is not being processed, a wait for I/O completion is made and the record address is saved in the DTF table.

A test is made to determine if the end of the track has been reached. If the erd of the track has been reached, the track index is searched to find the next track index entry. The current track index record number in the DTF table is then updated, and a test is made to determine if

there is any overflow record indicated in the track index entry just read. If the presence of an overflow entry is indicated, and there are two I/O areas present, this routine tests to determine if this is the first record. If it is not the first record, the record counter is set to 1. The overflow record address is moved to the seek/search address, and the overflow record is read and moved to the work area, if specified. Control then returns to the problem program.

If there is no overflow record indicated in the track index entry, the current address is updated by 1, the next record is read and moved to the work area, if specified, and control returns to the problem program.

### ISAM RETRVE, SEQNTL: PUT Macro Chart GF

Objective: To perform the sequential retrieval output function for an indexed sequential file.

Entry: From the PUT macro expansion.

Exit: To the problem program via linkage register 14.

Method: After initializing pointers to the DTFIS table, this routine tests whether a GET has been issued. If a GET has not been issued, there is an SVC 50 (errcr). Otherwise, the GET issued switch is turned off and the cutput bit in the retrieval byte in the DTFIS table is turned on. The record is moved from the work area to the I/O area, and the output bit in the retrieval byte is set off.

If the record is unblocked, or is in the overflow area, this routine writes the record and returns control to the problem program. If the records are blocked, this routine sets the bit in the retrieval byte to indicate that the PUT macro has been issued for this record, and returns control to the problem program. The GET macro routine causes the block to be written on DASD when it determines that all records in the block have been processed.

## ISAM RETRVE, SECNTL: SETL Macrc, \$\$BSETL Charts GG-GL

<u>Cbjective</u>: To initialize fcr sequential retrieval based on information supplied by the user in the SETL macro.

Entry: From the SETI macro expansion.

Exit: To the problem program.

Method: This logical transient first validates the limits of the DTFIS table and IOAREAS to ensure that they lie within the partition. If HOLD=YES has been specified in the DTF, \$\$BSETI1 is fetched to perform the SETL macro functions. If track hold has not been specified, \$\$BSETI then initializes for sequential retrieval based on the information supplied by the user in the SETL macro. The SETL macro specifies the type of reference used to identify the first record to be processed. The types of reference are:

- KEY. Key of starting record in the file.
- GKEY. Location of starting record in the file, identified by a record key within a desired group. The user supplies a key that identifies the high order bytes of the required group of keys. For example, a GKEY specification of D6430000 would permit file processing to start at the first key containing D643XXXX, regardless of the characters represented by the Xs.
- BOF. Beginning of the lcgical file.
- ID (MBBCCHHR). Location of starting record in the prime data area.

If sequential retrieval is to begin with a record associated with a particular key (KEY), the key of the beginning record must be placed in the field defined by the DIFIS entry KEYARG before issuing the SETL macro. This phase searches the master index (if present), cylinder index and track index until it finds the track index entry associated with the specified key. It determines whether the record with the desired key is on a shared or unshared prime data track or in the overflow area.

If the record is on a shared track, the search is initialized to begin after the remainder of the track index has been bypassed. If the record is on a prime data track and records are unblocked, the track index cverflow entry address associated with the desired record is calculated and stored in the DTFIS table, and the track is searched equal for the desired record. If the record is not found, a nc-record-found indicator is set in the DTFIS table (Filename.C).

If the file contains blocked records, the track is searched equal/high for the desired block. The user must supply (in the DTFIS entry KEYICC) the position of the key field in the data record. The block is

then searched. When the record with the matching key is found, its address is saved in the DTFIS table and control returns to the problem program. If the record is not found, the no-record-found indicator is set in the DTFIS table (Filename.C).

If the record with the desired key is in the overflow area, the track index normal and overflow entry addresses are stored in the DTFIS table, and the overflow chain is searched for the desired record. When the desired record is found, its address is saved in the DTFIS table. If the desired record is not found in the overflow chain, a no-record-found indicator is set in the DTFIS table (Filename.C) and control returns to the problem program.

If GKEY was specified in the SETL macro, the CCW chain to read the desired record is modified to search key equal or high. The search for the desired record then proceeds im the same manner as if KEY were specified in the SETL macro. However, in this case (GKEY), a no-record-found condition should not occur unless the key specified is higher than the existing highest key in the file.

If BOF was specified in the SETI macro, the address of the first prime data record in the file is saved in the sequential retrieve section of the DTFIS table, and the track index overflow entry address associated with the desired record is calculated and stored in the DTFIS table. Control then returns to the problem program.

If the starting record address is referenced by a symbolic name in the SETL macro, this phase analyzes the 8-byte DASD address (MBBCCHHR) in the field specified by the symbolic name for validity. If the address is invalid, an illegal ID indicator is set in the DTFIS table (Filename.C) and control returns to the problem program. If the starting address is valid, this phase saves the address in the DTFIS table, calculates the track index cverflow entry address associated with the desired record, stores it in the DTFIS table and returns control to the problem program.

In order to perform a search of the master, cylinder or track indexes, prime data area and overflow area for the starting record, a CCW string is built to search the required areas. Figures 64-67 give a description of the channel program built to perform the necessary search.

ISAM RETRVE, SEONTL: SETI Macro, \$\$BSETL1
Charts GM-GR

<u>Objective</u>: To initialize for sequential retrieval when HOLD=YES, based on information supplied by the user in the SETL macro.

Entry: From the SETI macro expansion.

Exit: To the problem program.

Method: This logical transient first validates the limits of the DTFIS table and IOAREAS to ensure that they lie within the partition. It then initializes for sequential retrieval based on the information supplied by the user in the SETL macro. The SETL macro specifies the type of reference used to identify the first record to be processed. The types of reference are:

- KEY. Key of starting record in the file.
- GKEY. Location of starting record in the file, identified by a record key within a desired group. The user supplies a key that identifies the high order bytes of the required group of keys. For example, a GKEY specification of D6430000 would permit file processing to start at the first key containing D643XXXX, regardless of the characters represented by the Xs.
- BOF. Beginning of the lcgical file.
- ID (MEECCHHR). Location of starting record in the prime data area.

If sequential retrieval is to begin with a record associated with a particular key (KEY), the key of the beginning record must be placed in the field defined by the DTFIS entry KEYARG before the SETL macro is issued. This phase searches the master index (if present), cylinder index, and track index until it finds the track index entry associated with the specified key. It determines whether the record with the desired key is on a shared or unshared prime data track or in the overflow area.

If the record is on a shared track, the search is initialized to begin after the remainder of the track index has been bypassed. If the record is on a prime data track and records are unblocked, the track index overflow entry address associated with the desired record is calculated and stored in the DTFIS table, and the track is searched equal for the desired record. If the record is not found, a no-record-found indicator is set in the DTFIS table (Filename.C).

If the file contains blocked records, the track is searched equal/high for the desired block. The user must supply (in the DTFIS entry KEYLOC) the position of the key field in the data record. The block is searched. When the record with the matching key is found, its address is saved in the DTFIS table, and control returns to the problem program. If the record is not found, the no-record-found indicator is set in the DTFIS table (Filename.C).

If the record with the desired key is in the overflow area, the track index normal and overflow entry addresses are stored in the DTFIS table, the appropriate index and data tracks are held, and the cverflow chain is searched for the desired record. When the desired record is found, its address is saved in the sequential retrieval section of the DTFIS table. If the desired record is not found in the overflow chain, a no-record-found indicator is set in the DTFIS table (Filename.C), the held index and data tracks are released, and control returns to the problem program.

If GKEY was specified in the SETL macro, the CCW chain to read the desired record is modified to search key equal or high. The search for the desired record proceeds in the same manner as if KEY were specified in the SETL macro. However, in this case (GKEY), a nc-record-found condition should not occur unless the key specified is higher than the existing highest key in the file.

If BOF was specified in the SETL macro. the address of the first prime data record in the file is saved in the sequential

retrieval section of the DTFIS table, and the track index overflow entry address associated with the desired record is calculated and stored in the DTFIS table. Control returns to the problem program.

If the starting record address is referenced by a symbolic name in the SETL macro, this phase checks the validity of the 8-byte DASD address (MBBCCHHR) in the field specified by the symbolic name. the address is invalid, an illegal ID indicator is set in the DTFIS table (Filename.C), and control returns to the problem program. If the starting address is valid, this phase saves the address in the DTFIS table, calculates the track index overflow entry address associated with the desired record, stores it in the DTFIS table, holds the required index and data tracks, and returns control to the problem program.

Before I/O is performed, a switch is tested to see if track hold has been specified. If it has, the data track(s) and the corresponding index track(s) are held until I/O is complete. If any error conditions occur (for example, no record found, wrong length record, or a DASD read error), any held tracks are freed before returning to the user.

In order to perform a search of the master, cylinder, or track indexes, prime data area, and overflow area for the starting record, a CCW string is built to search the required area. Figures 64-67 give a description of the channel program built to perform the necessary search.

    Label	CCW Builder   Control Code <sup>1</sup>	CCW Built	Function
	7441	X'69', &KEYARG, CC, Key  Length 	Search key equal or high the master/cylinder index. Key supplied by user in DTFIS table.
	0C40	X'08', Pointer to *+16, CC, Record Length	TIC tc *+16.
	В 0 4В	X'1A', <sup>2</sup> &Filename.S+3, CC, 5	Read home address into common seek/search area in DTFIS table.
-	506B	X'92', <sup>2</sup> &Filename.S+3, CC  and SLI, 5	Read ccunt (multiple track) -   CCHHR - into common seek/ search area   in DTFIS table.
	7441	X'69', &KEYARG, CC, Key  Length 	Search key equal or high the master/cylinder index. Key supplied by user.
   	0440	X'08', Fointer to *-16, CC, Record Length	TIC tc *-16.
	<b>11</b> 00	X'06', <sup>2</sup> &Filename.W, 00, 10	Read data (10-byte index level pointer) into random/sequential retrieval area in DTFIS table. The data field is then moved from the random/sequential retrieval area to the common seek/search area for the next search.

<sup>1--6</sup> See notes 1 through 6 in Figure 71.

Figure 64. Channel program builder for sequential retrieval -- CCW chain built to search master cylinder index.

Label	CCW Builder    Control Code1	CCW Built	Function
	8 <b>0</b> 60	X'31', <sup>2</sup> &Filename.S+3, CC  and SLI, 10	Search identifier equal the track index using the 10-byte pointer in the common seek/search area.
	0640	X'08', Pointer *-8, CC,  Record Length	TIC to *-8.
       	126C	X'06', &IOAREAS, CC and  SLI, 10 	Read data (10-byte track index pointer) into ICAREAS, input/output area for sequential retrieval supplied by user.
	506B	X'92', <sup>2</sup> &Filename.S+3, CC  and SII, 5 	Read count (multiple track) -  CCHHR - into common seek/search area    in DTFIS table.
	7441	X'69', &KEYARG, CC, Key Length	Search key equal or high the track index. Key supplied by user.
	0240	X'08', Pointer to *-24, CC, Reccrd Length	TIC tc *-24.
	1100	X'06', <sup>2</sup> &Filename.W, 00, 10	Read data (10-byte pcinter) into random/sequential retrieval area in ETFIS table. The data field is then moved from the random/sequential retrieval area for the next search.

<sup>1--6</sup> See notes 1 through 6 in Figure 71.

Figure 65. Channel program builder for sequential retrieval -- CCW chain ruilt to search track index.

Label	CCW Builder   Control Code <sup>1</sup>	CCW Built	Function
STRI1	08 <b>4</b> B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal the prime data area using the pointer (CCHHR) in the common seek/search area in the DTFIS table.
   	066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
	416C	X'12', <sup>2</sup> &Filename.W, CC and SLI, 10	Read ccunt into common     seek/search area in the DTFIS table.
	6441 or 7441	X'29' or X'69', &KEYARG,  CC, Key Length	If KEY is specified in the SETL macro and/or records are unblocked, this CCW searches key equal the prime data area. If GKEY is specified in the SETI macro and/or records are blocked, this CCW searches key equal or high the prime data area for the starting record.
	046B	X'08', Pointer to *-16, CC and SLI, 5	TIC tc *-16.
   	1202	X'06', &ICAREAS, 00, Block Size	Read data (block containing starting record) into IOAREAS.

<sup>1--6</sup> See notes 1 through 6 in Figure 71.

Figure 66. Channel program builder for sequential retrieval -- CCW chain built to find starting record in prime data area.

    Label	CCW Builder  Control Code1	CCW Built	Function
STRI3	804B	X'31', 2&Filename.S+3, CC, 5	Search identifier equal the cverflcw chain using the pointer (CCHHR) in the common seek/search area in the DTFIS table.
	0640	X'08', Pointer to *-8, CC,  Record length	TIC to *-8.
	6441 or 7441	X'29' cr X'69', &KEYARG,  CC, Key Length     	If KEY is specified in the SETL  macro, this CCW searches key  equal the overflow chain for the  starting record. If GKEY is specified   in the SETL macro, this CCW searches  key equal or high the overflow chain  for the starting record.
	112c	X'06', <sup>2</sup> &Filename.W, SLI, 10	Reads data (10-byte sequence link field) into random/sequential retrieval area in DTFIS table. This CCW is executed when the required cverflow record is not found in the overflow chain.
	1203	X'06', &ICAREAS, 00,  Record Length +10	Read data (sequence link field plus starting record) into IOAREAS.   This CCW is executed when the matching key is found in the overflow chain.

<sup>1--6</sup> See notes 1 through 6 in Figure 71.

Figure 67. Channel program builder for sequential retrieval -- CCW chain built to find starting record in overflow chain.

CCW Parameter <sup>3</sup>  Control Code	CCW Built	Function
0540   	X'31', 2&Filename.S+3, CC, 5	Search identifier equal the prime data area using the pointer (CCHHR) in the common seek/search area in the DTFIS table.
	X'0', Pointer tc *-8, CC, 0	TIC to *-8.
	X'05',4 &10AREAS, CC,5  Elock Length <sup>6</sup>	Write data (block) onto prime data area.
	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal to verify write operation.
	X'08', Pointer to *-8, CC, 0	TIC to *-8.
	X'06',4 &IOAREAS, SKIP,  Block Length <sup>6</sup>	Read data to verify write cperation.

<sup>1--6</sup> See notes 1 through 6 in Figure 71.

Figure 68. Channel program builder for sequential retrieval -- CCW chain built to write records.

CCW Parameter <sup>3</sup>	CCW Built	Function
0601	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal the track index area using the pointer (CCHHR) in the common seek/search area in the DTFIS table.
	X'08', Pointer to *-8, CC, 0	TIC to *-8.
	X'06',4 <sup>2</sup> &Filename.W, CC, <sup>5</sup> 10	Read data (10-byte index level pcinter) into random/sequential retrieval area in the DTFIS table.
	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal to verify read operation.
	X'08', Pointer to *-8, CC, 0	TIC to *-8.
	X'06', &IOAREAS, SKIP,  Block Size	Read data to verify read cperation.

<sup>1--6</sup> See notes 1 through 6 in Figure 71.

Figure 69. Channel program builder for sequential retrieval -- CCW chain built to search track index.

CCW Parameter <sup>3</sup>	CCW Built	Function
0600	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal the prime data area using the pointer (CCHHR) in the common seek/search area in the DTFIS table.
	X'08', Pointer to *-8 ,CC, 0	TIC to *-8.
	X'06',4 &IOAREAS, CC,5 Block Length <sup>3</sup>	Read data into ICAREAS.
	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal to verify read operation.
	X'08', Pointer to *-8 ,CC, 0	TIC to *-8.
	X'06',4 &IOAREAS, SKIP, Block Length4	Read data to verify read operation.

<sup>1--6</sup> See notes 1 through 6 in Figure 71.

Figure 70. Channel program builder for sequential retrieval -- CCW chain built to read records.

Note 1: The CCW chains are built by the B-transients, \$\$BSETL and \$\$BSETI1. The CCW builder control code references information in the \$\$BSETL and \$\$BSETI1 assemblies.

The first character of the control code references an operation code at IJHRCF.

The second character of the control code references a data area at IJHARA.

The third character of the control code references the following information:

1	<u>Control Character</u>	CCW Flag Field	<u>Meaning</u>
İ	0	X • 00 •	End cf CCW chain
Ì	2	x'20'	SLI (Suppress Length Indicator)
ĺ	4	x • 40 •	CC (Command Chaining)
İ	6	x'60'	CC and SLI

The fourth character of the control code references a byte count (length) field at REINT.

#### Note 2:

&Filename = DTF name supplied by user.

&Filename.X = X is suffix supplied by DTFIS for unique DTF labels.

#### Note 3:

The CCW parameter is found in the ISMCD assembly.

The first byte of the parameter is the command code.

The second byte of the parameter contains flags with the exception of the chain to search the track index. In this case, the second byte is an indicator to the channel program builder that the CCW chain is to search the track index.

 $Note_4$ : If the file contains unblocked records, the command code is ncdified to either Read Key and Data, or Write Key and Data.

Note 5: If the verify option has not been specified, the command chaining bit is not set on.

Note 6: If the file contains unblocked records, the byte count field contains the physical record length plus Key Length.

Figure 71. Channel program builder for sequential retrieval -- notes.

#### ISAM ADDRIR: ESETL Macro Chart JA

<u>Cbjective</u>: To write the last record, if necessary, and to reset the status byte in the DTFIS table.

Entry: From the ESETL macro expansion.

Exit: To the problem program via linkage
register 14.

Method: After initializing pointers to the DTFIS table, this routine sets the status byte in the DTFIS table to 0. A test is then made to determine if the PUT issued

bit is on in the retrieval byte cf the DTF table. If it is on, the last block cf records is written. A test for IOAREA2=YES is then made. If ICAREA2 is specified as an ISMCD macro parameter option to allow overlapping of I/O with processing, a bit is set in the DTF table to indicate the first record is being processed and a wait for I/C completion is made. Control then returns to the problem program.

#### ISAM ADDRTR: GET Macro Charts JB-JE

<u>Cbjective</u>: To perform the sequential retrieval input function for an indexed sequential file.

Entry: From the GET macro expansion.

Exit: To the problem program via linkage
register 14.

Method: This routine initializes pointers to the DTFIS table, and then tests for IOAREA 2=YES. If IOAREA 2 is not specified as an ISMOD macro parameter option, a test is made to determine if the last record read was in the overflow area. If the last record read was in the overflow area, the contents of the sequence-link field is moved to the seek/search area, and a test is made to determine if the end of the overflow chain has been reached. If it has not been reached, the overflow record is read, addresses are saved, and the record is moved to WORKS, if specified. Control then returns to the problem program.

If the end of the chain has been reached, the current disk address is updated to the next track and the next record is read. The record is moved to the work area, if specified, addresses are saved, and control returns to the problem program.

If the last record read was not in the overflow area, the routine determines if all records in the block have been processed. If all the records in the block have not been processed, the I/C area pointer is updated to the next logical record. If that record is not a padding record, it is moved to the work area, if specified, addresses are saved, and control returns to the problem program. If it is a padding record, the EOF indicator is set in the DTFIS table, and control passes to the problem program.

If all records in the block have been processed, a check is made to determine if the PUT macro has been issued. If it has, the record is written. A test is made to determine if the prime data device type is a 2321. If it is a 2321 data cell, the upper limit for the strip is initialized, and then modified if the last record was on the next-to-last subcell.

A test is made to determine if the end of the track has been reached. If it has been reached, the track index is searched to find the next track index entry. The current track index record number in the DTF table is then updated, and a test is

made to determine if there is any overflow record indicated in the track index entry just read. If the track index entry indicates an overflow record is present, that overflow record is read, and moved to the work area, if specified. Control returns to the problem program.

If there is no overflow record indicated in the track index entry, the current address is updated by 1, and the record is read and moved into the work area, if specified. Control returns to the problem program.

If IOAREA2 has been specified to allow overlapping of I/O with processing, a test is made to determine if the last record read was in the overflow area. If the last record read was in the overflow area, a test is made to determine if the record being processed by the user is an overflow record. If it is an overflow record, a wait for I/O completion is made, the next available I/O area address is obtained, the current record address is saved, and a test is made to determine if the first record is being processed.

If the first record is being processed, and if track hold has been specified, the appropriate index and data tracks are held, the overflow record is read, addresses are saved, and the record is moved to a workarea, if specified. Any held tracks are released, and control returns to the problem program.

If the first record is not being processed, the address of the next overflow record is moved to the seek/search address and a test is made to determine if the end of the overflow chain has been reached. If the end has not been reached, and if track hold has been specified, the appropriate index and data tracks are held, the overflow record is read, addresses are saved and the record is moved to the work area, if specified. Any held tracks are released and control returns to the problem program.

If the end of the overflow chain has been reached, the current disk address is updated to the next track and the next record is read. The record is moved to the work area, if specified, addresses are saved, and control returns to the problem program.

If the last record was not an overflow record and the current record is not an overflow record, this routine determines if all the records in the block have been processed. If all records in the block have not been processed, the I/O area pointer is updated to the next logical record and the record counter is updated

by 1. If the next logical record is not a padding record, it is moved to the work area, is specified, addresses are saved, and control returns to the problem program. If it is a padding record, the EOF indicator is set in the DTF table and control returns to the problem program.

When all the records in the block have been processed, a check is made to determine if the PUT macro has been issued. If the PUT macro has been issued, the record is written. A test is made to determine if the prime data device type is a 2321. If it is a 2321, the upper limit for the strip is initialized to 9 and then modified to 5 if the last record was on the next-to-last subcell. A test for the presence of two I/O areas is then made. the presence of two I/O areas is indicated in the DTF table, a test is made to determine if the first record is being processed. If the first record is not being processed, a wait for I/O completion is made and the record address is saved in the DTF table.

A test is made to determine if the end of the track has been reached. If the end of the track has been reached, the track index is searched to find the next track index entry. The current track index
record number in the DTF table is updated. If HOLD is specified, the index track is held while a test is made to determine if there is any overflow record indicated in the track index entry just read. If the presence of an overflow entry is indicated, and there are two I/O areas present, this routine tests to determine if this is the first record. If it is not the first record, the record counter is set to 1. The overflow record address is moved to the seek/search address, the overflow record is read, held if HOLD=YES is specified, and moved to the work area, if specified. control then returns to the problem program.

If there is no overflow record indicated in the track index entry, the current address is updated by 1, the next record is read and moved to the work area, if specified, and control returns to the profilem program.

### ISAM ADDRTR: PUT Macro Chart JF

Objective: To perform the sequential retrieval output function for an indexed sequential file.

Entry: From the PUT macro expansion.

Exit: To the problem program via linkage
register 14.

Method: After initializing pointers to the DTFIS table, this routine tests whether a GET has been issued. If a GET has not been issued, there is an SVC 50 (error). Ctherwise, the GET issued switch is turned off and the output bit in the retrieval byte in the DTFIS table is turned on. The record is moved from the work area to the I/O area, and the output bit in the retrieval byte is set off.

If the record is unblocked, or is in the overflow area, this routine writes out the record and returns control to the problem program. If the records are blocked, this routine sets the bit in the retrieval byte to indicate that the PUT macro has been issued for this record, and returns control to the problem program. The GET macro routine causes the block to be written on DASC when it determines that all records in the block have been processed.

### ISAL TETLIA: LEAD Macro, KEY Chart JG

<u>Chjective</u>: To perform the random retrieval input function for an indexed sequential file by searching the indexes to determine the track on which the desired record is stored.

Entry: From the READ, KEY macro.

Exit: To the problem program via linkage
requister 14.

Method: This routine first initializes pointers and status bits in the DTFIS table. It then constructs the CCW chain to search the master or cylinder index (Figure 72). It then determines the highest level index (master or cylinder) being used, and test IDL RRREXT=YdS. If ERREXT is specified, additional error conditions can be returned to the problem program. This allows the user greater flexibility in attempting to continue processing. This routine them searches the highest level index to get the address of the mext index to be searched.

A test of the F code from the index level pointer is made to determine if the next search is of the track index. The F code refers to the index level just searched. If the master index was just searched, the next search is on the cylinder index. If the next search is not on the track index, the routine gets the index entry type and determines the routine to process that type.

If the entry type is a normal entry, the routine returns to search the next index. If the entry type is a dummy end entry or an inactive entry, the routine branches to an error routine to set a no-record-found flag in the DTF table and return to the problem program. If the entry is a dummy chained entry, the routine returns to search the index by using the address supplied by the 10-byte index level pointer.

When the next search is found to be on the track index, a test is made to determine if the track index takes up one track or more. If the track index does not require a full track, the routine builds a new CCW chain to search the track index (Figure 73). The routine issues the EXCP to search the track index, and returns control to the problem program without issuing a WAIT. The WAIT function is left to the WAITF macro, which must be issued before the user can process the record.

# ISAM\_ADDRIR: SETL\_Macro, \$\$ESFIL\_ Charts\_JH-JK

<u>Objective</u>: To initialize for sequential retrieval based on information supplied by the user in the SETL macro (BOF or ID).

Entry: From the SETL macro expansion.

Exit: To the problem program or to
\$\$BSETL1.

Method: This logical transient first validates the limits of the DTFIS table and IOAREAS to ensure that they lie within the partition. \$\$BSETL ther initializes for sequential retrieval based on the information supplied by the user in the SETL macro. The SETL macro specifies the type of reference used to identify the first record to be processed. The types of reference are:

- KEY. Key of starting record in the file.
- 2. GKEY. Iccation of starting record in the file, identified by a record key within a desired group. The user supplies a key that identifies the high order bytes of the required group of keys. For example, a GKEY specification of D6430000 would permit file processing to start at the first key containing D643XXXX, regardless of the characters represented by the Xs.
- 3. BOF. Beginning of logical file.

 ID (MEECCHHR). Location of starting record in the prime data area.

Ncte: References 1 and 2 are processed by
\$\$ESETL1.

If sequential retrieval is to begin with a record associated with a particular key (KEY), the key of the beginning record must be placed in the field defined by the DTFIS entry KEYARG before the SETI macro is issued. This phase searches the master index (if present), cylinder index and track index until it finds the track index entry associated with the specified key. It determines whether the record with the desired key is on a shared or unshared prime data track or in the overflow area.

If the record is on a shared track, the search is initialized to begin after the remainder of the track index has been bypassed. If the record is on a prime data track and records are unblocked, the track index everflow entry address associated with the desired record is calculated and stored in the DTFIS table, and the track is searched equal for the desired record. If the record is not found, a no-record-found indicator is set in the DTFIS table (Filename.C).

If the file contains blocked records, the track is searched equal/high for the desired block. The user must supply (in the DTFIS entry KFYLOC) the position of the key field in the data record. The block is searched. When the record with the matching key is found, its address is saved in the DTFIS table, and control returns to the problem program. If the record is not found, the no-record-found indicator is set in the DTFIS table (Filename.C).

If the record with the desired key is in the overflow area, the track index normal and cverflow entry addresses are stored in the DTFIS table, and the cverflow chain is searched for the desired record. When the desired record is found, its address is saved in the sequential retrieval section of the DTFIS table. If the desired record is not found in the overflow chain, a no-record-found indicator is set in the DTFIS table (Filename.C), and control returns to the problem program.

If GKEY was specified in the SETL macre, the CCW chain to read the desired record is modified to search key equal or high. The search for the desired record proceeds in the same manner as if KEY were specified in the SETL macro. However, in this case (GKEY), a no-record-found condition should not occur unless the key specified is higher than the existing highest key in the file.

If BOF was specified in the SETL macro, the address of the first prime data record in the file is saved in the sequential retrieval section of the DTFIS table, and the track index overflow entry address associated with the desired record is calculated and stored in the DTFIS table. Control returns to the problem program.

If the starting record address is referenced by a symbolic name in the SETL macro, this phase analyzes the 8-byte DASD address (MBBCCHHR) in the field specified by the symbolic name for validity. If the address is invalid, an illegal ID indicator is set in the DTFIS table (Filename.C), andcontrol returns to the problem program. If the starting address is valid, this phase saves the address in the DTFIS table, calculates the track index cverflow entry address associated with the desired record, stores it in the DTFIS table, and returns control to the problem program.

In order to perform a search of the master, cylinder or track indexes, prime data area, and overflow area for the starting record, a CCW string is built to search the required areas. Figures 87-90 give a description of the channel program built to perform the necessary search.

# ISAM ADDRIR: SFTL Macro, \$\$ESETL1 Charts JL-JC

<u>Objective</u>: To initialize for sequential retrieval based on information supplied by the user in the SETL macro (KEY or GKEY).

Entry: From \$\$BSETL.

Exit: To the problem program.

<u>Method</u>: This logical transient initializes for sequential retrieval based on the information supplied by the user in the SETL macro. The SETL macro specifies the type of reference used to identify the first record to be processed. The types of reference are:

- KEY. Key of starting record in the file.
- 2. GKEY. Location of starting record in the file, identified by a record key within a desired group. The user supplies a key that identifies the high order bytes of the required group of keys. For example, a GKEY specification of D6430000 would permit file processing to start at the first key containing D643XXXX, regardless of the characters represented by the Xs.

- 3. BOF. Beginning of the logical file.
- ID (MEECCHHR). Location of starting record in the prime data area.

Note: References 3 and 4 are processed by \$\$BSETL.

If sequential retrieval is to begin with a record associated with a particular key (KEY), the key of the beginning record must be placed in the field defined by the DTFIS entry KEYARG before the SETI macro is issued. This phase searches the master index (if present), cylinder index, and track index until it finds the track index entry associated with the specified key. It determines whether the record with the desired key is on a shared or unshared prime data track or in the overflow area.

If the record is on a shared track, the search is initialized to begin after the remainder of the track index has been bypassed. If the record is on a prime data track and records are unblocked, the track index overflow entry address associated with the desired record is calculated and stored in the DTFIS table, and the track is searched equal for the desired record. If the record is not found, a no-record-found indicator is set in the DTFIS table (Filename.C).

If the file contains blocked records, the track is searched equal/high for the desired block. The user must supply (in the DTFIS entry KEYLCC) the position of the key field in the data record. The block is searched. When the record with the matching key is found, its address is saved in the DTFIS table, and control returns to the problem program. If the record is not found, the no-record-found indicator is set in the DTFIS table (Filename.C).

If the record with the desired key is in the overflow area, the track index normal and overflow entry addresses are stored in the DTFIS table, the appropriate index and data tracks are held, and the overflow chain is searched for the desired record. When the desired record is found, its address is saved in the sequential retrieval section of the DTFIS table. If the desired record is not found in the cverflow chain, a no-record-found indicator is set in the DTFIS table (Filename.C), the held index and data tracks are released, and control returns to the problem program.

If GKEY was specified in the SETL macro, the CCW chain to read the desired record is modified to search key equal or high. The search for the desired record proceeds in the same manner as if KEY were specified in the SETL macro. However, in this case (GKEY), a no-record-found condition should

not occur unless the key specified is higher than the existing highest key in the file.

If BOF was specified in the SETI macro, the address of the first prime data record in the file is saved in the sequential retrieval section of the DTFIS table, and the track index overflow entry address associated with the desired record is calculated and stored in the DTFIS table. Control returns to the problem program.

If the starting record address is referenced by a symbolic name in the SETL macro, this phase checks the validity of the 8-byte DASD address (MBECCHHR) in the field specified by the symbolic name. the address is invalid, an illegal ID indicator is set in the DTFIS table (Filename.C), and control returns to the problem program. If the starting address is valid, this phase saves the address in the DTFIS table, calculates the track index overflow entry address associated with the desired record, stores it in the DTFIS table, holds the required index and data tracks, and returns control to the problem program.

Before I/O is performed, a switch is tested to see if track hold has been specified. If it has, the data track(s) and the corresponding index track(s) are held until I/O is complete. If any error conditions occur (for example, no record found, wrong length record, or a DASD read error), any held tracks are freed before returning to the user.

In order to perform a search of the master, cylinder, or track indexes, prime data area, and overflow area for the starting record, a CCW string is built to search the required area. Figures 87-90 give a description of the channel program built to perform the necessary search.

## ISAM ADDRTR: WAITF Macro Charts KA-KE

## Objective:

- To ensure that the last EXCP issued has been completed and that the condition is normal.
- 2. If the operation is a READ, to locate the specified record and to complete the transfer of data to the I/C area specified by the DTFIS entry IOAREAR, and to the specified work area if the DTFIS entry WORKR is included in the file definition.

- If the operation is a WRITE (NEWKEY), to complete the addition of the record to an indexed sequential file, adjusting the indexes and other records as necessary.
- If the operation is a WRITE (KEY), to return control to the problem program.

Entry: From the WAITF macro expansion.

Exit: To the problem program via linkage
register 14.

Method: This routine first tests for ERREXT=YES. If ERREXT is specified, additional error conditions can be returned to the problem program. This allows the user greater flexibility in attempting to continue processing. After initializing pointers to the three sections of the DTFIS table, this routine tests the status byte in the DTF table to determine if the condition so far is normal. If not, control returns to the problem program.

If the condition is normal, the routine issues a WAIT to determine if the EXCP issued by the READ or WRITE routines has been completed, and also tests for errors. Then, if the operation is a WRITE (KEY), this routine returns control to the problem program.

If the operation is a READ, this routine must complete the READ operation by moving the data to the I/O area. If first sets the address of the track on which the desired record is stored in the seek/search area, and initializes pointers to KEYARG and the I/O area. It also gets the relative key location and key length.

The routine picks up the index entry type (F ccde) from the search address and determines the routine to process that type. If the entry is a normal entry on an unshared track, a new CCW chain is built to find the record in the prime data area (Figure 74). If blocked records are specified, the CCW command code is modified to search high or equal. An EXCP and WAIT are issued to find the record and read the block into the I/C area. If records are unblocked, the record is moved into WORKR (if specified), and control returns to the problem program. If records are blocked, this routine tests to determine if the key specified in KEYARG is less than the key in the first logical record. If so, the record has not been found, and the corresponding bit is set on in the DTF table. Otherwise, the corresponding key is found within the block and the rcutine moves the block to WCRKR, if specified.

Control is then returned to the problem program.

For a normal entry on a shared track, the routine decreases the record number in the search address by 1, and builds a new CCW chain to find records on a shared track (Figure 75). Processing continues as in the routine to process a normal entry on an unshared track.

If the entry is an overflow end entry or an overflow chained entry, this routine first constructs a CCW chain to search the overflow chain. An EXCP and a WAIT are issued to locate the record in the overflow chain. A test is made to determine if the desired record has been found. If not, the routine tests for a overflow end entry. If so, the no-record-found bit is set on in the DTF table. If the entry is not an overflow end entry, the sequence-link field is inserted in the seek/search address, and the overflow chain is searched again.

If the record has been fcund, cverflow bits are set on in the DTF table, and the first nonoverflow record ccunt is increased by 1. The logical record is moved to WCRKR, if specified. Control returns to the problem program via register 14.

If the entry is a dummy end entry or an inactive entry, the routine sets a no-record-found bit on in the DTF table, and returns control to the problem program.

If the operation is a WRITE (NEWKEY), this routine determines the type of add function to be performed. The three types of add functions are:

- Normal add to the prime data area.
- Add to the overflow area.
- EOF add.

Normal Add to the Prime Data Area: This routine first determines if the record is to be added to the last prime data track. If it is and the last prime data track is full, the overflow record address is calculated, an EXCP (Figure 81) is issued to search and read the prime data track to determine the point of insertion, and a wait for I/C completion is made. Figures 72 through 107 describe the channel program builder for the ADDRTR function. If the addition is not on the last prime data track, the cverflow record address is calculated and the prime data track is searched to determine the point of insertion for the record to be added to the file. When an equal/high key is found during the search, the count and data

fields of that location are read into a save area in the DTF table and IOAREAL respectively.

A test is made to determine if the prime data in core option was specified as an ISMOD macro parameter. If it was specified, as many records as can fit into the I/O area specified in the DTFIS operand IOAREAL are read from the prime data track into main storage. The key of the record to be added is compared to the keys of the existing records in the I/C area. If a duplicate key is found, the condition is indicated to the user in the DTF table entry labeled Filename.C. If no duplicate key is found, the records are shifted in main storage leaving the record with the highest key remaining in the user's work area, WORKL. The other records are rewritten directly onto the track. remaining records on the track are then read into the I/O area. The process continues until the last record on the track is set up as an overflow record. When the last prime data record on the track has been rewritten, the new cverflow record is written in the overflow area, the track index normal and overflow entries and the COCR are written on DASD, and control returns to the problem program.

If the prime data in core option has not been specified as an ISMCD macro parameter, a test for blocked records is made. If the file contains unblocked records, the record previously found on the search key equal/high is reread to get the key field. If it is a duplicate key, a switch is set on in the DTFIS table indicating a duplicate key has been sensed and a return to the problem program is made. If there are no duplicate keys, the user's key and data are written from the work area, WCRKL, cnto the DASD file. The record in the I/O area, IOAREAL, replaces the user's record in the work area. The next record on the track replaces the one in the I/C area. This process is repeated until the end of track is reached.

If the end-of-file (EOF) record is read during the process of shifting the records over one record position, this routine writes the last record over the EOF record and then writes a new ECF record (Figures 80, 88, 89).

If the file contains blocked records, this routine reads the block of records (cr as many as fit in the I/C area if IOAREAL was increased for reading and writing more than one record at a time) into ICAREAL. The key field within each logical record is analyzed to determine the correct position in which to insert the new record. If there is duplication of keys, a switch is

set on in the DTFIS table and control returns to the problem program.

If the key of the record to be inserted (contained in WORKL) is low, it is exchanged with the record presently in the This procedure continues with each succeeding record in the block until the last record is moved into the work area. The key field of the DASD record is then updated to reflect the highest key in the block. If the size of IOAREAL has been increased, succeeding blocks in the I/O area are also updated. The block (or blocks) is then written back onto DASD. The remaining blocks on the track are similarly processed until the last logical record on the track is moved into WORKL. This record is then set up as an overflow record with the correct sequence-link field added and written in the overflow area. The sequence-link field for the new overflow record is taken from the track index overflow entry. The indexes are updated, and control returns to the problem program for the next record to be added. If the overflow area is full, this information is indicated to the user in the DTF table entry labeled Filename.C.

The track index normal entry key field is updated to the key of the new last record, the track index overflow entry data field is updated to the address of the new overflow entry (that entry has the lowest key for the overflow for that track), and the COCR is updated. These records are written on the DASD file before control returns to the problem program.

If the last block in the prime data area is padded, the last record to be shifted is included in that block. If the EOF record is read during the process of shifting the records one record position, the last record is written as a new block and a new ECF record is written before returning control to the problem program.

Add to the Cverflow Area: This rcutine computes the new overflow record address and reads the cverflow chain to get the address of the record with the next highest This address is stored in the sequence-link field of the new record. The new overflow record is then written in either the cylinder overflow area cr independent overflow area (Figure 87). these areas are full, this condition is indicated to the user in the DTFIS table entry labeled Filename.C. Each time an overflow record is added to the independent overflow area, an EOF record is written to maintain the integrity of the indexed

sequential file (Figure 90). The next cverflcw record followed by an ECF record overlays the previous EOF record.

If the new overflow record has the lowest key in the overflow chain, its address is used to build a new track index overflow entry. The new overflow entry is then written on the DASD file, and control returns to the problem program. If a cylinder cverflow condition occurs, the updated COCR (cylinder overflow control record) is written on DASD before control returns to the problem program.

If the new overflow record does not have the lowest key, the sequence-link field of the record with the next lower key is updated to contain the address of the new overflow record. This overflow record is then rewritten on DASD and the CCCR is updated (Figures 84-86). Control returns to the problem program.

EOF Add: This routine first determines if the last prime data track is full. If the last prime data track is not full, the new record is inserted on it. If the file contains blocked records and the record can fit in the last block, the block is read and the new record is inserted.

If the file is not blocked, or if it is blocked and the last block is full, a new last prime data record address is stored and the new record is written at that address. A new EOF record is then written.

If the last prime data track is full, the new record is inserted in the cverflcw area. The new overflow record address is computed and the record is written in the cverflcw area.

If an cverflow chain is present, the next lower record in the chain is found and the address of the new record is moved to the sequence-link field of the next lower

If no cverflow chain is present, the address of the new overflow record is moved to the track index overflow entry. The track index overflow entry is then written with the new high key. The master index (if present) and the cylinder index are updated with the new high key. A test for the cylinder index in core option is then made. If it has not been specified, control returns to the problem program. the cylinder index in core option has been specified, the new key is inserted into the appropriate index in core entry before returning control to the problem program.

#### ISAM ADDRTR: WRITE Macro, KEY Chart KF

<u>Objective</u>: To perform the random retrieval output function for an indexed sequential file.

Entry: From WRITE, KEY macro expansion.

Exit: To the problem program via register 14.

<u>Method</u>: This routine first sets the write bit in the DTFIS table. If then tests for an uncorrectable DASD error, wrong length record error, or no record found error. If any of these errors exist, the no-record-found bit is set on in the DTF table, and control returns to the problem program.

If there are no errors, the status byte in the DTF table is reset, and pointers to the DTF table are initialized. This routine then picks up the count field of the record as saved by the read routine, the address of WORKR, and the address of the logical record within the I/C area. The record, or block of records, is moved to the I/O area from WORKR (if specified). The CCW chain to write records is then built (Figure 77).

If the entry to be written is not an overflow entry, the byte count field in the write and verify CCW's is modified to the block length from the DTF table. This routine then issues the EXCP to write the record, and returns control to the problem program without issuing a WAIT. The WAIT function is left to the WAITF macro, which must be issued before the user can continue processing.

# ISAM ADDRIR: WRITE Macro, NEWKEY Charts EE-EF

<u>Objective</u>: To perform the necessary
initialization to add a record to a file.

Entry: From the WRITE, NEWKEY macro
expansion.

Exit: To the problem program via linkage register 14.

<u>Method</u>: After initializing the pointers to the three parts of the DTFIS table, this routine gets the starting address of the

highest level index, builds a CCW chain to search the highest level index (Figure 78) and tests for ERREXT=YES. If ERREXT is specified, additional error conditions can he returned to the problem program. This allows the user greater flexibility in attempting to continue processing. The channel program is executed and a wait for I/O completion is made. It then tests the F code of the index level pointer to determine if the next search is of the cylinder or track index. The F ccde refers to the index level just searched. If the master index was just searched, the next search is on the cylinder index. See Figure 17 for a description of the F code.

If the F code indicates a dummy chained entry , the search of the master, cylinder or track index continues. If the index level pointer did not indicate a dummy chained entry , a test for an inactive or dummy end entry is made. If an inactive or dummy end entry is indicated, the EOF add indicator is set on in the DTFIS table, a CCW chain is built to read the last track index entries, the channel program to typass the last of the track index entries is executed, a wait for the I/C operation to be completed is made, and control returns to the problem program. Processing continues with the record following the last key.

If an inactive or dummy end entry is not indicated, a test for the presence of a master index is made. If the master index is not present, indicating the cylinder index was just searched, a search of the track index is performed, and a return to the problem program is made.

If the master index is present, a test is made to determine if the cylinder index in core option was specified as an ISMCD macro parameter. If it was not specified, an EXCP is issued to search the cylinder index, followed by a wait for I/O completion, an EXCP to search the track index, a wait for I/C completion, and a return to the problem program. If the cylinder index in core option was specified, a search of the track index is performed, and a return to the problem program is made.

Any tracks which have been held during update are released before control returns to the user.

CCW Builder	CCW Built	Function
7461 	X'69', &KEYARG, CC and SLI, Key Length	Search key equal or high the master/cylinder index. Key supplied by user in the DTFIS table.
0C 6B	X'08', Pointer to *+16, CC and SLI, 5	TIC to *+16.
D17B	X'1A', <sup>2</sup> &Filename.W, CC, SLI, and SKIP, 5	Read home address into random/sequential retrieval work area in DTFIS table.
536C	X'92', &IOAREAR, CC and SLI, 10	Read count (multiple-track) into ICAREAR.
7461   	X'69', &KEYARG, CC and SLI, Key Length	Search key equal or high the master/cylinder index. Key supplied by user in DTFIS table.
046B	X'08', Pointer to *-16, CC and SLI, 5	TIC to *-16.
110C	X'06', <sup>2</sup> &Filenaπe.W, 00, 10	Read data (10-byte index level pcinter) into random/sequential retrieval area in DTFIS table.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 72. Channel program builder for ADDRTR -- CCW chain built to search master-cylinder index for random retrieve function.

CCW Builder  Control Code1	CCW Built	Function
D36B	X'1A', &IOAREAR, CC and SLI, 5	Read home address into IOAREAR.
9461	X'E9', &KEYARG, CC and SLI, Key Length	Search key equal or high (multiple track) track index. Key supplied by user in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
110C	X'06', <sup>2</sup> &Filename.w, 00, 10	Read data (10-byte index level pcinter) into randcm/sequential retrieval area in DTFIS table.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 73. Channel program builder for ADDRTR -- CCW chain built to search track index for random retrieve function.

CCW Builder    Control Code1	CCW Built	Function
7461 	X'69', &KEYARG, CC and SLI, Key Length	Search key equal or high in prime   data area. Key supplied by user in DTFIS     table.
0C4B	X'08', Pointer to *+16, CC, 5	TIC to *+16.
D17B	X'1A', <sup>2</sup> &Filename.W, CC, SLI, and SKIP, 5	Read home address into random/sequential retrieval area in DTFIS table.
406C	X'12', <sup>2</sup> &Filename.S+3, CC and SLI, 10	Read count into common seek/search area in DTFIS table.
6461   or   7461 	X'29' or X'69', &KEYARG, CC and SLI, Key Length	If records are unblocked, search key equal in prime data area. If records are blocked, search key equal or high in prime data area. Key supplied by user in DTFIS table.
044B	X'08', Pointer to *-16, CC, 5	TIC to *-16.
1302	X'06', &IOAREAR, 00, Block Length	Read data (block) containing starting record into IOAREAR.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 74. Channel program builder for ADDRTR -- CCW chain built to find record in prime data area (unshared track) for random retrieve function.

CCW Builder    Control Code <sup>1</sup>	CCW Built	Function
804B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal the prime data area using the pointer (CCHHK) in the common seek/search area in the DTFIS table.
064B	X'08', Pointer to *-8 CC, 5	IIC to *-8.
406C	X'12', <sup>2</sup> &Filename.S+3, CC and SLI, 10	Read count into common seek/search area in DTFIS table.
6461   or   7461	X'29' or X'69', &KEYARG, CC and SLI, Key Length	If records are unblocked, search key equal the prime data area. If records are blocked, search key high or equal the prime data are. Key supplied by user in DTFIS table.
044B	X'08', Pointer to *-16, CC, 5	TIC to *-16.
1302	X'06', &IOAREAR, 00, Block Length	Read data (block) containing record into IOAREAR.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 75. Channel program builder for ADDRTR -- CCW chain built to find record in prime data area (shared track) for random retrieve function.

CCW Builder    Control Code¹	CCW Built	Function
804B	x'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal the overflow chain using the pointer (CCHHR) in the ccmmcn seek/search area in the DTFIS table.
064B	X'08', Pointer tc *-8, CC, 5	TIC to *-8.
6461	X'29', &KEYARG, CC and SLI, Key Length	Search key equal the overflow chain. Key supplied by user in DTFIS table.
116C	X'06', <sup>2</sup> &Filename.W, SII, 10	Read data (10-byte sequence link field) into random/sequential retrieval area in ETFIS table. This CCW is executed when the required overflow record is not found in the overflow chain.
1303	X'06', &IOAREAR, 00, Record Length + 10	Read data (sequence link field plus   logical record) into IOAREAR. This CCW is executed when the matching key is found in the cverflcw chain.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 76. Channel program builder for ADDRTR -- CCW chain built to find record in cverflow chain fcr random retrieve function.

CCW Builder  Control Code1	CCW Built	Function
804B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal prime data area using pointer (CCHHR) in common seek/search area in DTFIS table.
064B	X'08', Pointer to *-8, CC, 5	TIC to *-8.
B3C3	X'05', &IOAREAR, CC, Record Length + 10	Write data from ICAREAR.
804B	X'31, <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal the grime data area, using pointer (CCHHR) in ccmmon seek/search area in DTFIS table.
064B	X'08', Pointer to *-8, CC, 5	TIC to *-8
1333	X'06', &IOAREAR, SII and SKIP, Record Length + 10	Read data to verify record just written. Information is not transferred to main storage.

 $<sup>^{1--7}</sup>$  See notes 1 through 7 in Figures 106 and 107.

Figure 77. Channel program builder for ADDRTR -- CCW chain built to write record for random retrieve function.

CCW Builder  Control Code <sup>1</sup>	CCW Built	Function
7961 	X'69', &KEYARG, CC and SLI, Key Length	Search key equal or high the master/cylinder index. Key supplied by user in the DTFIS table.
0C6B	X'08', Pointer to *+16, CC and SLI, 5	TIC to *+16.
D1 <b>7</b> B	X'1A', <sup>2</sup> &Filename.D+8, CC, SLI and SKIP, 5	Read home address into work area for the current track index normal entry count field in the DTFIS table.
516C	X'92', <sup>2</sup> &Filenaπe.D+8, CC and SLI, 10	Read count (multiple track) into work area for the current track index normal entry count field in the DTFIS table.
7961   	X'69', &KEYARG, CC and   SLI, Key Length	Search key equal or high the master/cylinder index. Key supplied by user in the DTFIS table.
046B	X'08', Pointer to *-16, CC and SLI, 5	TIC to *-16.
150C	X'06', <sup>2</sup> &Filenaπe.D+40, 00, 10	Read data (next 10-byte index level pointer) into work area for track index normal entry data field in DTFIS table.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 78. Channel program builder for ADDRTR -- CCW chain built to search master cylinder index for add function.

CCW Builder    Control Code1	CCW Built	Function
8C4B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal the track index using the pointer (CCHHR) in the ccmmcn seek/search area.
066B	X'08', Pointer to *-8, CC   and SLI, 5	TIC to *-8.
106C	X'06', <sup>2</sup> &Filenaπe.D, CC and SLI, 10	Read data (COCR record) into the Cylinder cverflow record (CCCR) area.
516C	X'92', <sup>2</sup> &Filenaπe.D+8, CC and SLI, 10	Read count (multiple track) into work area for the current track index normal entry count field in the DTFIS table.
7941	X'69', &KEYARG,CC, Key Length	Search key equal or high the track index. Key supplied by user in the DTFIS table.
046B	X'08', Pointer to *-16, CC and SLI, 5	TIC to *-16.
156C	X'06', <sup>2</sup> &Filename.D+40, CC and SLI, 10	Read data (next 10-byte pointer to prime data record) into work area for track index normal entry data field in DTFIS table.
526C	X'92', <sup>2</sup> &Filename.D+16, CC and SLI, 10	Read count (multiple track) into work area for current track index overflow entry count field in DTFIS table.
1D0C	X'06', ²εFilenaπe.w, 00, 10	Read data (10-byte overflow entry) into random/sequential retrieval work area.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 79. Channel program builder for ADDRTR -- CCW chain built to search track index for add function.

CCW Builder  Control Code1	CCW Built	Function
8C4B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal for the last prime data record address using pointer, CCHHR, in common seek/search area in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
342C	X'10', <sup>2</sup> &Filename.D+32, SLI, 10	Write count, key and data of ECF record located in current overflow record count field in DTFIS table.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 80. Channel program builder for ADDRTR -- CCW chain built to write new ECF record for add function.

CCW Builder	CCW Built	Function
8C4B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier the prime data area using pointer, CCHHR, in common seek/search area in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
436C	X'12', <sup>2</sup> &Filename.D+24, CC   and SLI, 10	Read count for current prime data record.
7941 	X'69', &KEYARG, CC, Key   Length 	Search key equal or high the prime data area. Key supplied by user in DTFIS table.
046B	X'08', Pointer to *-16, CC and SLI, 5	TIC to *-16.
1B02	X'06', Address of ICAREAL+8 +KEYLEN, 00, Blcck Size	Read data (prime data block) into IOAREAL+8+Key Length.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 81. Channel program builder for ADDRTR -- CCW chain built to find prime data record for add function.

CCW Builder    Control Code¹	CCW Built	Function
8C4B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal the track index using pointer, CCHHR, in common seek/search area in DTFIS table.
066В	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
B06C	X'05', <sup>2</sup> &Filename.D, CC and SLI, 10	Rewrite COCR located in cylinder cverflow control record work area in DTFIS table.
E14B	X'B1', <sup>2</sup> &Filename.D+8, CC, 5	Search identifier equal (multiple track) for the pointer, CCHHR, in the normal entry count field.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
2A45	X'0D', Address cf IOAREAL+8, CC, Key Length + 10	Rewrite track index normal ertry located at IOAREAL+8.
E24B	X'B1', <sup>2</sup> &Filename.D+16, CC, 5	Search identifier equal (multiple track) for the pointer, CCHHR, in the cverflcw entry ccunt field.
066в	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
BDCC	X'05', <sup>2</sup> &Filename.W, CC and DC, 10	Rewrite overflow entry located in random/sequential retrieval work area.
824B	X'31', <sup>2</sup> &Filenaπe.D+16, CC, 5	Search identifier equal for the pointer, CCHHR, in the overflow entry count field.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
1D3C	X'06', <sup>2</sup> &Filename.W, SII and SKIP, 10	Read data to verify record just written. Information is not transferred to main stcrage.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Channel program builder for ADDRTR -- CCW chain built to rewrite track index entry for add function. Figure 82.

CCW Builder Control Code1	CCW Built	Function
8C4B	X'13', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal for RO using pointer, CCHHR, in common seek/search area in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
B06C	X'05', <sup>2</sup> &Filenaπe.D, CC and SLI, 10	Write data (updated COCR) from the cylinder overflow control record (COCR) area in the DTFIS table.
E14B	X'B1', <sup>2</sup> &Filename.D+8, CC, 5	Search identifier equal (multiple track) the track index using the pointer, CCHHR, in the work area for the current track index normal entry count field.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
BDCC	X'05', <sup>2</sup> &Filename.W, CC and DC, 10	Write data (track index cverflow entry) from the random/sequential retrieval wcrk area.
814B	X'31', 2&Filename.D+8, CC, 5	Search identifier equal the track index using the pointer, CCHHR, in the work area for the current track index normal entry count field.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
1D3C	X'06', <sup>2</sup> &Filenaπe.W, SLI and SKIP, 10	Read data to verify record just written. Information is not transferred to main storage.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 83. Channel program builder for ADDRTR -- CCW chain built to write track index entry for add function.

CCW Builder    Control Code1	CCW Built	Function
8C4B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal for RO using pointer, CCHHR, in common seek/search areal in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
B02C	X'05', <sup>2</sup> &Filename.D, SLI, 10	Write data (updated COCR) from the cylinder cverflow control record area in the DTFIS table.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 84. Channel program builder for ADDRTR -- CCW chain built to write COCR for add function.

CCW Builder    Control Code1	CCW Built	Function
8C 4B	x'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal using pointer, CCHHR, in common seek/search area in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
AA07	X'0E', Address cf IOAREAL+8, 00, Key Length + Record Length + 10	Read key and data of previously lcw cverflcw record into IOAREAL+8.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 85. Channel program builder for ADDRTR -- CCW chain built to read previous overflow record for add function.

CCW Builder  Control Code <sup>1</sup>	CCW Built	Function
8C4B	X'31', <sup>2</sup> &Filenaπe.S+3, CC, 5	Search identifier equal using pointer, CCHHR, in common seek/search area in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
2A47	X'OD', Address of ICAREAL+8, CC, Key Length + Record Length + 10	Write key and data of previously low overflow record located at IOAREAL+8.
8C4B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal using pointer, CCHHR, in common seek/search area in DTFIS table.
066В	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
AA37	X'0E', Address cf IOAREAL+8, SLI and SKIP, Key Length + Record Length + 10	Read key and data to verify record just written. Information is not transferred to main storage.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 86. Channel program builder for ADDRTR -- CCW chain built to write previous overflow record for add function.

CCW Builder  Control Code <sup>1</sup>	CCW Built	Function
8C4B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal for last cverflow record address using pointer, CCHHR, in common seek/search area in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
3 <b>7</b> C9	X'1D'. Address of ICAREAL, CC and DC, Key Length + Record Length + 18	Write count, key and data of new overflow record located at IOAREAL.
8C4B	X'31', 26Filenaπe.S+3, CC, 5	Search identifier equal for last overflow record using pointer, CCHHR, in common seek/search area in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
C <b>7</b> 39	X'1E', Address of ICAREAL,   SLI and SKIP, Key Length +     Record Length + 18	Read count, key and data to verify record just written. Information is not transferred to main storage.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 167.

Figure 87. Channel program builder for ADDRTR -- CCW chain built to write new overflow record for add function.

CCW Builder     Control Code <sup>1</sup>	CCW Built	Function
8C4B     	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal for present ECF record address minus one using pointer, CCHHR, in common seek/search area in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
37C8	X'1D', Address cf IOAREAL,   CC and DC, Key Length +   Elock Size + 8	Write count key and data of new record to be added located at IOAREAL.
8C4B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal for present EOF record address minus one using pcinter, CCHHR, in common seek/search area in DTFIS table.
066в 	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
C738	X'1E', Address of ICAREAL,   SLI and SKIP, Key Length +   Block Size + 8	Read ccunt, key and data to verify record just written. Information is not transferred to main storage.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 88. Channel program builder for ADDRTR -- CCW chain built to write over EOF record (blccked records) for add function.

CCW Builder    Control Code1	CCW Built	Function
8C4B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal for present ECF record address minus one using pcinter, CCHHR, in common seek/search area in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
37C8	X'1D', Address of ICAREAL, CC and DC, Key Length + Block Size + 8	Write count, key and data of new record to be added, located at IOAREAL.
8C4B	X'31', <sup>2</sup> &Filenaπe.S+3, CC, 5	Search identifier equal for present EOF record address minus one using pointer, CCHHR, in common seek/search area in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
C738	X'1E', Address cf IOAREAL, SLI and SKIP, Key Length + Block Size + 8	Read count, key and data to verify record just written. Information is not transferred to main storage.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 89. Channel program builder for ADDRTR -- CCW chain built to write over EOF record (unblocked records) for add function.

CCW Builder Control Code <sup>1</sup>	CCW Built	Function
8C4B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal for present EOF record address minus one using pointer, CCHHR, in common seek/search area in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
37AC	X'1D', Address cf IOAREAL, DC and SLI, 10	Write count, key and data of ECF record   located at IOAREAL.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 167.

Figure 90. Channel program builder for ADDRTR -- CCW chain built to write ECF record in independent overflow area for add function.

CCW Builder  Control Code <sup>1</sup>	CCW Built	Function
8C4B	X'31', <sup>2</sup> &Filenaπe.S+3, CC, 5	Search identifier equal the track index using the rointer (CCHHR) in the common seek/search area.
066B	X'08', Pointer to *-8, CC and SLI, 5	IIC to *-8.
106C	X'06', <sup>2</sup> &Filename.D, CC and SLI, 10	Read data (COCR record) into the cylinder overflow control record (COCR) area.
E14B	X'B1', <sup>2</sup> &Filename.D+8, CC, 5	Search identifier equal (multiple track) the track index for the last normal entry using information in the work area for the current track index normal entry count field.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
154C	X'06', <sup>2</sup> &Filename.D+40, CC, 10	Read data (last track index normal entry) into work area for track index normal entry data field.
526C	X'92', <sup>2</sup> &Filenaπe.D+16, CC and SLI, 10	Read count (multiple track) cf last track index overflow entry into work area for the current track index overflow entry count field.
1D0C	X'06', <sup>2</sup> &Filenaπe.w, 00, 10	Read data (last track index cverflcw entry) into random/sequential retrieval work area.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 91. Channel program builder for ADDRTR -- CCW chain built to read last track index entry for add function.

CCW Builder Control Code <sup>1</sup>	CCW Built	Function
8C4B	х'31', <sup>2</sup> &Filename.S+3, СС, 5	Search identifier equal the cverflcw chain using the rointer (CCHHR) in the common seek/search area.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
AA07	X'OE', Address of ICAREAL+8, 00, Key Length + Record Length + 10	Read key and data of overflow record into IOAREAL+8.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 92. Channel program builder for ADDRTR -- CCW chain built to read overflow record for add function.

CCW Builder Control Code <sup>1</sup>	CCW Built	Function
8C4B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal for last prime data record using pointer, CCHHR, in common seek/search area in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
1B02	X'06', Address of ICAREAL+ 8+KEYLEN, 00, Blcck Size	Read block into ICAREAL + 8 + KEYLEN.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 93. Channel program builder for ADDRTR -- CCW chain built to read last prime data record for add function.

CCW Builder  Control Code1	CCW Built	Function
8C4B	X'31', <sup>2</sup> &Filenaπe.S+3, CC, 5	Search identifier equal for last prime data record using pointer, CCHHR, in common seek/search area in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
2AC6	X'OD', Address of ICAREAL+8, CC and DC, Key Length + Block Size	Write key and data of prime data block located at IOAREAL+8.
8C4B	X'31', <sup>2</sup> &Filenaπe.S+3, CC, 5	Search identifier equal for last prime data record using pointer, CCHHR, in common seek/search area in DTFIS table.
ј 066в ј	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
AA36	X'0E', Address of ICAREAL+8, SLI and SKIP, Key Length + Block Size	Read key and data to verify record just written. Information is not transferred to main storage.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Channel program builder for ADDRTR -- CCW chain built to write block of prime data records and verify for add function. Figure 94.

CCW Builder  Control Code1	CCW Built	Function
8C4B	X'31', <sup>2</sup> &Filenaπe.S+3, CC, 5	Search identifier equal for last track index entry using pointer, CCHHR, in common seek/search area in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
2A 45	X'OD', Address of ICAREAL+8, CC, Key Length + 10	Write key and data of track index normal entry located at IOAREAL+8.
E24B	X'B1', <sup>2</sup> &Filename.D+16, CC, 5	Search identifier equal (multiple track) the track index for the last everflow entry using the count for the current track index overflow entry.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
2845	X'OD', Address of WCRKL, CC, Key Length + 10	Write key and data of track index overflow entry located at WORKL.
824B	X'31', <sup>2</sup> &Filename.D+16, CC, 5	Search identifier equal the track index for the last overflow entry using the cunt for the current track index overflow entry.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
A835	X'0E', Address of WCRKL, SLI and SKIP, Key Length + 10	Read key and data to verify record just written. Information is not transferred to main storage.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 95. Channel program builder for ADDRTR -- CCW chain built to write track index entry for add function.

CCW Builder    Control Code1	CCW Built	Function
8C4B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal the master/cylinder index using the pcinter, CCHHR, in the common seek/search area in the DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
150C	X'06', <sup>2</sup> &Filename.D+40, 00, 10	Read data (index entry) into work area for track index normal entry data field.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 96. Channel program builder for ADDRTR -- CCW chain built to read index entry for add function.

CCW Builder    Control Code1	CCW Built	Function
8C4B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal the master/cylinder index using pointer, CCHHR, in common seek/search area in DTFIS table.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
2A45	X'OD', Address of ICAREAL+8, CC, Key Length + 10	Write key and data of master/cylinder index entry located at IOAREAL+8.
8C4B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal the master/cylinder index using pointer, CCHHK, in common seek/search area in DTFIS table.
AA35 	X'OE', Address of ICAREAL+8,   SLI and SKIP, Key Length   + 10	Read key and data to verify record just written. No information is transferred to main storage.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 97. Channel program builder for ADDRTR -- CCW chain built to write index entry for add function.

CCW Builder	CCW Built	Function
8C4B   	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal the track index using the pointer, CCHHR, in the ccmmcn seek/search area in the CTFIS table.
066B	X'08', Pointer to *-8, CC   and SLI, 5	TIC to *-8.
B06C	X'05', <sup>2</sup> &Filename.D, CC and SLI, 10	Write data (COCR) from the cylinder cverflow control record work area in DTFIS table.
E24B	X'B1', <sup>2</sup> &Filename.D+16, CC, 5	Search identifier equal (multiple track) the track index using the pointer, CCHHR, in the work area for current track index overflow entry count field.
066B	X'08', Pointer to *-8, CC and SLI, 5	TIC to *-8.
AA35	X'OE', Address of ICAREAL+8, SLI and SKIP, Key Length + 10	Read key and data to verify record just written. Information is not transferred to main storage.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 98. Channel program builder for ADDRTR -- CCW chain built to write track index cverflow entry fcr add function.

CCW P	Parameter <sup>3</sup>	CCW Built	Function
0   	0540	X'31', <sup>2</sup> &Filenaπe.S+3, CC, 5	Search identifier equal the prime data area using the pointer (CCHHR) in the common seek/search area in the DTFIS table.
		  X'08', Pointer to *-8, CC, 0	TIC to *-8.
		X'05',4 &IOAREAS, CC,5 Block Length <sup>6</sup>	Write data (block) onto prime data area.
     		X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal to verify write operation.
   		X'08', Pointer to *-8, CC, 0	TIC to *-8.
   		X'06',4 &IOAREAS, SKIP, Block Length <sup>6</sup>	Read data to verify write operation.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 99. Channel program builder for ADDRTR -- CCW chain built to write records for sequential retrieve function.

CCW	Parameter <sup>3</sup>	CCW Built	Function
	0601	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal the track index area, using the pointer (CCHHR) in the common seek/search area in the DTFIS table.
		X'08', Pointer to *-8, CC, 0	TIC to *-8.
	1	X'06', <sup>2</sup> &Filename.W, CC, <sup>5</sup> 10  	Read data (10-byte index level pcinter) into random/sequential retrieval area in DTFIS table.
		X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal to verify read cperation.
		  X'08', Pointer to *-8, CC, 0	TIC to *-8.
		X'06', &IOAREAS, SKIP, Block Size	Read data to verify read operation.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 100. Channel program builder for ADDRTR -- CCW chain built to search track index for sequential retrieve function.

CCW Paramet	er³  CCW Built	Function
0600   	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal the prime data area using the pointer (CCHHR) in the common seek/search area in the DTFIS table.
	X'08', Pointer to *-8, CC, 0	TIC to *-8.
   	X'06',4 &IOAREAS, CC,5  Elock Length6	Read data into IOAREAS.
[ [ [	X'31', <sup>2</sup> &Filenaπe.S+3, CC, 5	Search identifier equal to verify read cperation.
	X'08', Pointer to *-8, CC, 0	TIC to *-8.
   	X'06', &IOAREAS, SKIP,  Block Length <sup>6</sup>	Read data to verify read operation.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 101. Channel program builder for ADDRTR -- CCW chain built to read record for sequential retrieve function.

Label	CCW Builder <sup>7</sup>  Control Code		Function
	7441 	X'69', &KEYARG, CC, Key  Length	Search key equal or high the  master/cylinder index. Key supplied  by user in DTFIS table.
	0C40	X'08', Pointer to *+16, CC,  Record Length	TIC to *+16.
	B04B	X'1A', <sup>2</sup> &Filename.S+3, CC, 5	Read home address into common seek/search area in DTFIS table.
	506B	X'92', <sup>2</sup> &Filename.S+3, CC  and SII, 5 	Read count (multiple track) -  CCHHR - into common seek/search area  in DTFIS table.
	7441	X'69', &KEYARG, CC, Key  Length	Search key equal or high the  master/cylinder index. Key supplied  by user.
	0440	X'08', Pointer to *-16, CC,  Record Length	TIC to *-16.
	110C	X'06', <sup>2</sup> &Filename.W, 00, 10   	Read data (10-byte index level   pointer) into random/sequential   retrieval area in DTFIS table. The   data field is then moved from the   random/sequential retrieval area to   the common seek/search area for the   next search.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 102. Channel program builder for ADDRTR -- CCW chain built by \$\$BSETL (1) to search MI for sequential retrieve function.

  Label	CCW Builder' Control Code	CCW Built	Function
	806C	X'31', <sup>2</sup> &Filename.S+3, CC and SII, 10	Search identifier equal the track index using the 10-byte pointer in the common seek/search area.
	0640	X'08', Pointer *-8, CC, Record Length	TIC to *-8.
	126C	X'06', &IOAREAS, CC and SLI, 10	Read data (10-byte track index pointer) into IOAREAS, input/output area for sequential retrieval supplied by user.
	506B	X'92', <sup>2</sup> &Filename.S+3, CC  and SII, 5 	Read count (multiple track) - CCHHR - into common seek/search area in DTFIS table.
	7441	X'69', &KEYARG, CC, Key Length	Search key equal or high the track index. Key supplied by user.
	0240	X'08', Pointer to *-24,  CC, Reccrd Length	TIC tc *-24.
	1100	X'06', <sup>2</sup> &Filename.W, 00, 10      -  -	Read data (10-byte pcinter) into random/sequential retrieval area in ETFIS table. The data field is then moved from the random/sequential retrieval area to the common seek/search area for the next search.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 103. Channel program builder for ADDRTR -- CCW chain built by \$\$BSETL (1) to search TI for sequential retrieve function.

Label	CCW Builder'   Control Code	CCW Built	Function
STRI1	804B	x'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal the prime data area using the pointer (CCHHR) in the connon seek/search area in the DTFIS table.
	066B	X'08', Pointer to *-8 ,CC and SLI, 5	TIC tc *-8.
	416C	X'12', <sup>2</sup> &Filename.W, CC  and SLI, 10	Read ccunt into common seek/ search area in the DTFIS table.
	6441 or 7441	X'29' or X'69', &KEYARG,  CC, Key Length	If KEY is specified in the SETL macro and/or records are unblocked, this CCW searches key equal the prime data area. If GKEY is specified in the SETI macro and/or records are blocked, this CCW searches key equal or high the prime data area for the starting record.
	046B	X'08', Pointer to *-16, CC and SLI, 5	TIC tc *-16.
   	1202	X'06', &ICAREAS, 00,  Block Size	Read data (block) containing     starting record into IOAREAS.

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 104. Channel program builder for ADDRTR -- CCW chain built by \$\$BSETL (1) to find first record in prime data area for sequential retrieve function.

Label	CCW Builder  Control Code7	CCW Built	Function
STRI3	804B	X'31', <sup>2</sup> &Filename.S+3, CC, 5	Search identifier equal the overflow chain using the pointer (CCHHR) in the common seek/search area in the DTFIS table.
i 	0640	X'08', Pointer to *-8, CC, Record Length	TIC to *-8.
	6441 or 7441	X'29' or X'69', &KEYARG, CC, Key Length	If KEY is specified in the SETI macro, this CCW searches key equal the overflow chain for the starting record. If GKEY is specified in the SETL macro, this CCW searches key equal or high the overflow chain for the starting record.
	112C	X'06', <sup>2</sup> &Filename.W, SLI, 10	Reads data (10-byte sequence link field) into random/sequential retrieval area in DTFIS table. This CCW is executed when the required overflow record is not found in the cverflow chain.
	1203	X'06', &IOAREAS, 00, Record Length +10	Read data (sequence link field

<sup>1--7</sup> See notes 1 through 7 in Figures 106 and 107.

Figure 105. Channel program builder for ADDRTR -- CCW chain built by \$\$BSETI (1) to find first record in cverflow chain for sequential retrieve function.

#### Note 1:

The CCW builder control code references information in the DTF DSECT section of the ISMOD assembly.

The first character of the control code references an operation code at IJHCSTRI.

The second character of the control code references a data area at either IJHCASAD for random retrieve function or IJHAHRAA for add function.

The third character of the control code references the following information:

<u>Control Character</u>	CCW Flag Field	<u>Meaning</u>
0	x • 00 •	End of CCW Chain
2	x • 20 •	SLI (Suppress Length Indicator)
3	x'30'	SLI and SKIP (Suppress data transfer)
<b>j</b> 4	X 40 4	CC (Command Chaining)
6	X • 60 •	CC and SLI
j 7	x <b>'7</b> 0'	CC, SLI, and SKIP
À A	X * A O *	SLI and DC (Data Chaining)
j c	X	CC and DC

The fourth character of the control code references a byte count (length) field at IJHCRESZ.

#### Note 2:

&Filename = DTF name supplied by user.

&Filename.X = X is suffix supplied by DTFIS for unique DTF labels.

#### Note 3:

The CCW parameter is located in the ISMOD assembly.

The first byte of the parameter is the command code.

The second byte of the parameter contains flags with the exception of the chain to search the track index. In this case, the second byte is an indicator to the channel program builder that the CCW chain is to search the track index.

Note 4: If the file contains unblocked records, the command code is modified to Read Key and Data, or Write Key and Data.

Note 5: If the verify option has not been specified, the command chaining bit is not set on.

Note 6: If the file contains unblocked records, the byte count field contains the physical record length plus Key Length.

Figure 106. Channel program builder for ADDRTR -- notes 1-6.

#### Note 7:

The CCW chains are built by the B-transients, \$\$BSETL and \$\$BSETL1. The CCW builder control code references information in the \$\$ESETL and \$\$BSETL1 assemblies.

The first character of the control code references an operation code at IJHROP.

The second character of the control code references a data area at IJHARA.

The third character of the control code references the following information:

Control Character	<u>CCW Flag Field</u>	<u>Meaning</u>
0	x'00'	End of CCW chain
2	X * 2 0 *	SLI (Suppress Length Indicator)
4	x • 40 •	CC (Command Chaining)
6	x'60'	CC and SLI

The fourth character of the control code references a byte count (length) field at REINT.

Figure 107. Channel program builder for ADDRTR -- note 7.

# ISAM INITIALIZATION AND TERMINATION PROCEDURES

When files are opened for indexed sequential (DTFIS) and the file is on more than one volume, all volumes must be opened, before processing of the file begins. All labels are checked/written at the initial file open.

Job control accepts label information previously supplied on VOL, DLAB, and XTENT statements (not valid for the 3330 family) as well as information on the simplified DLBI and EXTENT statements. Jcb control reads the DASD label information supplied on these statements, and stores the information on the SYSRES label information cylinder. The open monitor logical transient, \$\$BOPEN2, reads the DASD label information from SYSRES into the label save area in main storage for use by the ISAM open/close logical transients. See DOS/VS LICCS Volume 1, SY33-8559, for the format of the SYSRES DASD label information.

The extents in the SYSRES DASD label information record are checked for overlap on each other. If overlap exists, a message is issued, and the job is canceled. Checks are made to determine if all the correct packs are mounted, if serial

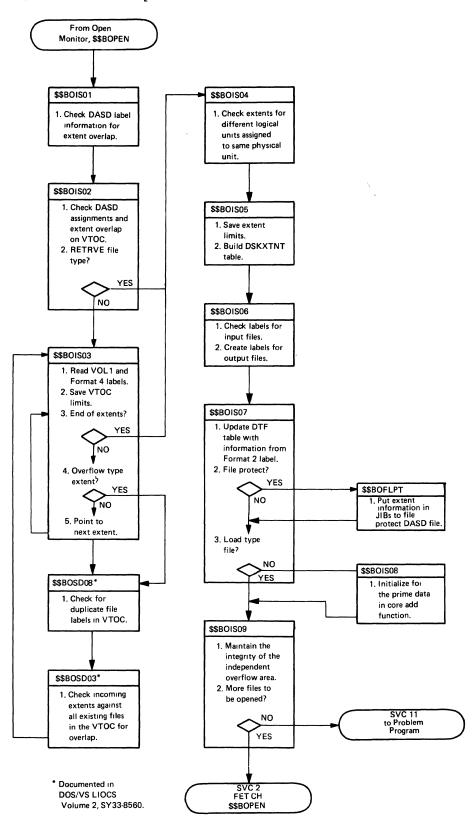
numbers match and if any extent limits overlap the VTOC. The extents for the master index (if specified) and cylinder index are checked to determine if they are contiguous, and the limits are saved. The routine checks the prime data extents for continuity, and a check is made for the last prime data extent. The overflow extents are checked and saved for future reference.

For output, file labels are created and written in their appropriate locations and sequence, and the extent information is inserted in the labels. The format-2 label for the file is read, and the DTF table is updated for each function. A DSKXTN table is created, which is located at the end of the DTF table. It contains the logical unit and cell number for each extent. This table is used by logical IOCS to reference the extent information in the DTF table.

When the file is closed, the format-1 and format-2 labels are updated from the DTF tables. Then the open switch is set off, and control returns to the close monitor or to the user.

For a more detailed description of DOS/VS lakel handling see <u>DOS/VS\_LIOCS</u> Volume 1, SY33-8559.

Chart 02. ISAM Open



For input and output files, the initial steps to open a file are the same. The SYSRES DASD label information is set up, and the extents are checked for overlap on each other and the VTOC. Then, a check is made to determine if all the packs for the file have been mounted by checking all volume labels against the SYSRES DASD label information in the label save area in main storage.

If the file is output, the volume and format-4 labels are read; the VTCC limits are saved; and the routine is initialized to search the VTOC. If the extent limits need to be checked against the VTOC limits, the sequential disk open routine, \$\$BOSDO8, is called in to do the checking. From this point, the input/output logic is similar. The extents are checked against the SYSRES DASD label information, the extent limit groups are saved, and the DSKXTN (logical unit and cell number) table at the end of the DTF table is built. The labels are checked, and the extent limits are inserted for input files. For cutput, the labels are created and written. The DTF tables are updated, and the routine returns control to the problem program or to the open monitor.

For input and output files the steps to close a file are the same. The format-1 and format-2 labels are updated and written back in the VTOC. Control returns to the problem program or to the close monitor.

## \$\$BCIS01: ISAM Open, Phase 1 Charts LA-LB

<u>Objective</u>: To check the DASD lakel information to determine if the extents overlap.

Entry: From the open monitor, \$\$BCPEN.

Exit: To \$\$BOIS02 or to \$\$BOMSG1 (if an error condition occurs).

Method: This phase determines the address of the first and last extent in the SYSRES DASD label information in the label save area. It determines if this is a creation of a file, and turns on an indicator if it is. The phase then clears the reserved field in the SYSRES DASD label information record.

This phase checks the extent limits for an overlap condition. If an overlay condition occurs, the routine initializes message 4240I and fetches the message

writer, \$\$BOMSG1, to write the message on SYSLOG. If it is an ADD or ADDRIR type file, this phase computes the number of tracks of the independent overflow extent limits on either the 2311, 2321, 2314, or 2319 devices and stores the result. It then fetches \$\$BOISO2.

## \$\$BOISC2: ISAM Open, Phase 2 Charts LC-LD

Objective: To determine if all the correct disk packs are mounted, if a DASD has been assigned to the file, if the format-5 label indicator is on, and if the extent limits overlap the VTOC.

Entry: Frcm \$\$BOIS01.

Exits: Tc \$\$BOIS03, or to \$\$BCMSG1 (if an error condition occurs).

Method: This phase checks to determine if a DASD has been assigned to the file. If a DASD has not been assigned, message 4283I is initialized and \$\$BCMSG1 is fetched to write the message on SYSLOG. If a DASD has been assigned, the VCL1 lakel is read. If no record is found, message 4206I is initialized and \$\$BCMSG1 is fetched to write the message on SYSLOG.

A check for correct pack mounted is then made and the format-4 lakel is read and checked. This phase determines if the extent limits overlap the VTCC. If they do, message 4241I is initialized and the message writer, \$\$BOMSG1, is fetched. If they do not overlap, a check for more extents is made. When all extents have been checked, this phase fetches \$\$BOISO3.

#### \$\$BOIS03: ISAM Open, Phase 3 Charts LE-LF

Ctjective: To read VOL1 and format-4 labels as initialization for the VTOC check phase, \$\$BCSDO8, and to set up the CCW chain and address of the format-4 labels.

Entry: From \$\$BOIS02 or \$\$BCSDC3.

Exit: To \$\$BOIS04, \$\$BOSD08 or \$\$BOMSG1
(if no record is found during read).

Method: This phase makes a series of tests to determine if entry was from \$\$BOSDO3, if this is an add type function, if this is the last extent, or if this is an overflow extent. If an overflow extent is indicated, this phase reads the VOL1 and format-4 labels, saves the VTOC limits, and fetches \$\$EOSDO8 to check the VTCC. If

there is no overflow extent found, this phase fetches \$\$BOIS04 to continue processing extents.

## \$\$BCIS04: ISAM Open, Phase 4 Chart LG

<u>Cbjective</u>: To check extents for different logical units assigned to the same physical units.

Entry: From \$\$BOIS03.

Exit: To \$\$BOIS05.

Method: This B-transient gets the logical and physical unit assignments for the index, prime data and independent cverflow type extents. It checks the extents for different logical units assigned to the same physical unit. If this condition exists, the logical unit assignment of the extent in the SYSRES DASD lakel information located in the label save area is modified to correct this condition. This process continues until all extents have been checked.

## \$\$BOIS05: ISAM Open, Phase 5 Charts LH-LK

Objective: To check the extents for valid ISAM format, to save extent limits and to build the DSKXTN table containing the logical unit and cell number of each extent.

Entry: From \$\$BOIS04.

Exit: To \$\$BOIS06 or to \$\$BCMSG1 when an error condition occurs.

Method: This phase first determines the extent type (overflow, index, or prime data), checks the type for validity, and branches to the appropriate routine to process the extent. If the extent type is not for an independent overflow, index, or prime data area, an error message is initialized, and the message writer, \$\$BOMSG1, is fetched to write the message on SYSLOG.

If an index type extent is indicated, this phase determines if a master index has been specified along with a cylinder index. If there is a master index and a cylinder index, they must be assigned to the same physical unit and they must be contiguous. If both conditions are satisfied, the limits for the master and cylinder indexes are saved and the next extent is processed. If either condition is not met, an error

message is initialized and the message writer is fetched.

If an overflow type extent is indicated, this phase saves the extent limits and tuilds an entry in the DSKXTN table. The DSKXTN table is located at the end of the DTFIS table and is used by ISAM to reference the extent information in the DTF tables. It contains the logical unit and cell number for each extent.

If a prime data type extent is indicated, it checks the first prime data extent upper limit to determine if more prime data extents are allowed. If more prime data extents are allowed, the phase checks the remaining prime data extent limits for continuity, and checks for the last prime data extent.

After all extents have been processed, this phase checks the index extent sequence numbers and fetches \$\$BCIS06 for execution.

#### \$\$POIS06: ISAM Cpen, Phase 6 Charts LL-LP

<u>Chjective</u>: For input files, checks format-1 labels and stores extent information in them. For output files, creates format-1 and format-2 labels and stores extent information in format-1 labels.

Entry: From \$\$BOIS05 or \$\$BCDSMW.

Exit: To \$\$BOIS07, \$\$BODSMW, cr \$\$BOMSG1
if an error condition occurs.

Method. If this is an input file, this phase sets up the 44-byte file name from the SYSRES DASD label information as the key, and reads the matching format-1 label from the VTOC. It checks the format-1 label, moves the extent limits into the latel, writes the updated format-1 label in the VTOC, and increases the volume sequence number by one. It continues processing until there are no more extents.

If this is an output file, this phase creates a format-1 label and then a format-2 label. For a description of the format-1 label and the format-2 label see DCS/VS\_LIOCS\_Volume\_1, SY33-8559.

After creating the format-1 and format-2 lakels, the phase increases the volume sequence number and continues processing until there are no more extents.

If this is a mixed input/cutput file, the phase updates the format-1 label and writes it back in the VTOC.

For an extend file, the format 1 label is checked for the data security indicator. If it is on, and the data security message has not been issued, it is issued via a fetch of \$\$BODSMW.

For a create file, the format 1 label is built with the data security indicator on if data security has been requested.

## \$\$BOIS07: ISAM Open, Phase 7 Charts MA-MC

Objective: To read the format-1 and format-2 labels for this file, and update the DTFIS table for each function.

Entry: From \$\$BOIS06 or \$\$BCRTV2.

Exit: To \$\$BOIS08, \$\$BOIS09 (for load-type file), \$\$BOFLPT (for file protection), or to \$\$BOMSG1 if an error condition occurs.

Method: For a load-create type file, this phase moves the prime data upper limit from the extent save area to the DTF table and checks for file protection. If there is DASD file protection, this phase fetches \$\$BOFLPT. If file protect is not specified, this phase fetches \$\$BCISO9 to write an EOF record at the beginning of the independent overflow area (if specified).

For all other type (load-extension, add, retrieve, and add-retrieve) files, this phase reads the VOL, fcrmat-1 and format-2 labels, updates the DTF table for each function and moves extent information from the save area to the DTF table. It then checks for file protection, and load-type file. If there is file protection, \$\$BOFLPT is fetched. If there is no file protection and it is a load-type file, \$\$BOIS09 is fetched to write an EOF record at the beginning of the independent overflow area (if specified). If there is no file protection and it is not a load-type file, \$\$BOIS08 is fetched for execution. For add and add-retrieve files which are already open, a check is made for the track hold specification. If HOLD=YES is specified, \$\$BOMSG1 is fetched to print out error message 691 FILE IS OPEN FOR ADD.

### \$\$BOIS08: ISAM Open, Phase 8 Charts MD-ME

<u>Objective</u>: To build CCW's in the high order bytes of IOAREAL, and to update the prime-data in-core add section of the DTF table.

Entry: From \$\$BOIS07 cr \$\$BOFLPT.

Exit: To \$\$BOIS09 or to \$\$BOMSG1 if an
error condition occurs.

<u>Method</u>: This phase determines if the prime-data-in-core add function is specified in the DTFIS table. If it is specified, this phase increases the size of IOAREAL (output area used for loading or adding records to a file) to permit the writing and reading of more than one physical record on or from a DASD (Direct Access Storage Device) per EXCP (Execute Channel Program).

The phase first calculates the maximum number of prime data records that can be read into or written from main storage at one time. It then calculates the starting address of the CCW build area in IOAREAL and aligns this address on a double-word boundary. It builds the following CCW's to write and read more than one physical record per EXCP.

#### 1. For a write:

r		r
A. CCW	to IOAREAL,	Search identifier   equal the prime  data reccrds in  IOAREAL.
B. CCW	x'08', Pointer to *-8	  TIC to *-8.   
c. ccm	x'08', Pointer to Read CCW's (see 2)	of read CCW's
D. CCV		data of prime    data record in    ICAREAL.   

### 2. For a read:

   A.       	CCW	to Seek/Search Address Area, Command Chaining, 5	
В.	CCW	X'08', Pointer to *-8	TIC to *-8.
  C.       	CCW	Command	Read ccunt, key, and data of   prime data record  into ICAREAL.

This phase continues to build the CCW's for a read or a write until the count for the maximum number of prime records in main storage reaches 0. This count is reduced by 1 each time the CCW's for a prime data record are built.

The CCW's built for a read or a write are preceded by a long seek CCW and TIC to either 1 or 2 depending on the operation to be performed. When the last read CCW is built, its flag field is set to 0, indicating the end of the CCW chain, and \$\$BOIS09 is fetched to search the independent cverflow area (if specified) for the EDF record.

\$\$BCIS09: ISAM Open, I: tegrity Phase 1 Charts MF-MH

## Objective:

- For a load-type file, to write an EOF record at the beginning of the independent overflow area.
- For an add-type file, tc search the independent overflow area for the EOF record.

Entry: From \$\$BOIS07 or \$\$BOIS08.

 $\underline{\mathtt{Exit}}\colon$  To the TES processor, \$\$BOPEN, to \$\$BOIS10, or to the problem program.

<u>Method</u>: This B-transient first determines if the file has an independent overflow area. If there is no independent overflow

area specified, a test for a load-create type file is made. If it is a lcad-create type file, a test for more files to be cpened is made. If more files are to be opened, \$\$BOPEN is fetched for execution. If no more files are to be opened, control is returned to the problem program. If it is not a lcad-create type file, \$\$BOIS10 is fetched for execution.

If an independent overflow area is specified, a test for file type is made. If it is a retrieve or load-extend type file, control is returned to either the TES processor (\$\$BOPEN) if more files are to be opened or to the problem program.

If it is a load-create type file, an ECF record is written at the beginning of the independent overflow area and a test is made to determine if a new independent cverflow extent has been specified. If it has not been specified, the number of tracks in the independent overflow area is calculated and stored in the DTF table and control is returned to either the TES processor (\$\$BOPEN) if more files are to be opened or to the problem program. If a new cverflow extent has been specified, control is returned to either the open monitor or to the problem program.

If it is an add-type file, the independent overflow area is searched for the EOF record. When the EOF record is found, the number of tracks in the independent overflow area is calculated and stored in the DTF table, the last prime updated, and \$\$BOIS10 is fetched to validate the last prime data record address.

\$\$BOIS10: ISAM Open, Integrity Phase 2
Charts MJ-MK

<u>Ctjective</u>: To validate the last prime data record address by scanning the prime data area for the end-of-file (ECF) record.

Entry: From \$\$BOIS09.

Exit: To the TES processor, \$\$BOPEN, or to the problem program.

Method: This routine searches for the EOF record in the prime data area. When the EOF record is read, the last prime data record address is saved in the DTF table and control is returned to either the TES processor (\$\$BOPEN) if more files are to be opened or to the problem program.

## \$\$BCISCA: ISAM Close Charts NA-NB

<u>Objective</u>: To close the file by updating the format-1 and format-2 labels with information from the DTFIS table.

Entry: From the close monitor, \$\$BCLOSE.

Exit: To the problem program or to the
close monitor, \$\$BCLOSE, if more files are
to be closed.

Method: If a load-create type file is to be closed, this phase updates the format-1 label with information from the DTFIS table, and writes the updated label back in the VTOC. It then updates the format-2 label with information from the DTFIS table, and writes the updated format-2 label back in the VTOC.

For all other type files, this phase updates only the format-2 label with information from the DTFIS table. It then writes the updated format-2 label tack in the VTOC. If more files are to be closed, the close monitor, \$\$BCLOSE, is fetched. If no more files are to be closed, control returns to the problem program.

## \$\$BCRIV1: ISAM RETRVE Open, Phase 1 Charts NC-NE

<u>Objective</u>: To open the RETRVE part of the DTFIS table by reading the format-1 label to get information needed for the table. To ensure that the correct pack is mounted.

## Entry:

- From the open monitor, \$\$BCPEN2.
- From the data security message writer, \$\$BODSMW.
- From \$\$BORTV2 if a new extent cn a different logical unit has been found.

Exits: To \$\$BORTV2 upon normal completion. To \$\$BOFLPT for DASD file protection. To \$\$BOMSG1, if messages are to be written on SYSLOG. To \$\$BODSMW to write the data security message.

Method: This phase first inserts the address of the CCW chain in the CCB, and relocates the CCW data addresses within the transient area. A test is then made to determine if entry was made to this phase from a phase other then \$\$BOPEN2. If so, control branches to a predetermined location within this phase. If nct,

processing continues in-line. Next, this phase computes the length of the incoming DASD label information and gets the address of the first extent. The phase sets the first byte of the cell number in each extent to 0. The first-time switch is set on, and a check is made for the expiration date in the DASD label information. If the expiration date is not present, this routine gets the retention period for the DASD labels and finds the expiration date.

The VOI1 label is read and a test is made to determine whether the extent serial number was omitted in the extent card. If not, a check is made to determine if the correct pack was mounted by comparing the volume serial number with the extent serial number. If equal, the correct pack is mounted. If not equal, the wrong pack is mounted, and a message is issued to that effect.

If the extent serial number was omitted and this is the first time through the routine, the serial number of the first volume is inserted in the DAJD label information in the label save area.

The format-4 label is read and the VTOC limits are saved. The format-1 label is then read and checked for the data security indicator. If the file has not been cpened, \$\$EODSMW is fetched to print the data security message. After determining the number of extents in format-1 label and getting the address of the first extent, this phase fetches \$\$BORTV2 for execution.

# \$\$BORTV2: ISAM RETRVE Cren, Phase 2 Charts NF-NG

<u>Cbjectives</u>: To open the RETRVE part of the DTFIS table by reading the fcrmat-2 label to get information needed for the table. To insert entries in the DSKXTN table located at the end of the DTF table.

Entry: From \$\$BORTV1.

Exits: To \$\$BOIS07 upon normal completion. To \$\$BOFLPI for DASD file protection. To \$\$BOMSG1 if messages are to be written on SYSLOG. To \$\$BORTV1 if a new extent on a different logical unit is found.

Method: This phase first inserts the address of the CCW chain in the CCB, and relocates the CCW data addresses within the transient area. A test is then made to determine if this phase was entered from a phase other than \$\$BORTV1. If it was, control branches to a predetermined location within this phase. If it was not, processing continues in-line.

This phase then gets the address of the first extent in the format-1 label and tests to determine if an extent is present. If an extent is not present, control branches to read the format-2 label. If an extent is present, a test is made to determine if the first prime data extent switch is on. If it is on, an entry is made in the DSKXTN table. If it is not on, a test is made to determine if the extent is a prime data extent. If it is a prime data extent, the extent sequence number and the extent lower limit are saved and the first prime data extent switch is set.

The routine then makes an entry in the DSKXTN table for each extent. Only three extents are possible per volume for an indexed sequential file: the master/cylinder index area, the prime data area, and the independent cverflcw area. Each entry in the DSKXTN table is four bytes, containing two bytes of the logical unit and two bytes of the cell number. The location of the entry within the table is determined by multiplying the extent sequence number by 4, and adding the result to the starting address of the table minus 4. If the DSKXTN table is full, an error condition exists and a message is issued to that effect.

After the DSKXTN entry is made, a test is made to determine if the file-protect option has been specified. If it has, the routine sets up the extent for file protect and fetches \$\$BOFLPT.

If the file-protect option has not been specified or if this phase was entered from

\$\$BOFLPT, this routine gets the address of the next extent in the format-1 label, and checks to determine if all extents in the label have been processed. If they have not been processed, control returns to process the next extent.

When all extents have been processed, the first time through B-transient switch is checked. If on, the switch is set off and a test is made to determine whether this is the first volume. If it is not the first volume, an error message is issued. If it is the first volume, a test is made to determine if the format-2 pointer is present in the format-1 label. If it is not present, an error message is issued. If the format-2 pointer is present, the format-2 label is read, and the master index and cylinder index lower limits from the label are saved.

A check is made to determine if all the extents have been processed. If they have not been processed, this routine scans the SYSRES DASD label information in the label save area to find the next extent cn a different logical unit, cr the end of the extents. If a new extent on a different logical unit is found, control returns to \$\$BORTV1 to read the volume label and process the extents for that logical unit.

When all extents have been processed, this phase sets the file-protect option indicator off (if it is cn) and exits to phase \$\$BOIS07.

## EXPLANATION OF FLOWCHART SYMBOLS

#### DESCRIPTION

A group of program instructions that perform a processing function of the program. The label, if any, is shown above the block.

\*B2
IF ANY ADDITIONAL EXPLANATION IS
REQUIRED, ITS LOCATION ON THE CHART
IS IDENTIFIED BY AN ASTERISK AND
THE BLOCK ID.

\*\*\*\*\*B1\*\*\*\* \*LABEL1 \*-\*-\*-\*-\*

Description or title of a routine that is detailed on another flowchart. The starting label of the routine and the flowchart ID appear



An instruction, or group of instructions, that changes portion of a routine or initializes a routine for a given condition.



A group of operations not detailed in the flowcharts in this manual, such as user rou-



Any function of an input/output device or program, usually branching to an I/O routine to perform the function stated in the block.



Points where the program branches to alternate processing, based upon variable conditions such as program switch settings and test results.



The beginning or end of a program or routine.

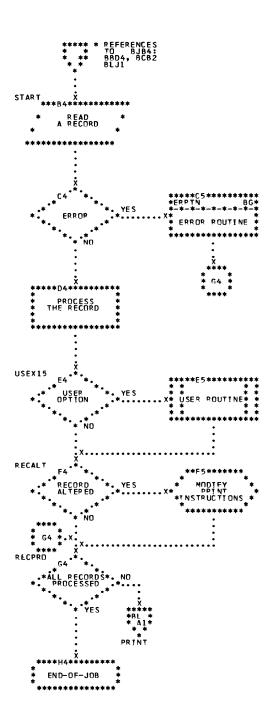


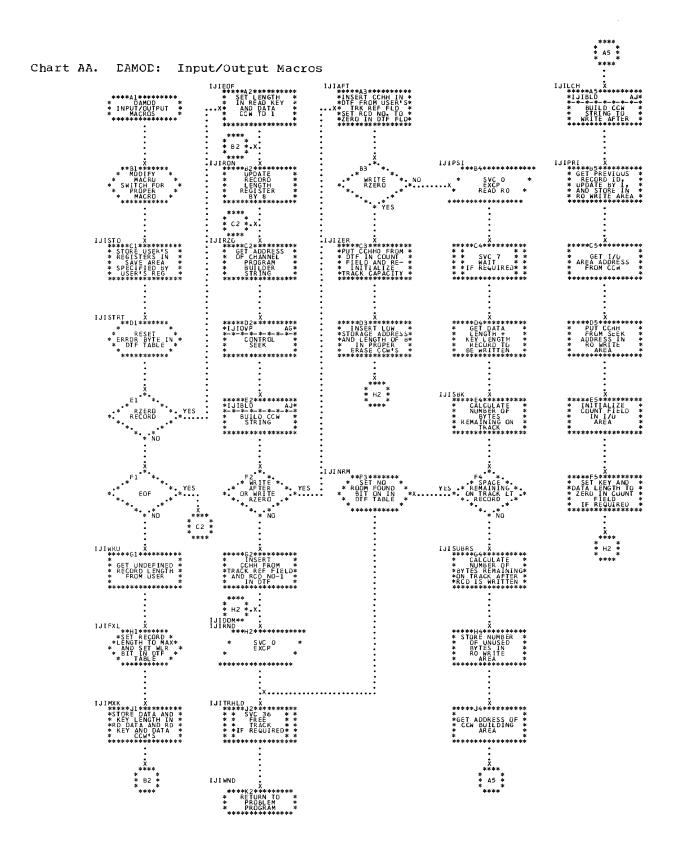
On-page connector. An entry from or an exit to another function on the same flowchart. The location in the connector identifies the block to which entry on a chart is made.

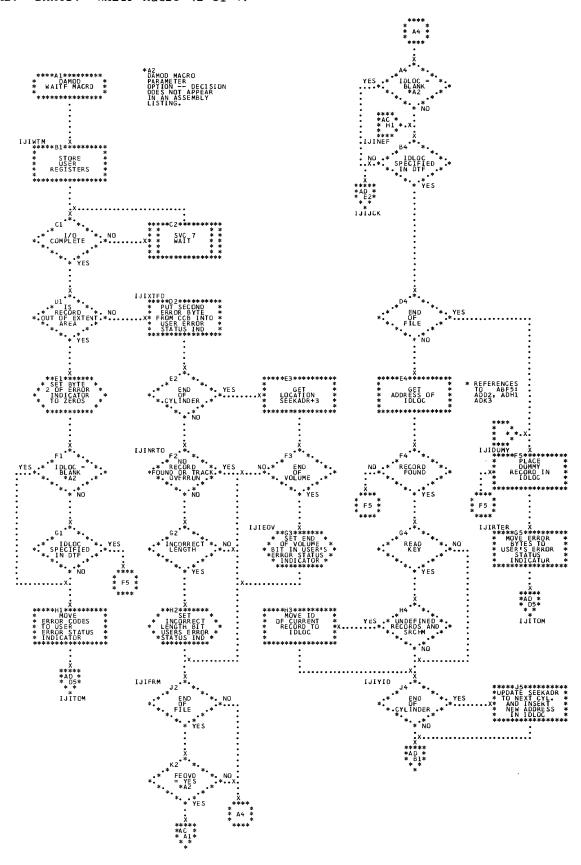


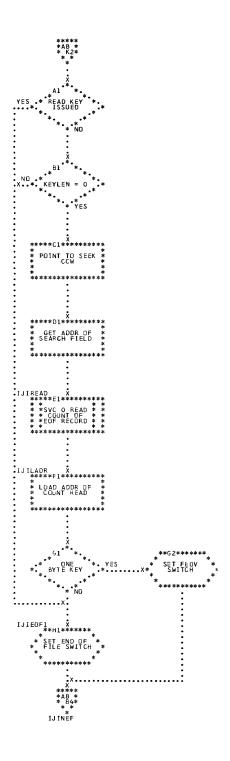
Off-page connector. An entry from, or exit to, a given point on another flowchart. The characters in the connector identify the chart and block to which or from which control is passed. The corresponding label, if any, is placed outside the outgoing connector. For multiple entries, an asterisk is placed in the connector and the locations from which control is passed are listed nearby.

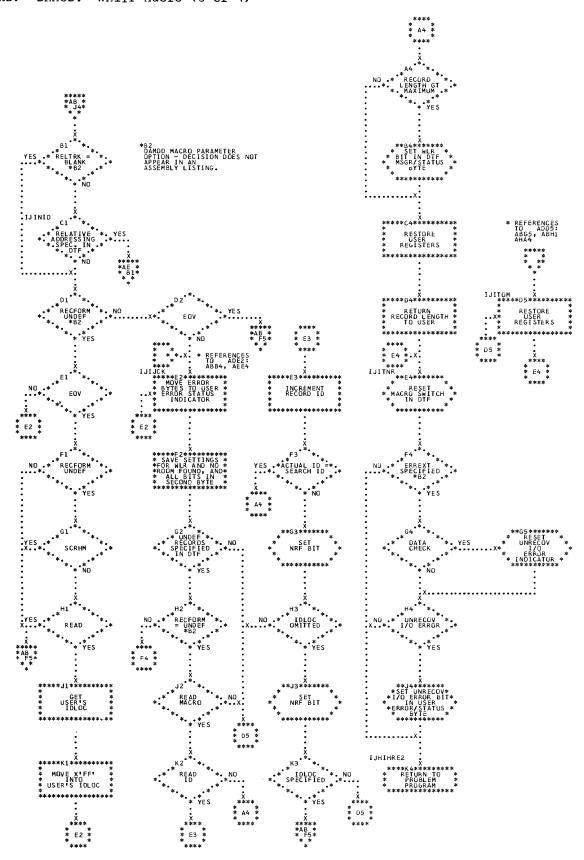
**EXAMPLE** 

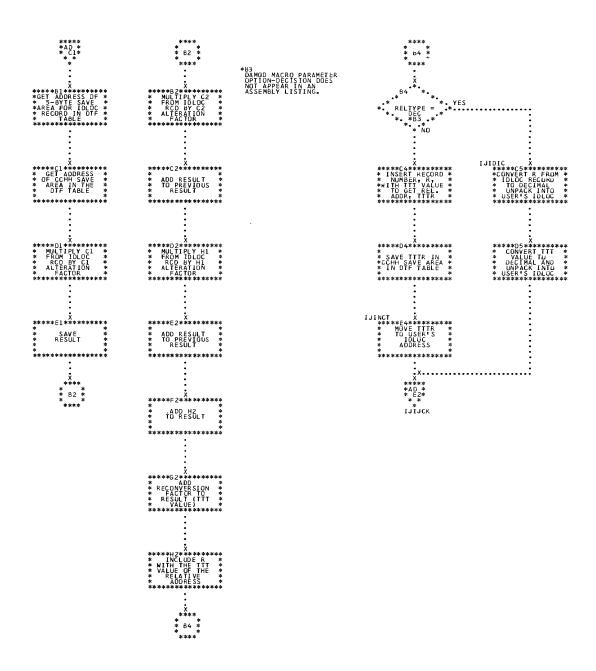


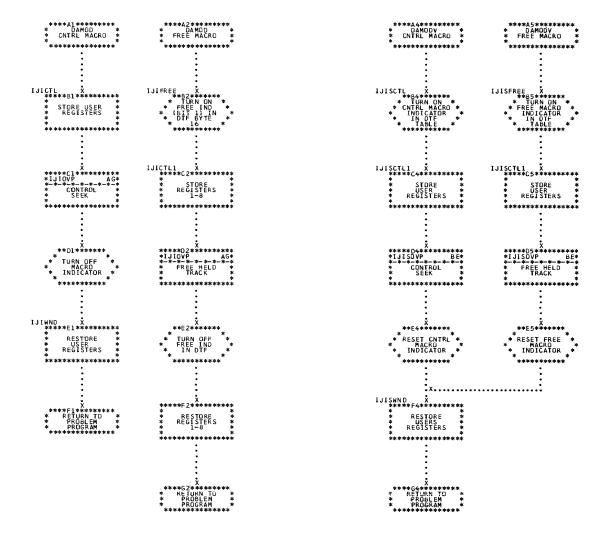












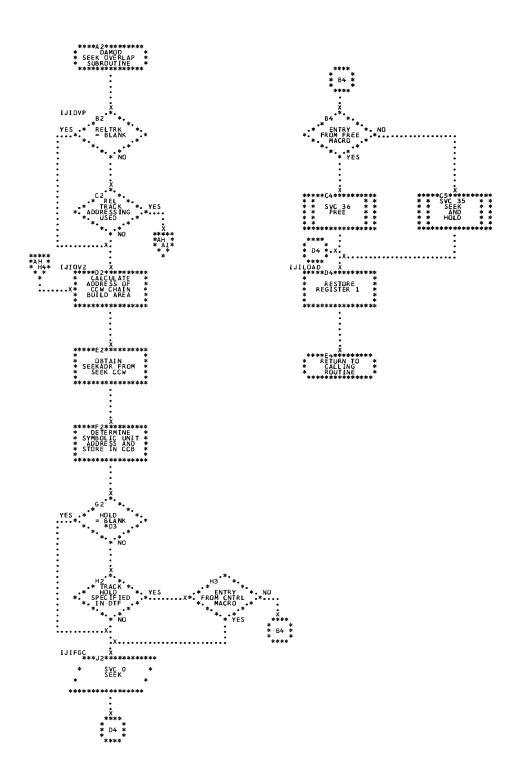
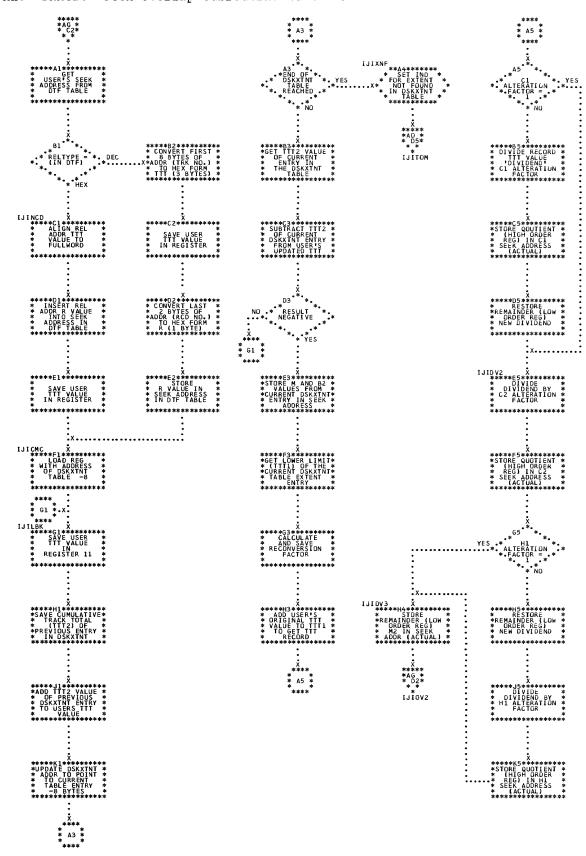


Chart AH. DAMOD: Seek Overlap Subroutine (2 cf 2)



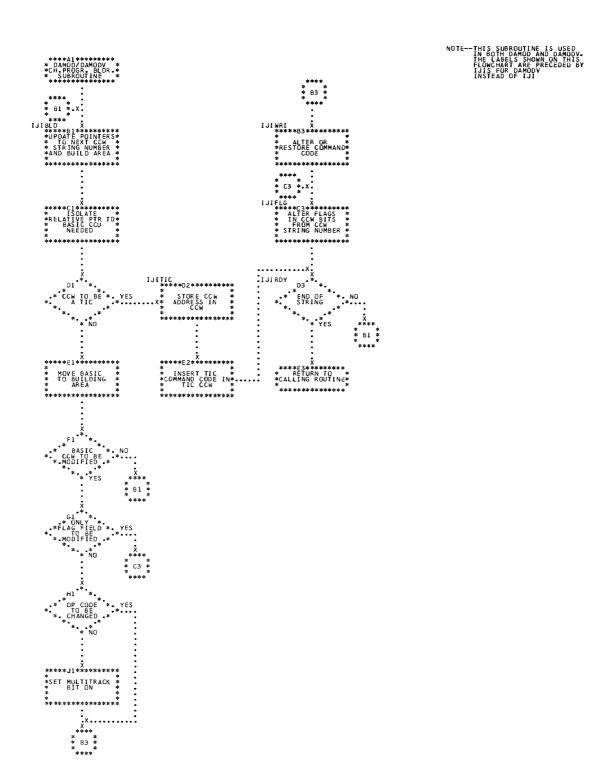


Chart AK. CAMODV: Input/Output Macros (1 of 4)

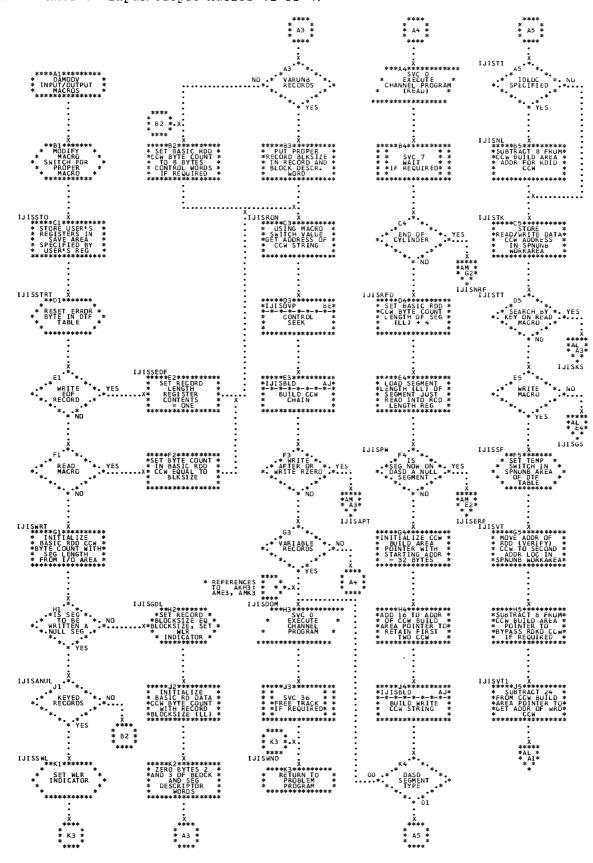


Chart AL. DAMODV: Input/Cutput Macros (2 of 4)

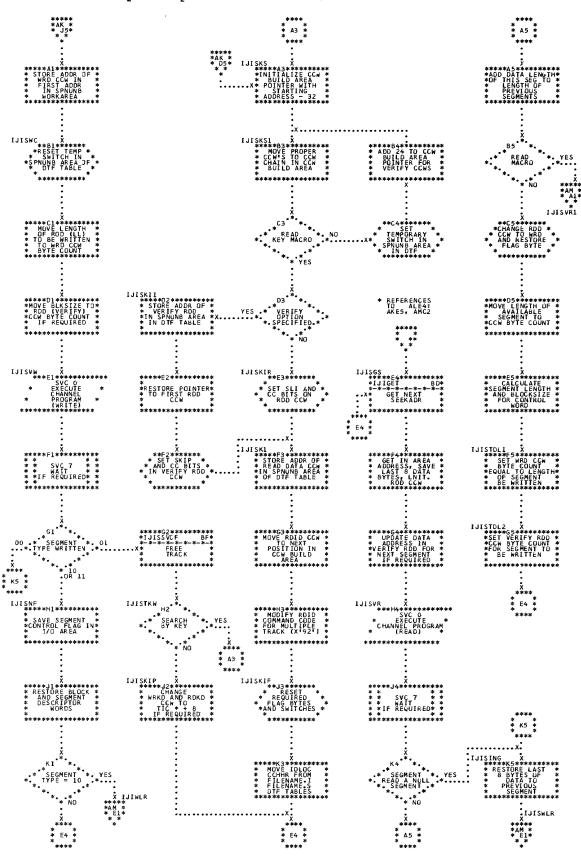


Chart AM. DAMODV: Input/Output Macros (3 of 4)

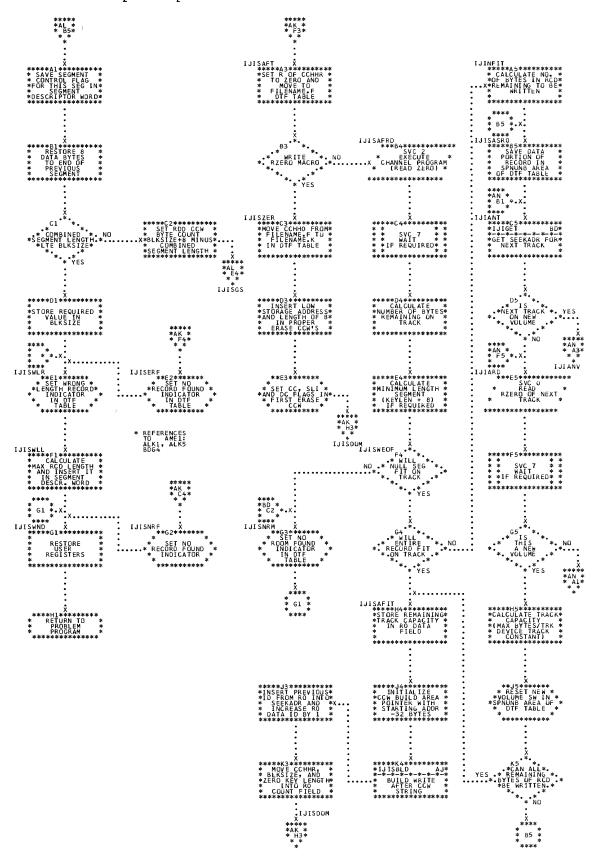
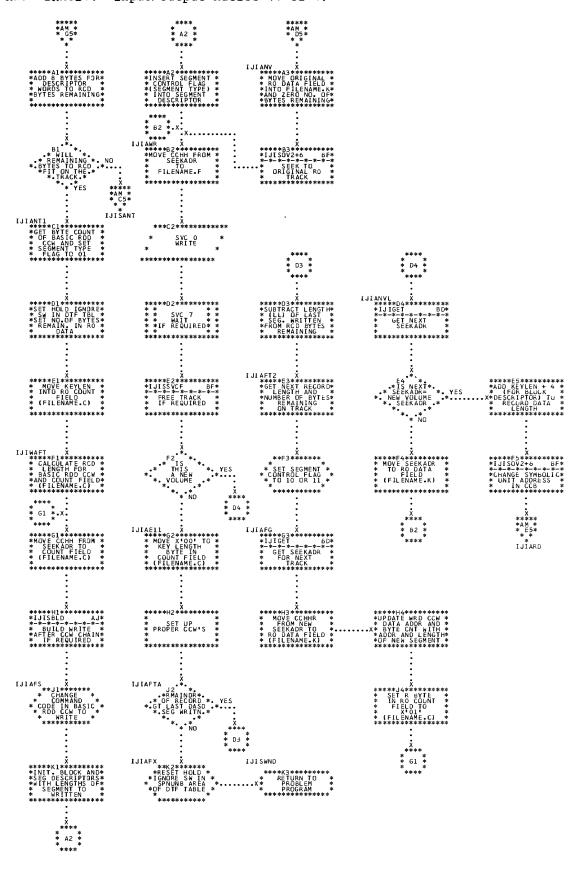
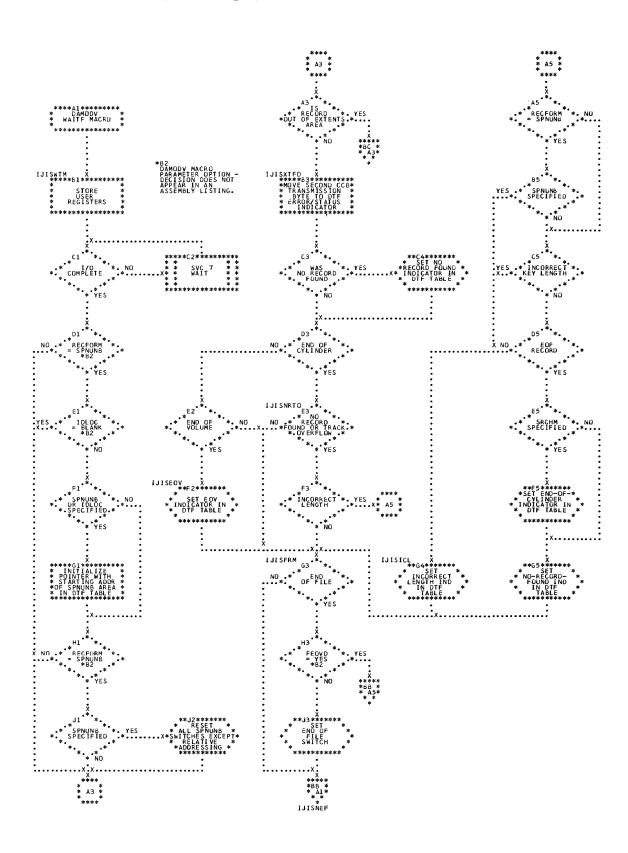
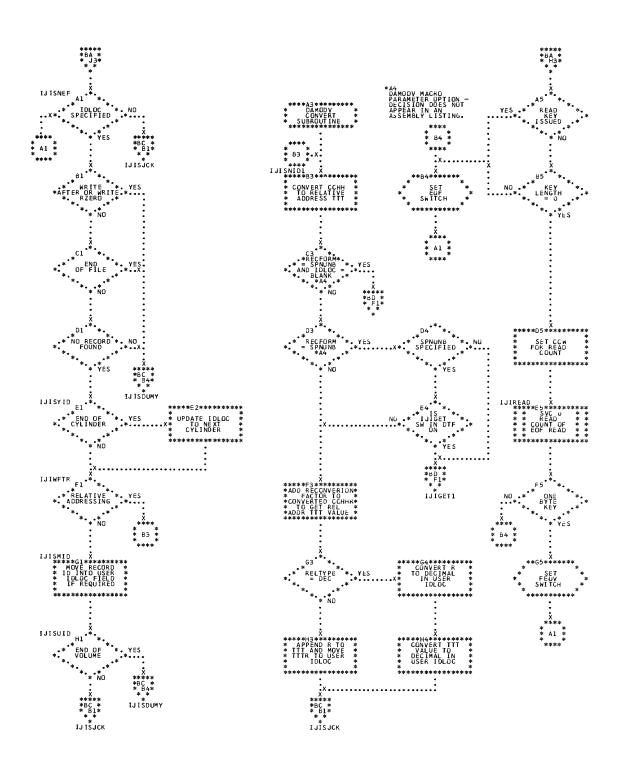


Chart AN. DAMODV: Input/Cutput Macros (4 of 4)







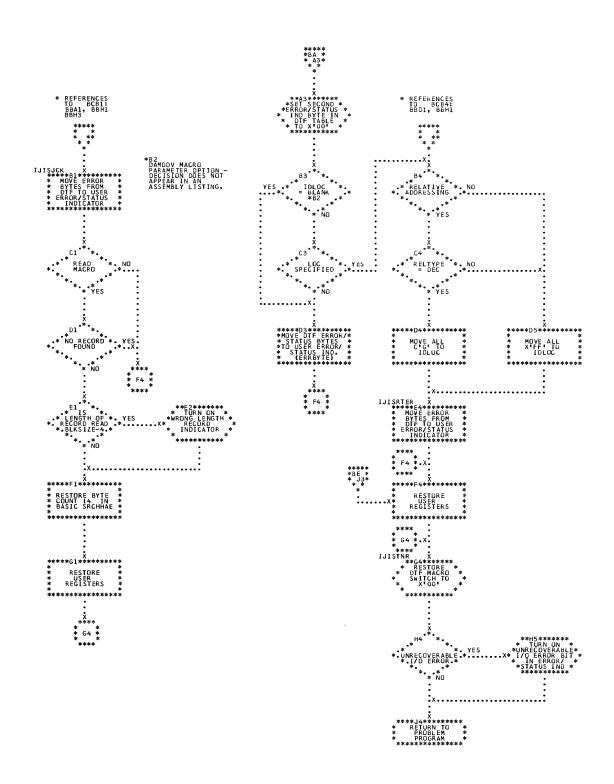
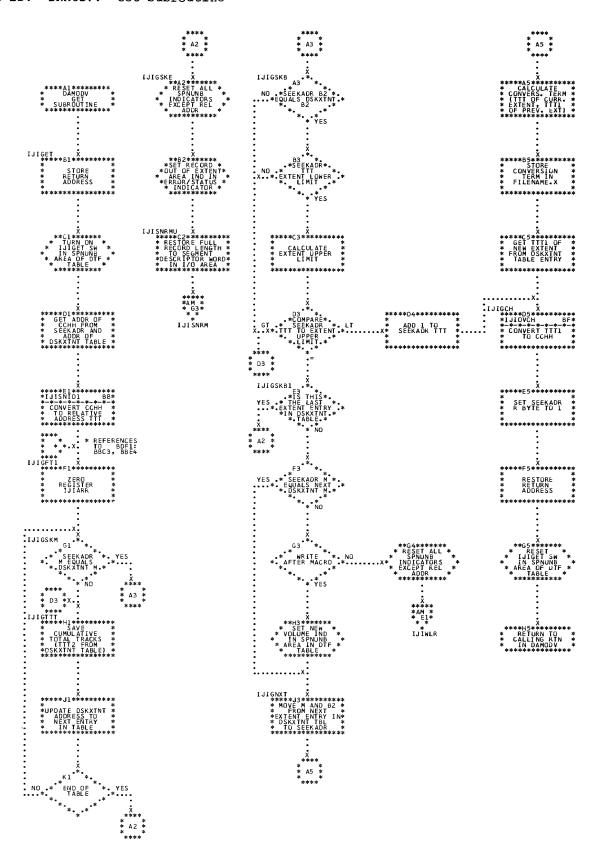


Chart BD. DAMODV: Get Subroutine



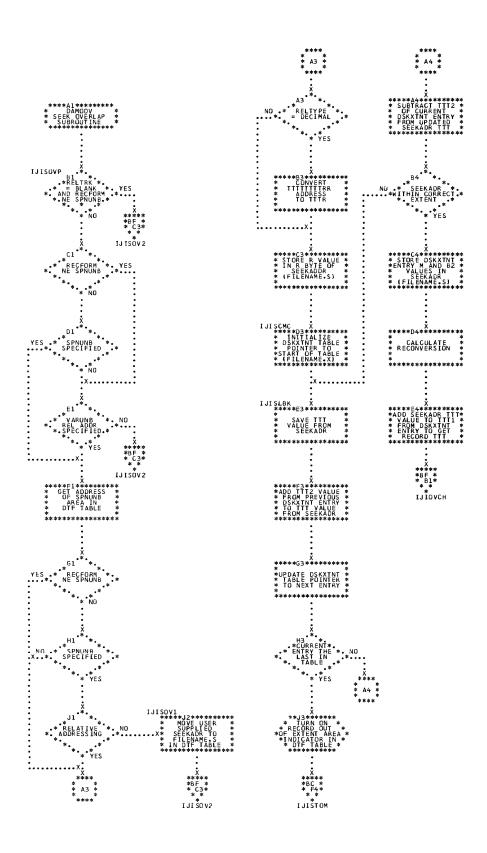


Chart BF. DAMODV: Seek Overlap Subroutine (2 of 2)

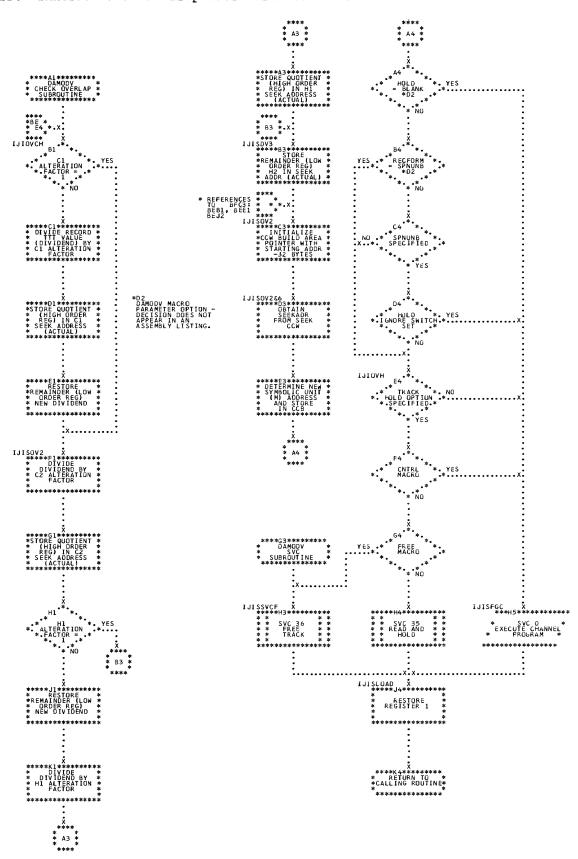
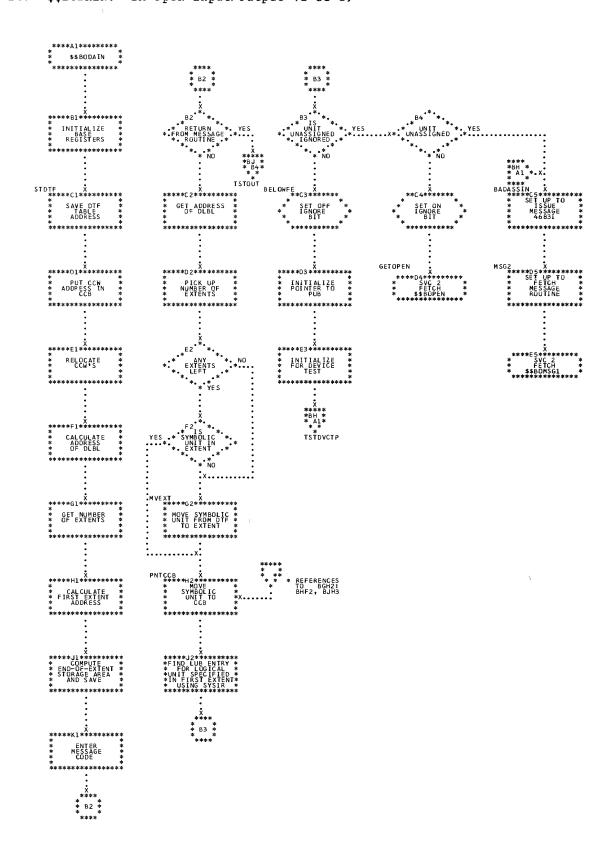
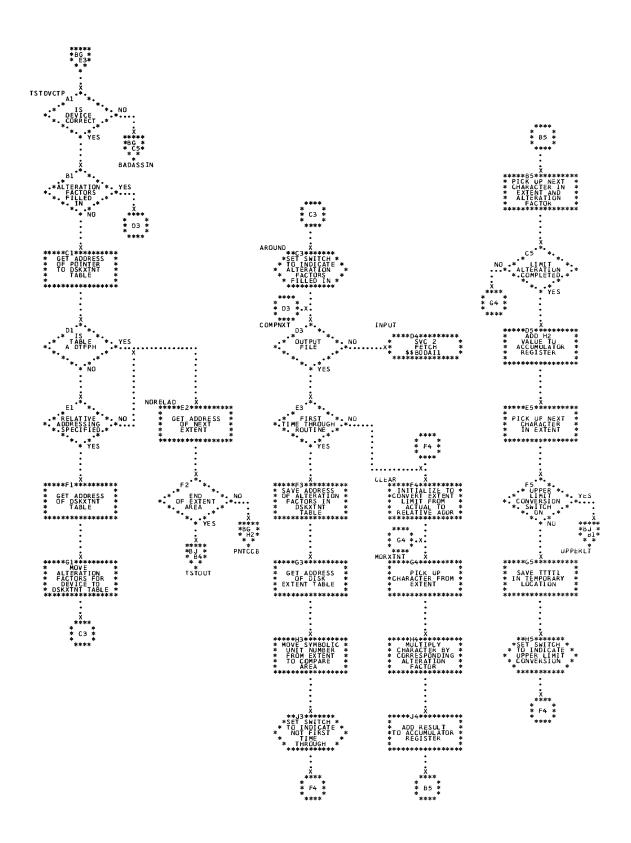
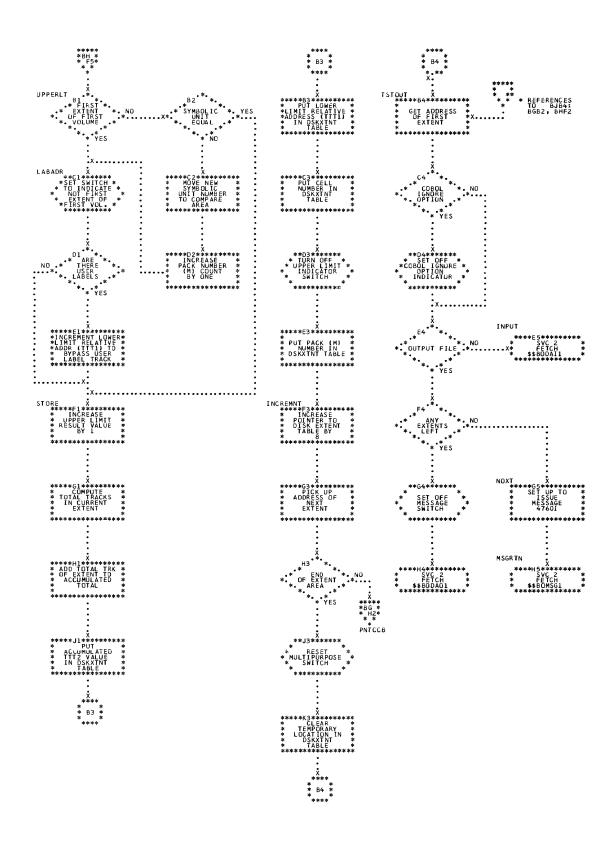
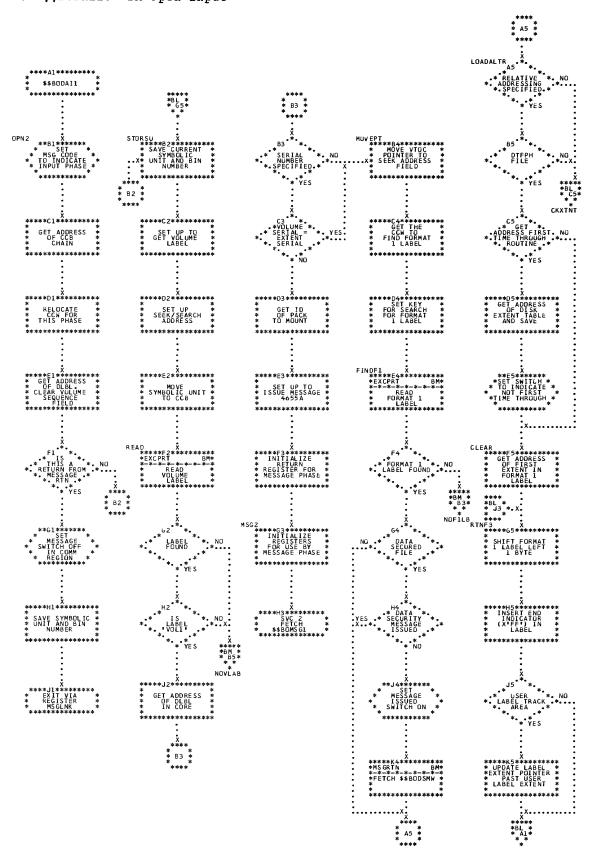


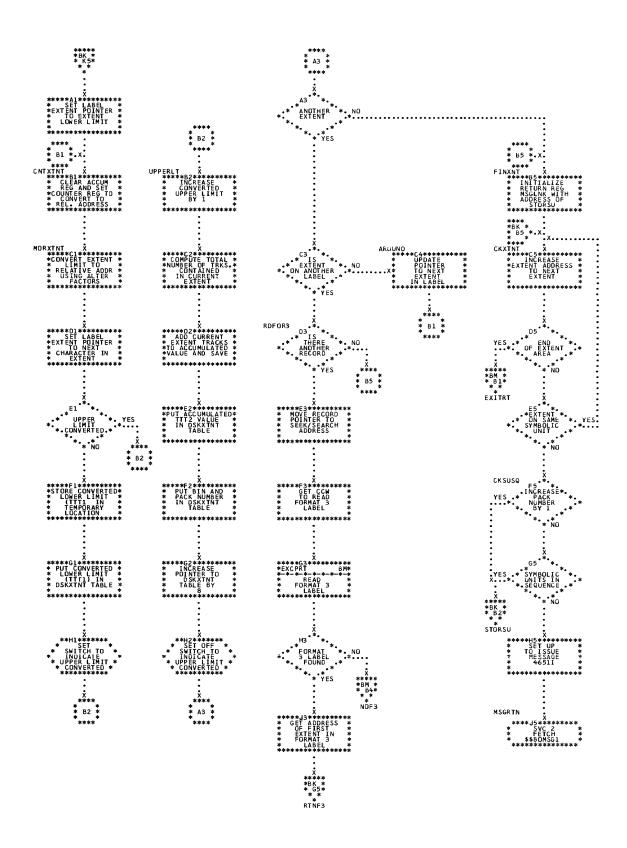
Chart BG. \$\$BODAIN: DA Open Input/Output (1 of 3)











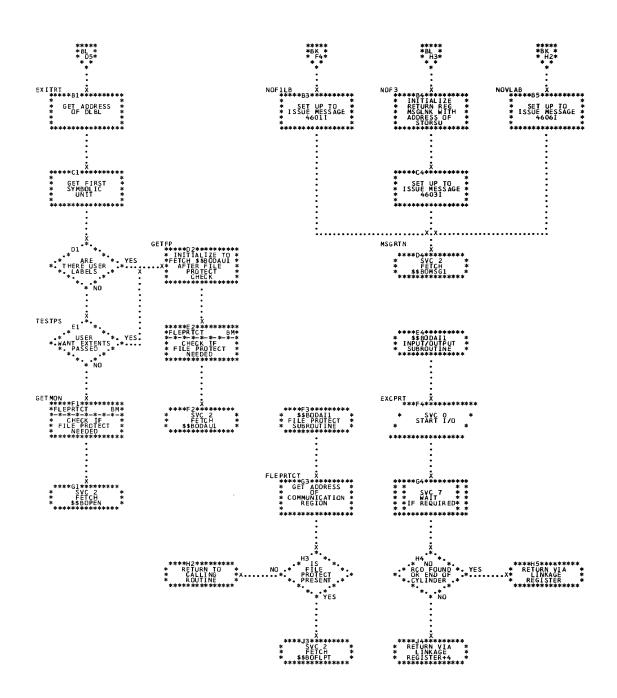
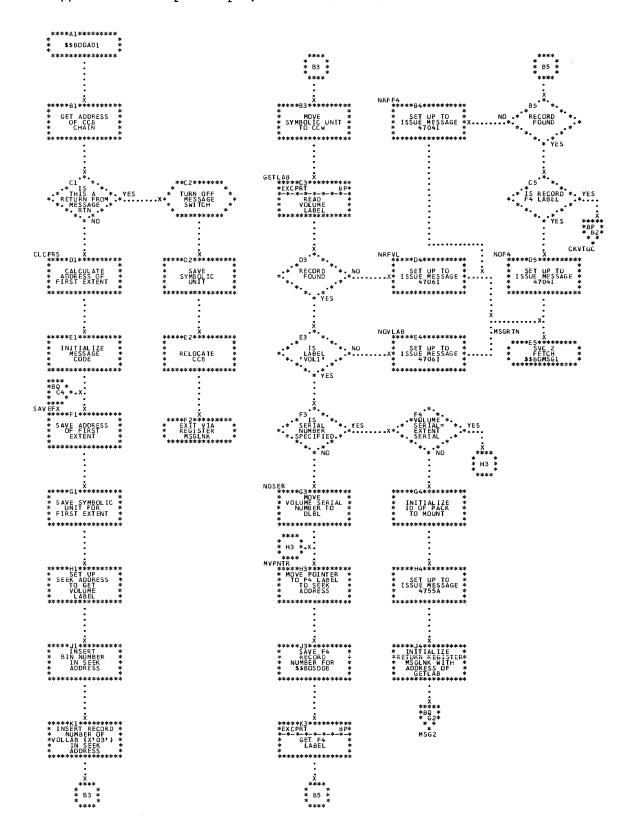
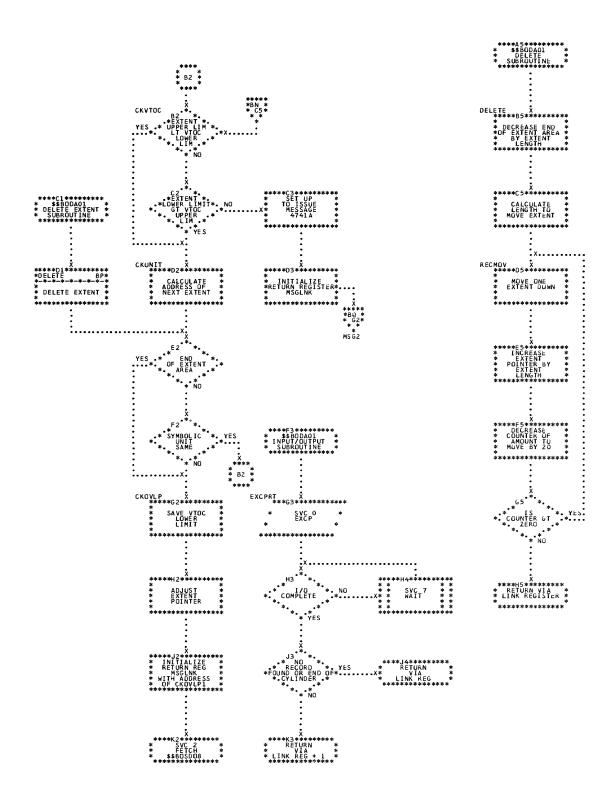


Chart BN. \$\$BODA01: DA Open Output, Phase 1 (1 of 3)





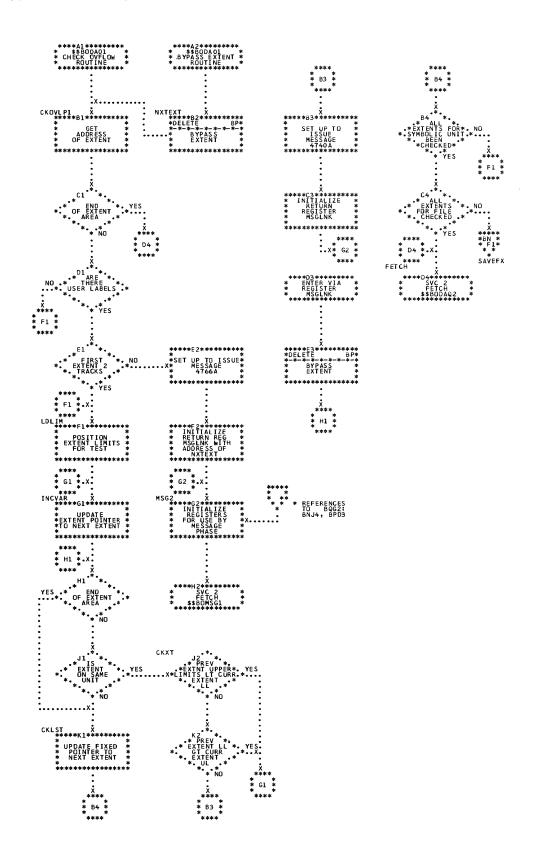
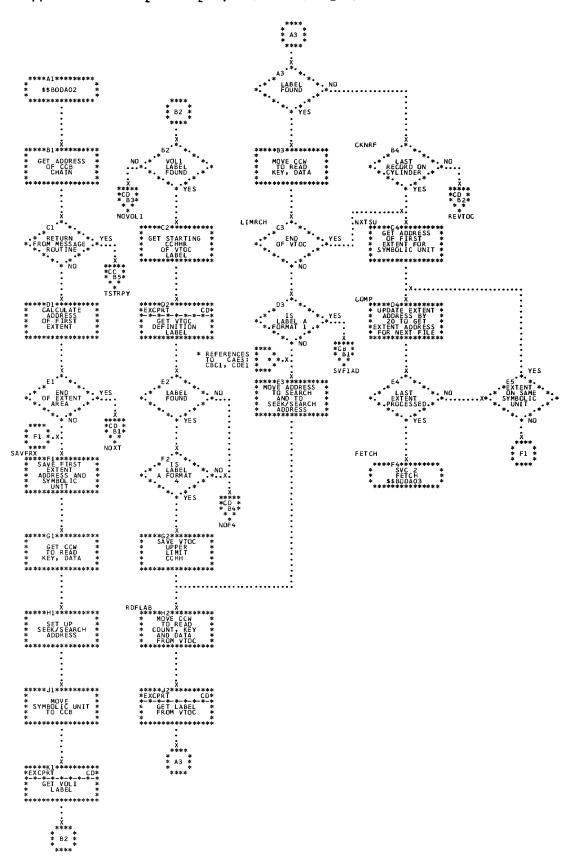


Chart CA. \$\$BODA02: DA Open Cutput, Phase 2 (1 of 4)



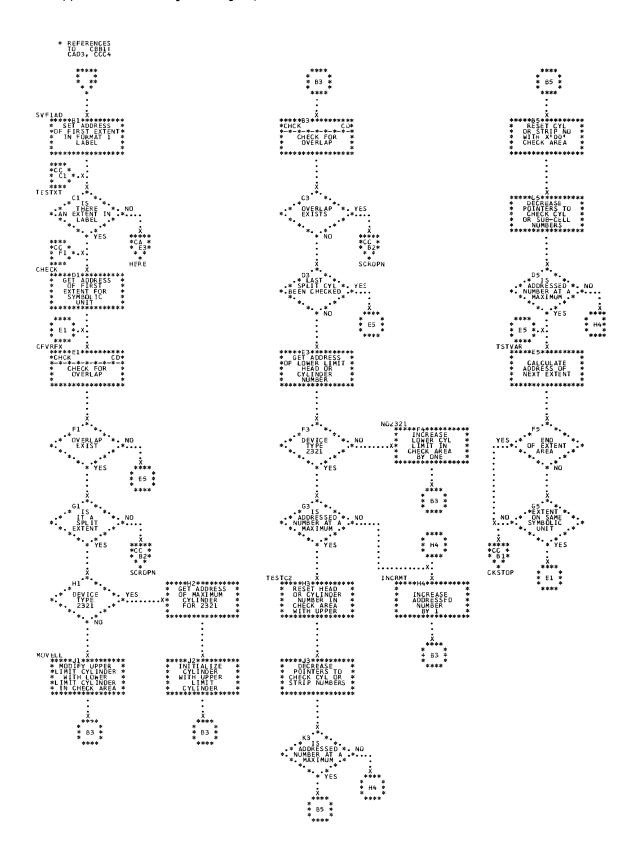
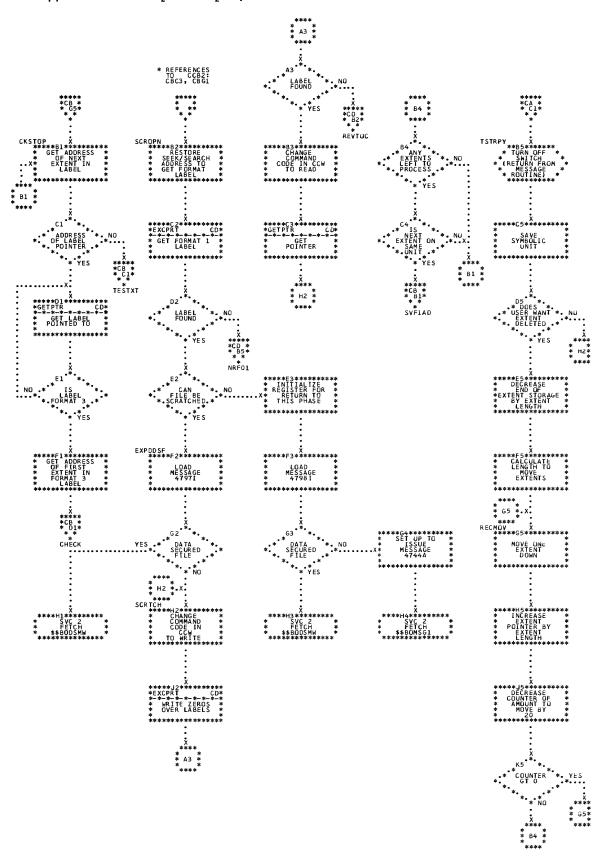


Chart CC. \$\$BODA02: DA Open Cutput, Phase 2 (3 of 4)



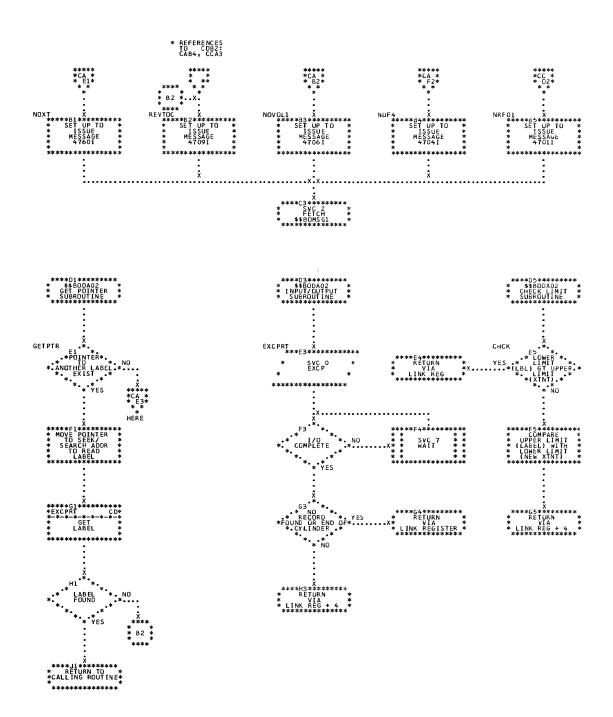
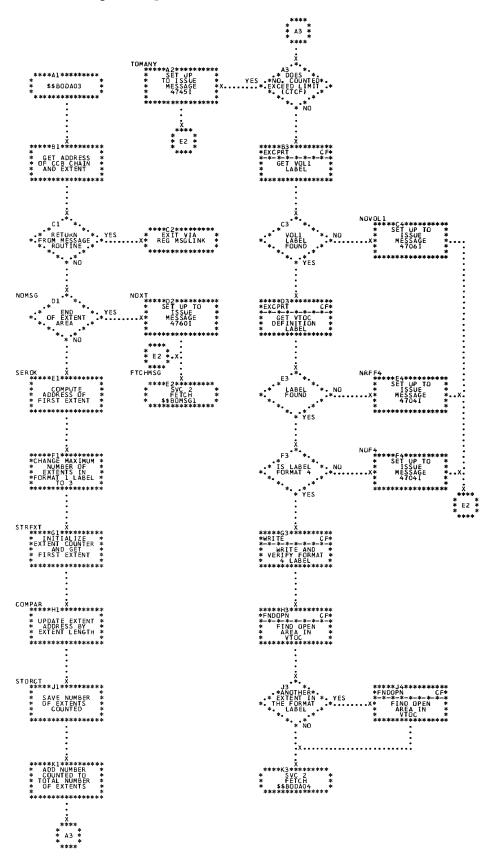


Chart CE. \$\$BODA03: DA Open Cutput, Phase 3 (1 cf 2)



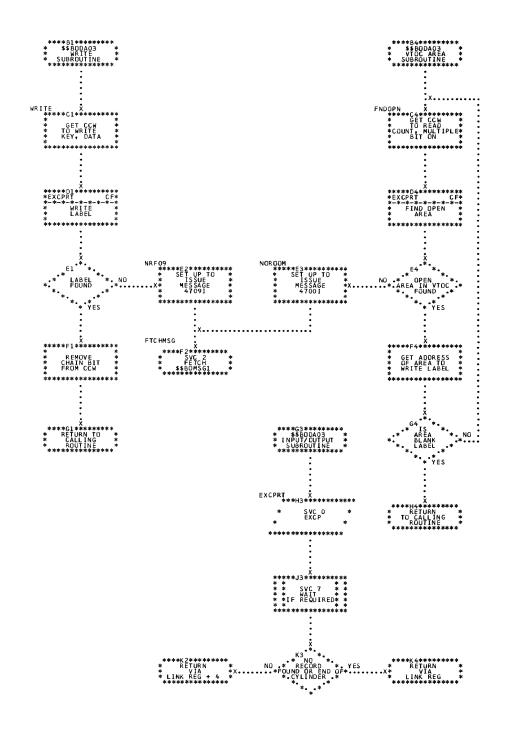
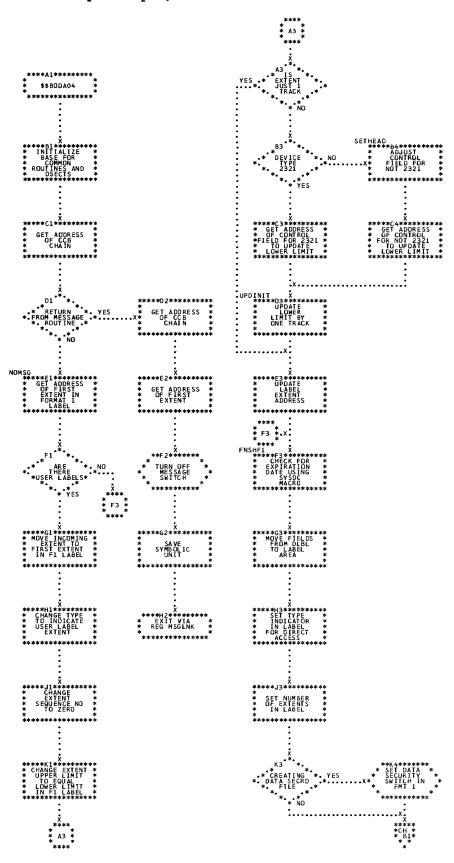
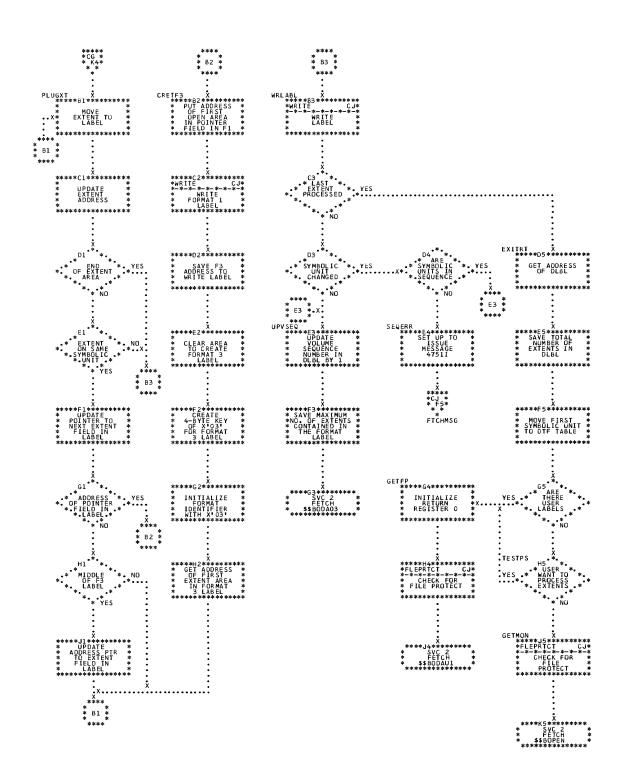


Chart CG. \$\$BODA04: DA Open Output, Phase 3 (1 of 3)





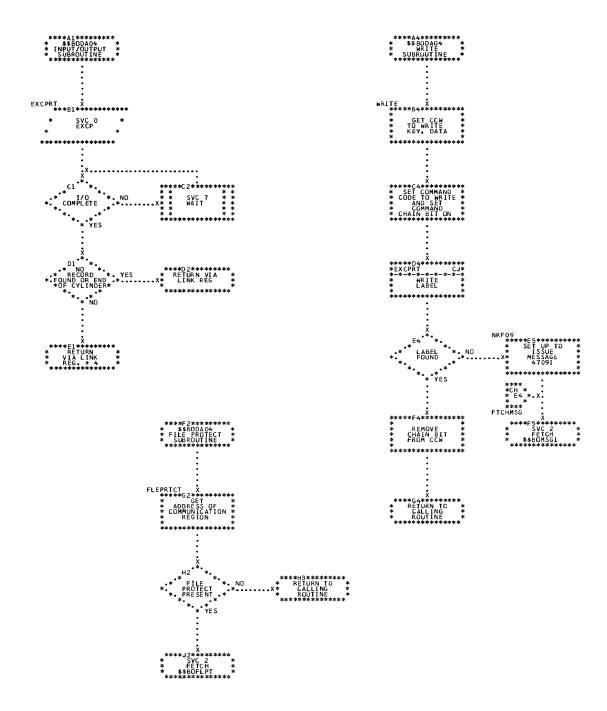


Chart CK. \$\$BODAU1: DA Open Input, Output (1 of 2)

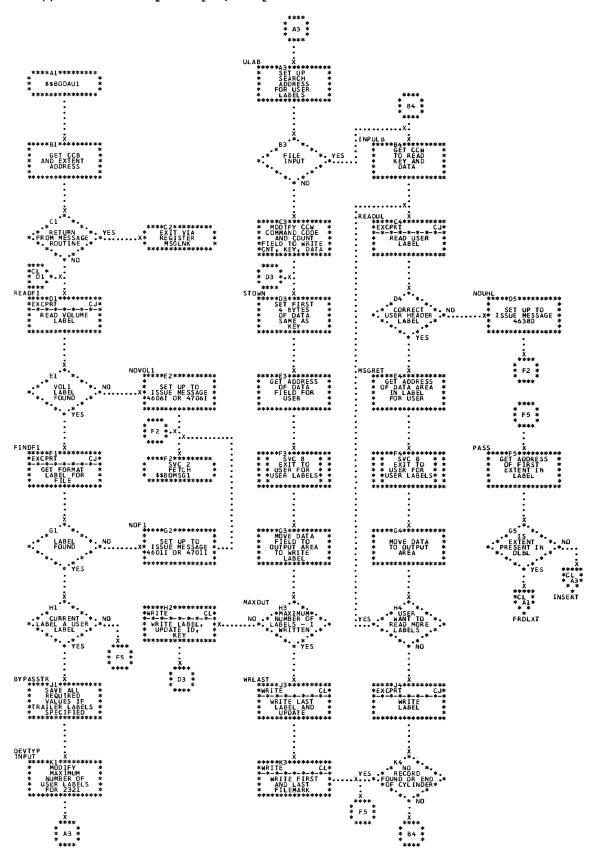
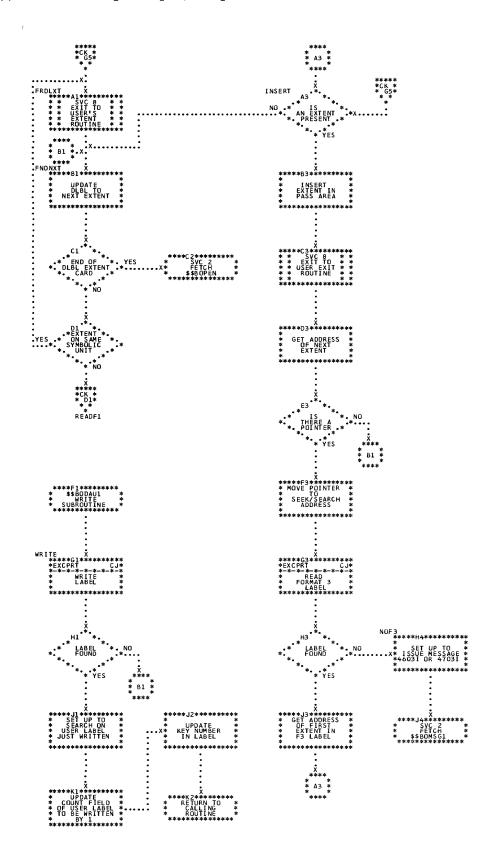
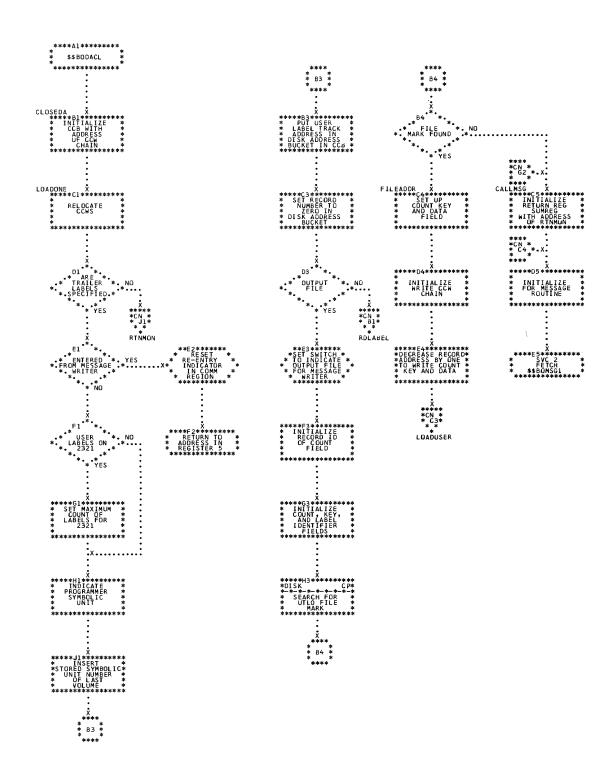
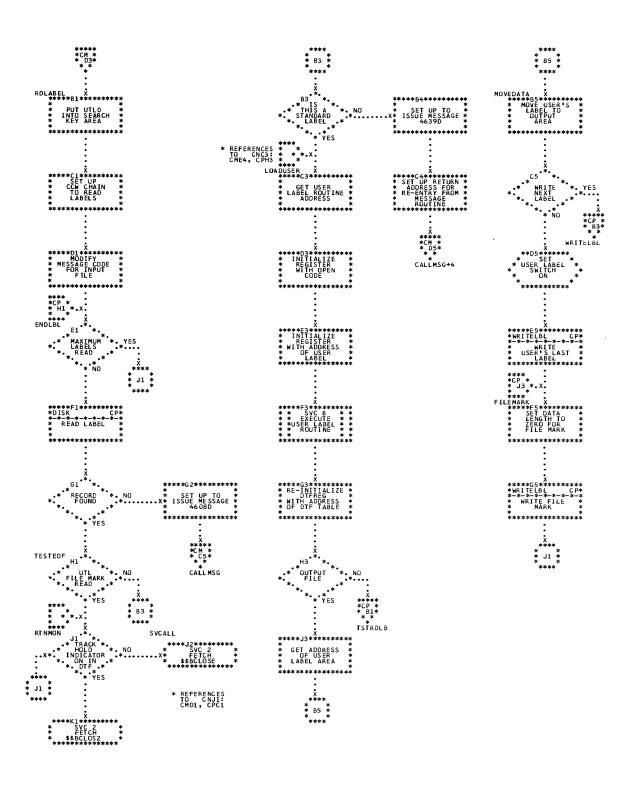


Chart CL. \$\$BODAU1: DA Open Input, Output (2 of 2)







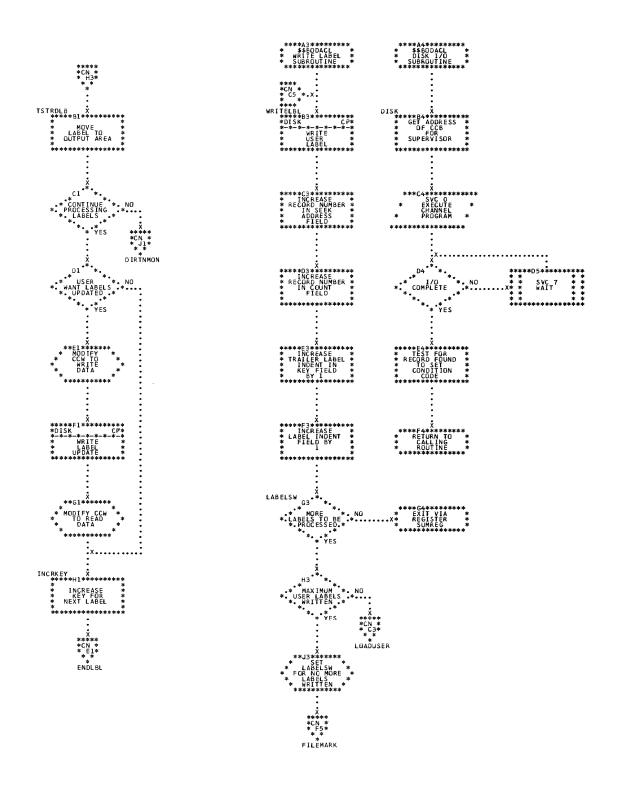
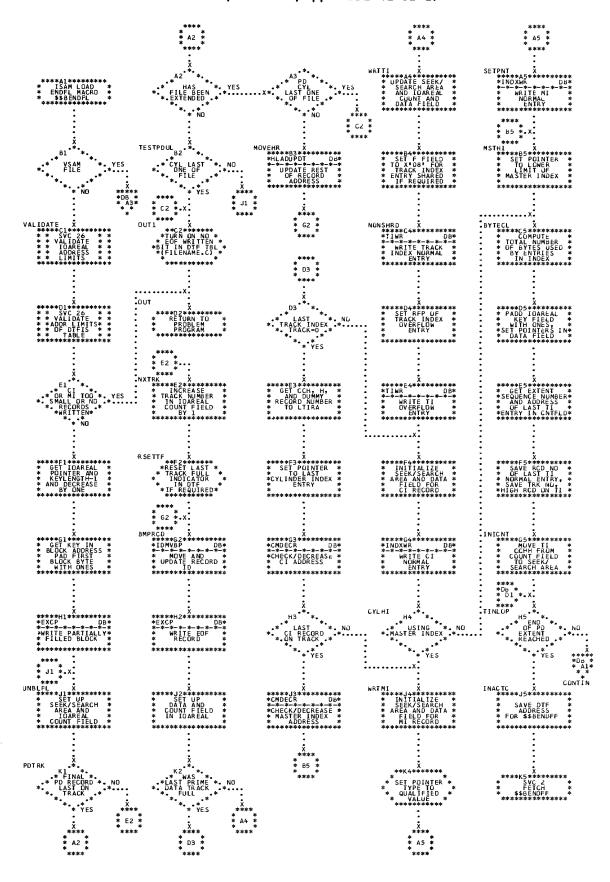
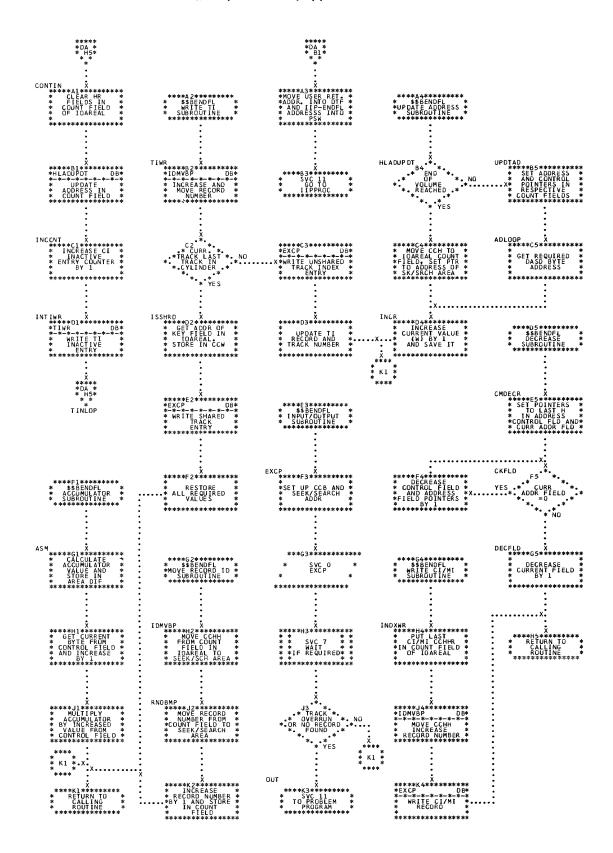
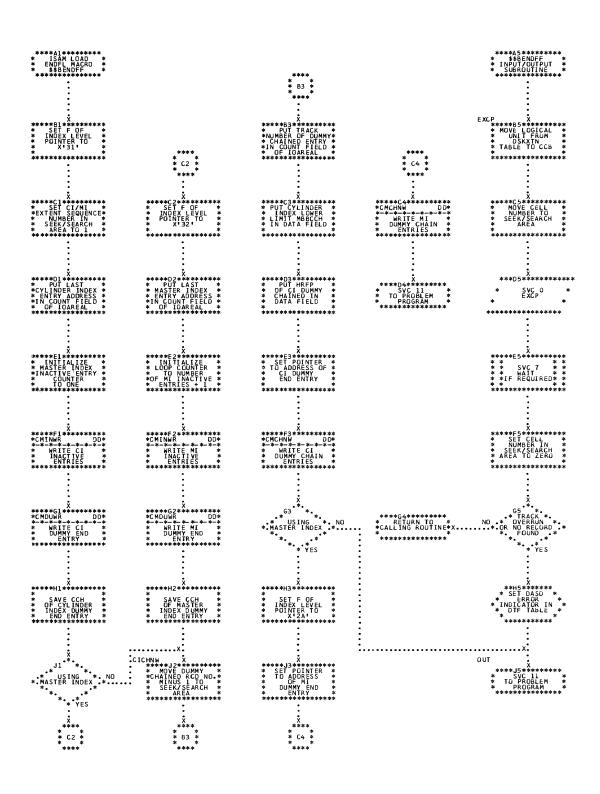


Chart DA. ISAM LOAD: ENDFL Macro, Phase 1, \$\$BENDFL (1 of 2)







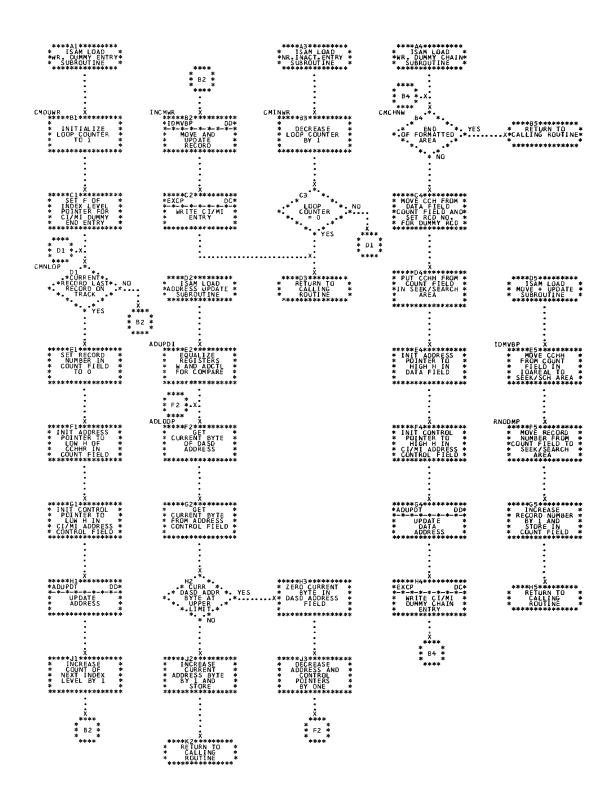
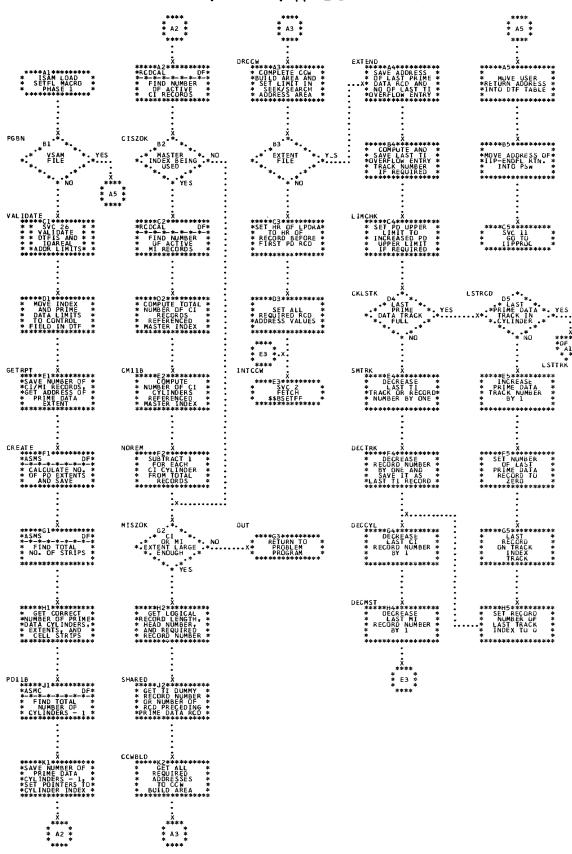
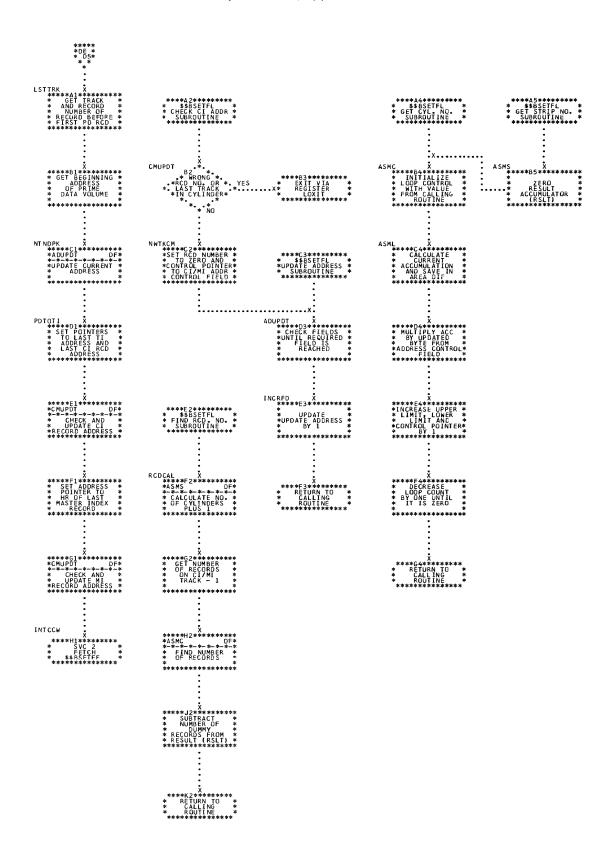
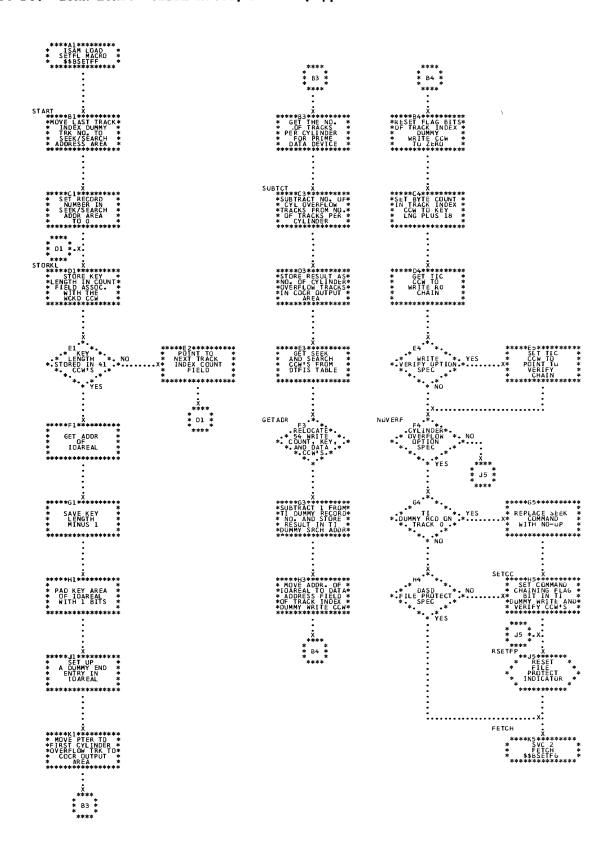
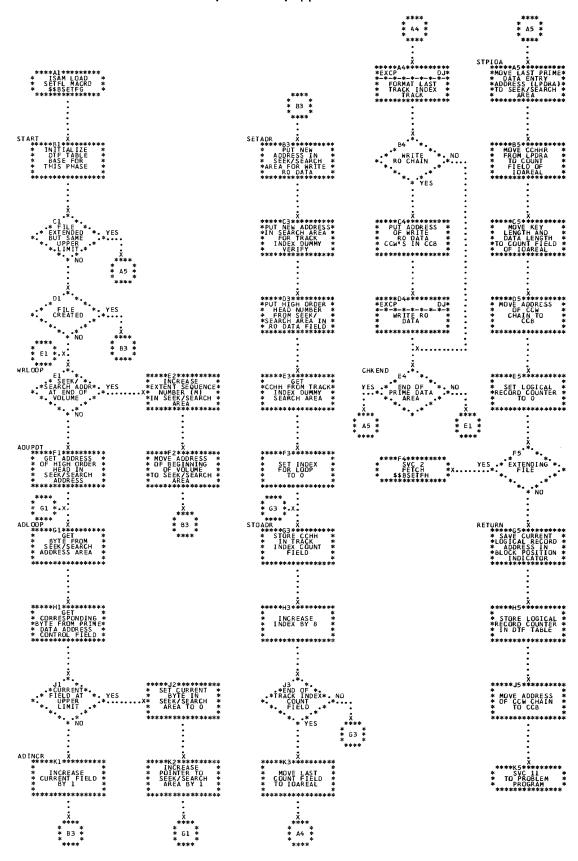


Chart DE. ISAM LOAD: SETFL Macro, Phase 1, \$\$BSETFL (1 of 2)









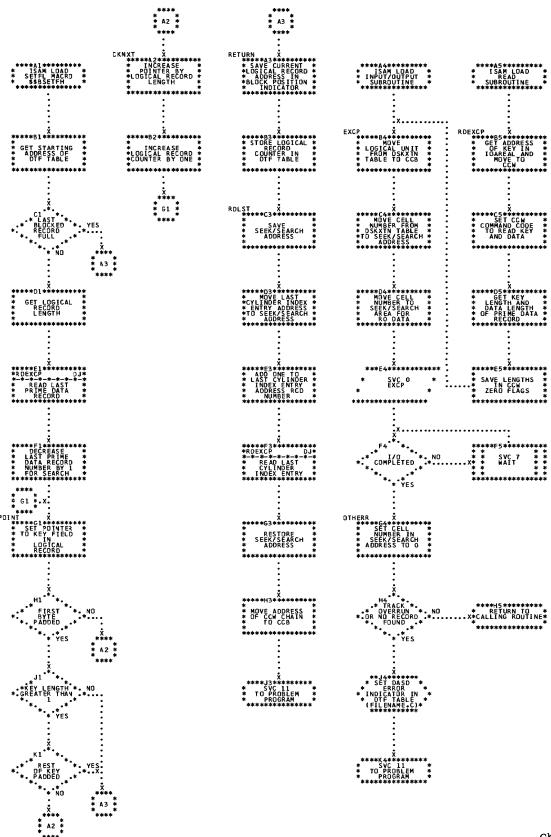


Chart DK. ISAM LOAD: WROTE Macre, NEWKEY (1 of 3)

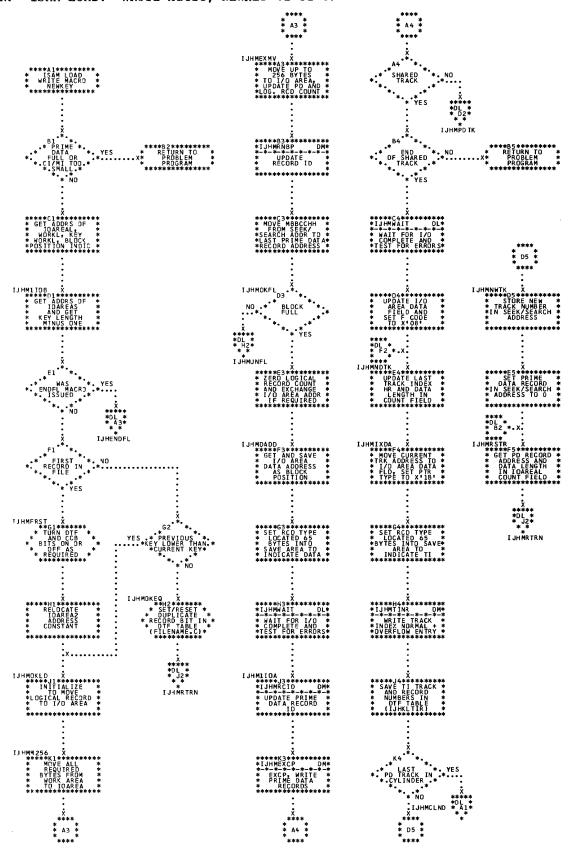
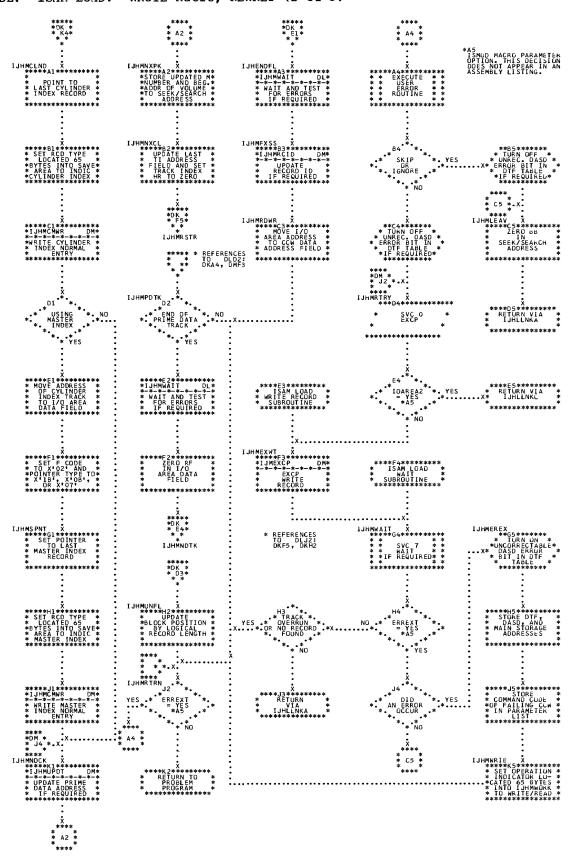
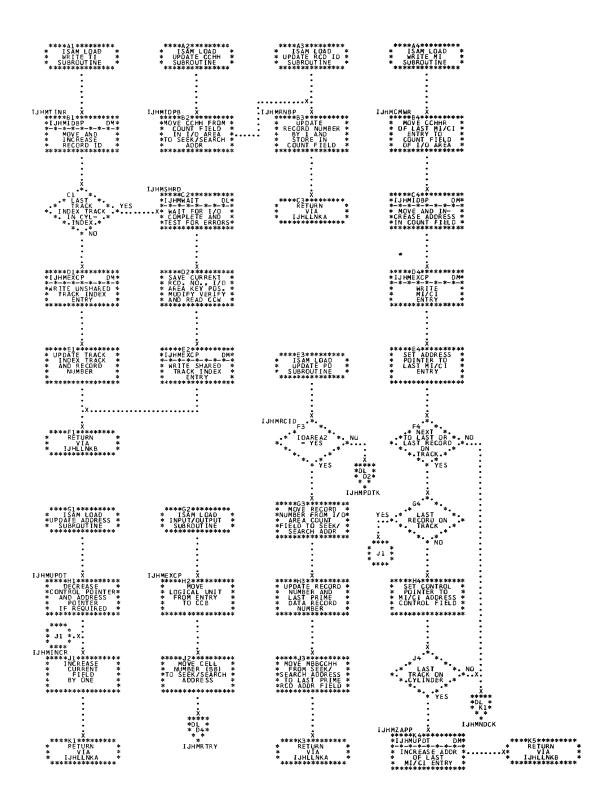


Chart DL. ISAM LOAD: WROTE Macro, NEWKEY (2 of 3)





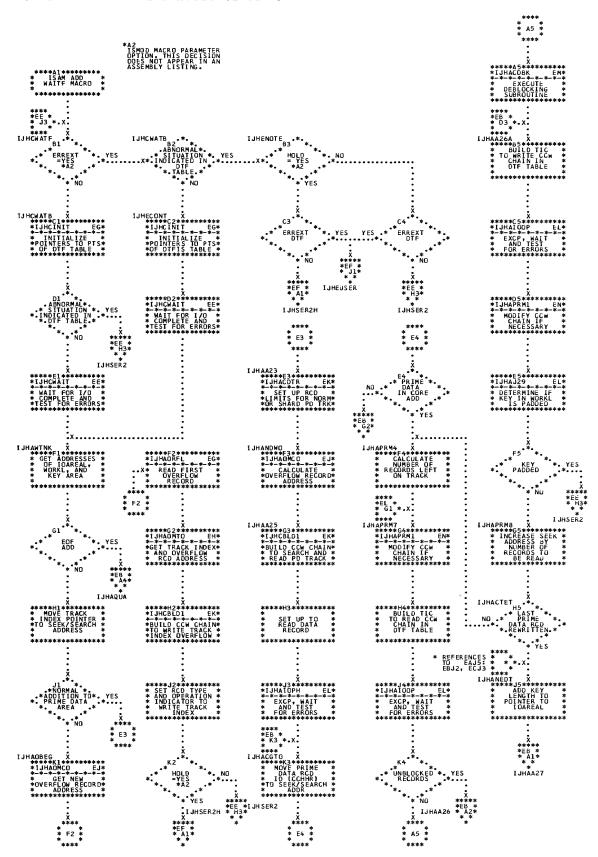
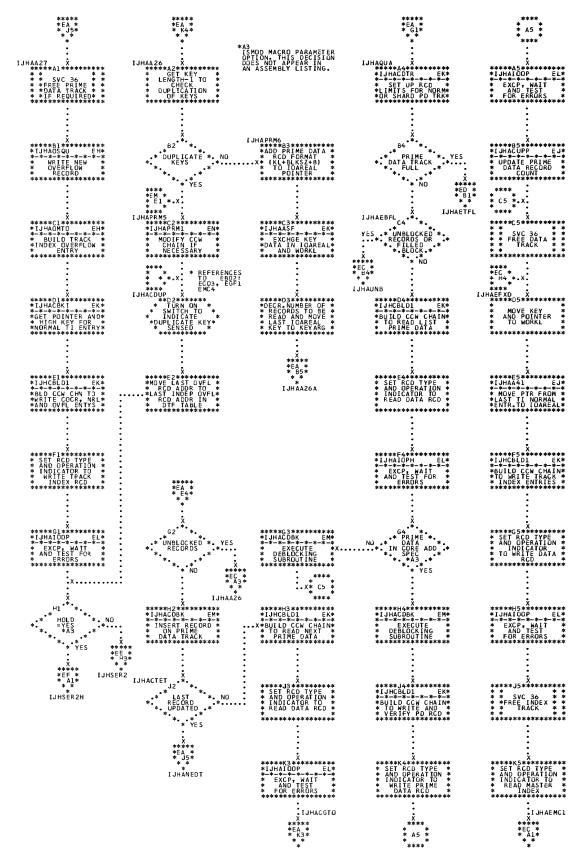


Chart EB. ISAM ADD: WAITF Macrc (2 of 4)



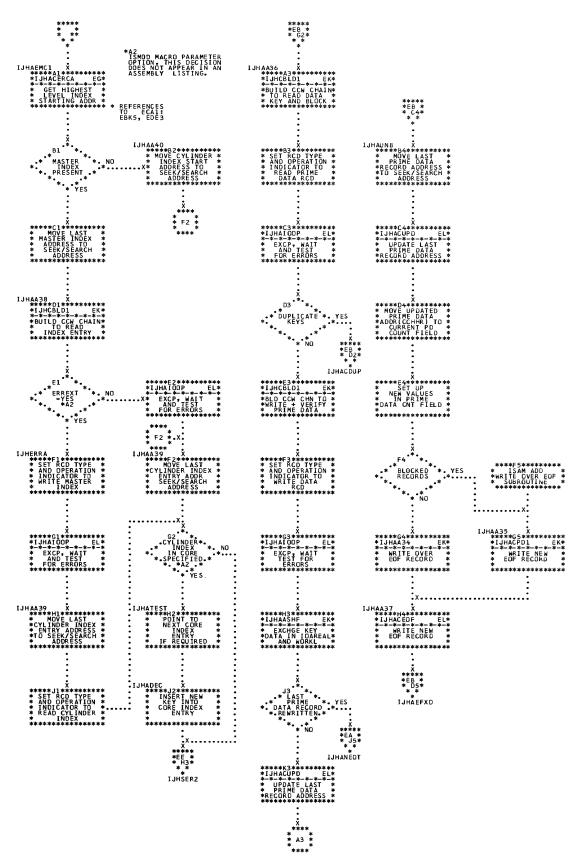
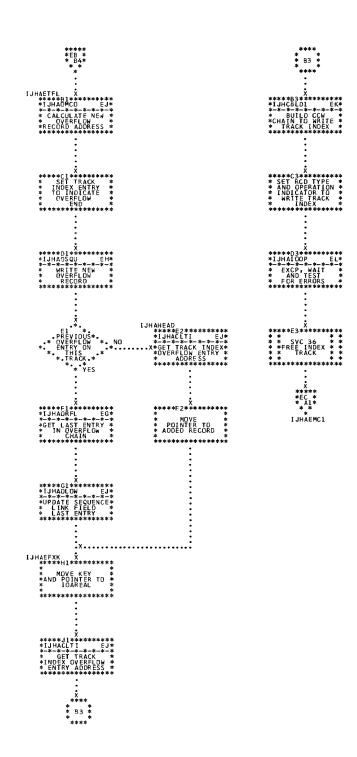


Chart EC. ISAM ADD: WAITF Macro (3 of 4)



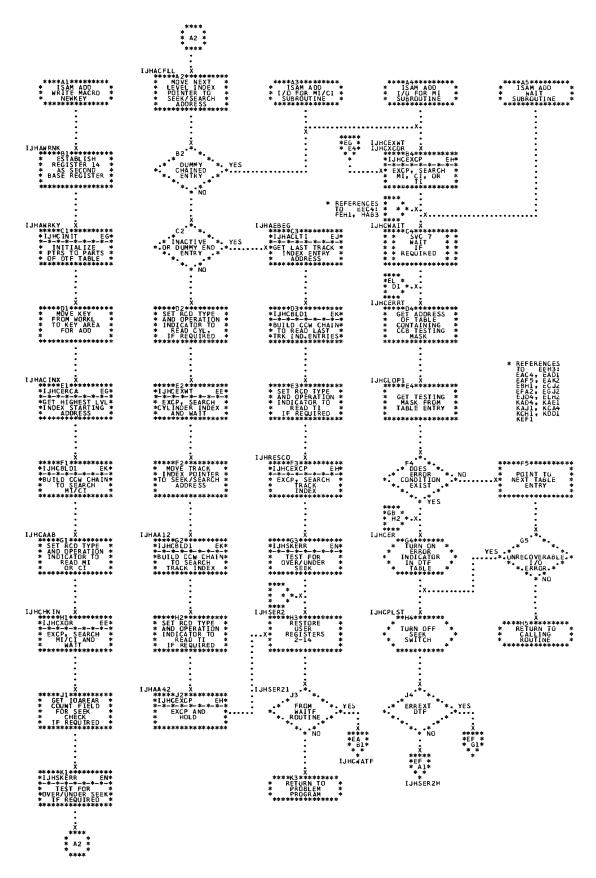
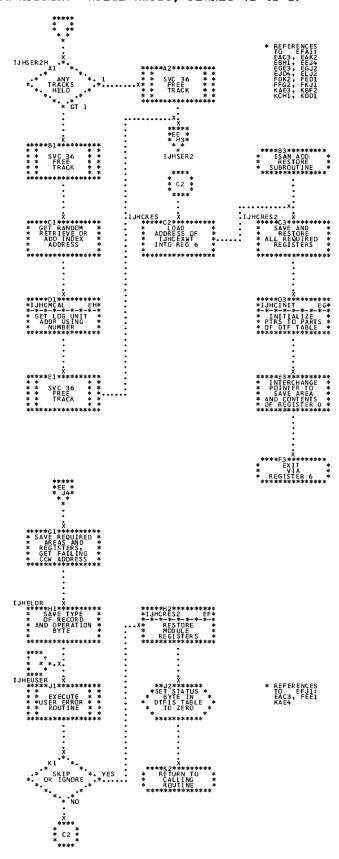


Chart EF. ISAM ADD and ADDRTR: WRITE Macro, NEWKEY (2 of 2)



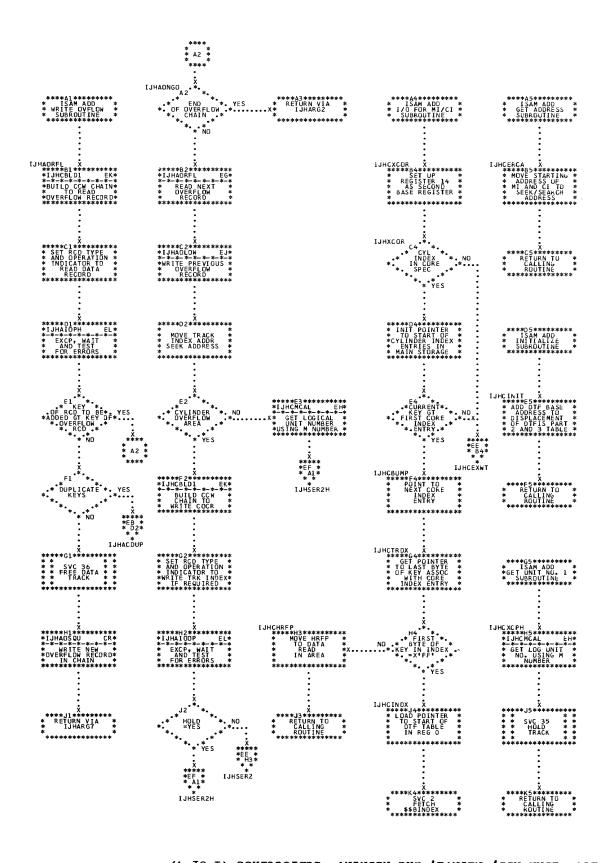
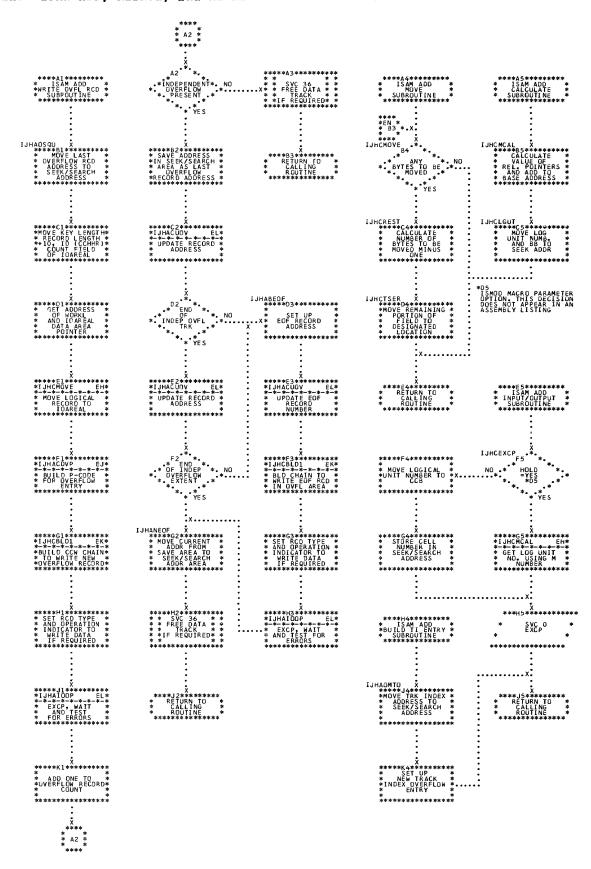


Chart EH. ISAM ADD, RETRVE, and ADDRTR: Subroutines (2 of 7)



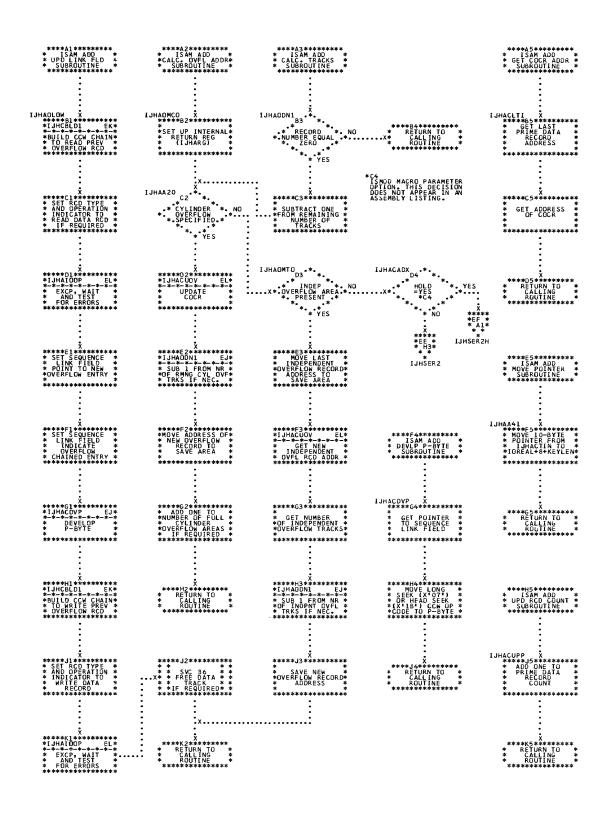
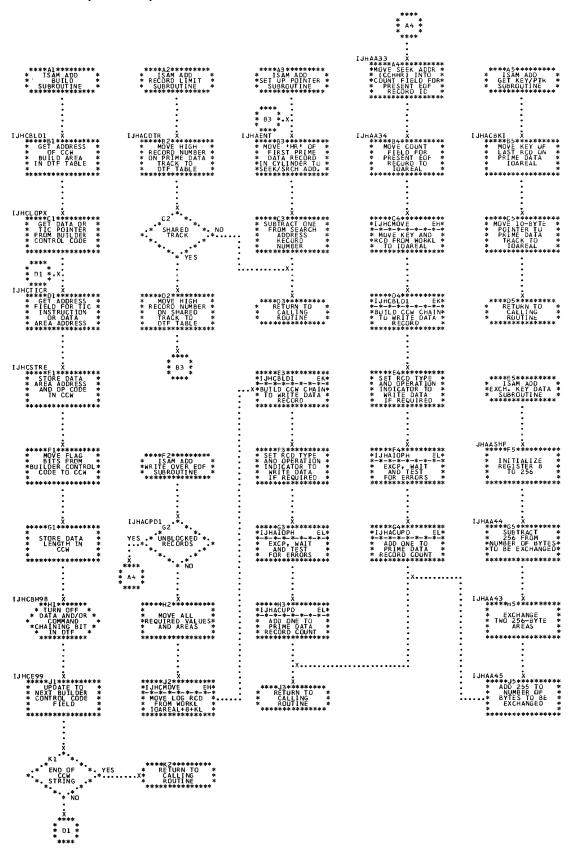


Chart EK. ISAM ADD, RETRYE, and ADDRIR: Subroutines (4 of 7)



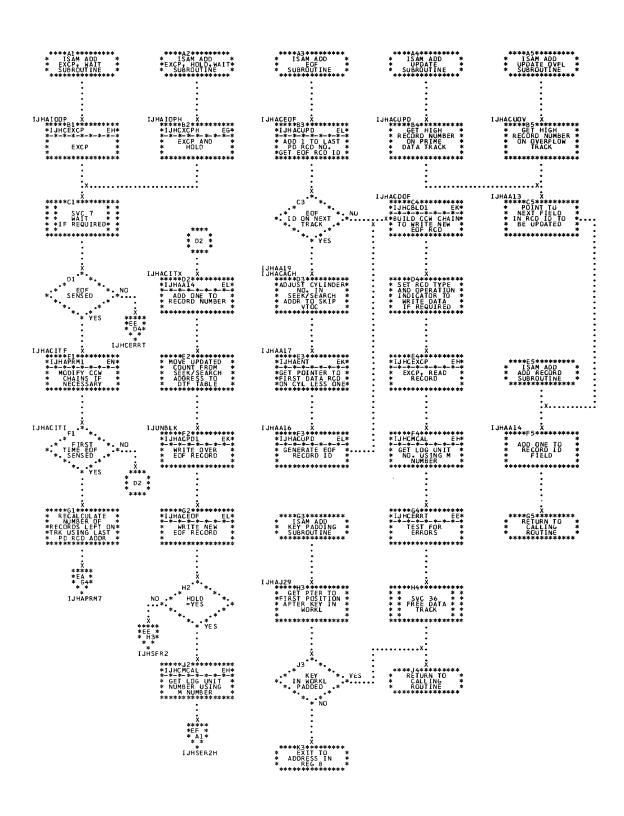
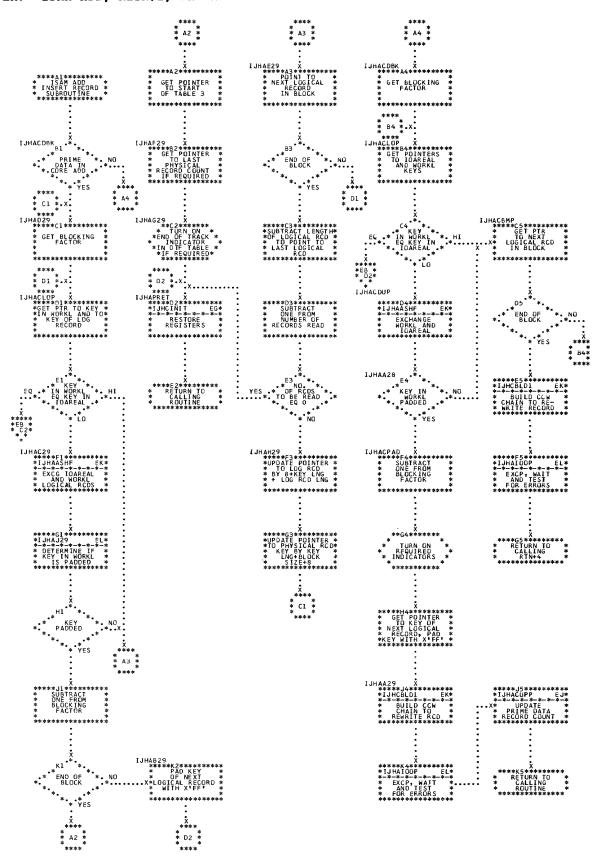


Chart EM. ISAM ADD, RETRYE, and ADDRIK: Subroutines (6 of 7)



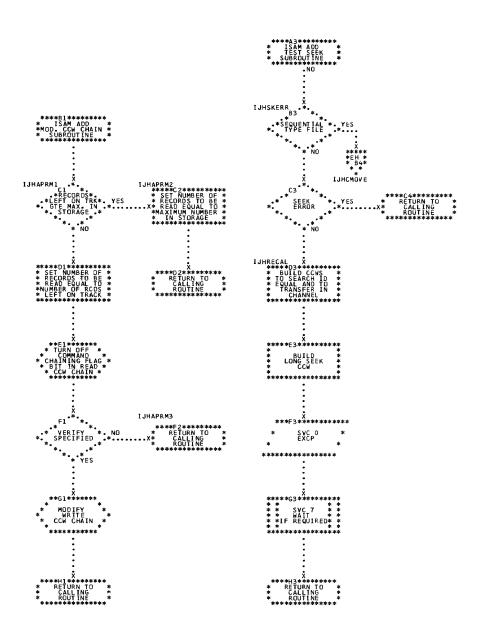
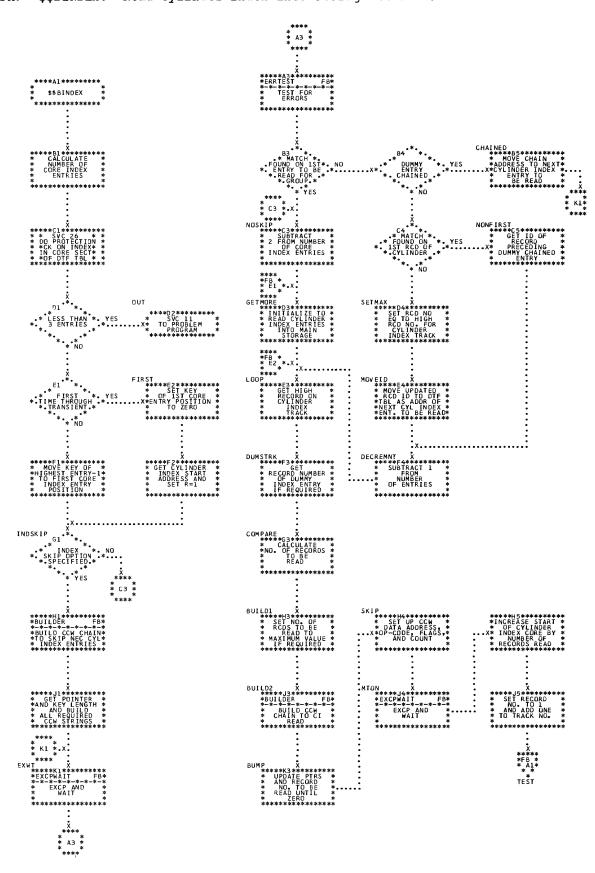


Chart FA. \$\$BINDEX: Read Cylinder Index into Storage (1 of 2)



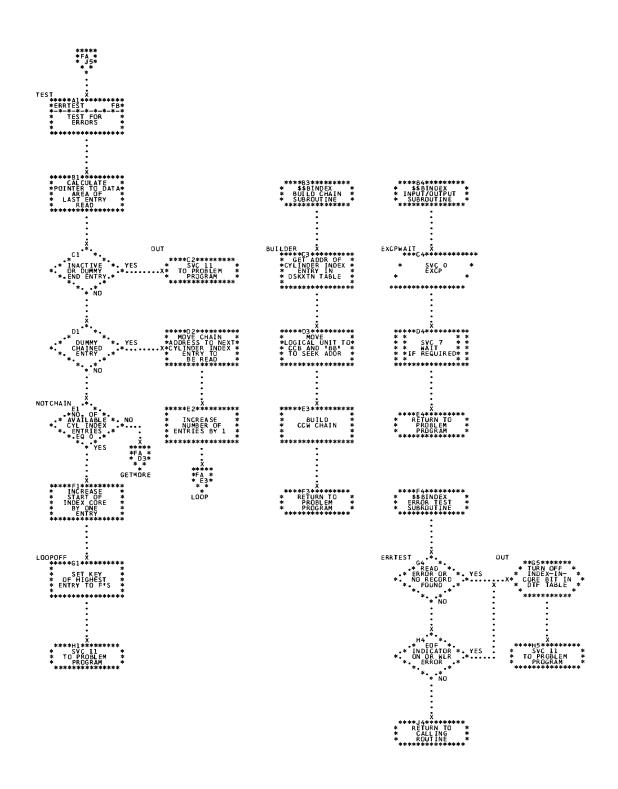


Chart FC. ISAM RETRVE, RANDCM: READ Macro, KEY

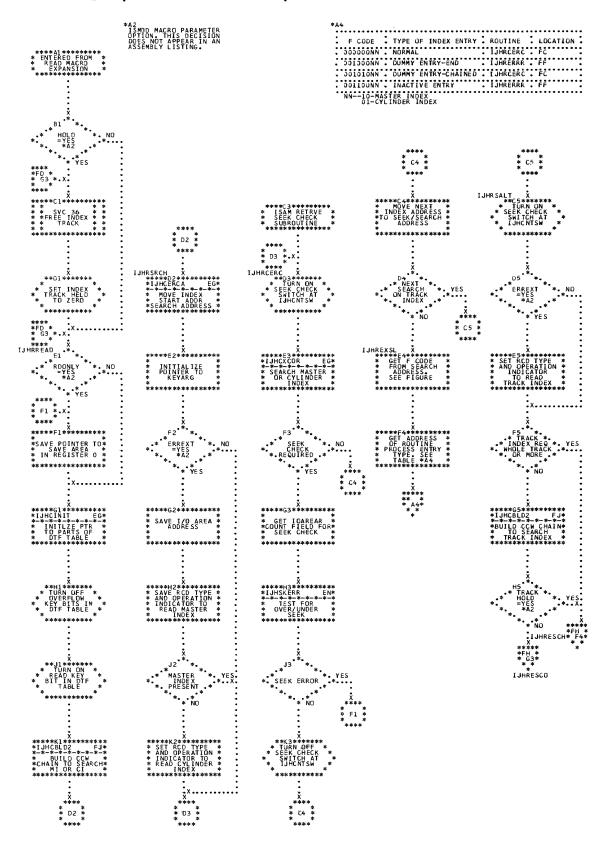
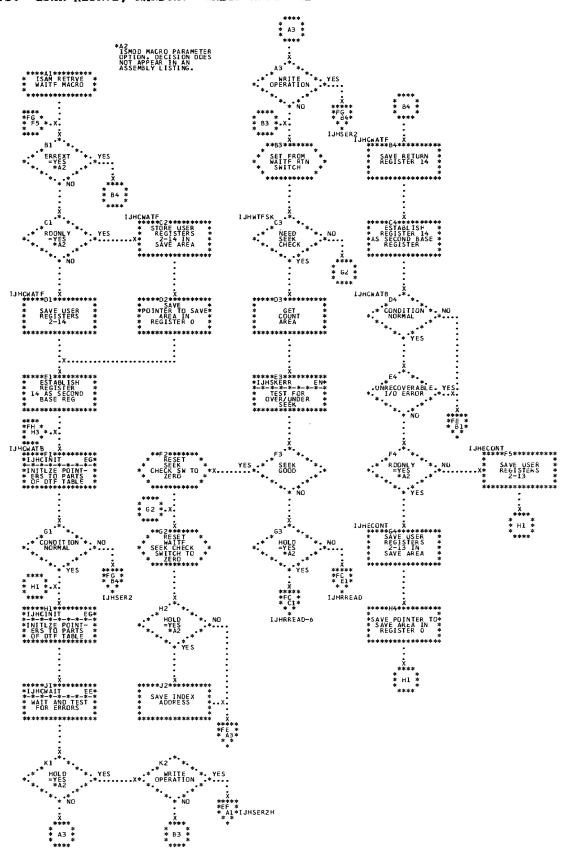
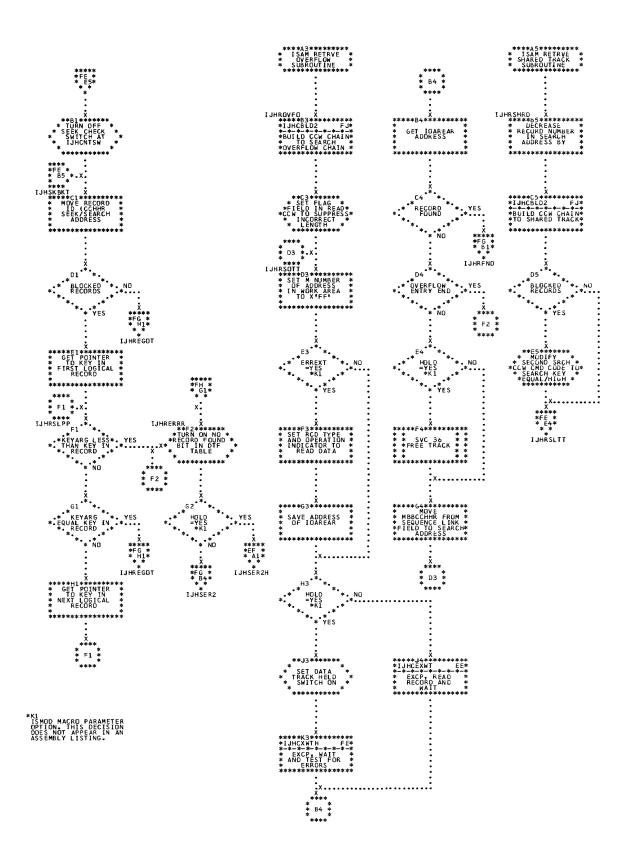
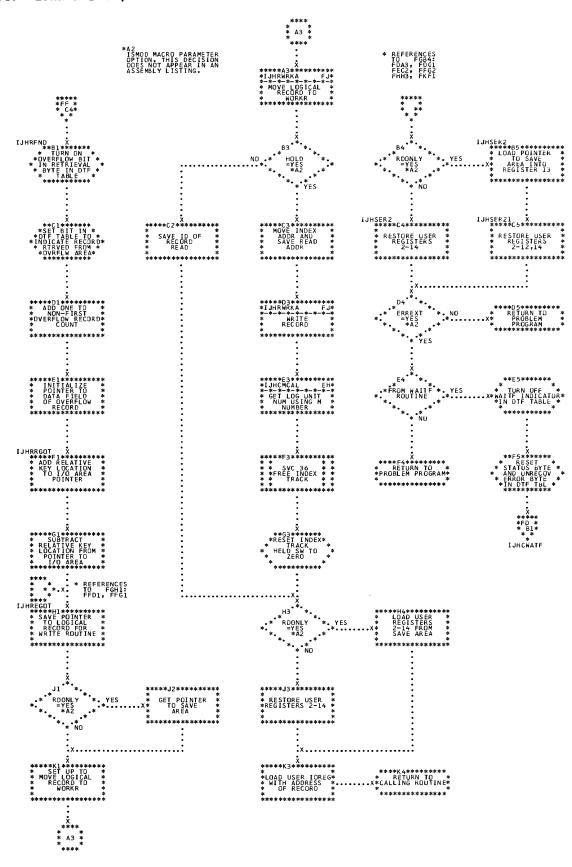
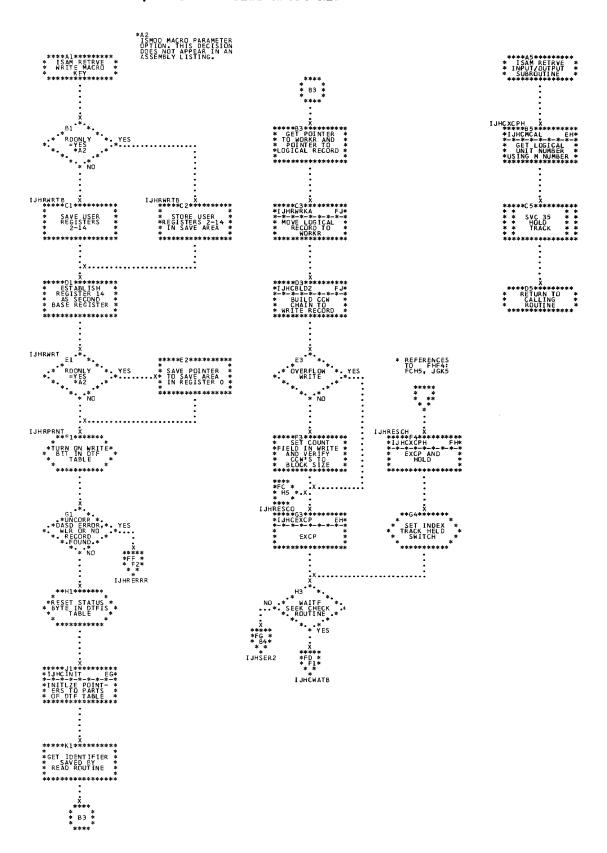


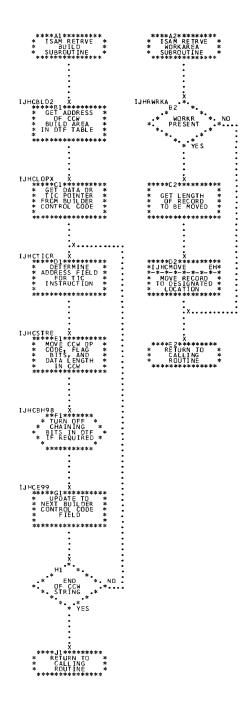
Chart FD. ISAM RETRVE, RANDOM: WAITF Macro (1 of 4)

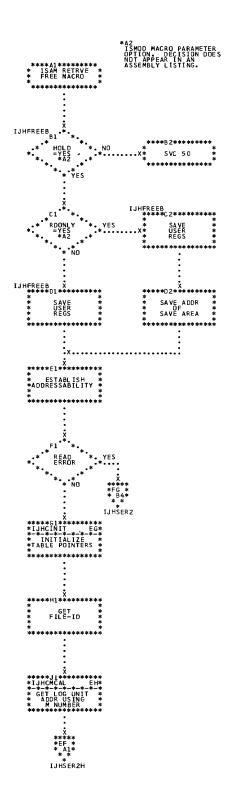












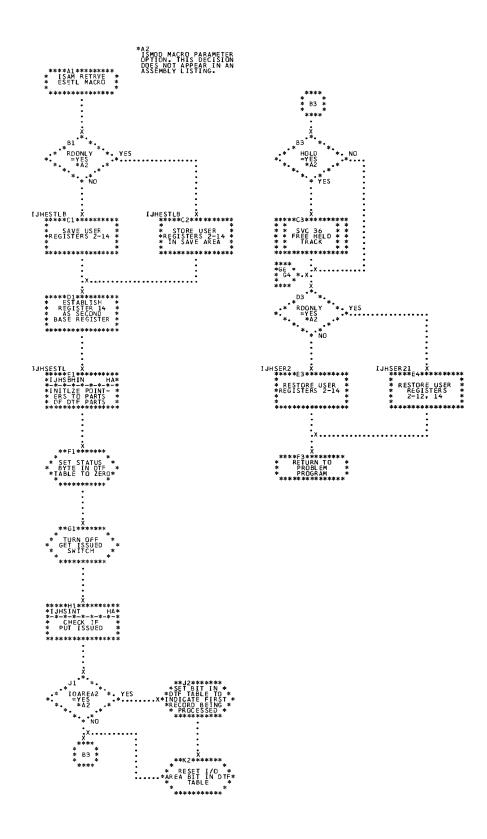


Chart GB. ISAM RETRVE, SEQNTL: GET Macro (1 cf 4)

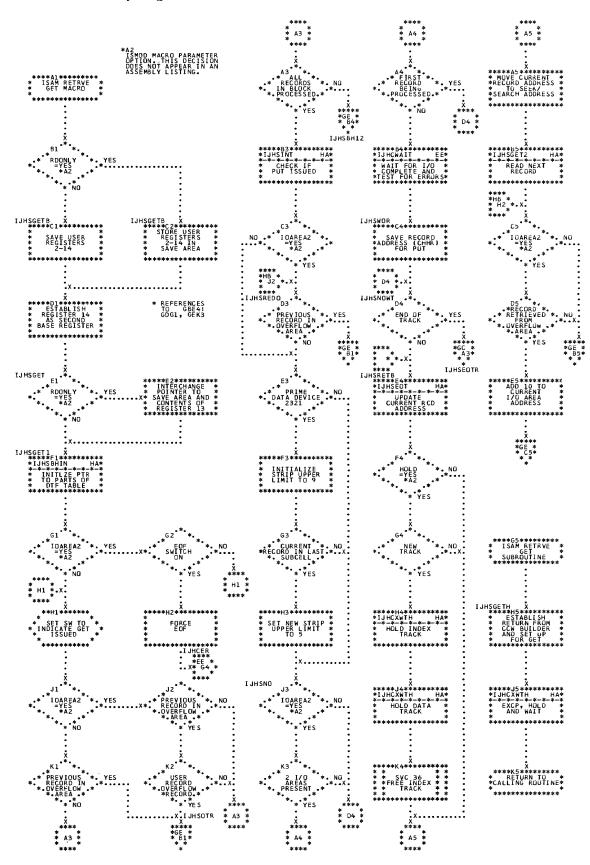
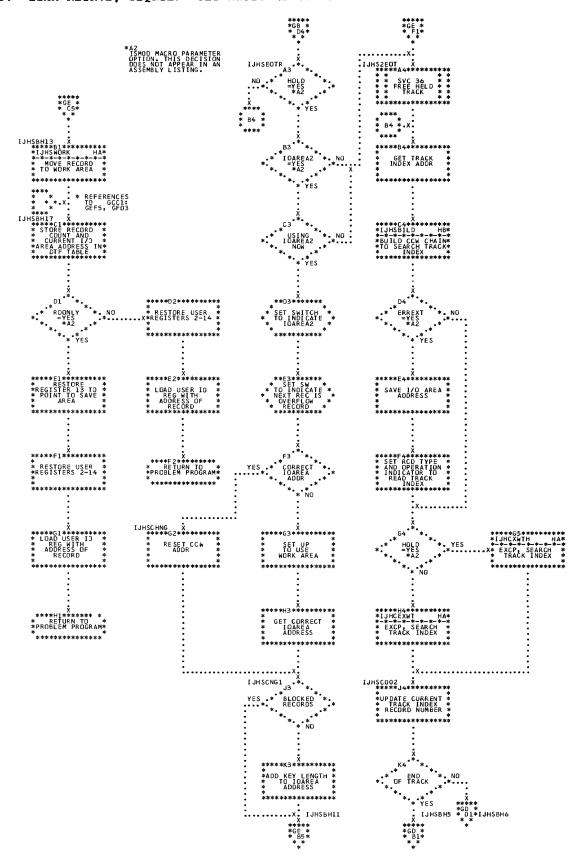
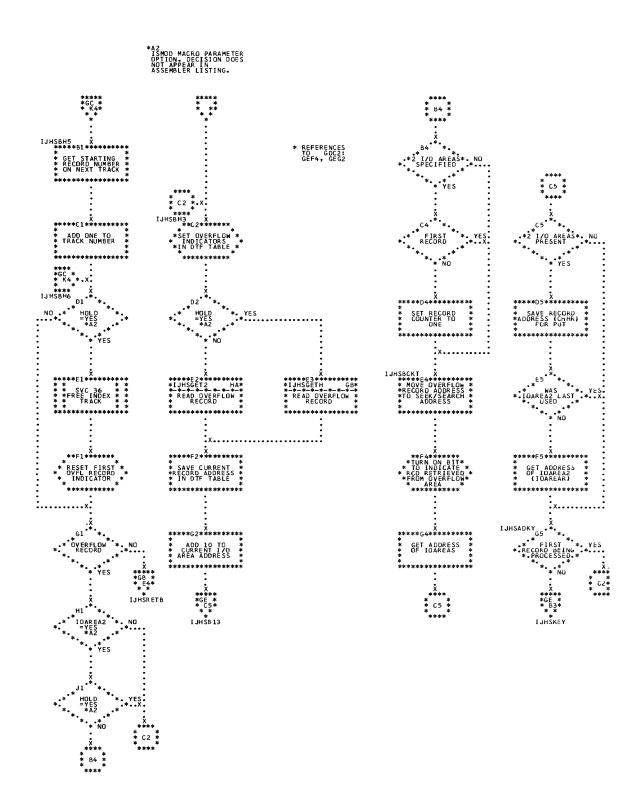


Chart GC. ISAM RETRVE, SEQNTL: GET Macro (2 cf 4)





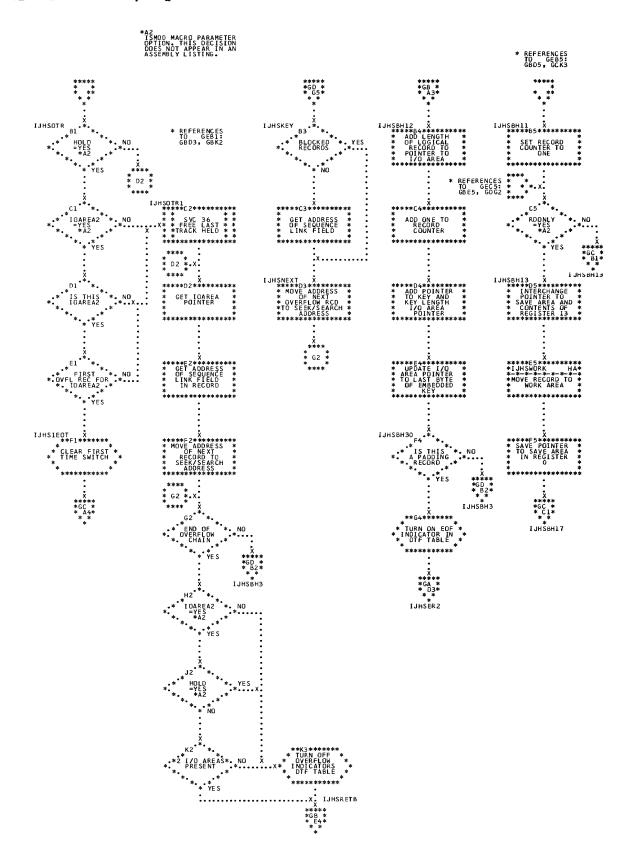


Chart GF. ISAM RETRVE, SEQNTL: PUT Macro

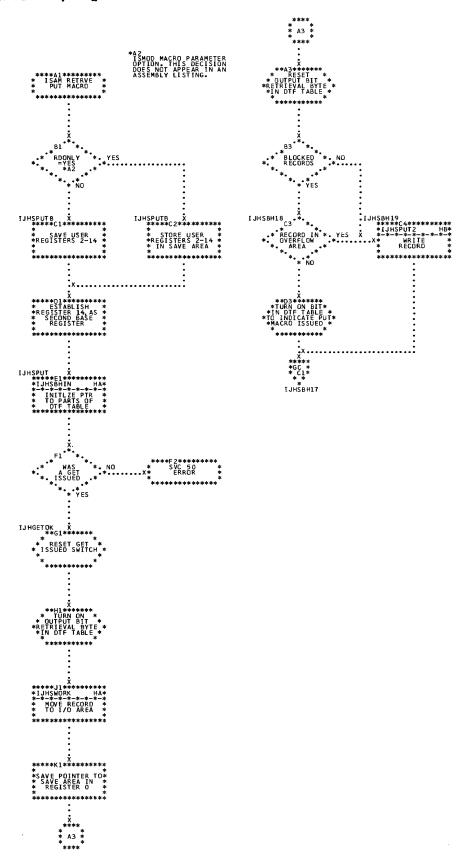


Chart GG. ISAM RETRVE, SEQNTL: SETL Macro, \$\$BSETL (1 of 5)

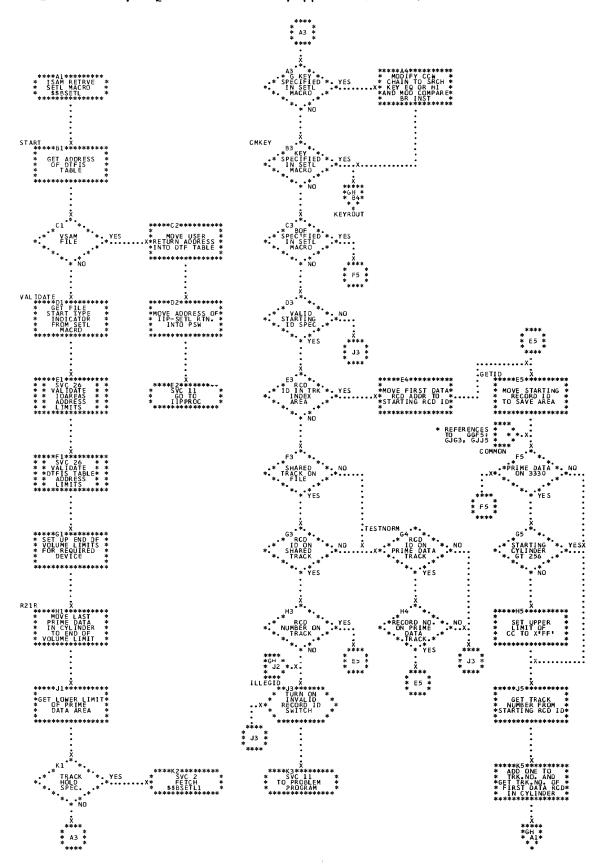
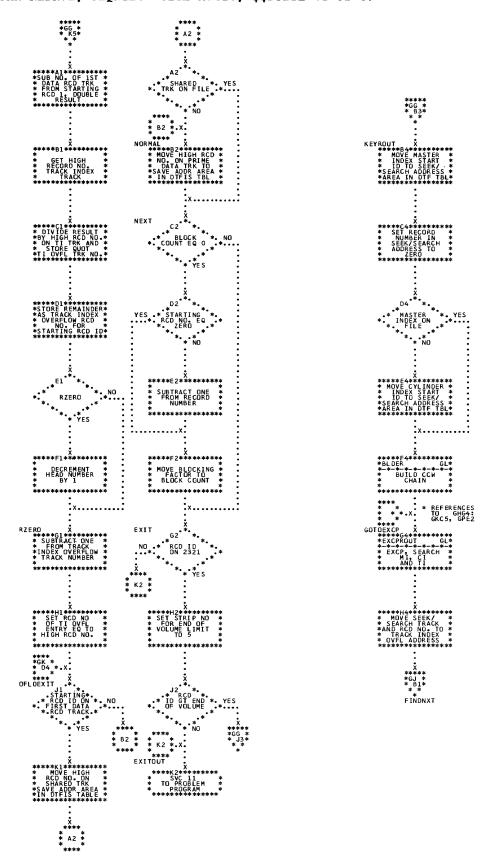
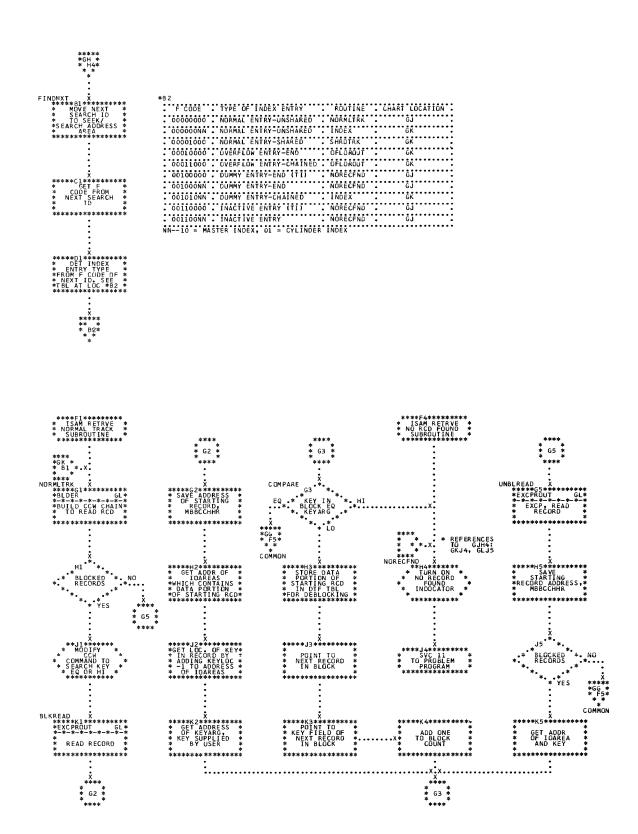


Chart GH. ISAM RETRVE, SEQNTL: SETL Macro, \$\$BSETL (2 of 5)





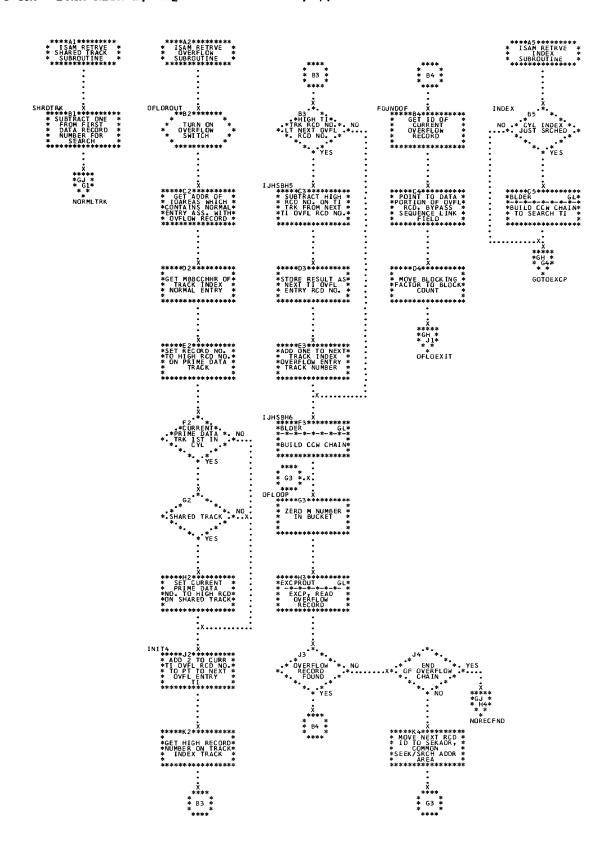


Chart GL. ISAM RETRVE, SEQNTL: SETL Macro, \$\$ESETL (5 of 5)

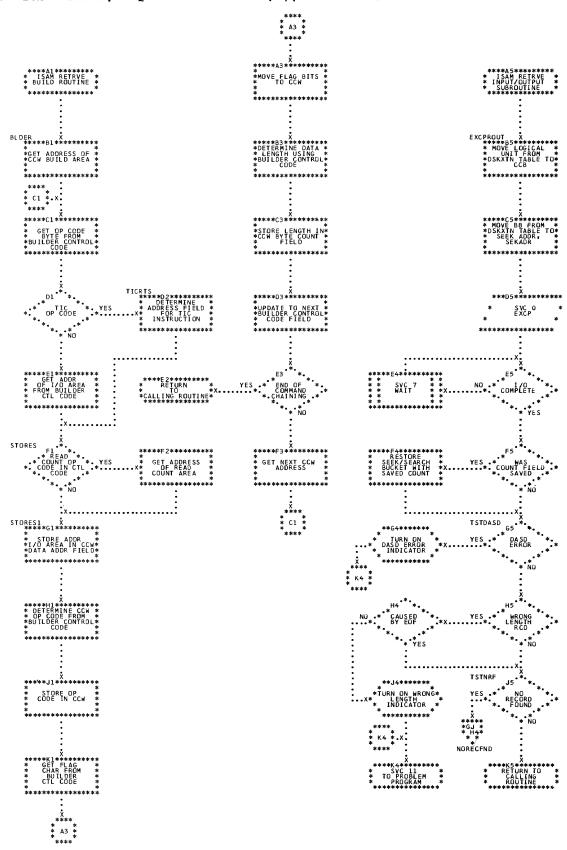
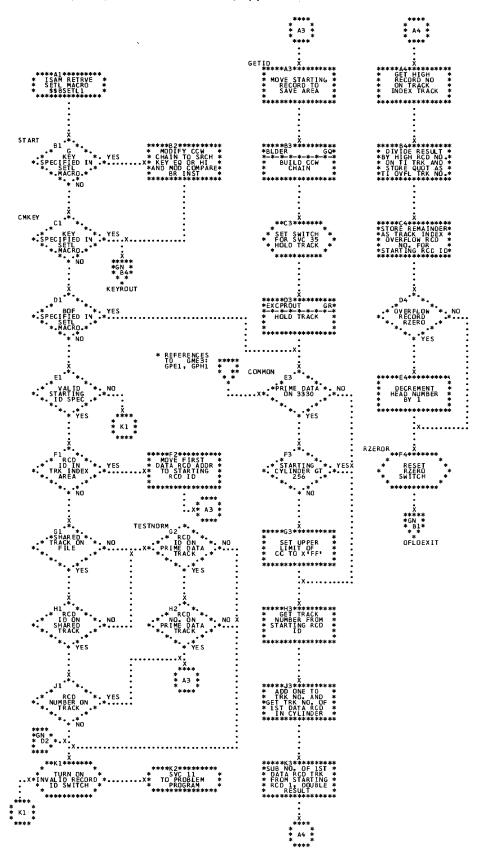
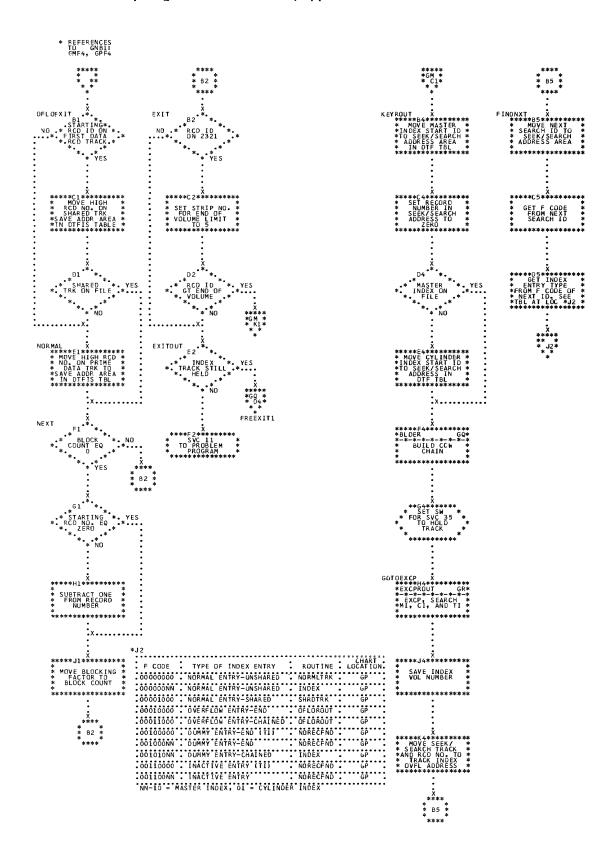


Chart GM. ISAM RETRVE, SEQNTL: SETL Macro, \$\$BSETL1 (1 of 5)





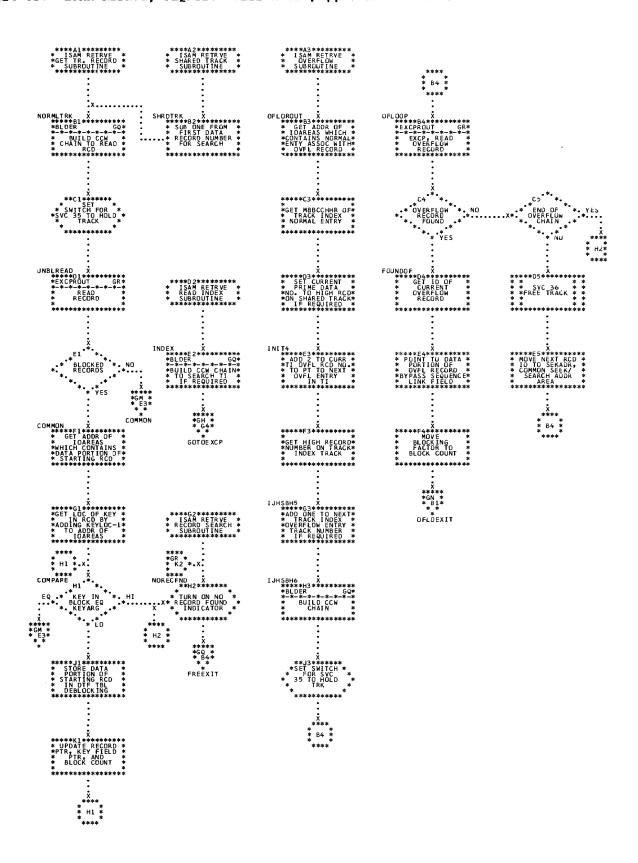
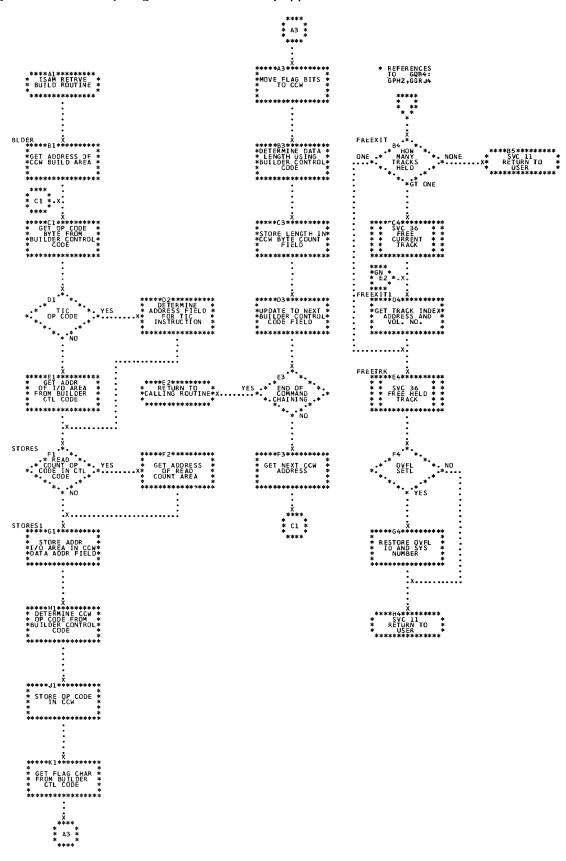
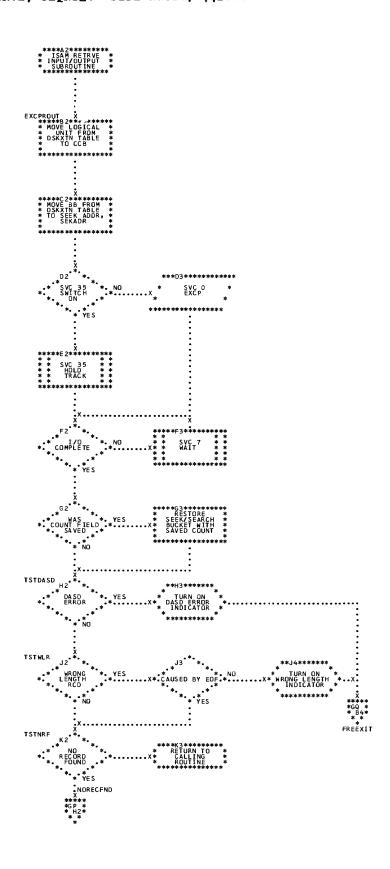
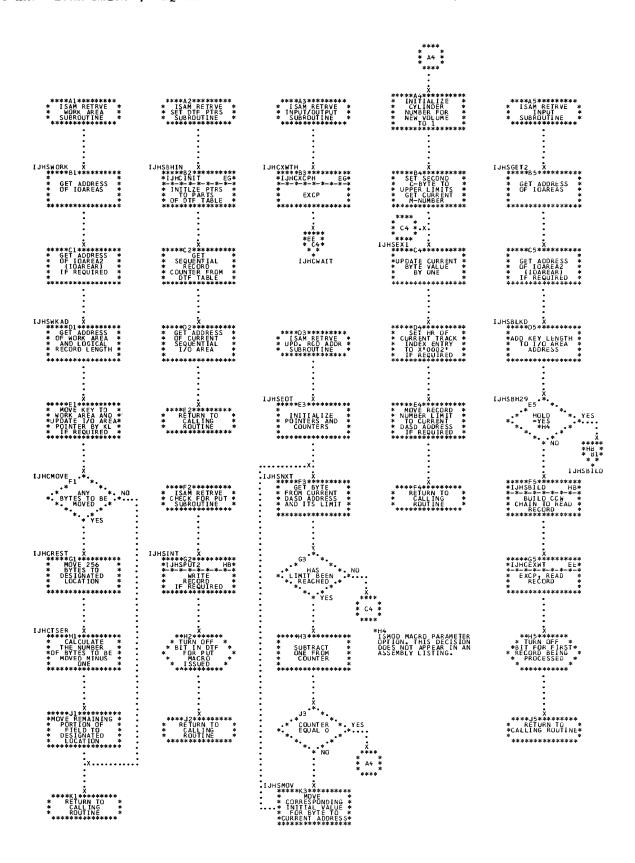
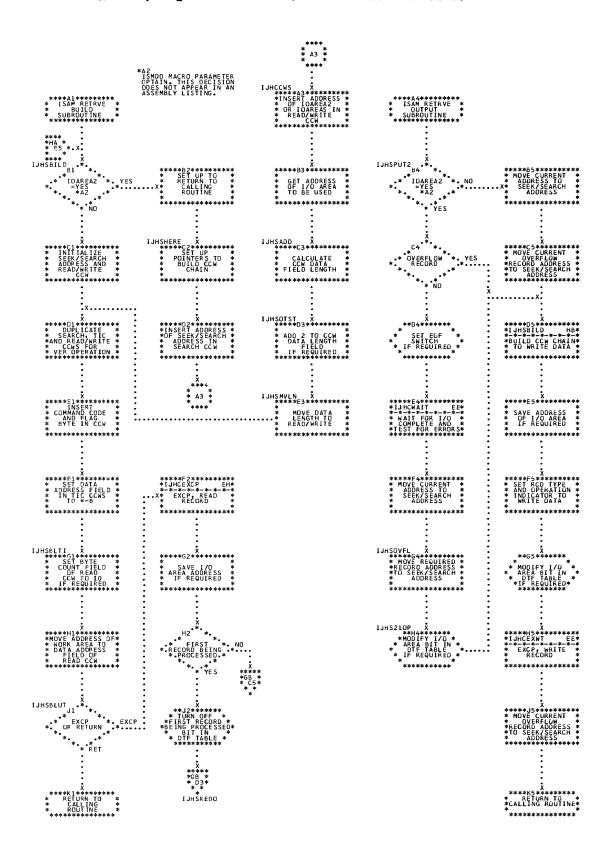


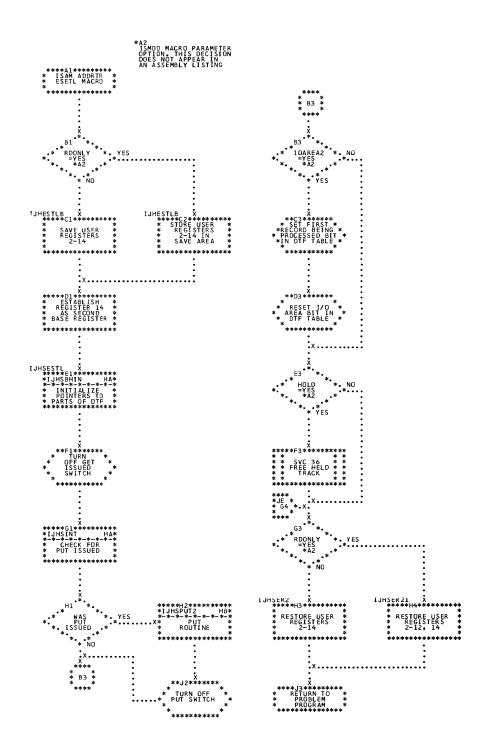
Chart GQ. ISAM RETRVE, SEQNTL: SETL Macro, \$\$BSETL1 (4 of 5)











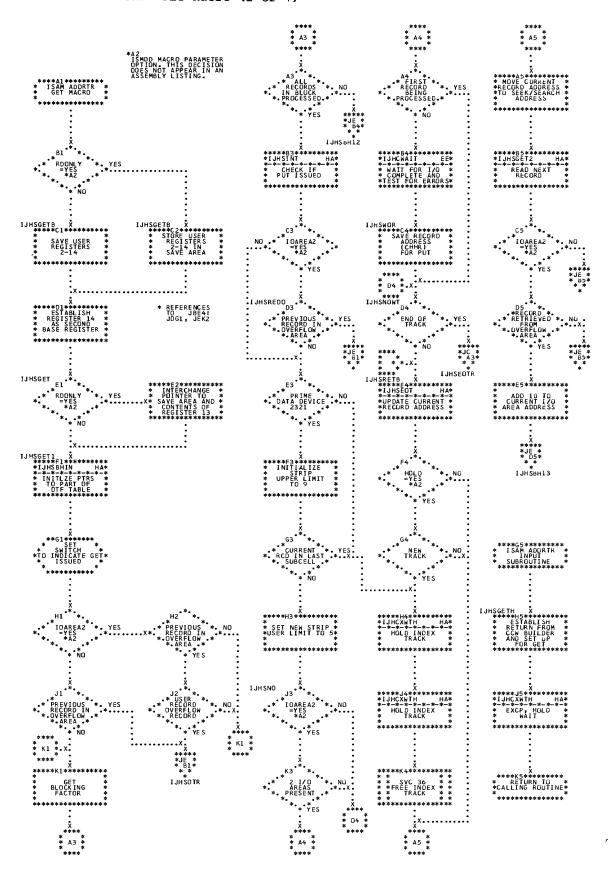
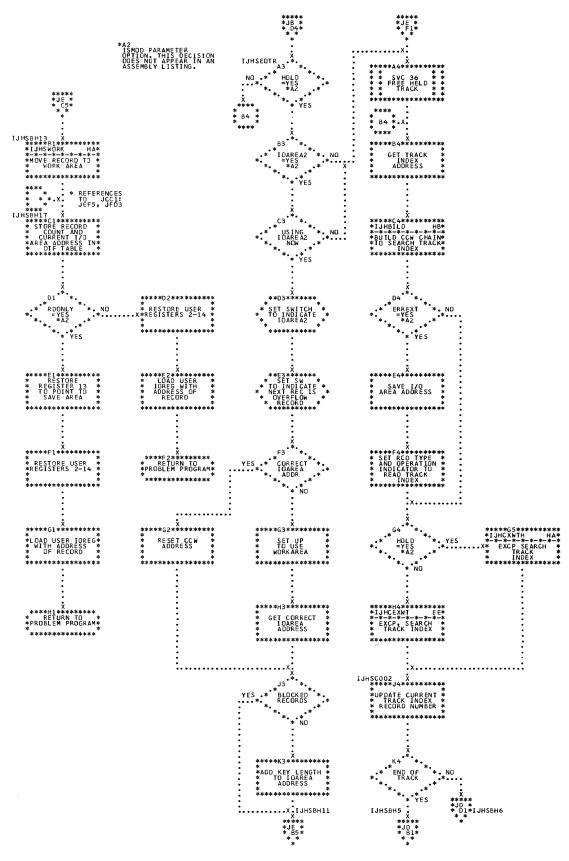


Chart JC. ISAM ADDRTR: GET Macro (2 of 4)



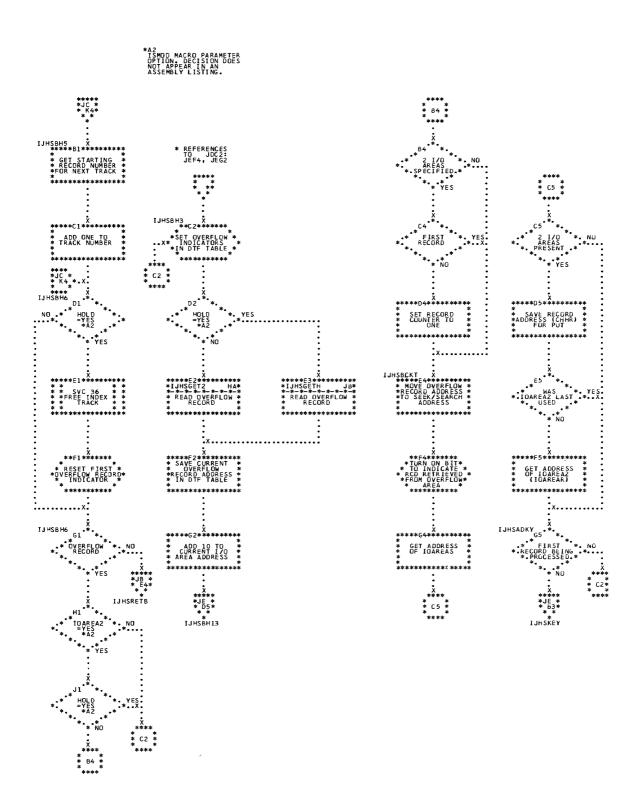


Chart JE. ISAM ADDRTR: GET Macro (4 of 4)

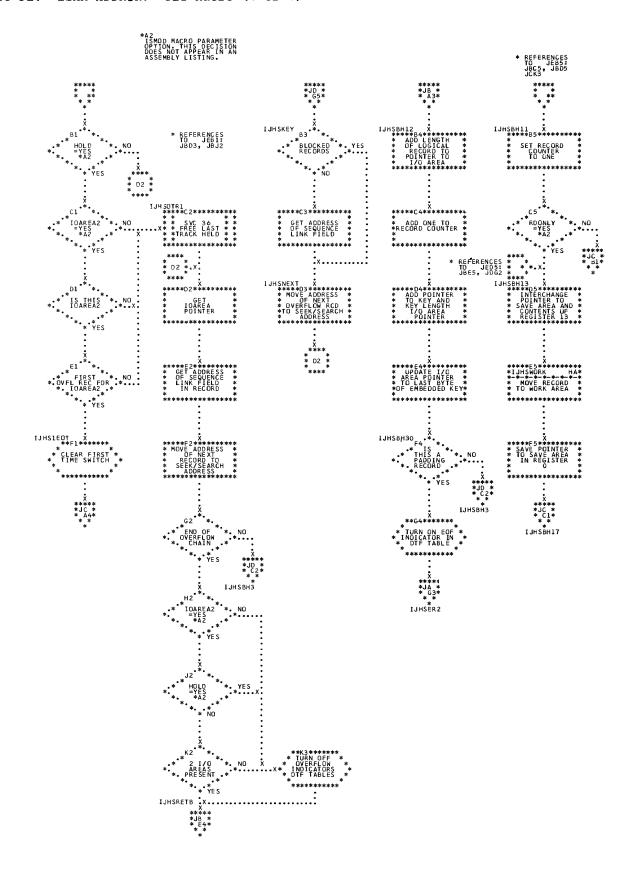


Chart JF. ISAM ADDRTR: PUT Macro

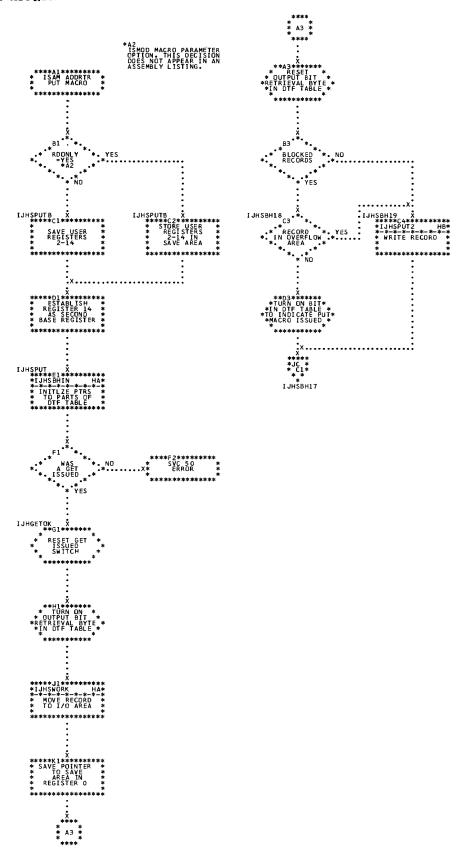
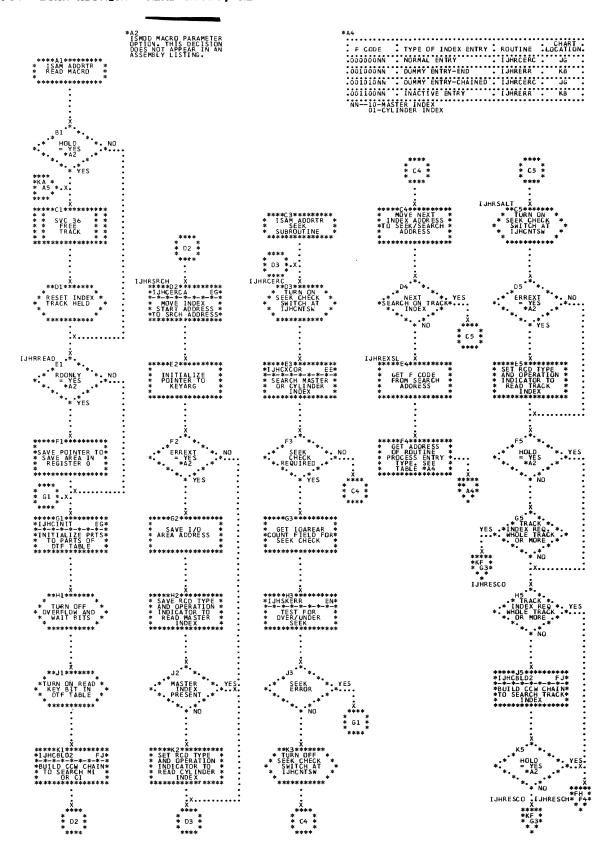


Chart JG. ISAM ADDRTR: KEAD Macro, KEY



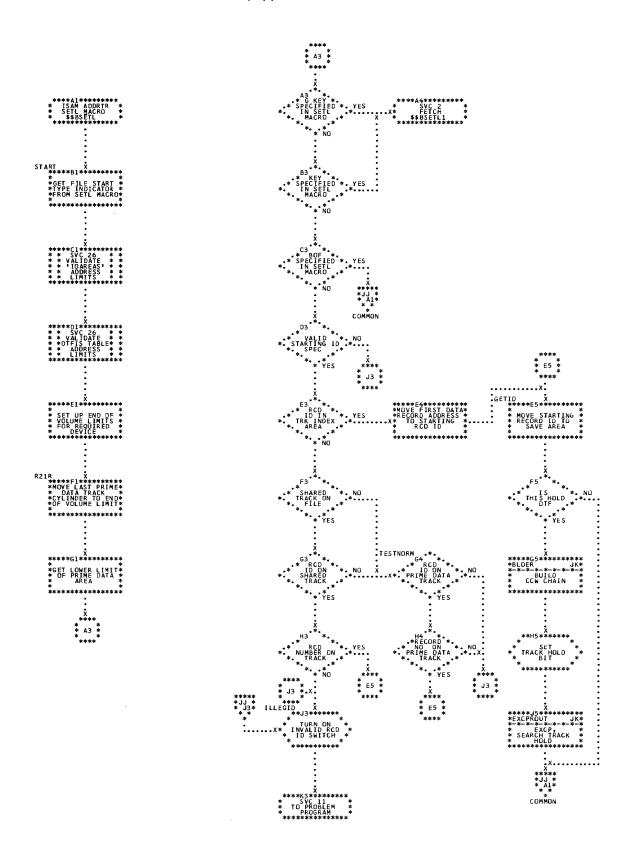
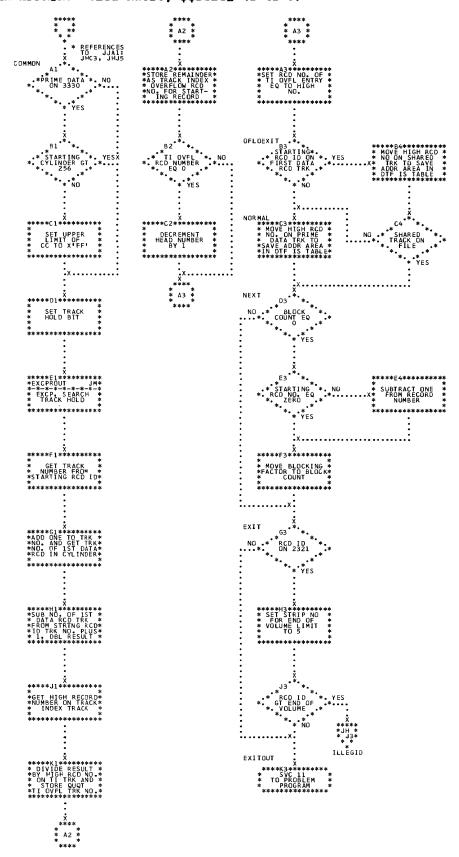


Chart JJ. ISAM ADDRTR: SETI Macro, \$\$BSETL (2 cf 3)



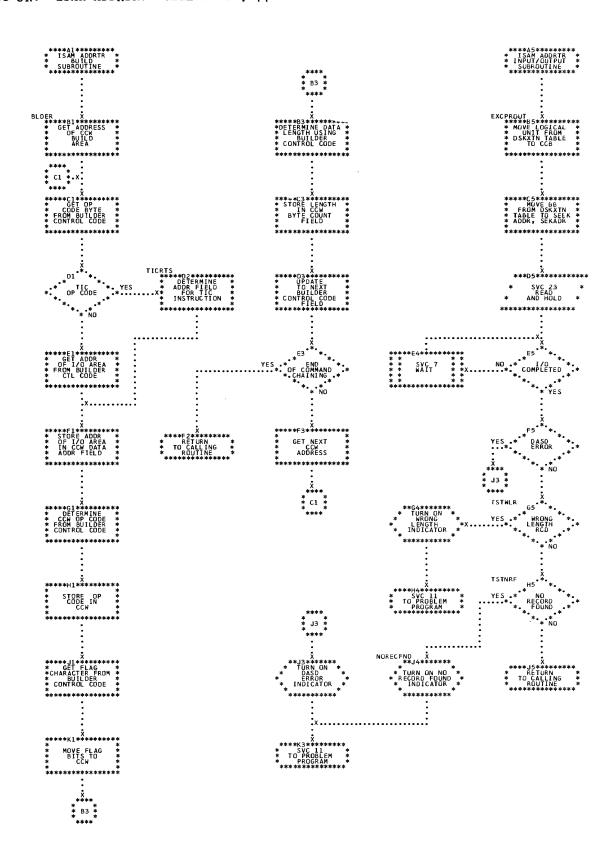


Chart JL. ISAM ADDRTR: SETI Macro, \$\$BSETL1 (1 of 5)

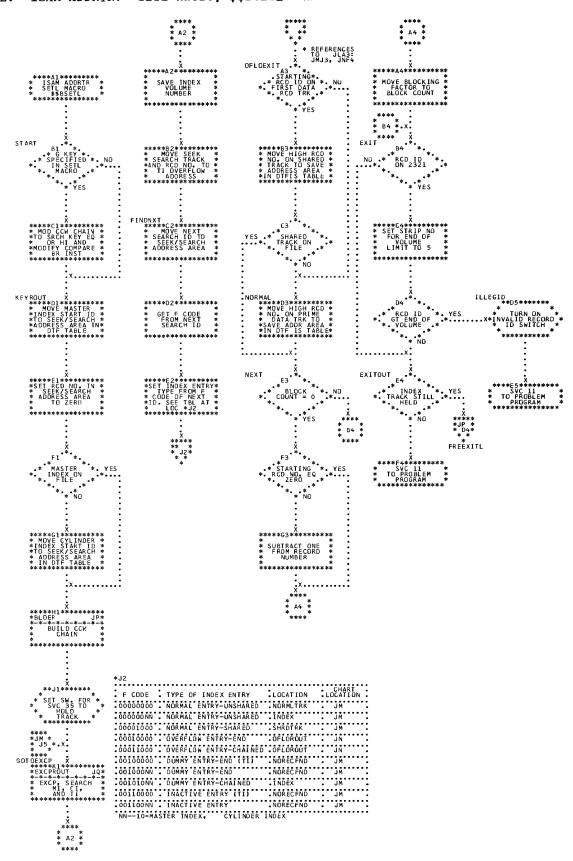


Chart JM. ISAM ADDRTR: SETL Macro, \$\$BSETL1 (2 of 5)

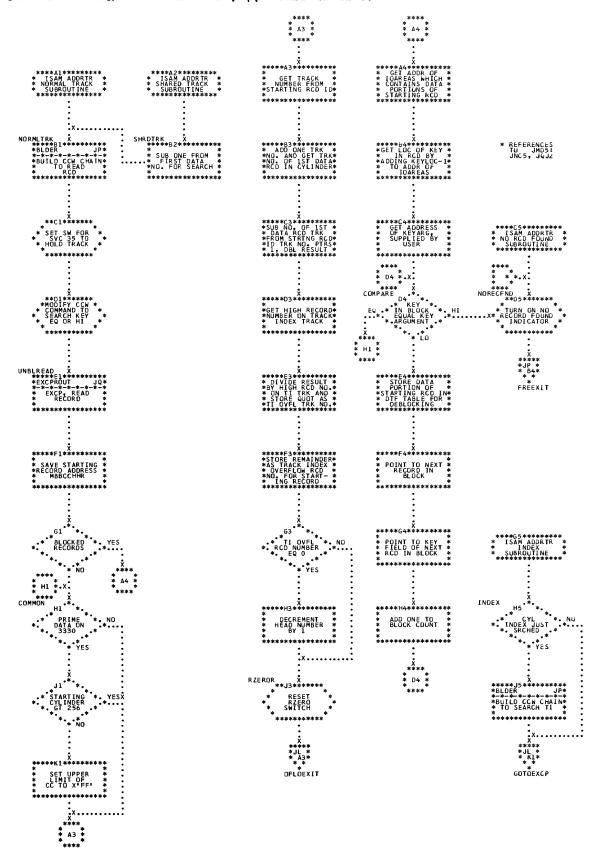


Chart JN. ISAM ADDRTR: SETI Macro, \$\$BSETL1 (3 of 5)

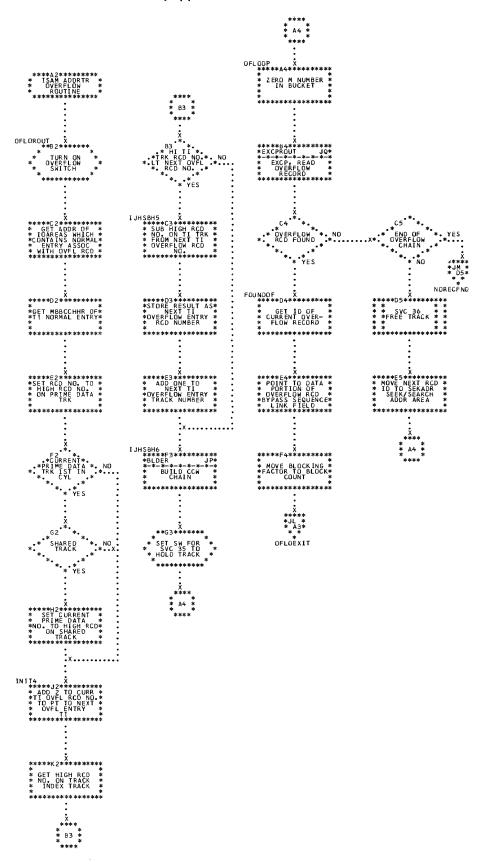
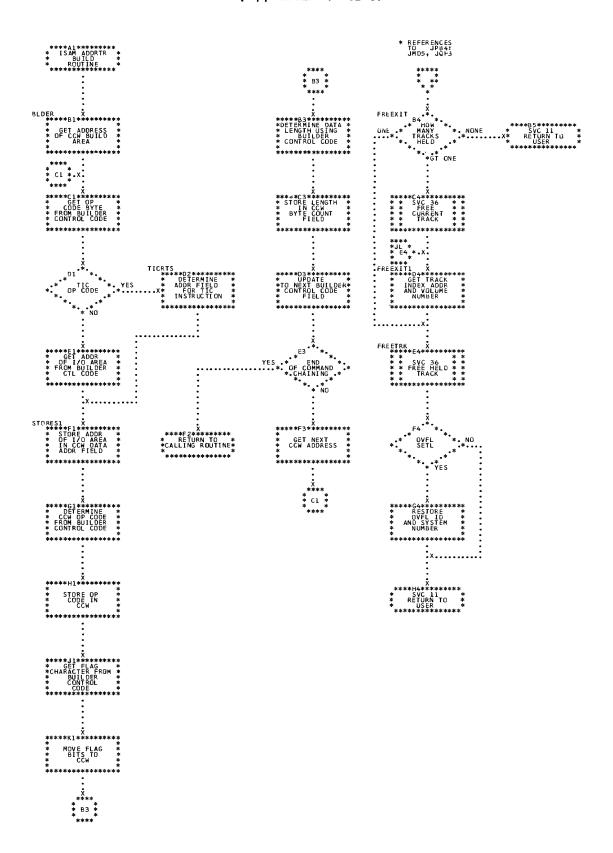


Chart JP. ISAM ADDRTR: SETL Macro, \$\$BSETL1 (4 of 5)



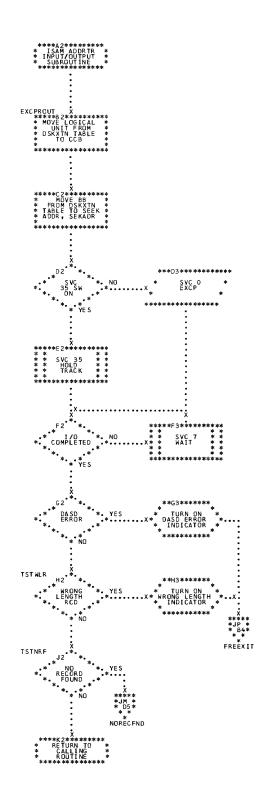
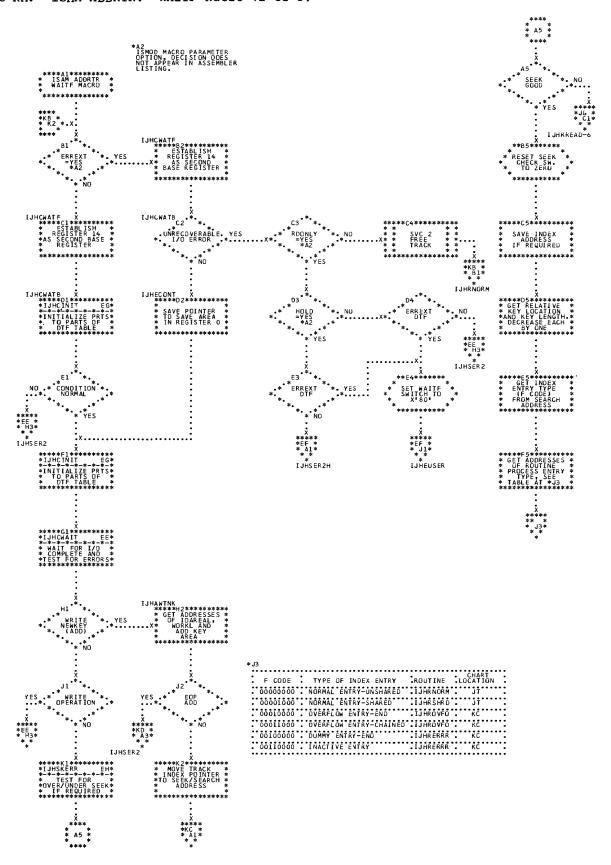


Chart KA. ISAM ADDRTR: WAITF Macro (1 of 5)



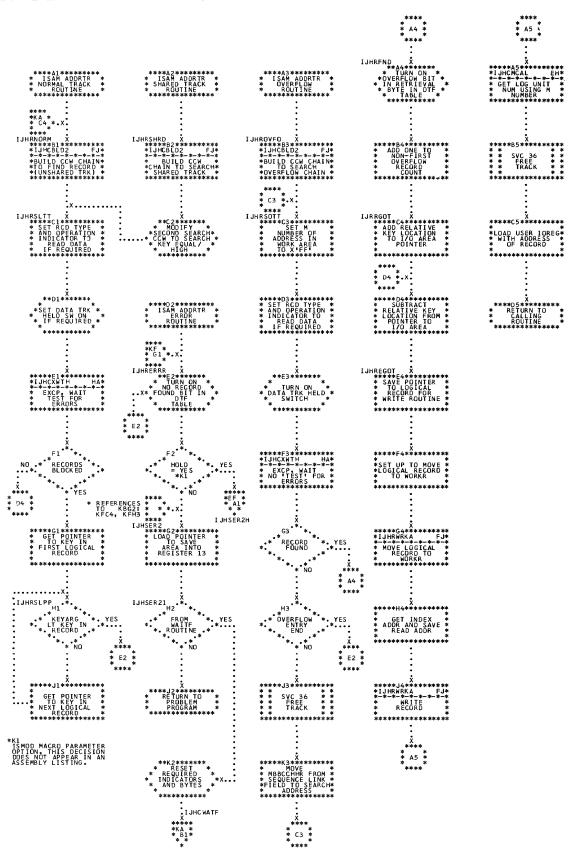


Chart KC. ISAM ADDRTR: WAITF Macro (3 of 5)

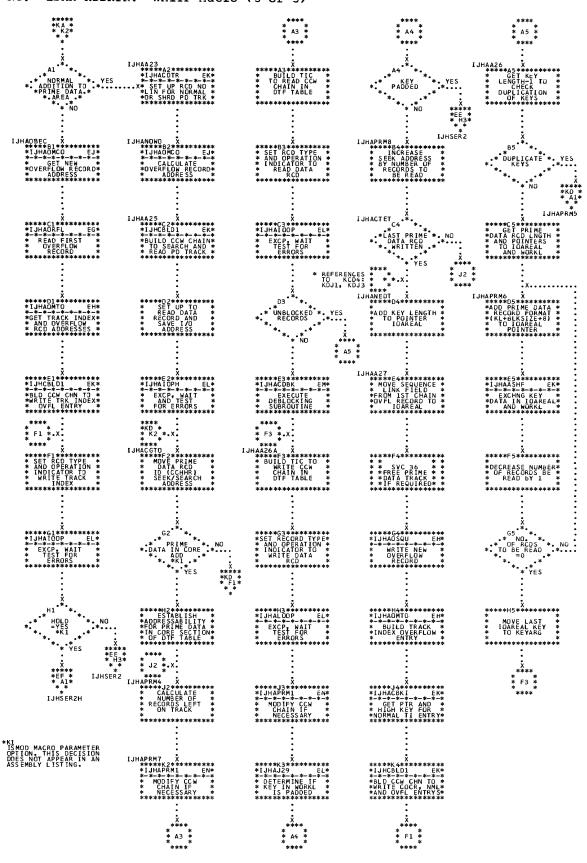
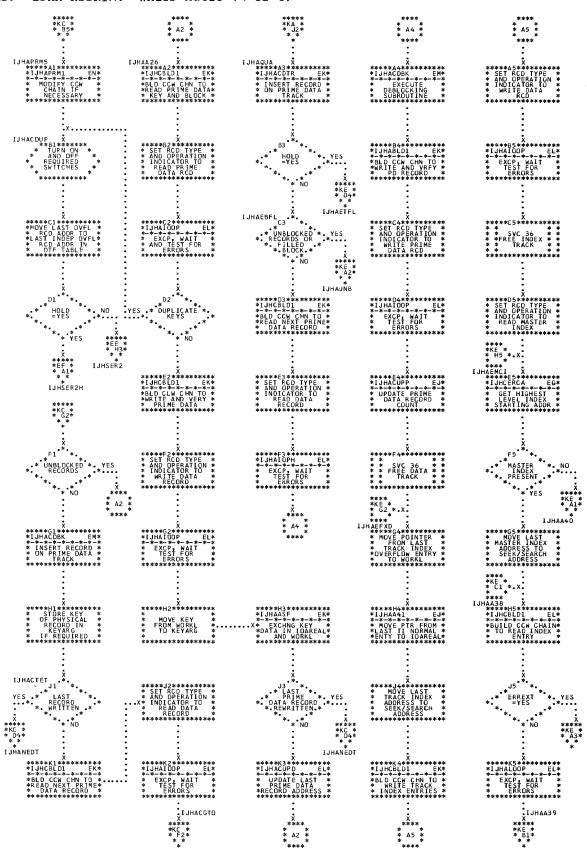
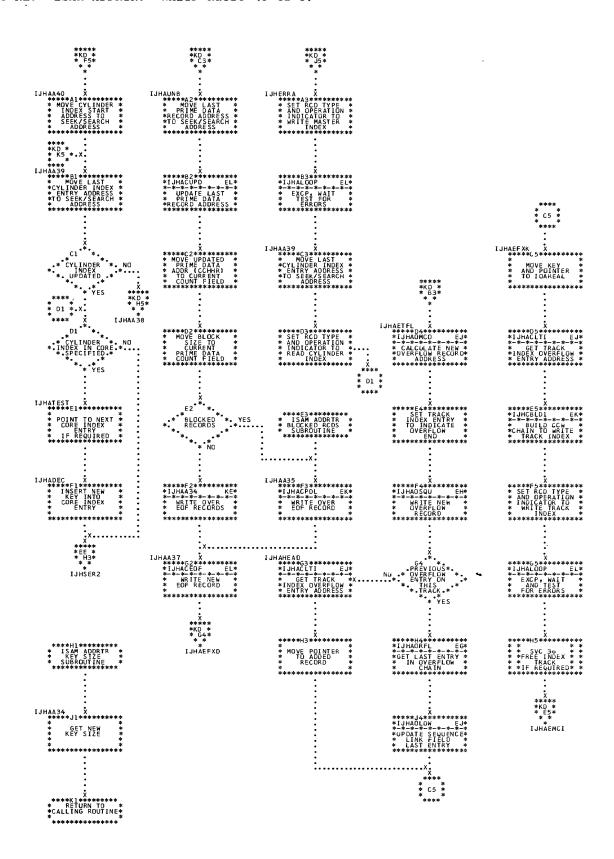
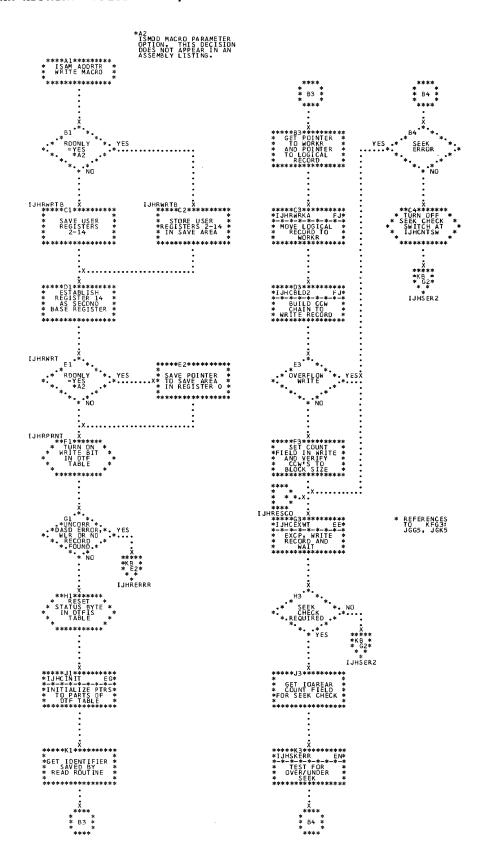
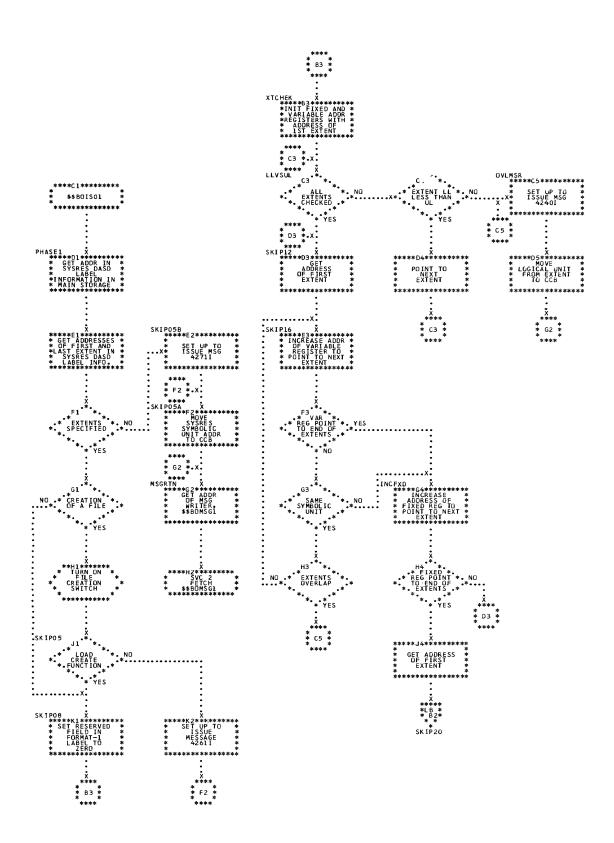


Chart KD. ISAM ADDRTK: WAITF Macro (4 of 5)









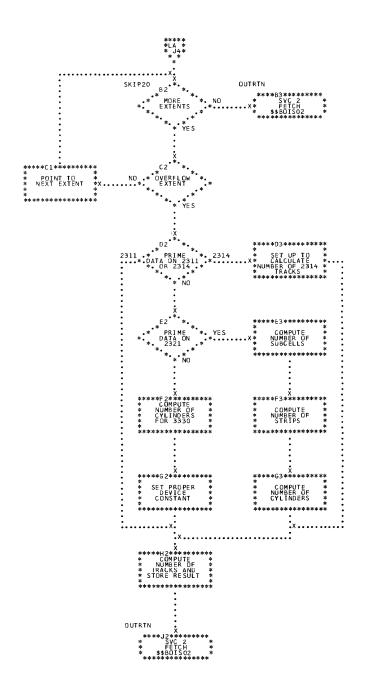
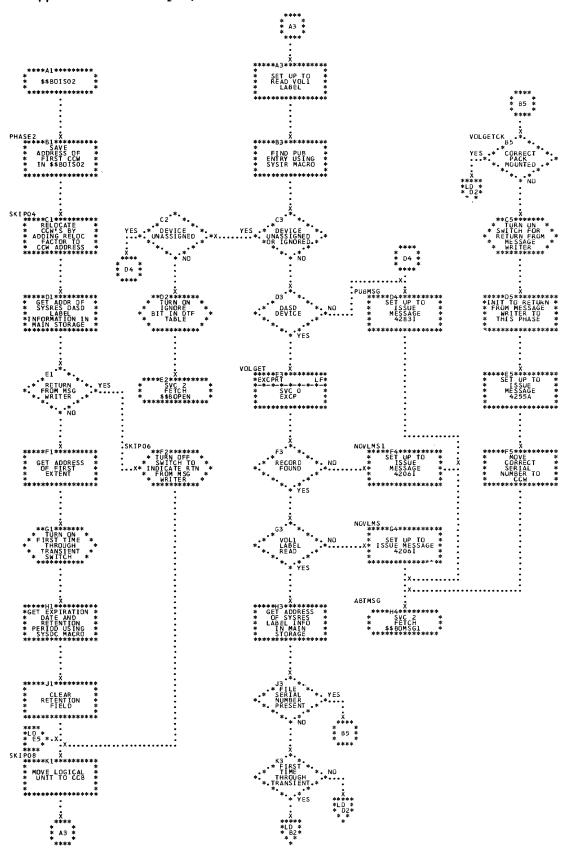
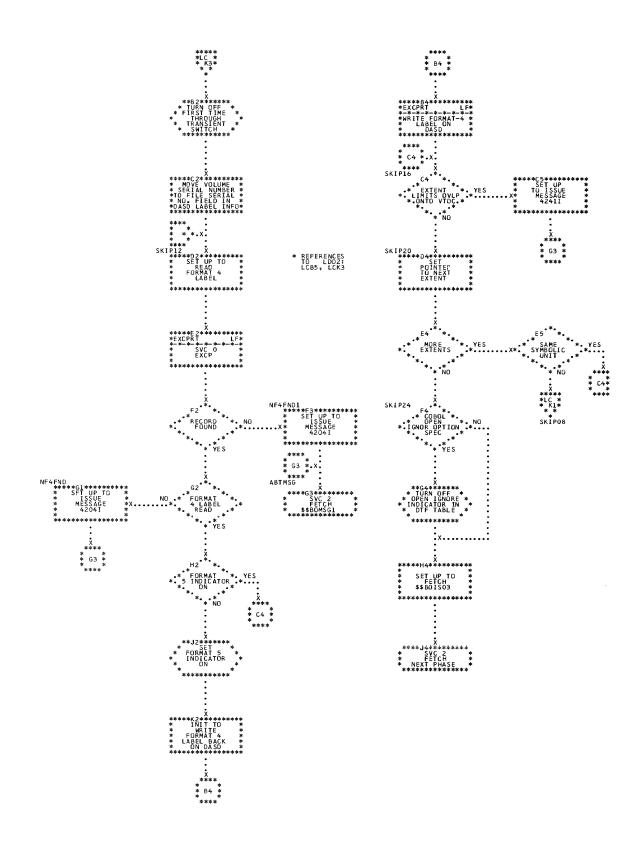
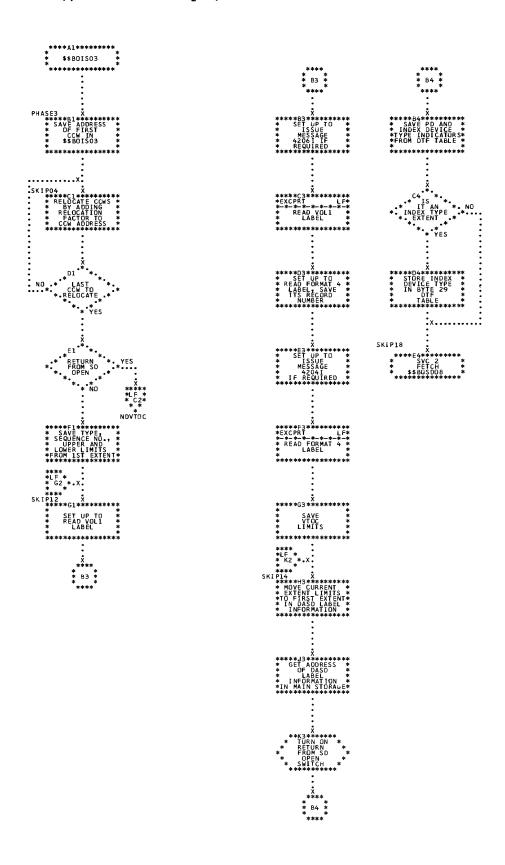
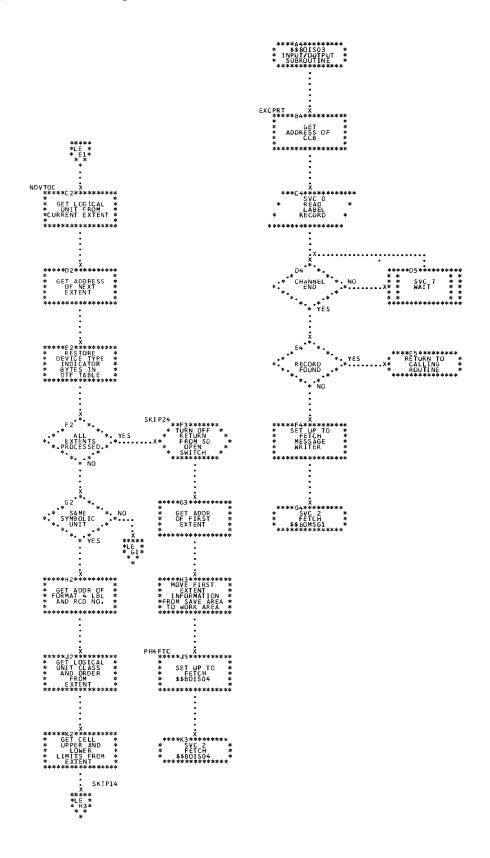


Chart LC. \$\$BOIS02: ISAM Open, Phase 2 (1 of 2)









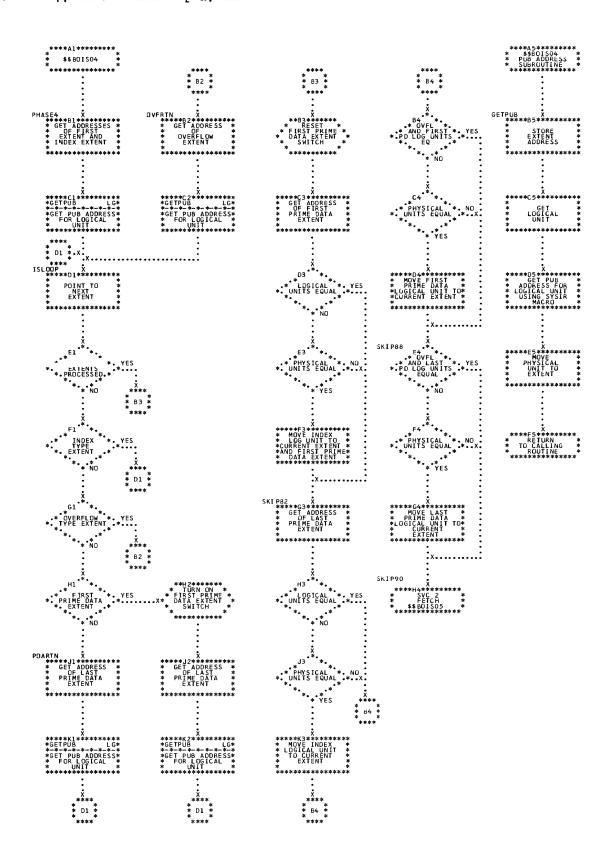
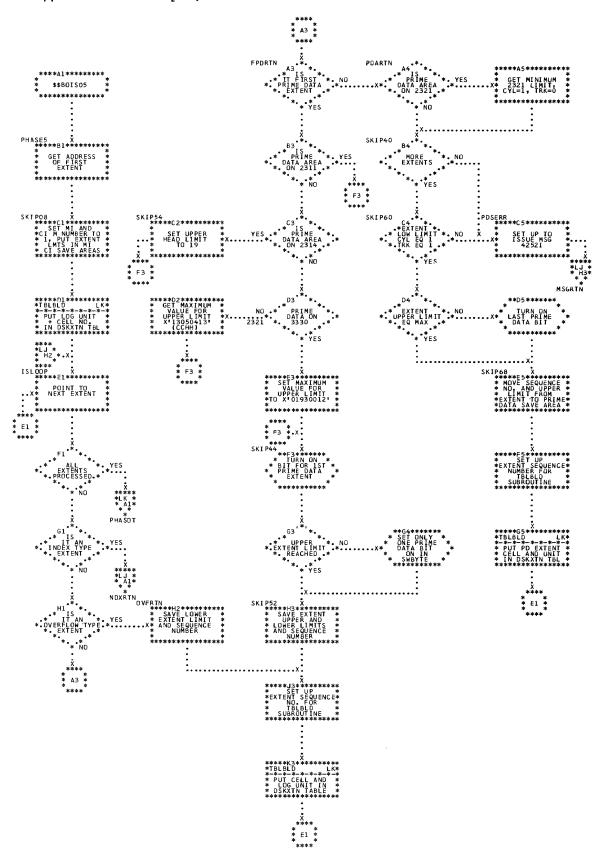
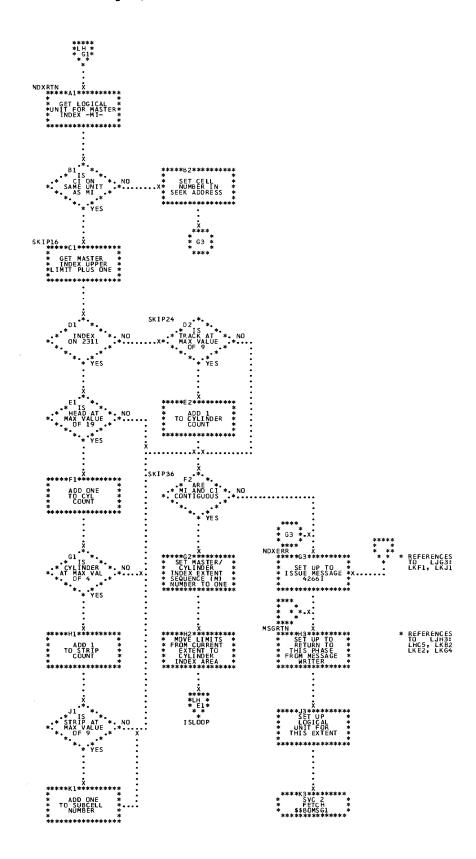


Chart LH. \$\$BOIS05: ISAM Open, Phase 5 (1 of 3)





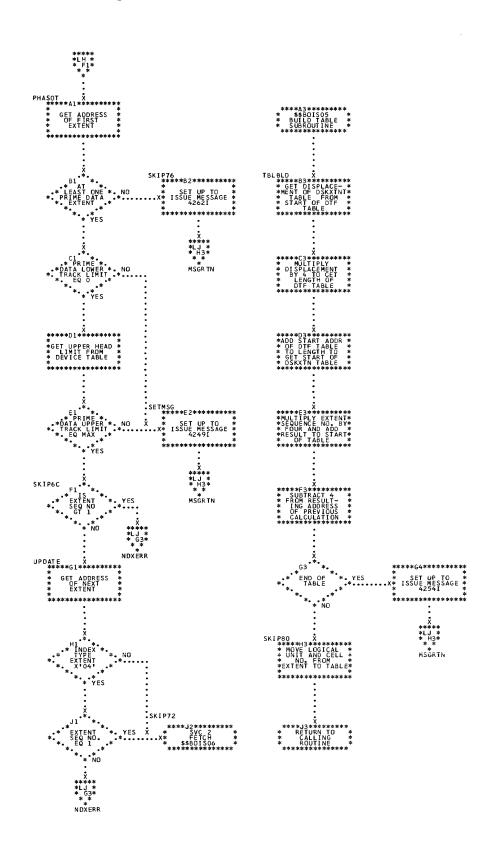
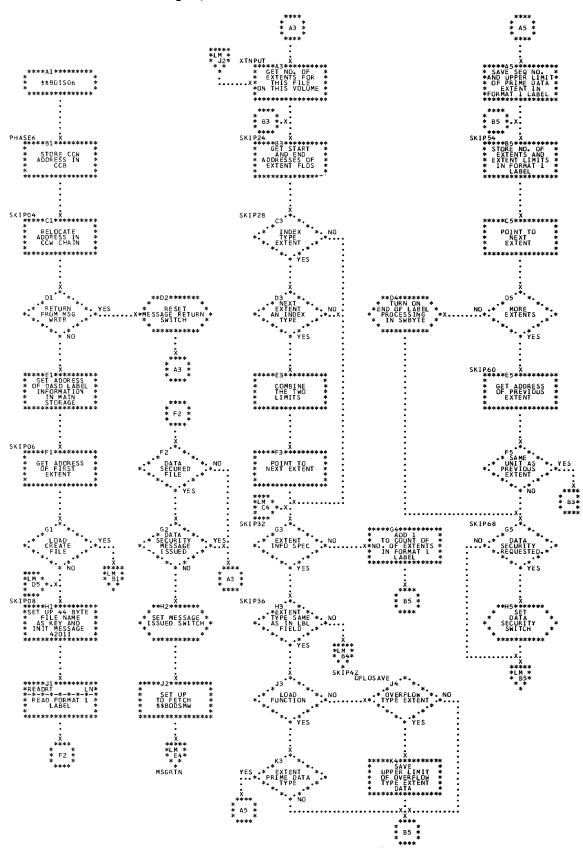
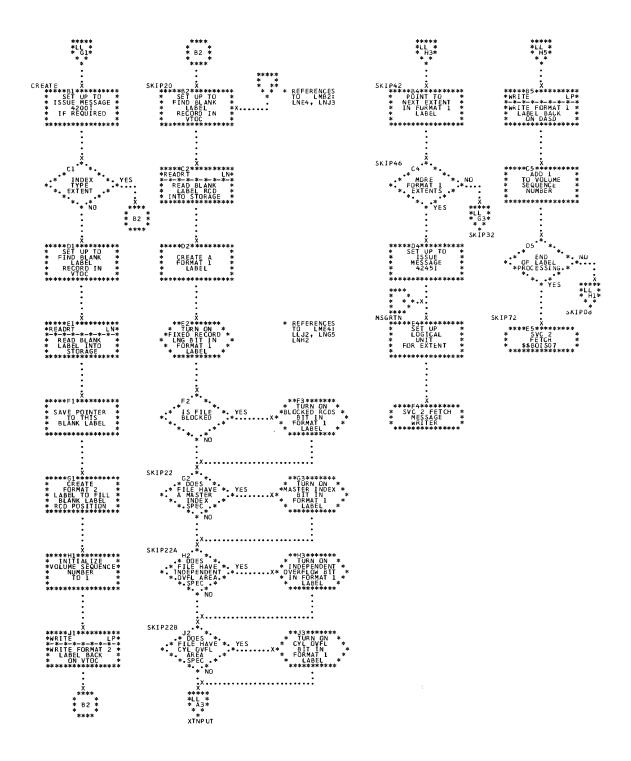
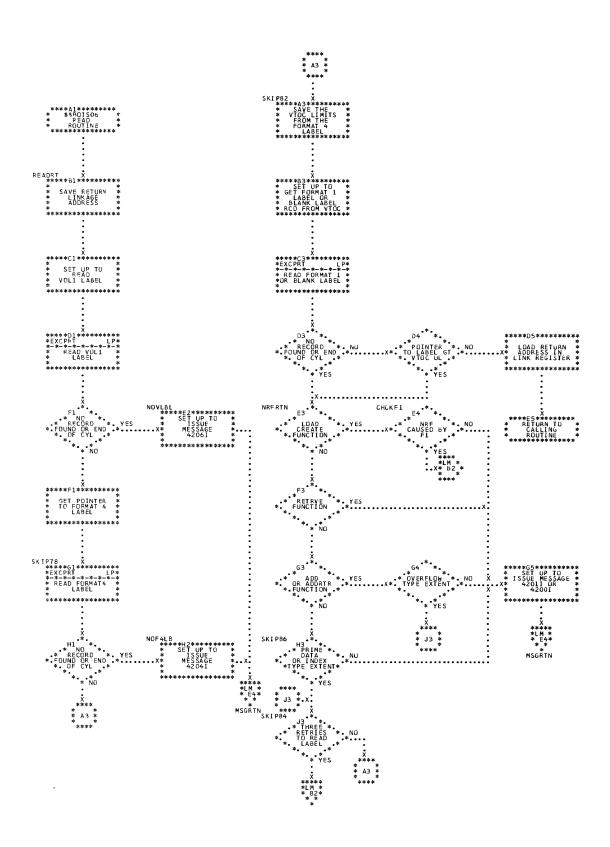


Chart LL. \$\$BOIS06: ISAM Open, Phase 6 (1 of 4)







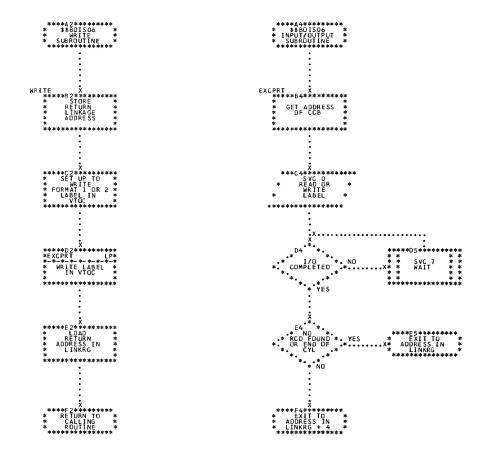
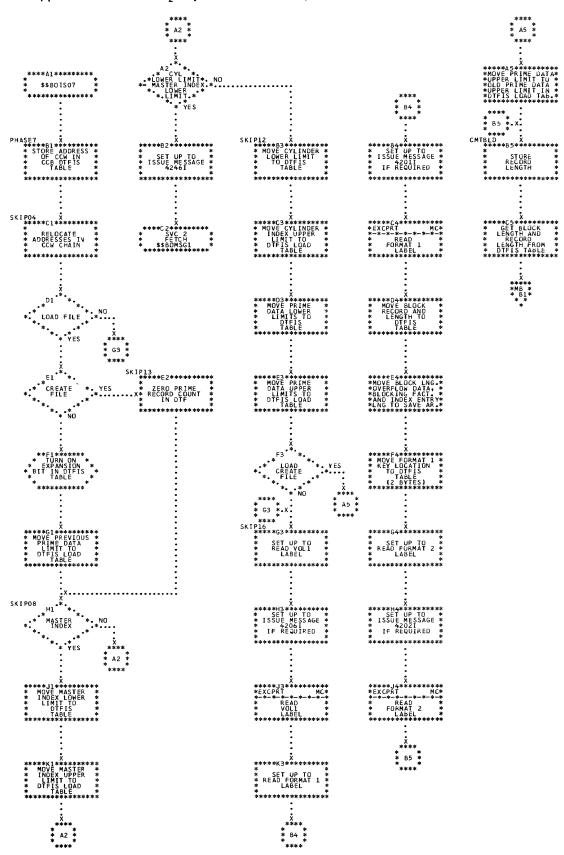


Chart MA. \$\$BOIS07: ISAM Open, Phase 7 (1 of 3)



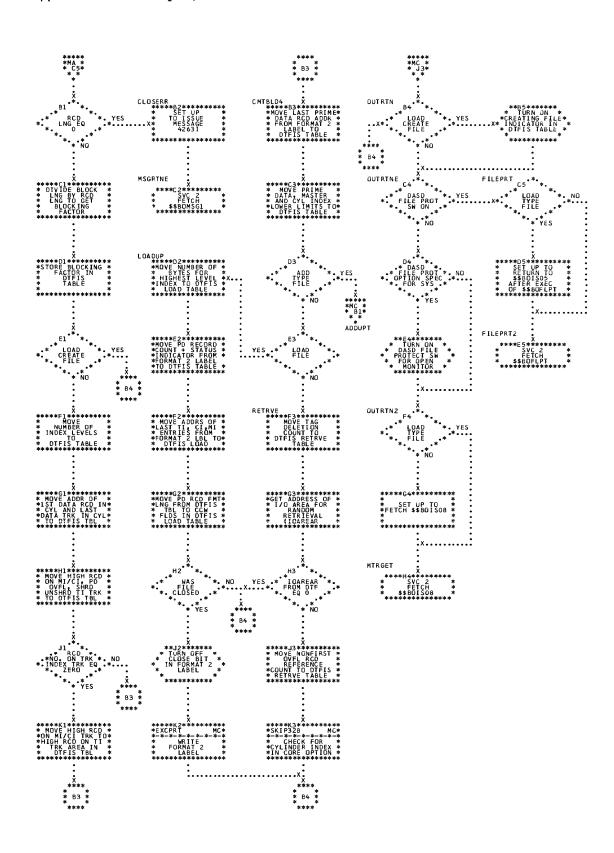
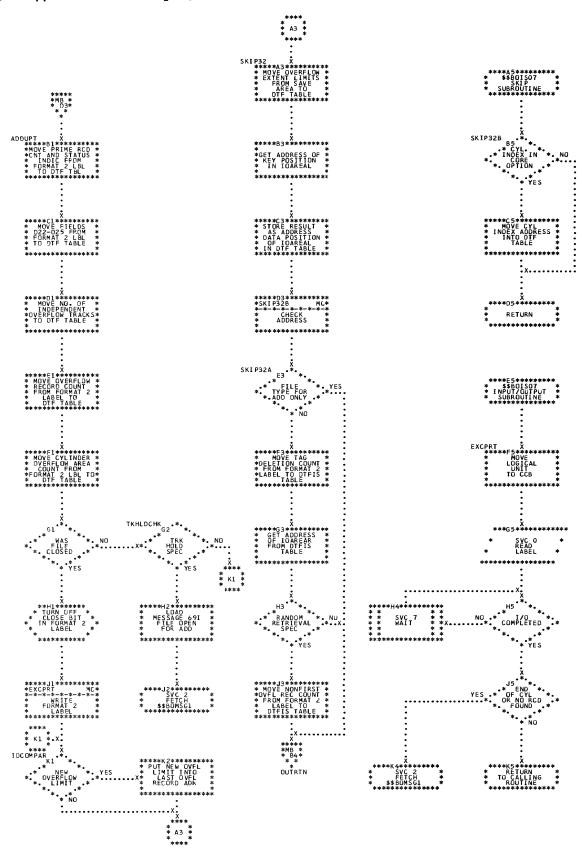
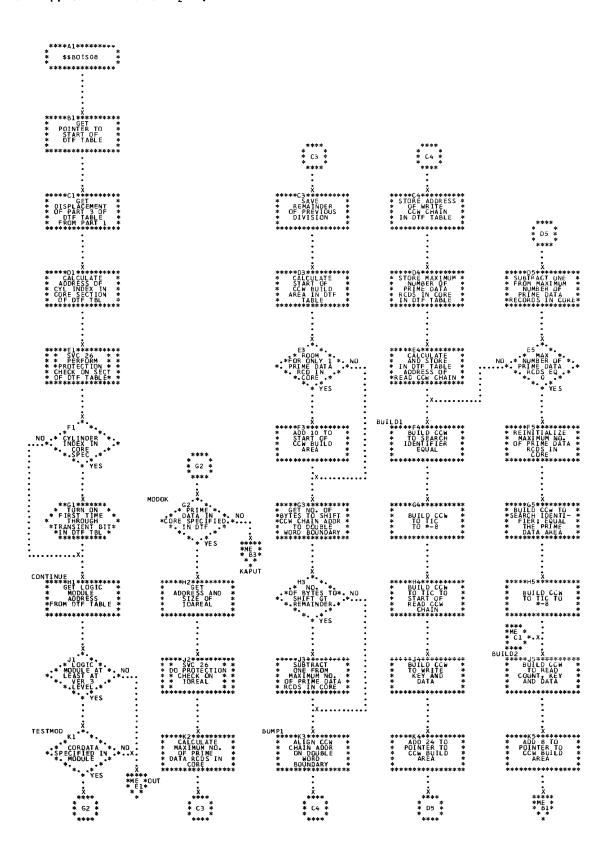


Chart MC. \$\$BOIS07: ISAM Open, Phase 7 (3 of 3)





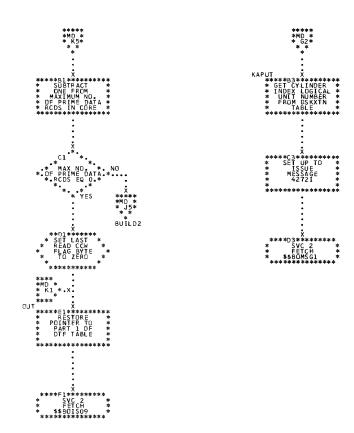


Chart MF. \$\$BOIS09: ISAM Open, Integrity Phase 1 (1 of 3)

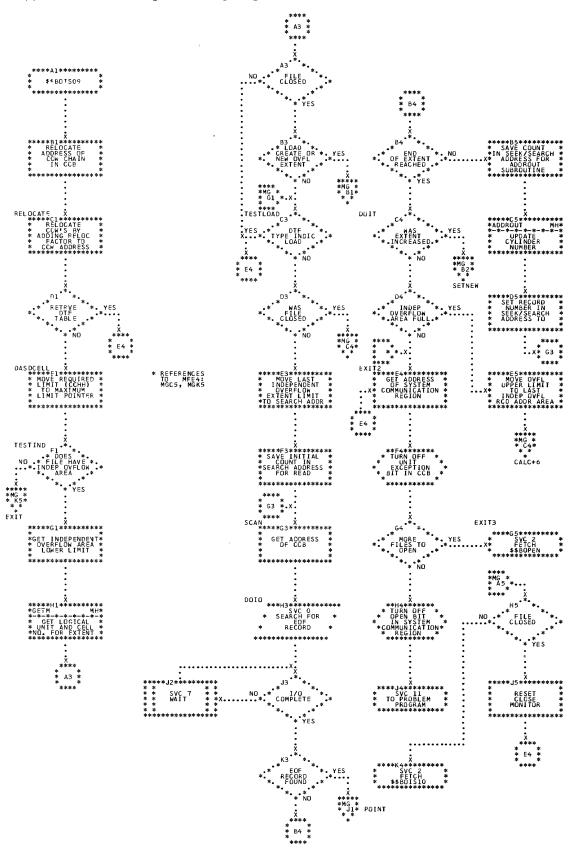
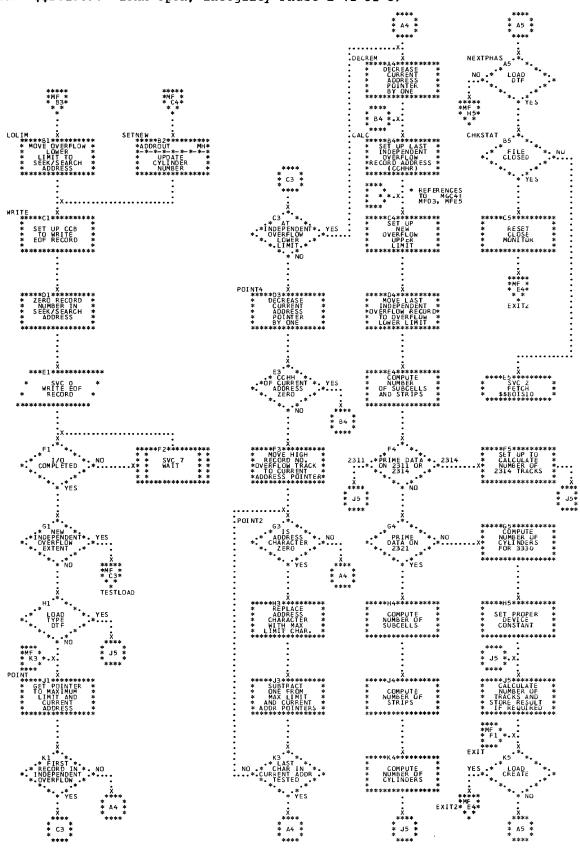


Chart MG. \$\$BOIS09: ISAM Open, Integrity Phase 1 (2 of 3)



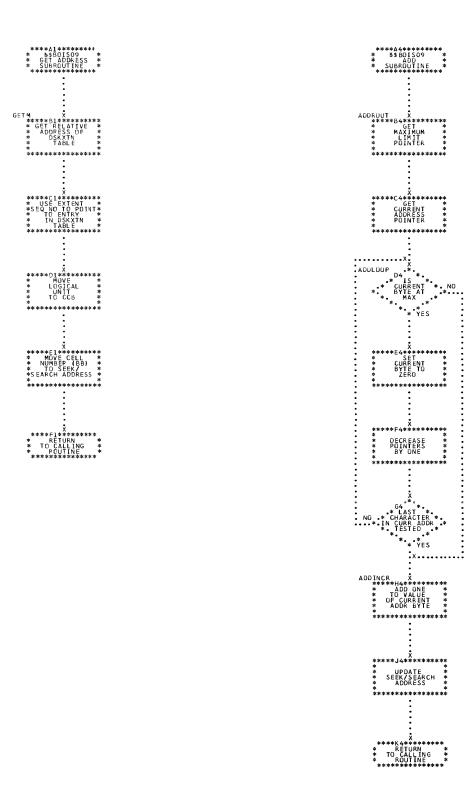
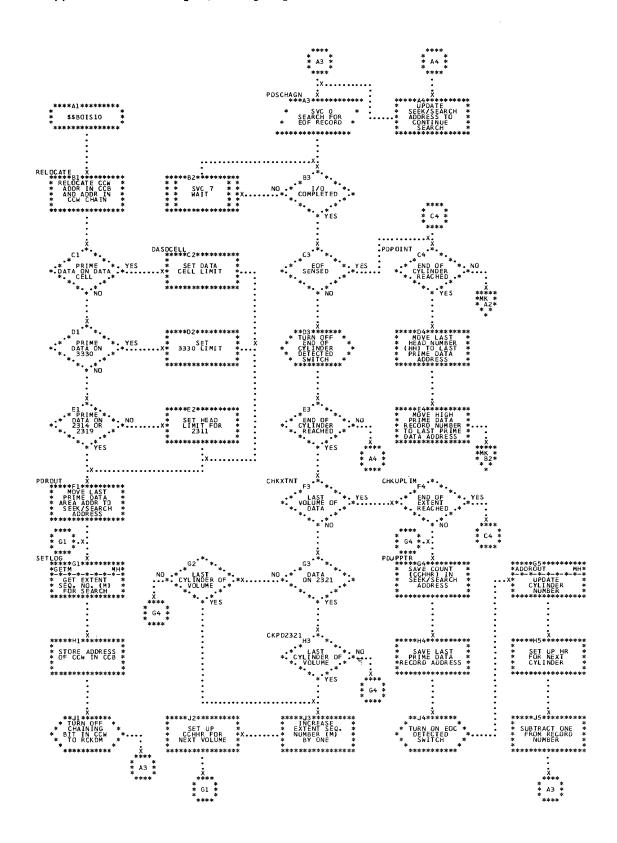
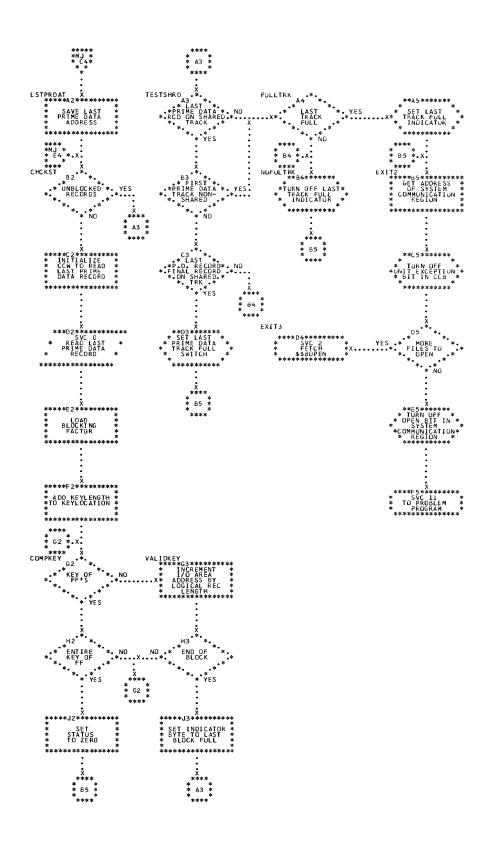
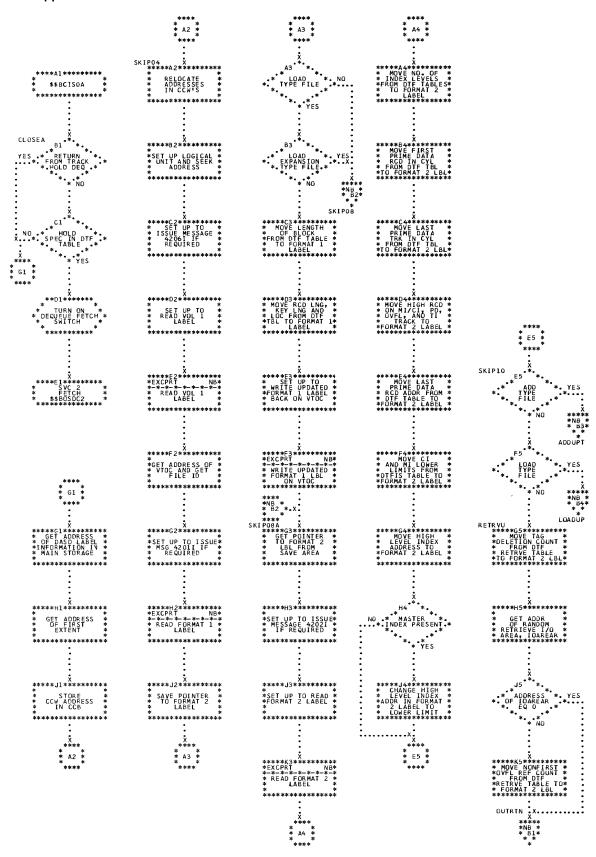


Chart MJ. \$\$BOIS10: ISAM Open, Integrity Phase 2 (1 of 2)







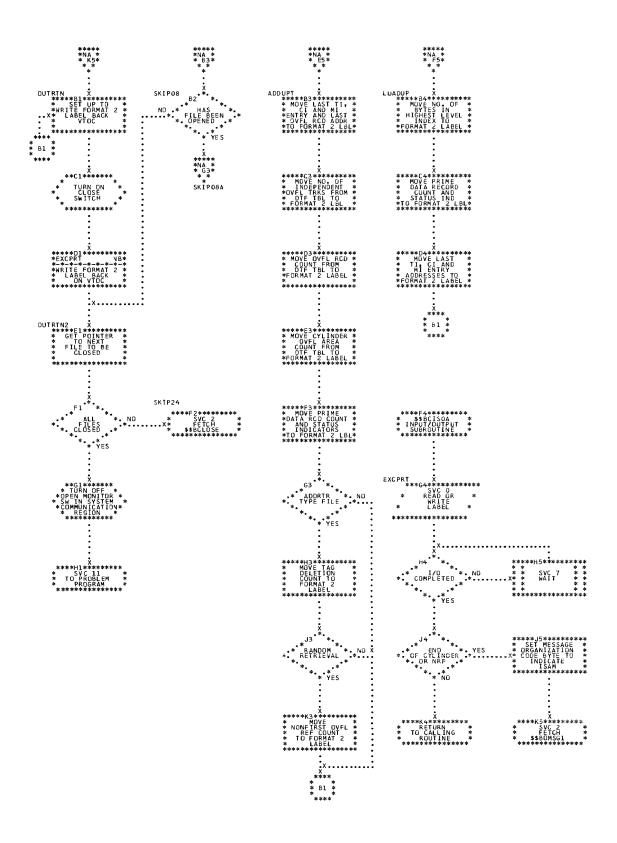


Chart NC. \$\$BORTV1: ISAM RETRVE Open, Phase 1 (1 of 3)

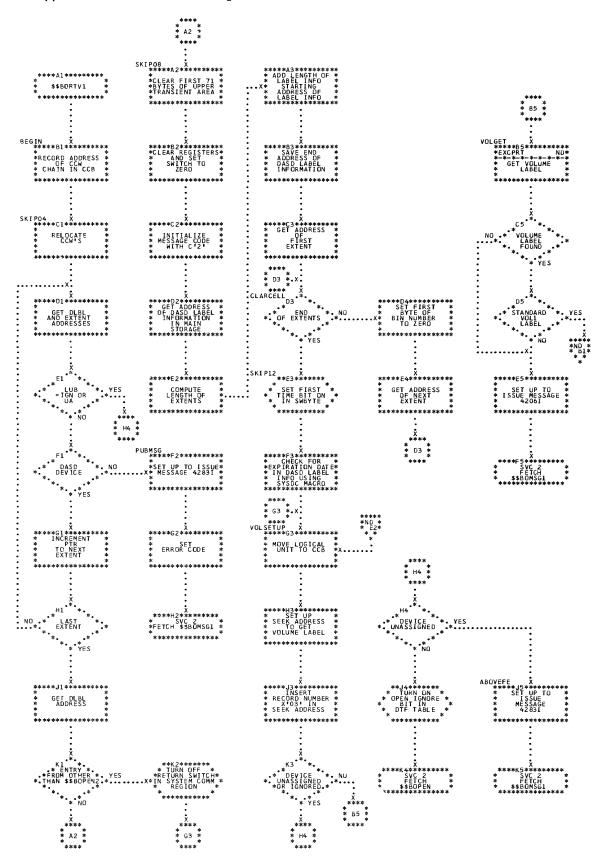
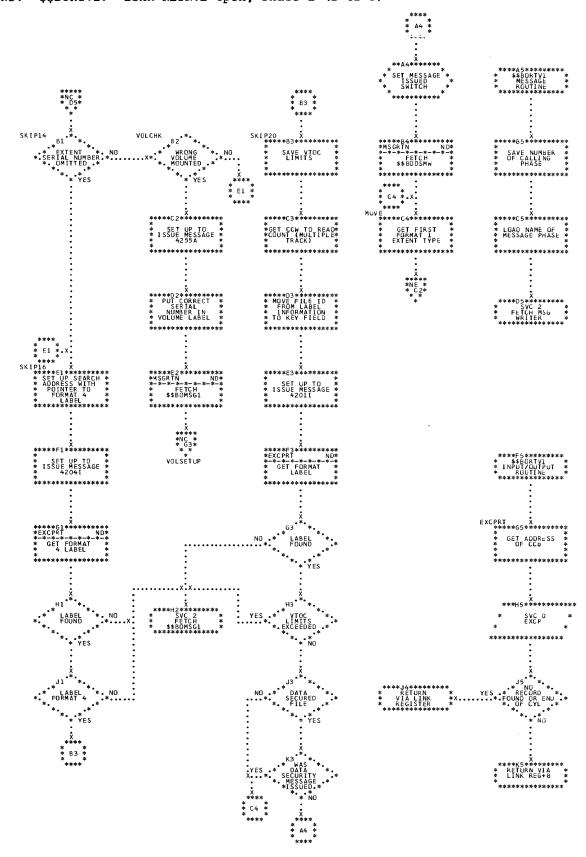


Chart ND. \$\$BORTV1: ISAM RETRVE Open, Phase 1 (2 of 3)



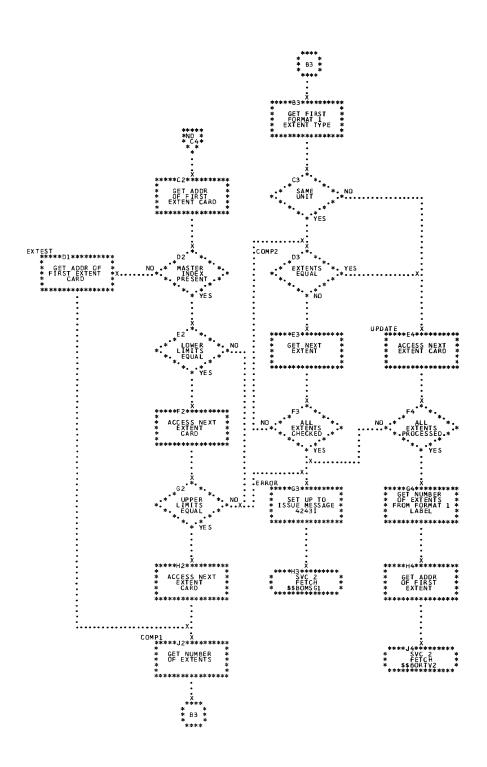
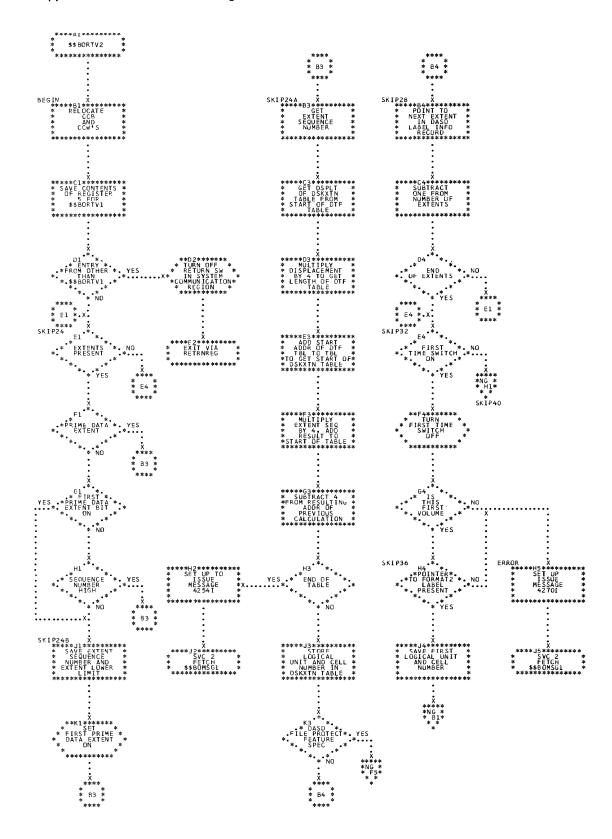
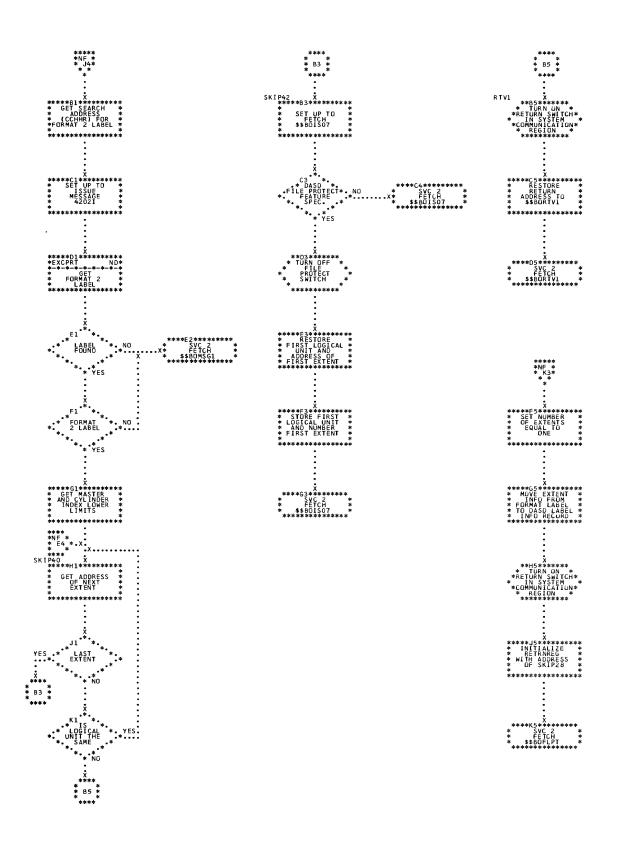


Chart NF. \$\$BORTV2: ISAM RETRVE Open, Phase 2 (1 of 2)





Label	Phase	Location	Label	Phase	Location
ABUVEFE	\$\$BORTV1	NC J5	CKNXT	IS.LOAD	DJA2
ABTMSG	\$\$BOI \$02	LCH4	CKOVLP	\$\$BODA01	BPG2
ABTMSG	\$\$BOI S02	LDG3	CKOVLP1	\$\$BODAO1	BQb1
ADDINCR	\$\$BOI S09	MHH4	CKPU2321	\$\$B0IS10	MJH3
ADDLOOP	\$\$BOI S09	MHD4	CKSTOP	\$\$B0DA02	CCB1
ADDROUT	\$\$BOIS09	MHB4	CKSUSU	\$\$BODAI1	BLF5
ADDUPT	\$\$BOI SO7	MCB1	CKUNIT	\$\$BODA01	BPD2
ADDUPT	\$\$BCISOA	NBB3	CKVTOC	\$\$BUDAO1	BPB2
AD INCR	IS.LOAD	DHK1	CKXT	\$\$BODAO1	BQJ2
ADLOOP	IS.LOAD	DBC5	CKXTNT	\$\$BODAI1	BLC 5
A DLOOP	IS.LOAD	DDF2	CLARCELL	\$\$BORTV1	NCD3
A DL OOP	IS.LOAD	DHG1	CLCFRS	\$\$BGDAG1	BND1
ADUPDI	IS.LOAD	DDE2	CLEAR	\$\$BODAIN	BHF4
A D U P D T	IS.LOAD	DFD3	CLEAR	\$\$BODAI1	BKF5
AOUPDT	IS.LOAD	DHF1	CLOSEA	\$\$BCISOA	NAB 1
AROUND	\$\$BODAIN	BHC3	CLOSEDA	\$\$BODACL	CMB1
AROUND	\$\$BODAI1	BLC4	CLOSERR	\$\$B0IS07	MBB2
ASM	IS.LOAD	DBG1	CMCHNw	IS.LOAD	DDB4
ASMC	IS.LOAD	DFB4	· CMDECR	IS.LOAD	DBE5
ASML	IS.LOAD	DFC4	CMDJWK	IS.LOAD	DDB1
ASMS	IS.LOAD	DFB5	CMINWR	IS.LOAD	DDB 3
			CMKEY	IS.RETRVE	GGB3
BADASSIN	\$\$BODAIN	BGC5	CMKEY	IS.RETRVE	GMC 1
BEGIN	\$\$BORTV1	NCB1	CMNLOP	IS.LOAD	DDD1
BEGIN	\$\$BORTV2	NFB1	CMTBLD	\$\$B0IS07	MAB5
BELOWFE	\$\$BODAIN	BGC3	CMTBLD4	\$\$B0IS07	MBB3
BLDER	IS.RETR VE	GLB1	CMUPUT	IS.LOAD	DFB2
BL DER	IS.RETRVE	GQB1	CM11B	IS.LOAD	DEE2
BLDER	IS.ADDRTR	JKB1	CNTXTNT	\$\$BODAI1	BLB1
BL DER	IS.ADDRTR	JPB1	COMMON	IS.RETRVE	GGF 5
BLKREAD	IS.RETRVE	GJK1	COMMON	IS.RETRVE	GME3
BMPRCD	IS.LOAD	DAG2	COMMON	IS.RETRVE	GPF1
BU IL DER	\$\$BINDEX	FBC3	COMMON	IS.ADDRTR	JJA1
BUILD1 BUILD1	\$\$BINDEX	FAH3	COMMON COMP	IS.ADDRTR \$\$BODAO2	JMH1 CAD4
	\$\$BOISO8	MDF4	COMPAR		CEH1
BUILD2	\$\$BINDEX	FAJ3 MDJ5	COMPARE	\$\$BODAO3 \$\$BINDEX	FAG3
BUILD2 BUMP	\$\$BOISO8 \$\$BINDEX	FAK3	COMPARE	IS.RETRVE	GJG3
BUMP1	\$\$BOI SO8	MDK3	COMPARE	IS.RETRVE	GPH1
BYPASSTR	\$\$B0DAU1	CKJ1	COMPARE	IS.ADDRTR	JMD4
BYTECL	IS.LOAD	DAC5	COMPKEY	\$\$B0IS10	MKG2
DITEGE	13. LUAD	DACS	COMPNXT	\$\$BODAIN	BHD3
CALC	\$\$B0IS09	MGB4	COMP1	\$\$BORTV1	NEJ2
CALLMSG	\$\$BODACL	CMC5	COMP2	\$\$BORTV1	NED3
CCWBLD	IS.LOAD	DEK2	CONTIN	IS.LOAD	DBA 1
CFVRFX	\$\$B0DA02	CB E1	CUNTINUE	\$\$B0IS08	MDH1
C HA INED	\$\$BINDEX	FAB5	CREATE	IS.LOAD	DEF1
CHCK	\$\$BODA02	CDE5	CREATE	\$\$BDISO6	LMB1
CHCKF1	\$\$B0I S06	LNE4	CRETF3	\$\$BODAO4	CHB 2
CHCKST	\$\$BOI \$10	MKB2	CYLHI	IS.LOAD	DAH4
CHECK	\$\$BODA02	CBD1			
C HK END	IS.LOAD	DHE4	DASDCELL	\$\$B0I\$09	MFE1
CHKSTAT	\$\$B0IS09	MGB5	DASDCELL	\$\$BOIS10	MJC2
CHKUPL IM	\$\$B0IS10	MJF4	DECCYL	IS.LOAD	DEG4
CHKXTNT	\$\$BOI S1 0	MJF3	DECFLD	IS.LOAD	DBG 5
C IC HNW	IS.LOAD	DCJ2	DECMST	IS.LOAD	DEH4
CISZOK	IS.LOAD	DEB2	DECREM	<b>\$\$</b> BOIS09	MGA4
CKFLD	IS.LOAD	DBF5	DECREMNT	\$\$BINDEX	FAF4
CKLST	\$\$BODA01	BQK1	DECTRK	IS.LOAD	DEF4
CKLSTK	IS.LOAD	DED4	DELETE	\$\$BODAO1	BPB <b>5</b>
CKNRF	\$\$BODA02	CAB4	DISK	\$\$BODACL	CPB4

Label	Phase	Location	Label	Phase	Location
D0 I0	\$\$BOIS09	MFH3	FOUNDOF	IS.RETRVE	GPD4
υυ Ī Í	\$\$BŨĪ SŨ9	MFC4	FOUNDOF	IS.ADDKTR	JND4
DUMSTRK	\$\$BINDEX	FAF3	<b>FPDRTN</b>	\$\$B0IS05	LHA3
			FRDLXT	<b>\$\$BODAU1</b>	CLA1
ENDLBL	\$\$BODAC L	CNE1	FREETRK	IS.RETRVE	GQE4
ERROR	\$\$BORTV1	NEG3	FREETRK	IS.ADDRTR	JPE4
ERROR	\$\$BORTV2	NFH5	FREEXIT	IS.RETRVE	GQB4
ERRTEST	\$\$BINDEX	FBG4	FREEXIT	IS.ADDR TR	JP84
EXCP	IS.LOAD	DBF3	FREEXIT1	IS.RETRVE	GQD4
EXCP	IS.LOAD	DCB5	FREEXIT1	IS.ADDRTR	JPD4
EXCP	IS.LOAD	DJ 64	FTCHMSG	\$\$B0DA03	CEE2
EXCPRDUT	IS.RETRVE	GLB5	FTCHMSG	\$\$BODAD3	CFF2
EXCPROUT	IS.RETR VE	GRB2	FTCHMSG	\$\$BUDAU4	CJF5
EXCPROUT	IS.ADDRTR	JKB5	FULLTRK	\$\$BUIS10	MKA4
EXCPROUT	IS.ADDRTR	JQB2			
EXCPRT	\$\$BODAI1	BMF4	GETADK	IS.LOAD	DGF3
EXCPRT	\$\$BODA01	BPG3	GETFP	\$\$bUDAI1	BMD2
EXCPRT	\$\$BODA02	CDE3	GETFP	\$\$BODAO4	CHG4
EXCPRT	\$\$BODAO3	CFH3	GETID	IS.RETRVE	GGE 5
EXCPRT	\$\$B OD A O4	CJB1	GETID	IS.RETRVE	GMA 3
EXCPRT	\$\$B0IS03	LFB4	GETID	IS.ADDRTK	JHE 5
EXCPRT	\$\$B0IS06	LPB4	GETLAB	\$\$B0DA01	BNC 3
EXCPRT	\$\$BOIS07	MC F5	GETM	\$\$B0IS09	MHB1
EXCPRT	\$\$BCISOA	NBG4	GETMON	\$\$BODAI1	BMF1
EXCPRT	\$\$BORTV1	NDG5	GETMON	\$\$BODAU4	CHJ5
EXCPWAIT	\$\$BINDEX	FBC4	GETMORE	\$\$BINDEX	FAD3
EXIT	IS.RETRVE	GHG2	GETOPEN	\$\$BUDAIN	BGD4
EXIT	IS.RETR VE	GNB2	GETPTK	\$\$B0DA02	CDE1
EXIT	IS.ADDRTR	JJG3	GETPJB	\$\$B0IS04	LGB 5
EXIT	IS.ADDRTR	JLB4	GETRPT	IS.LOAD	DEE1
EXIT	\$\$BOI S09	MGK5	GOTDEXCP	IS.RETRVE	GHG4
EXITOUT	IS.RETRVE	GHK2	GOTUEXCP	IS.RETRVE	GNH4
EXITOUT	IS.RETRVE	GNE2	GOTOEXCP	IS.ADDRTR	JLK1
EXITOUT	IS.ADDRTR	JJK3	COTBEXE	13 ADDININ	OLKI
EXITOUT	IS.ADDRTR	JLE4	HLADUPDT	IS.LOAD	DBB4
EXITRT	\$\$BODAI1	BMB1	TIEROOT ST	10120110	
EXITRT	\$\$BODAO4	CHD5	IDMVBP	IS.LUAD	DBH2
EXIT2	\$\$BOI S09	MFE4	IDMVBP	IS.LOAD	DDE 5
EXIT2	\$\$B0IS10	MKB5	IJHAASHF	IS.ADD	EKF5
EXIT3	\$\$BOI S09	MFG5	IJHAA12	IS.ADD	EEG2
EX IT3	\$\$BOI \$10	MKD4	IJHAA13	IS.ADD	ELC 5
EXPDDSF	\$\$BODAO2	CCF2	IJHAA14	IS.ADD	ELF5
EXTEND	IS.LOAD	DEA4	IJHAA16	IS.ADD	ELF3
EXTEST	\$\$BORTV1	NED1	IJHAA17	IS.ADD	ELE3
EXWT	\$\$BINDEX	FAK1	IJHAA20	IS.ADD	EJC2
			IJHAA23	IS.ADD	EAE3
FETCH	\$\$BODAO1	BQD4	IJHAA23	IS.ADDR TR	KCA2
FETCH	\$\$BODA02	CAF4	IJHAA25	IS.ADD	EAG3
FETCH	IS.LOAD	DGK5	IJHAA25	IS.ADDRTR	KCC 2
FILEADDR	\$\$BODACL	CMC4	IJHAA26	IS.ADD	EBA2
FILEMARK	\$\$BODACL	CNF5	IJHAA26	IS.ADDRTR	KCA5
FILEPRT	\$\$B01S07	MBC5	IJHAA26	IS.ADDRTR	KDA2
FILEPRT2	\$\$B0IS07	MBE5	IJHAA26A	IS.ADD	EAB5
FINDF1	\$\$BODAI1	BKE4	IJHAA26A	IS.ADDRTR	KCF3
FINDF1	\$\$BODAU1	CKF1	IJHAA27	IS.ADD	EBA1
FINDNXT	IS.RETR VE	GJB1	IJHAA27	IS.ADDRTR	KCE4
FINDNXT	IS.RETRVE	GNB5	IJHAA28	IS.ADD	EME4
FINDNXT	IS.ADDRTR	JLC2	IJHAA29	IS.ADD	EMJ4
FINXNT	\$\$BODAI1	BLB5	IJHAA33	IS.ADD	EKA4
FIRST	\$\$BINDEX	FAE2	IJHAA34	IS.ADD	EKB4
FLEPRTCT	\$\$BODAI1	BMG3	IJHAA34	IS.ADDRTR	KE <b>J1</b>
FLEPRTCT	\$\$BODA04	CJG2	IJHAA35	IS.ADD	ECG5
FNDNXT	\$\$BODAU1	CLB1	IJHAA35	IS.ADDRTR	KEF3
FNDOPN	\$\$BODAO3	CFC4	IJHAA36	IS.ADD	ECA3
FNSHF1	\$\$BODAO4	CGF3	IJHAA37	IS.ADD	ECH4
FOUNDOF	IS.RETRVE	GKB4	IJHAA37	IS.ADDRTR	KEG2

Label	Phase	Location	Label	Phase	Location
IJHA438	IS.ADD	EC D1	IJHAHEAD	IS.AUDRTR	KEG3
IJHAA38	IS. ADDR TR	KDH5	IJHAH29	IS.ADD	EMF3
IJHAA39	JS.ADD	ECF2	IJHAIOOP	IS.ADD	ELB1
IJ4AA39	IS.ADD	EC H1	IJHAIOPH	IS.ADD	ELB2
IJHAA39	IS.ADURTR	KEB1	IJHAJ29	IS.ADD	ELH3
IJHAA39	IS.ADDRTR	KEC3	IJHANDwü	IS.ADD	EAF3
IJHAA40	IS.ADD	EC⊕2	IJHANEDT	IS.ADD	EAJ5
IJHAA40	IS.ADDRTR	KEA1	IJHANEDT	IS.ADDRTR	KCD4
IJHAA41	IS.ADD	EJF5	IJHANEOF	IS.ADD	EHG2
IJHAA42	I S. ADD	EE J2	IJHANOWU	IS.ADDRTK	KCB2
IJHAA43	IS.ADD	EKH5	IJHAUBEC	IS.ADDRTR	KCB1
IJHAA44	IS.ADD	EKG5	IJHAUBEG	IS.ADD	EAK1
IJHAA45	IS.ADD	EKJ5	IJHAUDN1	IS.ADD	EJB3
IJHABEOF	IS.ADD	EHD3	IJHAOLUW	IS.ADD	EJB1
IJHAB29 IJHACACH	IS.ADD IS.ADD	EMK2 ELD3	IJHAOMCG IHAOMIO	IS.ADD IS.ADD	EJB2 EH <b>J4</b>
IJHACADX	IS.ADD	EJD4	IJHAOMTU IJHAOMTU	IS.ADD	EJD3
IJHACBKI	IS.ADD	EK85	IJHAONGU	IS.ADD	EGA2
IJHACBMP	IS.ADD	EMC5	IJHAORFL	IS.ADD	EGB1
IJHACDBK	IS.ADD	EMA4	IJHADSQU	IS.ADD	EHB 1
IJHACDBK	IS.ADD	EMB1	IJHAPRET	IS.ADD	EMD2
IJHACDOF	IS.ADD	ELC4	IJHAPRM1	IS.ADD	ENC 1
IJHACDTR	IS.ADD	EKB2	IJHAPRM2	IS.ADD	ENC 2
IJHACDUP	IS.ADD	EBD2	IJHAPRM3	IS.ADD	ENF 2
IJHACDUP	!S.ADDRTR	KDB1	IJHAPKM4	IS.ADD	EAF4
IJHACDVP	IS.ADD	EJG4	IJHAPRM4	IS.ADDRTR	KCJ2
IJHACEUF	IS.ADD	ELB3	IJHAPRM5	IS.ADD	EBC2
IJHACFLL	IS.ADD	EEA2	IJHAPKM5	IS.ADDRTR	KDA1
IJHACGTO	IS.ADD	EAK3	IJHAPRM6	IS.ADD	EBB3
IJHACGTO	IS.ADDRTR	KCF2	IJHAPRM6	IS.ADDRTR	KCD5
IJHACINX	IS.ADD	EEE1	IJHAPRM7	IS.ADD	EAG4
IJHACITE	IS.ADD	ELE1	IJHAPRM7	IS.ADDRTR	KCK2
IJHACITI	IS. ADD	ELF1	IJHAPRM8	IS.ADD	EAG5
IJHACITX	IS.ADD	ELD2 EM64	IJHAPRMS	IS.ADDRTR	KCB4
IJHACLOP IJHACLOP	IS.ADD IS.ADD	EMD1	AUQAHLI	IS.ADD IS.ADDRTR	EBA4 KDA3
IJHACLTI	IS.ADD	EJB5	IJHAQUA IJHATEST	IS.ADD	ECH2
IJHACPAD	IS.ADD	EMF4	IJHATEST	IS.ADDRTR	KEE1
IJHACPD1	IS.ADD	EKG2	IJHAUNB	IS.ADD	ECB4
IJHACTET	IS.ADD	Ë AH5	1 J HAUNB	IS.ADDRTR	KEA2
IJHACTET	IS.ADD	EBJ2	IJHAWRKY	IS.ADD	EEC 1
IJHACTET	IS.ADDRTR	KCC4	IJHAWRNK	IS.ADD	EEB1
IJHACTET	IS.ADDRTR	KD <b>J1</b>	IJHAWTNK	IS.ADD	EAF1
IJHACUOV	IS.ADD	ELB5	IJHAWTNK	IS.ADDRTR	KAH2
IJHACUPD	IS.ADD	ELB4	IJHCAAB	IS.ADD	EEG1
IJHACUPP	IS.ADD	EJJ5	IJHCBH98	IS.ADD	EKH1
IJHAC29	IS.ADD	EMF1	IJHCBH98	IS.RETRVE	FJF1
IJHADEC	IS.ADD	ECJ2	IJHCBLD1	IS.ADD	EKB1
IJHADEC	IS. ADDRTR	KEF1	IJHCBLD2	IS.RETRVE	FJB1
IJHAD29	IS.ADD	EMC1	IJHCBUMP	IS.ADD	EGF 4
IJHAEBEG IJHAEBFL	IS.ADD IS.ADD	EEC3 EBC4	I JHC C WS	IS.RETRVE	HBA3
IJHAEBFL	IS.ADDRTR	KDC3	IJHCER IJHCERCA	IS.ADD IS.ADD	EEG4 EGB5
IJHAEFXD	IS.ADD	EBD5	IJHCERRT	IS.ADD	EED4
IJHAEFXD	IS.ADDRTR	KDG4	IJHCEXCP	IS.ADD	EHF5
IJHAEFXK	IS.ADD	EDH1	IJHCE99	IS.ADD	EKJ1
IJHAEFXK	IS.ADDRTR	KEC5	IJHCE99	IS.RETRVE	FJGI
I J HA EMC I	IS.ADDRTR	KDE5	IJHCHKIN	IS.ADD	EEH1
IJHAEMC1	IS.ADD	ECA1	IJHCHRFP	IS.ADD	EGH3
IJHAENT	IS.ADD	EKB3	IJHCINDX	IS.ADD	EGJ4
IJHAETFL	IS.ADD	EDB1	IJHCINIT	IS.ADD	EGE5
IJHAETFL	IS.ADDRTR	KED4	IJHCLGUT	IS.ADD	EHÇ5
IJHAE29	IS.ADD	EMA3	IJHCLOPX	IS.ADD	EKC1
IJHAF29	IS.ADD	EMB2	IJHCLOPX	IS.RETRVE	FJC1
IJHAG29	IS.ADD	EMC 2	IJHCLOP1	IS.ADD	EEE4
IJHAHEAD	IS.ADD	EDE2	IJHCMCAL	IS.ADD	EHB5

Label	Phase	Location	Label	Phase	Location
IJHCMOVE	IS.ADD	EHø4	IJHMM256	IS.LOAD	DKK1
IJHCMOVE	IS.RETRVE	HAF1	IJHMNDCK	IS.LOAD	DLK1
I J HC PL ST	IS.ADD	EEH4	IJHMNDIK	is.Luáu	DKE4
IJHCRES	IS.ADD	EFC2	IJHMNWTK	IS.LOAD	DKD5
IJHCREST	IS.ADD	EHC4	IJHMNXCL	IS.LOAD	DLB2
IJHCREST	IS.RETRVE	HAG1	IJHMNXPK	IS.LOAD	DLA2
IJHCRES2	IS.ADD	EFC3	IJHMOKEQ	IS-LOAD	DKH2
IJHCSTRE IJHCSTRE	IS.ADD IS.RETRVE	EKE1 FJE1	IJHMÚKFL IJHMOKLD	IS.LOAD IS.LOAD	DKD3 DK <b>J</b> 1
IJHCTICR	IS.ADD	EKD1	IJHMPDTK	IS.LUAD	DLD2
IJHCTICR	I S. RETR VE	FJD1	IJHMRCID	IS.LUAD	DMF3
IJHCTRDX	IS.ADD	EGG4	IJHMRDWR	IS.LOAD	DLC3
IJHCTSER	IS.ADD	EHD4	IJHMRNBP	IS.LUAD	DMB3
IJHCTSER	IS.RETRVE	HAH1	IJHMRSTR	IS.LOAD	DKF5
IJHCWAIT	IS.ADD	EEC4	IJHMRTRN	IS.LUAD	DLJ2
IJHCWATB	IS.ADD	EAB2	IJHMRTRY	IS.LUAD	DLD4
IJHCWATB	IS.ADD	EAC1	IJHMSHRD	IS.LOAD	DMC 2
IJHCWATB	IS.RETRVE	FDD4	IJHMSPNT	IS-LOAD	DLG1
IJHCWATB IJHCWATB	IS.RETRVE IS.ADDRTR	FDF1 KAC2	IJHMTINR IJHMJNFL	IS.LOAD IS.LOAD	DMB1 DLH2
IJHCWATB	IS. ADDRTR	KAD1	IJHMUPDT	IS.LOAD	DMH1
IJHCWATE	IS.ADD	EAB1	IJHMWAIT	IS.LOAD	DLG4
IJHCWATE	IS.RETRVE	FDB4	IJHMWRIE	IS.LUAD	DLK5
IJHCWATE	IS.RETRVE	FDC2	IJHMZAPP	IS.LOAD	DMK4
IJHCWATF	IS.RETRVE	FDD1	IJHM1IDA	IS.LUAD	DKJ3
IJHCWATF	IS.ADDRTR	KAB2	IJHMlIOB	IS.LOAD	DKD1
IJHCWATF	IS.ADDRTR	KAC1	<b>I</b> JHRC ERC	IS.RETRVE	FCD3
I J HC XCOR	IS.ADD	EEB4	IJHRCERC	IS.ADDRTR	<b>J</b> GD3
IJHCXCOR	IS.ADD	EGB4	IJHRECAL	IS.ADD	END3
IJHCXCPH	IS.ADD	EGH5	I JHR É GOT	IS.RETRVE	FGH1
IJHCXCPH IJHCXWTH	IS.RETRVE IS.RETRVE	FHB5 FEH1	IJHKEGOT IJHRERRR	IS.ADDRTR IS.RETRVE	KBE4 FFF2
IJHCXWTH	IS.RETRVE	HAB3	IJHRERRK	IS.ADDRTR	KBE2
IJHECONT	IS. ADD	EAC2	IJHRESCH	IS.RETRVE	FHF4
IJHECONT	IS.RETRVE	FDF5	IJHRESCO	IS.ADD	EEF3
IJHECONT	IS.RETR VE	FDG4	IJHRESCO	IS.RETRVE	FHG3
IJHECONT	IS.ADDRTR	KAD2	IJHRESCO	IS.ADDRTR	KFG3
IJHELDR	IS.ADD	EFH1	IJHREXSL	IS.RETRVE	FCE4
IJHENDFL	IS.LOAD	DLA3	<b>IJ</b> HREXSL	IS.RETRVE	FED3
IJHENOTE	IS.ADD	EAB3	IJHREXSL	IS.ADDRTR	JGE4
IJHERRA	IS.ADD	ECF1	IJHREND	IS.RETRVE	FGB 1
IJHERRA IJHESTLB	IS.ADDRTR IS.RETR <b>V</b> E	KEA3 GAC1	IJHŘEND I HENORM	IS.ADDRTR IS.RETRVE	KBA4 FEA4
IJHESTLB	IS.RETRVE	GAC2	IJHRNORM IJHRNORM	IS.ADDRTR	KBB1
IJHESTLB	IS.ADDRTR	JAC1	IJHROVED	IS.RETRVE	FFB3
IJHESTLB	IS.ADDRTR	JAC2	IJHROVFO	IS.ADDRTR	KBB3
IJHEUSER	IS.ADD	EFJ1	IJHRPRNT	IS.RETRVE	FHF1
IJHFREEB	IS.RETRVE	FKB1	<b>IJHRPRNT</b>	IS.ADDRTR	KFF1
<b>IJ</b> HFREEB	IS.RETR VE	FKC2	<b>IJ</b> HRREAD	IS.RETRVE	FCE1
IJHFREEB	IS.RETRVE	FKD1	IJHRREAD	IS.ADDRTR	JGE1
IJHGETOK	IS.RETRVE	GFG1	IJHRRGOT	IS.RETRVE	FGF1
IJHGETOK	I S. ADDRTR	JFG1	IJHRSALT	IS.RETRVE	FCC5
IJHIHRE2	DAMOD IS.LOAD	ADK4 DLA1	IJHRS ALT IJHRS HRD	IS.ADDRTR IS.RETRVE	JGC5 FFB5
IJHMCLND IJHMCMWR	IS.LOAD	DMB4	IJHRSHRD	IS.ADDRTR	KBB2
IJHMDADD	IS.LOAD	DKF3	IJHRSLPP	IS.RETRVE	FFF1
IJ HM ER E X	IS.LOAD	DLG5	IJHRSLPP	IS.ADDRTR	KBH1
IJHMEXCP	IS.LOAD	DMH2	IJHRSLTT	IS.RETRVE	FEE4
IJHMEXMV	IS.LOAD	DKA3	IJHRSLTT	IS.ADDRTR	KBC1
IJHMEXWT	IS.LOAD	DLF3	IJHRSOTT	IS.RETRVE	FFD3
IJHMFRST	IS.LOAD	DKG1	IJHRSOTT	IS.ADDRTR	KBC3
IJHMFXSS	IS.LOAD	DLB3	IJHRSRCH	IS.RETRVE	FCD2
IJHMIDPB	IS.LOAD	DMB2	IJHRSRCH	IS ADDRTR	JGD2
IJHMINCR	IS.LOAD	DMJ1	IJHRWRKA	IS-RETRVE	FJB2
IJHMIXDA	IS.LOAD	DKF4	IJHRWRT TIMBWDT	IS.RETRVE	FHE1
IJHMLEAV	IS.LOAD	DLC5	IJHRWRT	IS.ADDRTR	KFE1

Label	Phase	Location	Label	Phase	Location
IJHRWRTB	IS.RETRVE	FHC1	IJHSG	ET IS.RETRVE	GBE1
IJHRWRTB	IS.RETRVE	FHC2	IJHSG		JBE1
IJ HRWRTB	IS.ADDRTR	KFC1	IJHSG		GBC1
IJHRWRTB	IS.ADDRTR	KFC2	I JHS 6	ETB IS.RETRVE	GBC2
IJHSADD	IS.RETRVE	HBC3	IJHSG		JBC1
I J HSADKY	IS.RETRVE	GDG5	IJHSG	ETB IS.ADDRTR	JBC 2
IJHSADKY	IS.ADDRTP	JDG5	IJHS6	ETH IS.KETRVE	GBH5
IJHSBCKT	IS.RETRVE	GDE4	IJHSG	ETH IS.ADDRTR	JBH5
IJHSBCKT	IS.ADDRTR	JDE4	I J HS G	ET1 IS.RETRVE	GBF1
IJHSBHIN	IS.RETRVE	HAB2	IJHSG		JbF1
IJHSBH11	IS.RETRVE	GEB5	IJHSG		HAB5
IJHSBH11	IS. ADDRTR	JEB5	IJHSH		HBC 2
IJHSBH12	IS-RETRVE	GEB4	IJHSI		HAG2
IJHSBH12	IS.ADDRTR	JEB4	IJHSK		FFC1
IJHSBH13	IS.RETRVE	GCB1	IJHSK		ENB3
IJHSBH13	IS.RETRVE	GED5	IJHSK		GE#3
IJHSBH13 IJHSBH13	IS.ADDRTR IS.ADDRTR	JCB1 JED5	IJHSK IJHSM		JEB3 HAK3
IJHSBH17	IS.RETRVE	GCC1	IJHSM		HBE3
IJHSBH17	IS.ADDRTR	JCC1	IJHSN		GED3
IJHSBH18	IS.RETRVE	GFC3	IJHSN		JED3
IJHSBH18	IS.ADDRTR	JFC3	NAHLI		GBJ3
IJHSBH19	IS.RETRVE	GFC4	NZHLI		JBJ3
IJHSBH19	IS.ADDRTR	JFC4	IJHSN		GBD4
IJHSBH29	IS.RETRVE	HAE5	IJHSN		JBD4
IJHSBH3	IS.RETR VE	GDC2	IJHSN	•	HAF3
IJHSBH3	IS. ADDRTR	JDC2	IJHSO		GEB1
IJHSBH30	IS.RETRVE	GEF4	IJHSO	TK1 IS.RETRVE	GEC 2
IJHSBH30	IS.ADDRTR	JEF4	IJHSO	TR1 IS.ADDRTR	JEC 2
IJHSBH5	IS.RETRVE	GDB1	<b>I</b> JHSU	TST IS.RETRVE	HBD3
IJHSBH5	IS.RETRVE	GKC3	IJHSO		HBG4
IJHSBH5	IS.RETRVE	GPG3	IJHSP		GFE1
IJHSBH5	IS.ADDRTR	JDB1	IJHSP		JFE1
IJHSBH5	IS.ADDRTR	JNC3	IJHSP		GFC1
IJHSBH6	IS-RETRVE	GDD 1	IJHSP		GFC2
IJHSBH6	IS.RETRVE	GKF3	IJHSP		JFC1
IJHSBH6	IS.RETRVE	GPH3	IJHSP		JFC2
IJHSBH6 IJHSBH6	IS.ADDRTR	JDD1	IJHSP IJHSR		HBB4 GBD3
IJHSBH6	IS.ADDRTR IS.ADDRTR	JDG1 JNF3	IJHSR		JBD3
IJHSBILD	IS.RETRVE	HBB1	IJHSR		GBE4
IJHSBLKD	IS.RETRVE	HAD5	IJHSR		JBE4
IJHSBLTI	IS.RETRVE	HBG1	IJHSW		HAD1
IJHSBLUT	IS.RETRVE	HBJ1	IJHSW		GBC4
IJHSCHNG	IS.RETR VE	GCG2	IJHSW		JBC4
IJHSCNG1	IS.RETRVE	GCJ3	IJHSW		HAB1
IJHSC002	IS.RETRVE	GCJ4	IJHS1	EDT IS.RETRVE	GEF1
IJHSC002	IS.ADDR TR	JC J4	IJHS1	EOT IS.ADDRTR	JEF1
IJHSEOT	IS.RETRVE	HAE3	IJHS2		GCA4
IJHSEOTR	IS.RETRVE	GCA3	IJHS2		HBH4
IJHSEOTR	IS.ADDRTR	JCA3	IJHWT		FDC3
IJHS ER 2	IS.ADD	EEH3	IJHXC		EGC4
IJHSER 2	IS.RETRVE	FGB5	IJIAE		ANG2
IJHSER 2	IS.RETRVE	FGC4	IJIAF		ANG 3
IJHSER 2	IS.RETRVE	GAE3	IJIAF		ANJ1
IJHSER2	IS.ADDRTR IS.ADDRTR	JAH3	IJIAF		AAA3 Anj2
IJHSER2 IJHSER2H	IS.ADDRIK	KBG2 EFA1	IJIAF IJIAF		ANJZ ANE3
IJHSER 21	IS.ADD	EEJ3	IJIAF		ANK 2
IJHSER 21	IS.RETRVE	FGC5	IJIAN		ANC 5
IJHSER 21	IS.RETRVE	GAE4	IJIAN		ANC 1
IJHSER 21	IS. ADDRTR	JAH4	IJIAN		ANA3
IJHSER 21	IS. ADDRTR	KBH2	IJIAN		AND4
IJHSESTL	IS.RETRVE	GAE1	IJIAR		AME 5
IJHSESTL	IS.ADDRTR	JAE1	IJIAW		ANB2
IJHSEX1	IS.RETRVE	HAC4	IJIBL	D DAMOD	AJB1

Label	Phase	Location	Label	Phase	Location
IJICMC	DAMOD	AHF1	IJISFRM	DAMODV	BAG3
IJICTL	DAMOD	AFB1	IJISGDL	DAMODV	AKH2
IJICTL1	DAMOD	AFC2	IJISGS	DAMODY	ALE4
IJIDIC	DAMOD	AEC5	1JISICL	DAMODV	BAG4
IJIDUMY	DAMOD	ABF5	IJISING	DAMODV	ALK 5
IJIDV2	DAMOD	AHE5	IJISJCK	DAMODV	BCB1
IJIDV3	DAMOD	AHH4	IJISKIF	DAMODV	ALJ3
IJIEOF	DAMOD	AAA2	IJISKIP	DAMODV	ALJ2
IJIEOF1	DAMOD	ACH1	IJISKIR	DAMODV	ALE3
IJIEOV	DAMOD	ABG3	IJISKI1	DAMODV	ALD2
IJIFGC	DAMOD	AGJ2	IJISKS	DAMODV	ALA3
IJIFLG	DAMOD	AJC3	IJISKS1	DAMODV	ALB3
IJIFREE	DAMOD	AFB2	IJISK1	DAMODV	ALF3
IJIFRM	DAMOD	ABJ2	IJISLBK	DAMODV	BEE3
IJIFXL	DAMOD	AAH1	IJISLOAD	DAMODV	BFJ4
IJ IGCH	DAMODV	BDD5	IJISMID	DAMODV	BBG1
IJ IGET	DAMODV	BDB1	IJISNEF	DAMODV	BBA1
IJIGFT1	DAMODV	BDF1	IJISNF	DAMODV	ALH1
IJIGNXT	DAMODV	BDJ3	IJISNID1	DAMODV	BBB3
IJIGSKB	DAMODV	BDA3	IJISNL	DAMODV	AKB5
IJIGSKB1	DAMODV DAMODV	BDE3	IJISNRF	DAMODV	AMG2
IJIGSKE IJIGSKM		BDA2	IJISNRM	DAMODY	AMG3
	DAMODV	BDG1	IJISNRMU	DAMODV	BDC2
IJIGTTT IJIJCK	DAMODV DAMOD	BDH1	IJISNRTU	DAMODV	BAE3
IJILADR	DAMOD	ADE2 ACF1	IJISOVP	DAMODY	BEB1
IJILBK	DAMOD	AHG1	IJISOV1	DAMODV DAMODV	BEJ2
IJILCH	DAMOD	<b>AAA</b> 5	IJISOV2 IJISOV2	DAMODV	BFC3 BFF1
IJILOAD	DAMOD	AGD4	IJISBV2&6	DAMODV	BFD3
IJIMXK	DAMOD	AAJ1	IJISPW	DAMODV	AKF4
IJINCD	DAMOD	AHC1	IJISRFD	DAMODV	AKD4
IJINCT	DAMOD	AEE4	IJISKUN	DAMODV	AKC3
IJINEF	DAMOD	ABB4	IJISRTEK	DAMODV	BCE4
IJINFIT	DAMODV	AMA5	IJISSEDF	DAMODV	AKE2
IJINID	DAMOD	ADC 1	IJISSF	DAMODV	AKF5
IJINRM	DAMOD	AAF3	IJISSTO	DAMODV	AKC1
IJINRTO	DAMOD	ABF2	IJISSTRT	DAMODV	AKD1
IJIOVCH	DAMODV	BFb1	IJISSVCF	DAMODV	BFH3
IJ IOVH	DAMODV	BFE4	IJISSWL	DAMODV	AKK1
IJ IOVP	DAMOD	AGB2	IJISTDL1	DAMODV	ALF5
IJIOV2	DAMOD	AGD2	IJISTDL2	DAMODV	ALG5
IJIPRI	DAMOD	AAB5	IJISTI	DAMODV	AKA5
IJIPSI	DAMOD	AAB4	IJISTK	DAMODV	AKC 5
IJ IRDY	DAMOD	AJD3	IJISTKN	DAMODV	ALH2
IJIREAD	DAMOD	ACE1	IJISTNR	DAMODV	BCG4
IJIREAD	DAMODV	BBE5	IJISTO	DAMOD	AAC1
IJ IRND	DAMOD	AAH2	IJISTRT	DAMOD	AAD1
IJIRON	DAMOD	AAB2	IJISTT	DAMODV	AKD5
IJIRTER	DAMOD	ABG5	IJISUBRS	DAMOD	AAG4
IJIRZO	DAMOD	AAC2	IJISUID	DAMODV	BBH1
IJISAFIT	DAMODV	AMH4	IJISVR	DAMODV	ALH4
IJISAFRD IJISAFT	DAMODV	AMB4	IJISVT	DAMODV	AKG5
IJISANUL	DAMODV DAMODV	AMA3 AKJ1	IJISVT1 IJISVW	DAMODV DAMODV	AKJ5
IJISASRO	DAMODV	AMB5	IJISWC	DAMODV	ALE1 ALB1
IJISBK	DAMOD	AAE4	IJISWEDF	DAMODV	ALDI AMF4
IJISCMC	DAMODV	BED3	IJISWLL	DAMODV	AMF1
IJISCTL	DAMOD	AFB4	IJISWLR	DAMODV	AME1
IJISCTL1	DAMOD	AFC4	IJISWID	DAMOD	AFF4
IJISCTLI	DAMOD	AFC5	IJISWND	DAMODV	AKK3
IJISDOM	DAMODV	AKH3	IJISWND	DAMODV	AMG1
IJISDV3	DAMODV	BFB3	IJISWND	DAMODV	ANK3
IJ ISEOV	DAMODV	BAF2	IJISWRT	DAMODV	AKG1
IJISERF	DAMODV	AME2	IJISHTM	DAMODV	BAB1
IJISFGC	DAMODV	BFH5	IJISXTFD	DAMODV	BAB3
IJISFREE	DAMOD	AFB5	IJISYID	DAMODV	BBE1

Label	Phase	Location	Label	Phase	Location
IJISZER	DAMODV	AMC3	LOADUP	\$\$B0IS07	MBD2
IJITIC	DAMOD	AJD2	LOADJP	\$\$BCISOA	NBB 4
IJ ITNR	DAMOD	ADE4	LUADJSER	\$\$BODACL	CNC 3
IJITOM	DAMOD	ADD5	LOLIM	<b>\$\$BOISO9</b>	MGB1
I <b>J</b> ITRHLD	DAMOD	AAJ2	LUOP	\$\$BINDEX	FAE3
IJIWAFT	DAMODV	ANF1	LOOPOFF	\$\$BINDEX	FBG1
IJ IWFTR	DA MOD V	BBF1	LSTPRDAT	\$\$BOIS10	MKA2
IJIWND	DAMOD	AAK2	LSTRCD	IS.LOAD	DED5
IJIWND IJIWRI	DAMOD DAMOD	AFE1 AJB3	LSTTRK	IS.LOAD	DFA1
IJIWRU	DAMOD	AAG1	MAXUUT	\$\$BODAU1	СКНЗ
IJIWTM	DAMOD	ABB1	MISZOK	IS.LOAD	DEG 2
IJIXNF	DAMOD	AHA4	MODOK	\$\$B0IS08	MDG2
IJ IXTFD	DAMOD	ABD2	MORXTNT	\$\$BODAIN	BHG 4
IJIYID	DAMOD	ABJ4	MORXINI	\$\$BODAI1	BLC 1
IJIZER	DAMOD	AAC3	MOVE	\$\$BORTV1	NDC 4
IJRRGOT	IS.ADDRTR	KBC4	MOVEDATA	\$\$BODACL	CNB 5
IJUNBLK	IS.ADD	ELF2	MOVEHR	IS.LOAD	DAB3
ILLEGID ILLEGID	IS.RETRVE IS.ADDRTR	GGJ3 JHJ3	MOVELL	\$\$BINDEX \$\$BODAO2	FAE4 CBJ1
ILLEGID	IS.ADDRTR	JLD5	MOVEPT	\$\$BODA02	BKB4
INACTO	IS.LOAD	DAJ5	MSGRET	\$\$BODAU1	CKE4
INCCNT	IS.LOAD	DBC1	MS GRT N	\$\$BODAIN	BJH5
INCFXD	\$\$B0IS01	LAG4	MSGRTN	\$\$BODAI1	BLJ5
INCMWR	IS.LOAD	DDB2	MSGRTN	\$\$BODAI1	BMD4
INCR	IS.LOAD	DBD4	MSGRTN	\$\$BODA01	BNE 5
INCREMNT	\$\$BODAIN	BJF3	MSGRTN	\$\$B0IS01	LAG2
INCRFD	IS.LOAD	DFE3	MSGRTN	\$\$B0IS05	LJH3
INCRKEY	\$\$BODAC L	CPH1	MSGRTN	\$\$B0IS06	LME4
INCRMT	\$\$BODAO2	CBH4	MSGRTNE	\$\$BOISO7	MBC 2
INCVAR INDEX	\$\$BODAO1 IS•RETRVE	BQG1 GKB5	MSG2 MSG2	\$\$BODAIN \$\$BODAI1	BGD5 BKG3
INDEX	IS.RETRVE	GPE2	MS G2	\$\$BODA11 \$\$BODA01	BQG2
INDEX	IS.ADDRTR	JMH5	MSTHI	IS.LOAD	DAB 5
INDSKIP	\$\$BINDEX	FAG1	MTON	\$\$BINDEX	FAJ4
INDXWR	IS.LOAD	DBH4	MTRGET	\$\$B0IS07	MBH4
INICNT	IS.LOAD	DAG5	MVEXT	\$\$BODAIN	BGG 2
IN IT4	IS.RETRVE	GKJ2	MV PNT R	\$\$BODA01	BNH3
IN IT4	IS.RETRVE	GPE3			
INIT4	IS.ADDRTR	JNJ2	NDVTOC	\$\$B0IS03	LFC2
INPULB INPUT	\$\$BODAU1 \$\$BODAIN	CKB4 BHD4	NDXERR NDXRTN	\$\$B0IS05	LJG3
INPUT	\$\$BODAIN	BJE5	NEXT	\$\$BOISO5 IS•RETRVE	LJA1 GHC2
INPUT	\$\$BODATN	CKK1	NEXT	IS.RETRVE	GNF 1
INSERT	\$\$BODAU1	CLA3	NEXT	IS.ADDRTR	JJD3
INTCCW	IS.LOAD	DEE3	NEXT	IS.ADDRTR	JLE3
INTCCW	IS.LOAD	DFH1	NEXTPHAS	<b>\$\$BOISO9</b>	MGA5
INTIWR	IS.LOAD	DBD1	NF4FND	<b>\$\$BOISO2</b>	LDG1
IOCOMPAR	\$\$BDISO7	MCK1	NF4FND1	\$\$B0IS02	LDF3
ISLOOP	\$\$BOIS04	LGD1	NOFULTRK	\$\$B0IS10	MKB4
ISLOOP	\$\$BOISO5 IS.LOAD	LHE1	NOF1	\$\$BODAU1	CKG2
ISSHRD	13. LUAU	DBD2	NOF1LB NOF3	\$\$BODAI1 \$\$BODAI1	BMB 3 BMB 4
KAPUT	\$\$B0IS08	MEB3	NOF3	\$\$BODATI	CLH4
KEYROUT	IS.RETRVE	GHB4	NOF4	\$\$BODA01	BND5
KEYROUT	IS.RETRVE	GNB4	NOF4	\$\$B0DA02	CDB4
KEYROUT	IS.ADDRTR	JLD1	NOF4	\$\$B0DA03	CEF4
			NOF4LB	\$\$BDIS06	LNH2
LABADR	\$\$BODAIN	BJC1	NOMS G	<b>\$\$</b> BODAO3	CED1
LABELSW	\$\$BODACL	CPG3	NOMS G	\$\$BODAO4	CGE1
LDLIM	\$\$BODA01	BQF1	NONFIRST	\$\$BINDEX	FAC 5
LIMCHK	IS.LOAD	DEC4	NONS HRD	IS.LOAD	DAC 4
LIMRCH	\$\$BODA02	CAC3	NORECEND	IS.RETRVE	GJH4
LLVSUĽ LO <b>A</b> DALTR	\$\$BOISO1 \$\$BODAI1	LAC3 BKA5	NOREC FND NOREC FND	IS.RETRVE IS.ADDRTR	GPH2
LOADONE	\$\$BODACL	CMC1	NOREC FND	I S. ADDRTR	JKJ4 JMD5
LONDONL	44500MOE	Onoi	HONEOIAU	T 2 S M D D V I IV	כטווט

NORMAR	Label	Phase	Location	Label	Phase	Location
NUMBER   15.10AD	NORELAD	\$\$BODAIN	BHE2	OUTRINE	\$\$B0IS07	MBC4
NORMAL   IS.RETRYE   GHE						
NGRMAL IS.ADDRTR JUG3 OVFRTN \$5801505 LGS NORMAL IS.ADDRTR JUD3 OVFRTN \$5801505 LHB2 NORMAL IS.ADDRTR JUD3 OVFRTN \$5801505 LHB2 NORMALT IS.ADDRTR JUD3 OVFRTN \$5801505 LHB2 NORMALTR IS.RETRYE GPB1 NORMALTRY IS.RETRYE GPB1 NORMALTRY IS.ADDRTR JUB3 OVFRTN \$5801505 LAG5 NORMALTRY IS.ADDRTR JUB3 OVFRTN \$5801505 LAG5 NORMALTRY IS.ADDRTR JUB3 PASS NORMALTRY IS.ADDRTR JUB						
NORMAL	NORMAL					
NORMITAK   IS.ADDRTR   JLD3   OVFKTN   \$801050	NORMAL	IS.ADDRTR		OVFRTN	\$\$B0IS04	
NORMITAK   IS.RETRYE   GJ61	NORMAL	IS.ADDRTR			\$\$B0IS05	
NORMITRK	NORMLTRK		GJG1	OVLMSR		LAC5
NORDOM  \$\$800A03  NOSSER \$\$800A01  \$\$8051510  NOSSER \$\$800A01  \$\$801S10  NOSSER \$\$800A01  \$\$801S10  NOTCHAIN \$\$81510EX FAC3  PDROUT \$\$801S10  MJF1  NOTCHAIN \$\$81510EX FAC3  PDROUT \$\$801S10  MJF1  NOTCHAIN \$\$81510EX FAC3  PDROUT \$\$801S10  MJF1  NOTCHAIN \$\$81510EX FAC3  PDROUT \$\$801S10  MJF1  NOTCHAIN \$\$81510EX FAC3  PDROUT \$\$801S10  MJF1  NOTCHAIN \$\$81510EX FAC3  PDSCHARC \$\$801S05  LHC5  NOVERE 1S.LOAD DGF4 PDTUTI IS.LOAD DGA1  NOVLAB \$\$800A01  BME4 PDTUTI IS.LOAD DAK1  NOVLAB \$\$800A01  BME4 PDUPPTR \$\$801S10  AK1  NOVLAB \$\$800A01  BME4 PDUPPTR \$\$801S10  AK1  NOVLAB \$\$801S06  LNE2 PD11B  IS.LOAD DEJ1  NOVLAS \$\$801S06  LNE2 PD11B  IS.LOAD DEJ1  NOVLAS \$\$801S02  LCF4 PHASE1 \$\$801S02  LCF4 PHASE3 \$\$801S05  LGB1  NOVULI \$\$800A03  CEC4 PHASE3 \$\$801S05  LGB1  NOVLI \$\$800A03  CEC4 PHASE3 \$\$801S05  LBB1  NOVXT \$\$800A01  KRE2 PHASE4 \$\$801S06  LGB1  NOXT \$\$800A03  CEC4 PHASE3 \$\$801S05  LBB1  NOXT \$\$800A03  CEC4 PHASE4 \$\$801S06  LBB1  NOXT \$\$800A03  CEC4 PHASE5 \$\$801S05  LBB1  NOXT \$\$800A03  CEC4 PHASE6 \$\$801S05  LBB1  NOXT \$\$800A03  CEC4 PHASE6 \$\$801S05  LBB1  NOXT \$\$800A03  CEC4 PHASE6 \$\$801S05  LBB1  NOXT \$\$800A03  CEC4 PHASE6 \$\$801S05  LBB1  NOXT \$\$800A03  CEC4 PHASE6 \$\$801S05  LBB1  NOXT \$\$800A03  CEC4 PHASE6 \$\$801S05  LBB1  NOXT \$\$800A03  CEC4 PHASE6 \$\$801S05  LBB1  NOXT \$\$800A03  CEC4 PHASE6 \$\$801S05  LBB1  NOXT \$\$800A03  CEC4 PHASE6 \$\$801S05  LBB1  NOXT \$\$800A03  CEC4 PHASE6 \$\$801S05  LBB1  NOXT \$\$800A03  CEC4 PHASE6 \$\$801S05  LBB1  NOXT \$\$800A03  CEC4 PHASE6 \$\$801S05  LBB1  NOXT \$\$800A03  CEC4 PHASE6  PHASE6 \$\$801S05  LBB1  NOXT \$\$800A03  CEC4 PHASE6  PHASE6 \$\$801S05  LBB1  NOXT \$\$800A03  CEC4 PHASE6  PHASE6  \$\$801S05  LBB1  NOXT \$\$800A03  CEC4 PHASE6  PHASE6  \$\$801S05  LBB1  NOXT \$\$800A03  CEC4 PHASE6  PHASE6  \$\$801S05  LBB1  NOXT \$\$800A03  CEC4 PHASE6  PHASE6  \$\$801S05  LBB1  NOXT \$\$800A03  CEC4 PHASE6  PHASE6  \$\$801S05  LBB1  NOXT \$\$800A03  CEC4 PHASE6  PHASE6  \$\$801S05  LBB1  NOXT \$\$800A03  CEC4 PHASE6  PHASE6  \$\$801S05  LBB1  NOXT  \$\$800A03  CEC4 PHASE6  PHASE6  \$\$801S05  LBB1  NOXT  \$\$800A03  CEC4 PHASE6  PHA	NORMLTRK	IS.RETR VE	GPB1			
NOSKER \$\$800A01 8NG3 PPDGINT \$\$801S10 MJC4 NOSKER \$\$81NDEX FAC3 PPROUT \$\$801S10 MJF1 NOTCHAIN \$\$800A01 KR55 PPSCARR \$\$801S10 MJF3 NOUHL \$\$800A01 KR55 PPSCARR \$\$801S10 MJF3 NOUHL \$\$800A01 BM65 PPSCARR \$\$801S10 MJF3 NOVLAB \$\$800A01 BM65 PPTGR IS.LOAD DADA NOVLAB \$\$800A01 BM64 PPTGR IS.LOAD DADA NOVLAB \$\$800A01 BM64 PPDGR IS.LOAD DADA NOVLAB \$\$800A01 BM64 PPDGR IS.LOAD DEBT NOVLAS \$\$801S02 LCG4 PPBM IS.LOAD DEBT NOVLAS \$\$801S02 LCG4 PPBM IS.LOAD DEBT NOVLAS \$\$801S02 LCG4 PPBM IS.LOAD DEBT NOVLAS \$\$801S02 LCG4 PPBM IS.LOAD DEBT NOVLAS \$\$801S02 LCG4 PPBM IS.LOAD DEBT NOVLAS \$\$801S02 LCG4 PPBM IS.LOAD DEBT NOVLAS \$\$801S02 LCG4 PPBM IS.LOAD DEBT NOVLAS \$\$801S02 LCG4 PPBM IS.LOAD DEBT NOVLAS \$\$801S02 LCG4 PPBM IS.LOAD DEBT NOVLAS \$\$800A03 CG2 PPHASE2 \$\$801S02 LCG81 NOVLAS \$\$800A03 CG2 PPHASE3 \$\$801S02 LCG81 NOVLAS \$\$800A03 CG2 PPHASE5 \$\$801S02 LCG81 NOVLAS \$\$800A03 CG2 PPHASE5 \$\$801S02 LCG81 NOVLAS \$\$800A03 CG2 PPHASE5 \$\$801S02 LCG81 NOVLAS \$\$800A03 CG2 PPHASE5 \$\$801S02 LCG81 NOVLAS \$\$800A03 CG2 PPHASE5 \$\$801S02 LGB1 NOVLAS \$\$800A03 CG2 PPHASE5 \$\$801S02 LGB1 NOVLAS \$\$800A03 CG2 PPHASE5 \$\$801S02 LGB1 NOVLAS \$\$800A03 CG2 PPHASE7 \$\$801S02 LGB1 NOVLAS \$\$800A03 CG2 PPHASE7 \$\$801S02 LGB1 NOVLAS \$\$800A03 CG2 PPHASE7 \$\$801S02 LGB1 NOVLAS \$\$800A03 CG2 PPHASE7 \$\$801S02 LGB1 NOVLAS \$\$800A03 CG2 PPHASE7 \$\$801S02 LGB1 NARFF4 \$\$800A03 CG2 PPHASE7 \$\$801S02 LGB1 NARFF4 \$\$800A03 CG2 PPHASE7 \$\$801S02 LGB1 NARFF4 \$\$800A03 CG2 PPHASE7 \$\$801S02 LGB1 NARFF4 \$\$800A03 CG2 PPHASE7 \$\$801S02 LGB1 NARFF4 \$\$800A03 CG2 PPHASE7 \$\$801S02 LGB1 NARFF4 \$\$800A03 LGB2 PPHASE7 \$\$801S02 LGB1 NARFF4 \$\$800A03 LGB2 PPHASE7 \$\$801S02 LGB1 NARFFA \$\$800A03 LGB2 PPHASE7 \$\$801S02 LGB1 NARFFA \$\$800A03 LGB2 PPHASE7 \$\$801S02 LGB1 NARFFA \$\$800A03 LGB2 PPHASE7 \$\$801S02 LGB1 NARFFA \$\$800A03 LGB2 PPHASE7 \$\$801S02 LGB2 NARFFA \$\$800A03 LGB2 PPHASE7 \$\$801S02 LGB2 NARFFA \$\$800A03 LGB2 PPHASE7 \$\$801S02 LGB2 NARFFA \$\$800A03 LGB2 PPHASE7 \$\$801S02 LGB2 NARFFA \$\$800A03 LGB2 PPHASE7 \$\$801S02 LGB2 NARFFA \$\$800A03 LGB2 PPHASE7 \$\$801S02 LGB2 NARFFA \$\$800A03 LGB2 PPHASE7 \$\$801S02 LGB2 N	NORMLTRK	IS.ADDRTR	JMB1	PASS	\$\$BODAU1	CKF5
NOSK IP \$\$81NDEX FAC3 PPROUT \$\$801S10 MJF1 NOTCHAIN \$\$81NDEX FBE1 PPSCHARC \$\$801S10 MJAF1 NOUHL \$\$800AU1 CKD5 PPSEKR \$\$801S15 MJAS NOUHL \$\$800AU1 CKD5 PPSEKR \$\$801S05 LHC5 NOVERF IS.LOAD DGF4 PPTUTI IS.LOAD DGF1 NOVLAB \$\$800AU1 BM55 PPTKK IS.LOAD DAK1 NOVLAB \$\$800AU1 BM55 PPTKK IS.LOAD DAK1 NOVLAB \$\$801S06 LME2 PPUPPTK \$\$601S10 MJG4 NOVLAB \$\$801S06 LME2 PPUPPTK \$\$601S10 MJG4 NOVLAB \$\$801S06 LME2 PPUPPTK \$\$601S10 MJG4 NOVLAB \$\$801S06 LME2 PPUPPTK \$\$601S10 MJG4 NOVLAB \$\$801S06 LME2 PPUPPTK \$\$601S10 MJG4 NOVLAB \$\$801S06 LME2 PPUPPTK \$\$601S10 LAD1 NOVLAB \$\$801S02 LCF4 PHASE1 \$\$801S01 LAD1 NOVLAB \$\$801S02 LCF4 PHASE1 \$\$801S01 LAD1 NOVLL \$\$800AU3 CEC4 PHASE3 \$\$801S03 LEB1 NOVLAT \$\$800AU3 CEC4 PHASE3 \$\$801S03 LEB1 NOVXT \$\$800AU3 CEC4 PHASE3 \$\$801S03 LEB1 NOXXT \$\$800AU3 CEC4 PHASE3 \$\$801S03 LEB1 NOXXT \$\$800AU3 CEC4 PHASE3 \$\$801S05 LHB1 NOXXT \$\$800AU3 CEC2 PHASE4 \$\$801S05 LHB1 NOXXT \$\$800AU3 CEC2 PHASE4 \$\$801S05 LHB1 NOXXT \$\$800AU3 CEC2 PHASE4 \$\$801S05 LHB1 NOXXT \$\$800AU3 CEC2 PHASE4 \$\$801S05 LHB1 NOXXT \$\$800AU3 CEC2 PHASE4 \$\$801S05 LHB1 NOXXT \$\$800AU3 CEC2 PHASE4 \$\$801S05 LHB1 NOXXT \$\$800AU3 CEC2 PHASE4 \$\$801S05 LHB1 NOXY \$\$800AU3 CEC2 PHASE4 \$\$801S05 LHB1 NOXY \$\$800AU3 CEC2 PHASE4 \$\$801S05 LHB1 NOXY \$\$800AU3 CEC2 PHASE4 \$\$801S05 LHB1 NOXY \$\$800AU3 CEC2 PHASE4 \$\$801S05 LHB1 NOXY \$\$800AU3 CEC2 PHASE4 \$\$801S05 LHB1 NOXY \$\$800AU3 CEC2 PHASE4 \$\$801S05 LHB1 NOXY \$\$800AU3 CEC2 PHASE4 \$\$801S05 LHB1 NOXY \$\$800AU3 CEC2 PHASE4 \$\$801S05 LHB1 NOXY \$\$800AU3 CEC2 PHASE4 \$\$801S05 LHB1 NOXY \$\$800AU3 CEC2 PHASE4 \$\$801S05 LHB1 NOXY \$\$800AU3 CEC2 PHASE4 \$\$801S05 LHB1 NOXY \$\$800AU3 CEC2 PHASE4 \$\$800AU3 LFJJ NOXY \$\$800AU3 CEC4 PHASE5 S\$801S05 LGC4 NOXY S\$800AU3 CEC2 PHASE4 S\$801S05 LGC4 NOXY S\$800AU3 CEC2 PHASE4 S\$801S05 LGC4 NOXY S\$800AU3 CEC2 PHASE4 S\$801S05 LGC4 NOXY S\$800AU3 CEC2 PHASE5 S\$801S05 LGC4 NOXY S\$800AU3 CEC2 PHASE4 S\$801S05 LGC4 NOXY S\$800AU3 CEC2 PHASE4 S\$801S05 LGC4 NOXY S\$800AU3 CEC2 PHASE5 S\$801S05 LGC4 NOXY S\$800AU3 CEC2 PHASE5 S\$801S05 LGC4 NOXY S\$800AU3 CEC2 PHASE5 S\$801S05 LGC4 NOXY S\$800AU3 CEC2 PHASE5 S\$801S05 LGC4	NOROOM	\$\$BODAO3	CFE3	PDARTN	\$\$B0I\$05	LHA4
NOTCHAIN \$86 NOEX FBEI POSCHAGN \$4501S10 MJA3 NOUHL \$880DOUL CKD5 POSERR \$4501S05 LHC5 NOVERR IS.LOAD DGF4 POTUTI IS.LOAD DFD1 NOVLAB \$450DOUL BM65 POTUTI IS.LOAD DAK1 NOVLAB \$450DOUL BM65 POTUTI IS.LOAD DAK1 NOVLAB \$450DOUL BM64 POUPTR \$450LS10 MJG4 NOVLBS \$450DOUL BM64 POUPTR \$450LS10 MJG4 NOVLBS \$450DOUL BM64 POUPTR \$450LS10 MJG4 NOVLBS \$450DOUL CKG4 PGBN IS.LOAD DEJ1 NOVLMS \$450LS02 LCG4 PGBN IS.LOAD DEJ1 NOVLMS \$450LS02 LCG4 PGBN IS.LOAD DEJ1 NOVLMS \$450LS02 LCG4 PGBN IS.LOAD DEJ1 NOVLUS \$450DOUL SA50DOUL CKG2 PHASE1 \$450LS02 LCG1 NOVOLL \$450DOUL CKG2 PHASE3 \$450LS02 LCG1 NOVOLL \$450DOUL CKG2 PHASE3 \$450LS03 LEB1 NOVXT \$450DOUL CKG2 PHASE4 \$450LS03 LEB1 NOVXT \$450DOUL CKG2 PHASE4 \$450LS03 LEB1 NOXXT \$450DOUL CKG2 PHASE5 \$450LS03 LEB1 NOXXT \$450DOUL CKG2 PHASE5 \$450LS03 LEB1 NOXXT \$450DOUL CKG2 PHASE5 \$450LS03 LEB1 NOXXT \$450DOUL CKG2 PHASE5 \$450LS03 LEB1 NOXXT \$450DOUL CKG2 PHASE6 \$450LS03 LEB1 NOXXT \$450DOUL CKG2 PHASE6 \$450LS03 LEB1 NOXXT \$450DOUL CKG2 PHASE6 \$450LS03 LEB1 NOXXT \$450DOUL CKG2 PHASE6 \$450LS03 LEB1 NOXXT \$450DOUL CKG2 PHASE6 \$450LS03 LEB1 NOXXT \$450DOUL CKG2 PHASE6 \$450LS03 LEB1 NOXXT \$450DOUL CKG2 PHASE6 \$450LS03 LEB1 NOXXT \$450DOUL CKG2 PHASE6 \$450LS03 LEB1 NOXXT \$450DOUL CKG2 PHASE6 \$450LS03 LEB1 NOXXT \$450DOUL CKG2 PHASE6 \$450LS03 LEB1 NOXXT \$450DOUL CKG2 PHASE6 \$450LS03 LEB1 NOXXT \$450DOUL CKG2 PHASE6 \$450LS03 LEB1 NOXXT \$450DOUL CKG2 PHASE6 \$450LS03 LEB1 NOXXT \$450DOUL CKG4 PHASE6 \$450LS03 LEB1 NOXXT \$450DOUL CKG4 PHASE0 \$450LS03 LEB1 NOXXT \$450DOUL CKG4 PHASE6 \$450LS03 LEB1 NOXXT \$450DOUL CKG4 PHASE6 \$450LS03 LEB1 NOXXT \$450DOUL CKG4 PHASE6 \$450LS03 LEB1 NOXXT \$450DOUL CKG4 PHASE6 \$450LS03 LEB1 NOXXT \$450DOUL CKG4 PHASE6 \$450LS03 LEB1 NOXXT \$450DOUL CKG4 PHASE6 \$450LS03 LEB1 NOXXT \$450DOUL CKG4 PHASE6 \$450LS03 LEB1 NOXXT \$450DOUL CKG4 PHASE6 \$450LS03 LEB1 NOXXT \$450DOUL CKG4 PHASE6 \$450LS03 LEB1 NOXXT \$450DOUL CKG4 PHASE6 \$450LS03 LEB1 NOXXT \$450DOUL CKG4 PHASE6 \$450LS03 LEB1 NOXXT \$450DOUL CKG4 PHASE6 \$450LS03 LEB1 NOXXT \$450DOUL CKG4 PHASE6 \$450LS03 LEB1 NOXXT \$450DOUL CKG4 PHASE6	NOSER	<b>\$\$BODAO1</b>		PDPOINT	\$\$B0IS10	MJC4
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NOVERF	NOTC HA IN			PDSCHAGN	<b>\$\$BOIS10</b>	
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NOVOL1						
NOXT \$\$60DA1N BJG5 PHASE5 \$\$801S05 LHB1 NOXT \$\$60DA02 CDB1 PHASE7 \$\$601S06 LLB1 NOXT \$\$60DA02 CBF4 PHASE7 \$\$601S07 MAB1 NO2321 \$\$80DA02 CBF4 PHASE7 \$\$601S07 MAB1 NO2321 \$\$80DA02 CBF4 PHASET \$\$601S05 LKA1 NRFF4 \$\$60DA01 BNB4 PH4FTC \$\$601S03 LFJ3 NRFF4 \$\$60DA01 BNB4 PH4FTC \$\$601S03 LFJ3 NRFF4 \$\$60DA01 BND4 PHASE0 \$\$80DA04 CHB1 NRFRYN \$\$60DA01 BND4 POARTN \$\$801S04 LGJ1 NRFO1 \$\$80DA01 BND4 POARTN \$\$801S04 LGJ1 NRF00 \$\$80DA02 CDB5 POINT 15.LOAD DJG1 NRF00 \$\$80DA03 CFE2 POINT \$\$801S09 MGJ1 NRF00 \$\$80DA03 CFE2 POINT \$\$801S09 MGJ1 NRF00 \$\$80DA04 CJE5 POINT \$\$801S09 MGJ1 NRF00 \$\$80DA04 CJE5 POINT \$\$801S09 MGJ3 NTNDPK IS.LOAD DFC1 POINT \$\$801S09 MGG3 NTNDPK IS.LOAD DFC2 PUBMSG \$\$80RTV1 NCF2 NXTRX \$\$60DA01 BOB2 PUBMSG \$\$80RTV1 NCF2 NXTRX \$\$60DA02 CAC4 RCDCAL IS.LOAD DFF2 NXTSU \$\$60DA02 CAC4 RCDCAL IS.LOAD DJG5 OFLOEXIT IS.RETRVE GHJ1 RUFLAB \$\$80DA02 CAH2 DFLOEXIT IS.RETRVE GMB1 ROFORB \$\$80DA01 BLD3 OFLOEXIT IS.RETRVE GMB1 ROFORB \$\$80DA01 BLD3 OFLOEXIT IS.RETRVE GMB1 ROFORB \$\$80DA01 BLD3 OFLOEXIT IS.RETRVE GMB1 ROFORB \$\$80DA01 BLD3 OFLOEXIT IS.ADDRTR JJB3 ROLABEL \$\$80DA01 CKB1 DFLOEXIT IS.ADDRTR JJB3 ROLABEL \$\$80DA01 CKB1 OFLOEXIT IS.ADDRTR JJB3 ROLABEL \$\$80DA01 CKB1 OFLOEXIT IS.ADDRTR JJB3 ROLABEL \$\$80DA01 CKB1 OFLOEXIT IS.ADDRTR JJB3 ROLABEL \$\$80DA01 CKB1 OFLOEXIT IS.ADDRTR JJB3 ROLABEL \$\$80DA01 CKB1 OFLOEXIT IS.ADDRTR JJB3 ROLABEL \$\$80DA01 CKB1 OFLOEXIT IS.ADDRTR JJB4 READT \$\$80DA01 CKC1 OFLOEXIT IS.ADDRTR JJB4 READT \$\$80DA01 CKC1 OFLOEXIT IS.ADDRTR JJB4 READT \$\$80DA01 CKC1 OFLOEXIT IS.ADDRTR JJB4 READT \$\$80DA01 CKC1 OFLOEXIT IS.ADDRTR JJB4 READT \$\$80DA01 CKC1 OFLOEXIT IS.ADDRTR JJB5 REEM \$\$80DA01 CKC1 OFLOEXIT IS.ADDRTR JJB4 READT \$\$80DA01 CKC1 OFLOEXIT IS.ADDRTR JJB5 REEM \$\$80DA01 CKC1 OFLOEXIT IS.ADDRTR JJB5 REEM \$\$80DA01 CKC1 OFLOEXIT IS.ADDRTR JJB5 REEM \$\$80DA01 CKC1 OFLOEXIT IS.ADDRTR JJB4 REEM \$\$80DA01 CKC1 OFLOEXIT IS.ADDRTR JJB5 REEM \$\$80DA01 CKC1 OFLOEXIT IS.ADDRTR JJB6 REEM \$\$80DA01 CKC1 OFLOEXIT IS.ADDRTR JJB6 REEM \$\$80DA01 CKC2 OFLOEXIT IS.LOAD DGG5 REEM \$\$80DA01 CKC2 OFLOEXIT IS.LOAD DGG5 REEM \$\$80DA01 CKC						
NOXT \$\$8 0DA02 CDB1 PHASEC \$\$801506 LLB1 NOXTX \$\$8 0DA02 CBF4 PHASET \$\$801507 MAB1 NO2321 \$\$8 0DA02 CBF4 PHASET \$\$801505 LKA1 NRFF4 \$\$8 0DA01 BNB4 PH4FTC \$\$801503 LFJ3 NRFF4 \$\$8 0DA01 BNB4 PH4FTC \$\$801503 LFJ3 NRFF4 \$\$8 0DA01 BNB4 PH4FTC \$\$801503 LFJ3 NRFF4 \$\$8 0DA01 BNB4 PH4FTC \$\$801503 LFJ3 NRFF4 \$\$8 0DA01 BNB4 PH4FTC \$\$801503 LFJ3 NRFF4 \$\$8 0DA01 BNB4 PH4FTC \$\$801504 LGB1 NRFRTN \$\$8 0T506 LNE3 PHICEB \$\$80DA1N BGH2 NRFVL \$\$8 0DA01 BND4 POAKTN \$\$801504 LGJ1 NRF01 \$\$8 0DA02 CDB5 POINT \$\$801504 LGJ1 NRF00 \$\$8 0DA02 CDB5 POINT \$\$801509 MGJ1 NRF09 \$\$8 0DA03 CFE2 POINT \$\$801509 MGG3 NTNDPK IS LOAD DFC1 POINT2 \$\$801509 MGG3 NTNDPK IS LOAD DFC2 PUBNSG \$\$801502 LCD4 NXTRK IS LOAD DFC2 PUBNSG \$\$801502 LCD4 NXTRX \$\$80DA01 BQB2 PUBNSG \$\$801502 LCD4 NXTRX IS LOAD DA62 NXTRX IS LOAD DA65 NXTRX IS LOAD DA65 NXTRX IS LOAD DA65 NXTRX IS LOAD DA65 NXTRX IS LOAD DA65 NXTRX IS LOAD DA65 NXTRX IS LOAD DA65 NXTRX IS LOAD DA65 NXTRX IS LOAD DA65 NXTRX IS LOAD DA65 NXTRX IS LOAD DA64 NXTRX IS LOAD DA65 NXTRX IS LOAD DA65 NXTRX IS LOAD DA65 NXTRX IS LOAD DA65 NXTRX IS LOAD DA65 NXTRX IS LOAD DA65 NXTRX IS LOAD DA65 NXTRX IS LOAD DA65 NXTRX IS LOAD DA65 NXTRX IS LOAD DA65 NXTRX IS LOAD DA65 NXTRX IS LOAD DA65 NXTRX IS LOAD DA65 NXTRX IS LOAD DA65 NXTRX IS LOAD DA65 NXTRX IS LOAD DA65 NXTRX IS LOAD DA65 NXTRX IS LOAD DA66 NXTRX IS LOAD DA66 NXTRX IS LOAD DA66 NXTRX IS LOAD DA66 NXTRX IS LOAD DA66 NXTRX IS LOAD DA66 NXTRX IS LOAD DA66 NXTRX IS LOAD DA66 NXTRX IS LOAD DA66 NXTRX IS LOAD DA66 NXTRX IS LOAD DA66 NXTRX IS LOAD DA66 NXTRX IS LOAD DA66 NXTRX						
NOXT						
NOZ221						
NRFF4						
NRFF4	-					
NR FRTN						
NRFVL						
NRF01						
NRF09						
NFF09	-					
NTNDPK						
NMTKCM						
NXTEXT						
NXTRK						
NXTSU				FUDM3 G	##DOV! AT	NCF 2
OFLOEXIT         IS.RETRVE         GHJI         RDEXCP         IS.LOAD         DJB5           OFLOEXIT         IS.RETRVE         GNB1         RDFOR3         \$\$B0DA02         CAH2           OFLOEXIT         IS.ADDRTR         JJB3         RDLABEL         \$\$B0DACL         CNB1           OFLOEXIT         IS.ADDRTR         JLA3         RDLST         IS.LOAD         DJC3           OFLOOP         IS.AETRVE         GKG3         READ         \$\$B0DA11         BKF2           OFLOOP         IS.RETRVE         GPB4         READF1         \$\$B0DAU1         CKD1           OFLOOP         IS.ADDRTR         JNA4         READRT         \$\$B0DAU1         CKC4           OFLOROUT         IS.RETRVE         GPB4         READRT         \$\$B0DAU1         CKC4           OFLOROUT         IS.RETRVE         GPB3         RECMUV         \$\$B0DAU1         CKC4           OFLOROUT         IS.ADDRTR         JNB2         RECMUV         \$\$B0DAU2         CCG5           OFLOROUT         IS.ADDRTR         JNB2         RECMUV         \$\$B0DAU2         CCG5           OFLOROUT         IS.ADDRTR         JNB2         RECMUV         \$\$B0DAU2         CCG5           OFLOROUT         IS.ADDRTR				BCDCM	15 1000	DEE 2
OFLOEXIT         IS.RETRVE         GHJ1         RDFLAB         \$\$80DAO2         CAH2           OFLOEXIT         IS.RETRVE         GNB1         RDFOR3         \$\$80DA01         BLD3           OFLOEXIT         IS.ADDRTR         JJB3         RDLABEL         \$\$80DACL         CNB1           OFLOEXIT         IS.ADDRTR         JLA3         RDLST         IS.LOAD         DJC3           OFLOOP         IS.RETRVE         GKG3         READ         \$\$80DA01         CKC1           OFLOOP         IS.RETRVE         GPB4         READF1         \$\$80DA01         CKC1           OFLOROUT         IS.RETRVE         GKB2         READUL         \$\$80DA01         CKC4           OFLOROUT         IS.RETRVE         GKB2         RECMUV         \$\$80DA01         CKC4           OFLOROUT         IS.RETRVE         GPB3         RECMUV         \$\$80DA01         CKC4           OFLOROUT         IS.ADDRTR         JNB2         RECMUV         \$\$80DA01         CKC4           OFLOROUT         IS.ADDRTR         JNB2         RECMUV         \$\$80DA01         CKC4           OFLOROUT         IS.ADDRTR         JNB2         RECMUV         \$\$80DA01         DWD           OFLOROUT         IS.ADDRTR <td>47130</td> <td>44000A0Z</td> <td>CACT</td> <td></td> <td></td> <td></td>	47130	44000A0Z	CACT			
OFLOEXIT         IS.RETRVE         GNB1         RDFOR3         \$\$BODAII         BLD3           OFLOEXIT         IS.ADDRTR         JJB3         RDLABEL         \$\$BODACL         CNB1           OFLOEXIT         IS.ADDRTR         JLA3         RDLST         IS.LOAD         DJC3           OFLOOP         IS.RETRVE         GKG3         READ         \$\$BODAII         BKF2           OFLOOP         IS.RETRVE         GPB4         READRT         \$\$BODAUI         CKD1           OFLOROUT         IS.RETRVE         GKB2         READUL         \$\$BODAUI         CKC4           OFLOROUT         IS.RETRVE         GFB3         RECMUV         \$\$BODAUI         CKC4           OFLOROUT         IS.ADDRTR         JNB2         RECMUV         \$\$BODAUI         CKC4           OFLOROUT         IS.ADDRTR         JNB2         RECMUV         \$\$BODAUI         BPD5           OFLOROUT         IS.ADDRTR         JNB2         RECMUV         \$\$BODAUI         BPD5           OFLOROUT         IS.ADDRTR         JNB2         RECMUV         \$\$BODAUI         BPD5           OFLOROUT         IS.ADDRTR         JNB2         RECMUV         \$\$BODAUI         BPD5           OFLOSAVE         \$\$BODAUI <td>DELDEXIT</td> <td>IS.RETRVE</td> <td>GH 11</td> <td></td> <td></td> <td></td>	DELDEXIT	IS.RETRVE	GH 11			
OFLOEXIT         IS.ADDRTR         JJB3         RDLABEL         \$\$BODACL         CNB1           OFLOEXIT         IS.ADDRTR         JLA3         RDLST         IS.LOAD         DJC3           OFLOOP         IS.RETRVE         GKG3         READ         \$\$BODAI1         BKF2           OFLOOP         IS.RETRVE         GPB4         READF1         \$\$BODAU1         CKD1           OFLOOP         IS.ADDRTR         JNA4         READRT         \$\$BODAU1         CKD1           OFLOROUT         IS.RETRVE         GKB2         READWL         \$\$BODAU1         CKC4           OFLOROUT         IS.RETRVE         GKB2         RECMOV         \$\$BODAU1         BPD5           OFLOROUT         IS.ADDRTR         JNB2         RECMOV         \$\$BODAU1         BPD5           OFLOROUT         IS.ADDRTR         JNB2         RECMOV         \$\$BODAU1         BPD5           OFLOROUT         IS.ADDRTR         JNB2         RECMOV         \$\$BODAU1         BPD5           OFLOROUT         IS.ADDRTR         JNB2         RECMOV         \$\$BODAU2         CCG5           OFLOROUT         IS.ADDRTR         JNB2         RECMOV         \$\$BODAU2         CCG5           OFLOROUT         \$\$BODAU1						
OFLOEXIT         IS.ADDRTR         JLA3         RDLST         IS.LOAD         DJC3           OFLOOP         IS.RETRVE         GKG3         READ         \$\$B0DAII         BKF2           OFLOOP         IS.RETRVE         GPB4         READF1         \$\$B0DAUI         CKD1           OFLOOP         IS.ADDRTR         JNA4         READRT         \$\$B0DAUI         CKD1           OFLOROUT         IS.RETRVE         GKB2         READUL         \$\$B0DAUI         CKC4           OFLOROUT         IS.RETRVE         GPB3         RECMOV         \$\$B0DAUI         CKC4           OFLOROUT         IS.ADDRTR         JNB2         RECMOV         \$\$B0DAUI         CKC4           OFLOROUT         IS.ADDRTR         JNB2         RECMOV         \$\$B0DAUI         CKC4           OFLOROUT         IS.ADDRTR         JNB2         RECMOV         \$\$B0DAUI         CKC4           OFLOROUT         IS.ADDRTR         JNB2         RECMOV         \$\$B0DAUI         CKC4           OFLOROUT         IS.ADDRTR         JNB2         RECMOV         \$\$B0DAUI         CCG5           OFLOROUT         IS.ADDRTR         JNB2         RECMOV         \$\$B0ISO0         MFC1           OPN2         \$\$B0ISO0						
OFLOOP         IS.RETRVE         GKG3         READ         \$\$BODAII         BKF2           OFLOOP         IS.RETRVE         GPB4         READF1         \$\$BODAUI         CKD1           OFLOOP         IS.ADDRTR         JNA4         READRT         \$\$BODAUI         CKC4           OFLOROUT         IS.RETRVE         GKB2         READUL         \$\$BODAUI         CKC4           OFLOROUT         IS.RETRVE         GPB3         RECMOV         \$\$BODAUI         BPD5           OFLOROUT         IS.ADDRTR         JNB2         RECMOV         \$\$BODAO2         CCG5           OFLOSAVE         \$\$BOTSO6         LLJ4         RELOCATE         \$\$BOISO9         MFC1           OPN2         \$\$BODAII         BKB1         RELOCATE         \$\$BOISO9         MFC1           OPN2         \$\$BODAII         BKB1         RETRVE         \$\$BOISO7         MBF3           OTHERR         IS.LOAD         DJG4         RETRVE         \$\$BOISO7         MBF3           OUT         IS.LOAD         DAO2         RETJRN         IS.LOAD         DJG3           OUT         IS.LOAD         DCJ5         REVTOC         \$\$BODAO2         CDB2           OUT         \$\$BINDEX         FAD2						
OFLOOP         IS.RETRVE         GPB4         READF1         \$\$B0DAU1         CKD1           OFLOOP         IS.ADDRTR         JNA4         READRT         \$\$B0ISO6         LNB1           OFLOROUT         IS.RETRVE         GKB2         READUL         \$\$B0DAU1         CKC4           OFLOROUT         IS.RETRVE         GPB3         RECMOV         \$\$B0DAU1         BPD5           OFLOROUT         IS.ADDRTR         JNB2         RECMOV         \$\$B0DAU1         BPD5           OFLOROUT         IS.ADDRTR         JNB2         RECMOV         \$\$B0DAU1         BPD5           OFLOROUT         IS.ADDRTR         JNB2         RECMOV         \$\$B0DAU1         BPD5           OFLOROUT         IS.ADDRTR         JNB2         RECMOV         \$\$B0DAU1         BPD5           OFLOROUT         IS.ADDRTR         JNB2         RECMOV         \$\$B0DAU2         CCG5           OFLOSAVE         \$\$B0ISO6         LLJ4         RELOCATE         \$\$B0ISO9         MFC1           OPN2         \$\$B0ISO6         LLJ4         RELOCATE         \$\$B0ISO7         MB51           OFLOSAVE         \$\$B0ISO7         DEA3         RETRVE         \$\$B0ISO7         MB65           OFLOSAVE         IS.LOAD						
OFLOOP         IS.ADDRTR         JNA4         READRT         \$\$BOISO6         LNB1           OFLOROUT         IS.RETRVE         GKB2         READUL         \$\$BODAU1         CKC4           OFLOROUT         IS.RETRVE         GPB3         RECMUV         \$\$BUDAO1         BPD5           OFLOROUT         IS.ADDRTR         JNB2         RECMOV         \$\$BODAO2         CCG5           OFLOSAVE         \$\$BOTSO6         LLJ4         RELOCATE         \$\$BOISO9         MFC1           OPN2         \$\$BODATI         BKB1         RELOCATE         \$\$BOISO9         MFC1           OPN2         \$\$BODATI         BKB1         RELOCATE         \$\$BOISO7         MBF3           ORCCW         IS.LOAD         DEA3         RETRVE         \$\$BOISO7         MBF3           OTHERR         IS.LOAD         DJG4         RETRVU         \$\$BCISOA         NAG5           DUT         IS.LOAD         DBK3         RETJRN         IS.LOAD         DHG5           DUT         IS.LOAD         DCJ5         REVTOC         \$\$BODAO2         CDB2           DUT         IS.LOAD         DEG3         RNOBMP         IS.LOAD         DBJ2           OUT         \$\$BINDEX         FAD2         R						
OFLOROUT         IS.RETRVE         GKB2         READUL         \$\$BODAU1         CKC 4           OFLOROUT         IS.RETRVE         GPB3         RECMÜV         \$\$BDAU1         BPD5           OFLOROUT         IS.ADDRTR         JNB2         RECMÜV         \$\$BDAU2         CCG5           OFLOSAVE         \$\$BOTSO6         LLJ4         RELOCATE         \$\$BOISO9         MFC1           OPN2         \$\$BODA11         BKB1         RELOCATE         \$\$BOISO7         MB73           ORCCW         IS.LOAD         DEA3         RETRVE         \$\$BOISO7         MB73           OTHERR         IS.LOAD         DJG4         RETRVU         \$\$BCISOA         NAG5           DUT         IS.LOAD         DAO2         RETJRN         IS.LOAD         DHG5           DUT         IS.LOAD         DBK3         RETJRN         IS.LOAD         DJA3           DUT         IS.LOAD         DEG3         RNOBMP         IS.LOAD         DBJ2           DUT         \$\$BINDEX         FAD2         RNOBMP         IS.LOAD         DGJ5           OUT         \$\$BINDEX         FBC2         RSETFP         IS.LOAD         DGJ5           OUT         \$\$BINDEX         FBC5         RSETTF						
OFLOROUT         IS.RETRVE         GPB3         RECMUV         \$\$BUDAU1         BPD5           OFLOROUT         IS.ADDRTR         JNB2         RECMUV         \$\$BUDAU2         CCG5           OFLOSAVE         \$\$ROTSO6         LLJ4         RELOCATE         \$\$BOISO9         MFC1           OPN2         \$\$BODA11         BKB1         RELOCATE         \$\$BOISO         MJB1           ORCCW         IS.LOAD         DEA3         RETRVE         \$\$BOISOO7         MBF3           OTHERR         IS.LOAD         DJG4         RETRVU         \$\$BCISOA         NAG5           OUT         IS.LOAD         DAD2         RETJRN         IS.LOAD         DHG5           OUT         IS.LOAD         DBK3         RETJRN         IS.LOAD         DJA3           OUT         IS.LOAD         DCJ5         REVTOC         \$\$BODAO2         CDB2           OUT         \$\$BINDEX         FAD2         RNOBMP         IS.LOAD         DGJ5           OUT         \$\$BINDEX         FBC2         RSETFP         IS.LOAD         DGJ5           OUT         \$\$BINDEX         FBC5         RSETTF         IS.LOAD         DAF2           OUT         \$\$BODAO1         LBB3         RTNMON						
OFLOROUT         IS.ADDRTR         JNB2         RECMOV         \$\$BODA02         CCG5           OFLOSAVE         \$\$BOISO6         LLJ4         RELOCATE         \$\$BOISO9         MFC1           OPN2         \$\$BODAII         BKB1         RELOCATE         \$\$BOISO0         MJB1           ORCCW         IS.LOAD         DEA3         RETRVE         \$\$BOISO7         MBF3           OTHERR         IS.LOAD         DJG4         RETRVU         \$\$BCISOA         NAG5           OUT         IS.LOAD         DAO2         RETJRN         IS.LOAD         DHG5           OUT         IS.LOAD         DCJ5         REVTOC         \$\$BODAO2         CDB2           OUT         IS.LOAD         DEG3         RNOBMP         IS.LOAD         DBJ2           OUT         \$\$BINDEX         FAD2         RNOBMP         IS.LOAD         DDF5           OUT         \$\$BINDEX         FBC2         RSETFP         IS.LOAD         DGJ5           OUT         \$\$BINDEX         FBG5         RSETFF         IS.LOAD         DGJ5           OUT         \$\$BINDEX         FBG5         RSETFF         IS.LOAD         DAF2           OUT         \$\$BOISO8         MEE1         RTNF3         \$	OFLOROUT	IS.RETRVE				
OFLOSAVE         \$\$801506         LLJ4         RELOCATE         \$\$801509         MFC1           OPN2         \$\$80DAI1         BKB1         RELOCATE         \$\$80IS00         MJB1           ORCCW         IS.LOAD         DEA3         RETRVE         \$\$80IS07         MBF3           OTHERR         IS.LOAD         DJG4         RETRVU         \$\$8CISOA         NAG5           DUT         IS.LOAD         DAD2         RETURN         IS.LOAD         DHG5           DUT         IS.LOAD         DBK3         RETURN         IS.LOAD         DJA3           DUT         IS.LOAD         DCJ5         REVTOC         \$\$8DDA02         CDB2           DUT         IS.LOAD         DEG3         RNOBMP         IS.LOAD         DBJ2           DUT         \$\$BINDEX         FAD2         RNODMP         IS.LOAD         DGJ5           OUT         \$\$BINDEX         FBG2         RSETFP         IS.LOAD         DGJ5           OUT         \$\$BINDEX         FBG5         RSETTF         IS.LOAD         DAF2           OUT         \$\$BOISO8         MEE1         RTNF3         \$\$BODACL         CNJ1           OUTRTN         \$\$BOISO1         LBJ2         RTV1         \$\$BORTV		IS.ADDRTR				
OPN2         \$\$B0DAII         BKB1         RELOCATE         \$\$B0IS10         MJB1           ORCCW         IS.LOAD         DEA3         RETRVE         \$\$B0IS07         MBF3           OTHERR         IS.LOAD         DJG4         RETRVU         \$\$BCISOA         NAG5           OUT         IS.LOAD         DAD2         RETJRN         IS.LOAD         DHG5           OUT         IS.LOAD         DCJ5         REVTOC         \$\$BODAO2         CDB2           OUT         IS.LOAD         DEG3         RNOBMP         IS.LOAD         DBJ2           OUT         \$\$BINDEX         FAD2         RNODMP         IS.LOAD         DDF5           OUT         \$\$BINDEX         FBC2         RSETFP         IS.LOAD         DGF5           OUT         \$\$BINDEX         FBC2         RSETFF         IS.LOAD         DGF5           OUT         \$\$BINDEX         FBC5         RSETFF         IS.LOAD         DAF2           OUT         \$\$BOISO8         MEE1         RTNF3         \$\$BODACL         CNJ1           OUTRTN         \$\$BOISO1         LBB3         RTNMON         \$\$BODACL         CNJ1           OUTRTN         \$\$BOISO7         MBB4         RZERO         IS.RETRVE	OFLOSAVE	\$\$B0IS06		RELOCATE	\$\$B0IS09	
ORCCW         IS.LOAD         DEA3         RETRVE         \$\$BOISO7         MBF3           OTHERR         IS.LOAD         DJG4         RETRVU         \$\$BCISOA         NAG5           DUT         IS.LOAD         DAD2         RETJRN         IS.LOAD         DHG5           DUT         IS.LOAD         DCJ5         REVTOC         \$\$BODAO2         CDB2           DUT         IS.LOAD         DEG3         RNOBMP         IS.LOAD         DBJ2           DUT         \$\$BINDEX         FAD2         RNODMP         IS.LOAD         DDF5           OUT         \$\$BINDEX         FBC2         RSETFP         IS.LOAD         DGJ5           OUT         \$\$BINDEX         FBG5         RSETTF         IS.LOAD         DAF2           OUT         \$\$BOISO8         MEE1         RTNF3         \$\$BODAI1         BKG5           OUTRTN         \$\$BOISO1         LBB3         RTNMON         \$\$BODACL         CNJ1           OUTRTN         \$\$BOISO1         LBJ2         RTV1         \$\$BORTV2         NGB5           OUTRTN         \$\$BOISO7         MBB4         RZERO         IS.RETRVE         GHG1	OPN2	\$\$BODAI1	BKB1	RELOCATE		
OTHERR         IS.LOAD         DJG4         RETRVU         \$\$BCISOA         NAG5           DUT         IS.LOAD         DAD2         RETJRN         IS.LOAD         DHG5           DUT         IS.LOAD         DBK3         RETJRN         IS.LOAD         DJA3           DUT         IS.LOAD         DCJ5         REVTOC         \$\$BODAO2         CDB2           DUT         IS.LOAD         DEG3         RNOBMP         IS.LOAD         DBJ2           DUT         \$\$BINDEX         FAD2         RNODMP         IS.LOAD         DDF5           OUT         \$\$BINDEX         FBC2         RSETFP         IS.LOAD         DGJ5           OUT         \$\$BINDEX         FBG5         RSETTF         IS.LOAD         DAF2           OUT         \$\$BOISO8         MEE1         RTNF3         \$\$BODAI1         BKG5           DUTRTN         \$\$BOISO1         LBB3         RTNMON         \$\$BODACL         CNJ1           OUTRTN         \$\$BOISO1         LBJ2         RTV1         \$\$BORTV2         NGB5           DUTRTN         \$\$BOISO7         MBB4         RZERO         IS.RETRVE         GHG1						
DUT         IS.LOAD         DAD2         RETJRN         IS.LOAD         DHG5           DUT         IS.LOAD         DBK3         RETJRN         IS.LOAD         DJA3           DUT         IS.LOAD         DCJ5         REVTOC         \$\$BODAO2         CDB2           DUT         IS.LOAD         DEG3         RNOBMP         IS.LOAD         DBJ2           DUT         \$\$BINDEX         FAD2         RNODMP         IS.LOAD         DDF5           OUT         \$\$BINDEX         FBC2         RSETFP         IS.LOAD         DGJ5           OUT         \$\$BINDEX         FBG5         RSETTF         IS.LOAD         DAF2           OUT         \$\$BOISO8         MEE1         RTNF3         \$\$BODAI1         BKG5           DUTRTN         \$\$BOISO1         LBB3         RTNMON         \$\$BODACL         CNJ1           OUTRTN         \$\$BOISO1         LBJ2         RTV1         \$\$BORTV2         NGB5           DUTRTN         \$\$BOISO7         MBB4         RZERO         IS.RETRVE         GHG1	OTHERR					
OUT         IS.LOAD         DCJ5         REVTOC         \$\$BODAO2         CDB2           OUT         IS.LOAD         DEG3         RNOBMP         IS.LOAD         DBJ2           OUT         \$\$BINDEX         FAD2         RNODMP         IS.LOAD         DDF5           OUT         \$\$BINDEX         FBC2         RSETFP         IS.LOAD         DGJ5           OUT         \$\$BINDEX         FBG5         RSETTF         IS.LOAD         DAF2           OUT         \$\$BOISO8         MEE1         RTNF3         \$\$BODAI1         BKG5           OUTRTN         \$\$BOISO1         LBB3         RTNMON         \$\$BODACL         CNJ1           OUTRTN         \$\$BOISO1         LBJ2         RTV1         \$\$BORTV2         NGB5           OUTRTN         \$\$BOISO7         MBB4         RZERO         IS.RETRVE         GHG1	DUT			RETURN	IS.LUAD	DHG 5
OUT         IS.LOAD         DCJ5         REVTOC         \$\$BODAO2         CDB2           OUT         IS.LOAD         DEG3         RNOBMP         IS.LOAD         DBJ2           OUT         \$\$BINDEX         FAD2         RNODMP         IS.LOAD         DDF5           OUT         \$\$BINDEX         FBC2         RSETFP         IS.LOAD         DGJ5           OUT         \$\$BINDEX         FBG5         RSETTF         IS.LOAD         DAF2           OUT         \$\$BOISO8         MEE1         RTNF3         \$\$BODAI1         BKG5           OUTRTN         \$\$BOISO1         LBB3         RTNMON         \$\$BODACL         CNJ1           OUTRTN         \$\$BOISO1         LBJ2         RTV1         \$\$BORTV2         NGB5           OUTRTN         \$\$BOISO7         MBB4         RZERO         IS.RETRVE         GHG1			DBK3	RETURN		DJA3
OUT         IS.LOAD         DEG3         RNOBMP         IS.LOAD         DBJ2           OUT         \$\$BINDEX         FAD2         RNODMP         IS.LOAD         DDF5           OUT         \$\$BINDEX         FBC2         RSETFP         IS.LOAD         DGJ5           OUT         \$\$BINDEX         FBG5         RSETTF         IS.LOAD         DAF2           OUT         \$\$BOISO8         MEE1         RTNF3         \$\$BODAI1         BKG5           OUTRTN         \$\$BOISO1         LBB3         RTNMON         \$\$BODACL         CNJ1           OUTRTN         \$\$BOISO1         LBJ2         RTV1         \$\$BORTV2         NGB5           OUTRTN         \$\$BOISO7         MBB4         RZERO         IS.RETRVE         GHG1		IS.LOAD	DCJ5	REVTOC		
OUT         \$\$BINDEX         FBC2         RSETFP         IS.LOAD         DGJ5           OUT         \$\$BINDEX         FBG5         RSETTF         IS.LOAD         DAF2           OUT         \$\$BOISO8         MEE1         RTNF3         \$\$BODAI1         BKG5           OUTRTN         \$\$BOISO1         LBB3         RTNMON         \$\$BODACL         CNJ1           OUTRTN         \$\$BOISO1         LBJ2         RTV1         \$\$BORTV2         NGB5           OUTRTN         \$\$BOISO7         MBB4         RZERO         IS.RETRVE         GHG1						
OUT         \$\$BINDEX         FBG5         RSETTF         IS.LOAD         DAF2           OUT         \$\$BOISO8         MEE1         RTNF3         \$\$BODAI1         BKG5           OUTRTN         \$\$BOISO1         LBB3         RTNMON         \$\$BODACL         CNJ1           OUTRTN         \$\$BOISO1         LBJ2         RTV1         \$\$BORTV2         NGB5           OUTRTN         \$\$BOISO7         MBB4         RZERO         IS.RETRVE         GHG1		\$\$BINDEX		RNODMP	IS.LOAD	DDF5
OUT       \$\$BOIS08       MEE1       RTNF3       \$\$BODAI1       BKG5         OUTRTN       \$\$BOIS01       LBB3       RTNMON       \$\$BODACL       CNJ1         OUTRTN       \$\$BOIS01       LBJ2       RTV1       \$\$BORTV2       NGB5         OUTRTN       \$\$BOIS07       MBB4       RZERO       IS.RETRVE       GHG1						
DUTRTN         \$\$BOIS01         LBB3         RTNMON         \$\$BODACL         CNJ1           DUTRTN         \$\$BOIS01         LBJ2         RTV1         \$\$BORTV2         NGB5           DUTRTN         \$\$BOIS07         MBB4         RZERO         IS.RETRVE         GHG1						
OUTRTN         \$\$BOIS01         LBJ2         RTV1         \$\$BORTV2         NGB5           OUTRTN         \$\$BOIS07         MBB4         RZERO         IS.RETRVE         GHG1				RTNF3		
DUTRTN \$\$BOISO7 MBB4 RZERO IS.RETRVE GHG1						
OUTRTN \$\$BCISOA NBB1 RZEROR IS.RETRVE GMF4						
	OUTRTN	\$\$BCISOA	NBB1	RZEROR	IS.RETRVE	GMF4

Label	Phase	Location	Label	Phase	Location
RZEROR	IS.ADDRTR	JMJ3	SKIP24	\$\$B0IS03	LFF3
R21R	IS.RETR VE	GGH1	SKIP24	\$\$B0IS05	LJD2
R21R	IS.ADDRTR	JHF1	SK IP 24	\$\$BUIS06	LLB3
			SKIP24	\$\$BCISOA	NBF 2
SAVEFX	\$\$BODAG1	BNF1	SKIP24	\$\$BORTV2	NFE1
SAVFRX	\$\$BODA02	CAF1	SKIP24A	\$\$BURTV2	NFB3
SCAN	\$\$B0IS09	MFG3	SKIP24B	\$\$BORTV2	NFJ1 LLC3
SCROPN	\$\$BODAO2 \$\$BODAO2	CCB2 CCH2	SKIP28 SKIP28	\$\$BOIS06 \$\$BORTV2	NFB4
SCRTCH SEQERR	\$\$BODA02	CHE4	SKIP20	\$\$B0IS06	LLG3
SEROK	\$\$BODA03	CEE1	SKIP32	\$\$B0IS07	MCA3
SETADR	IS.LOAD	DHB3	SKIP32	\$\$BORTV2	NFE4
SETCC	IS.LOAD	DGH5	SKIP32A	\$\$B0IS07	MCE3
SETHEAD	\$\$BODA04	CGB4	SKIP32B	\$\$B0IS07	MCB5
SETLOG	\$\$B0IS10	MJG1	SK IP36	\$\$B0IS05	LJF2
SETMAX	\$\$BINDEX	FAD4	SKIP36	\$\$BOISO6	LLH3
SETMSG	\$\$BOISO5	LKE2	SKIP36	\$\$BORTV2	NFH4
SETNEW	\$\$BOISO9	MGB2	SKIP40	\$\$B0IS05	LHB4
SETPNT	IS.LOAD	DAA5	SKIP40	\$\$BORTV2	NGH1
SHARED SHRDTRK	IS•LOAD IS•RETRVE	DEJ2 GKB1	SKIP42 SKIP42	\$\$B0IS06 \$\$B0RTV2	LMB4 NGB3
SHRDTRK	IS.RETRVE	GPB2	SKIP44	\$\$BOIS05	LHF3
SHRDTRK	IS.ADDRTR	JMB2	SKIP46	\$\$B0IS06	LMC 4
SKIP	\$\$BINDEX	FAH4	SKIP52	\$\$B0IS05	LHH3
SK IPO4	\$\$B0IS02	LCC1	SKIP54	\$\$B0IS05	LHC 2
SK IPO4	\$\$B0IS03	LEC1	SKIP54	\$\$B0IS06	LLB5
SK IPO4	\$\$BOIS06	LLC1	SK IP6C	<b>\$\$BOISO5</b>	LKF1
SKIPO4	<b>\$\$BOIS07</b>	MAC1	SKIP60	\$\$B0IS05	LHC 4
SK IPO4	\$\$BCISOA	NAA2	SKIP60	\$\$B0IS06	LLE5
SK IPO4	\$\$BORTV1	NCC1	SKIP68	\$\$B0IS05	LHE5
SKIPO5	\$\$BOIS01	LAJ1	SKIP68	\$\$B0IS06	LLG5
SK IPO5A	\$\$BOIS01 \$\$BOIS01	LAF2	SKIP72 SKIP72	\$\$B0IS05 \$\$B0IS06	LKJ2 LME5
SKIPO5B SKIPO6	\$\$B01301 \$\$B01S02	LAE2 LCF2	SKIP76	\$\$B01306 \$\$B01S05	LKB2
SK IPO6	\$\$B01302	LLF1	SK IP 78	\$\$B0IS06	LNG1
SK IP 08	\$\$B0IS01	LAK1	SKIP80	\$\$B0IS05	LKH3
SK IP 08	\$\$B0IS02	LCK1	SK IP82	\$\$B0IS04	LGG3
SKIPO8	<b>\$\$</b> BOIS05	LHC1	SKIP82	\$\$B0IS06	LNA3
SK IPO8	<b>\$\$</b> BOI <b>SO</b> 6	LLH1	SK IP8 <b>4</b>	<b>\$\$BOISO6</b>	LNJ3
SKIPO8	\$\$B0IS07	MAH1	SK IP86	\$\$B0IS06	LNH3
SK IP 08	\$\$BCISOA	NBB2	SKIP88	\$\$B0IS04	LGE4
SKIPO8	\$\$BORTV1	NCA2	SKIP90	\$\$B0IS04	LGH4
SK IP 08A SK IP 10	\$\$BCISOA \$\$BCISOA	NAG3 NAE5	SMTRK START	IS.LOAD IS.LOAD	DEE4 DGB1
SK IP12	\$\$BOIS01	LAD3	START	IS.LOAD	DHB1
SK IP12	\$\$B 01 S02	LDD2	START	IS.RETRVE	GGB1
SK IP12	\$\$B 0I S03	LEG1	START	IS.RETRVE	GMB1
SK IP12	\$\$BOI S07	MAB3	START	IS.ADDRTR	JHB1
SK IP 12	\$\$B OR T V 1	NCE3	START	IS.ADDRTR	JLB1
SK IP13	\$\$BOI SO7	MAE2	STDIF	\$\$BODAIN	BGC 1
SK IP14	\$\$BOI S03	LEH3	STOADR	IS.LOAD	DHG3
SK IP14	\$\$BORTV1	NDB1	STORCT	\$\$BODAO3	CEJ1
SK IP 16	\$\$BOISO1	LAE3	STORE	\$\$BODAIN	BJF1
SK IP16	\$\$BOI \$02	LDC4	STORES Stores	IS•RETRVE IS•RETRVE	GLF1 GQF1
SK IP16 SK IP16	\$\$BOI \$05 \$\$BOI \$07	LJC1 MAG3	STORES1	IS.RETRVE	GLG1
SK IP 16	\$\$B OR TV1	NDE1	STORES1	IS.RETRVE	GQG1
SK IP 18	\$\$BOI SO3	LEE4	STORES1	IS.ADDRTR	JPF1
SK IP20	\$\$BOIS01	LBB2	STORKL	IS.LOAD	DGD1
SK IP20	\$\$B0IS02	LDD4	STORSU	\$\$BODAI1	BKB2
SK IP 20	\$\$B0IS06	LMB2	STOWN	\$\$BODAU1	CKD3
SK IP 20	\$\$BORTV1	NDB3	STPIOA	IS-LOAD	DHA 5
SK IP 22	\$\$BOI SO6	LMG2	STREXT	\$\$BODA03	CEG1
SK IP 22A	\$\$BOI SO6	LMH2	SUBTCT	IS.LOAD	DGC 3
SK IP 22B	\$\$BOI SO6	LMJ2	SVCALL SVF1AD	\$\$BODACL \$\$BUDAO2	CNJ2 CBB1
SK IP 24	\$\$BOI SO2	LDF4	SALIAD	<b>₽</b> ₽ D U D A U ∠	CDDI

Label	Phase	Location	Label	Phase	Location
TBLBLD	\$\$B0IS05	LKB3	UL AB	\$\$BODAU1	CKA3
TEST	\$\$DINDEX	FBA1	UNBLFL	IS.LOAD	DAJ1
TESTC2	\$\$B0DA02	СВНЗ	UNBLREAD	IS.RETRVE	GJG5
TESTEDF	\$\$BODACL	CNH1	UNBLREAD	<pre>IS.RETRVE</pre>	GPD1
TESTIND	\$\$BDI \$09	MFF1	UNBLREAD	IS.ADDRTR	JME1
TESTLOAD	\$\$BOI S09	MFC3	UPDATE	\$\$B0IS05	LKG1
TESTMOD	\$\$B0IS08	MDK1	UPDATE	\$\$BORTV1	NEE4
TESTNORM	IS.RETRVE	GGG4	UPDINIT	\$\$B OD A O 4	CGD3
TESTNORM	IS.RETRVE	GMG2	UP DT AD	IS.LOAD	DBB5
TESTNORM	IS.ADDRTR	JHG4	UPPERLT	\$\$BODAIN	B <b>J</b> B <b>1</b>
TESTPDUL	IS.LOAD	DAB2	UPPERLT	\$\$BODAI1	BLB2
TESTPS	\$\$BODAI1	BME1	UPVSEQ	\$\$BODA04	CHE3
TESTPS	\$\$BODAO4	CHH5			
TESTSHRD	\$\$BOIS10	MK A3	VALIDATE	IS.LOAD	DAC1
TESTXT	\$\$B0DA02	CBC1	VALIDATE	IS.LOAD	DEC1
TICRTS	IS.RETR VE	GLD2	VAL IDATE	IS.RETRVE	GGD1
TICRTS	IS.ADDRTR	JKD2	VALIDKEY	\$\$B0IS10	MKG3
TICPTS	IS.ADDRTR	JPD2	VOLCHK	\$\$BORTV1	NDB2
TINLOP	IS.LOAD	DAH5	VOL GET	\$\$B0IS02	LCE3
TIWR	IS.LOAD	DBB2	<b>VOLGET</b>	\$\$BORTV1	NCB5
TKHLDCHK	\$\$B0IS07	MCG2	VOL GET CK	\$\$BOI SO2	LCB5
TOMANY	\$\$B0DA03	CEA2	VOLSETUP	\$\$BORTV1	NCG3
TSTDASD	IS.RETR VE	GLG5			
TSTDASD	IS.RETR VE	GRH2	WRITE	\$\$BODAU3	CFC1
TSTDVCTP	\$\$BODAIN	BH A1	WR ITE	\$\$BODA04	CJB4
TSTNRF	IS.RETR VE	GLJ5	WRITE	\$\$BODAU1	CLG1
TSTNRF	IS.RETRVE	GRK2	WRITE	\$\$B0I \$06	LPB2
TSTNRF	IS.ADDRTR	JKH5	WRITE	\$\$BOI S09	MGC1
TSTNRF	IS.ADDRTR	JQJ2	WR ITELBL	\$\$BODACL	CPB3
TSTOUT	\$\$BODAIN	BJB4	WKLABL	\$\$BUDAU4	CHB3
TSTRDLB	\$\$BODACL	CPB1	WRLAST	\$\$BODAU1	CKJ3
TSTRPY	\$\$BODA02	CCB5	WRLUOP	IS.LOAD	DHE 1
TSTVAR	\$\$B OD A O 2	CBE5	WRTMI	IS.LOAD	DAJ4
TSTWLR	IS.RETRVE	GRJ2	WRTII	IS.LUAD	DAA4
TSTWLR	IS.ADDRTR	JKG5			
TSTWLR	IS.ADDRTR	JQH2	XTCHEK	\$\$BUIS01	LAB3
			XTNPUT	\$\$B0IS06	LLA3

For explanations and actions to be taken for the various messages, refer to  $\underline{\text{DOS/VS}}$  Messages, GC33-5372.

 $\underline{\text{Note}}$ : The second digit of the message number indicates the type of DASD file issuing the message. The file types are:

- 2 = ISAM file
- 6 = Direct access Input
- 7 = Direct access Output

Message   Number	Issuing Phase	Chart Identification	Message
4200I	\$\$BOIS06 \$\$BOIS06	LM LN	NC LABEL SPACE IN VTCC  Or  NO RECORD FOUND
4 <b>7</b> 00I	\$\$BODA03	CF	NO RECORD TOURS
4201I     	\$\$BOIS06 \$\$BOIS06 \$\$BOIS07 \$\$BOIS0A \$\$BORTV1	LL LN MA NA ND	NO FORMAT 1 LABEL FCUND or NO RECORD FOUND
4601I   	\$\$BODAI1 \$\$BODAU1	BM CK	
4 <b>7</b> 01I	\$\$BODAO2 \$\$BODAU1	CD CK	
4202I   	\$\$BOIS07 \$\$BCIS0A \$\$BORTV2	MA NA NG	NC RECORD FOUND
4603I	\$\$BODAI1 \$\$BODAU1	BM CL	NO FORMAT 3 LABEL FCUND
4 <b>7</b> 03I	\$\$BODAU1	CL	
420 <b>4</b> I     	\$\$BOIS02 \$\$BOIS03 \$\$BOIS06 \$\$BORTV1	LD LE LN ND	NO FORMAT 4 LABEL FOUND or NO RECORD FOUND
4 <b>7</b> 041   	\$\$BODAO1 \$\$BODAO2 \$\$BODAO3	BN CD CE	

Figure 108. Message cross-reference list (1 of 3).

Message Number	Issuing Phase	Chart Identification	Message
4206I   	\$\$BOIS02 \$\$BOIS03 \$\$BOIS06 \$\$BOIS07 \$\$BCIS0A \$\$BORTV1	LC LE LN MA NA NC	NO STANDARD VOL1 LABEL FOUND  OR  NO RECORD FOUND
4606I	\$\$BODAI1 \$\$BODAU1	BM CK	
4 <b>7</b> 06I	\$\$BODAO1 \$\$BODAO2 \$\$BODAO3 \$\$BODAU1	B.N CD CE CK	
4608D	\$\$BODACL \$\$BOIS03	CN LE	NO RECORD FOUND
4 <b>7</b> 091	\$\$BODA02 \$\$BODA03 \$\$BODA04	CD CF CJ	NO RECORD FOUND
4638D	\$\$BODAU1	CK	USER HDR LBL IS NOT STD.
4639D	\$\$BODACL	CN	USER TRL IBL IS NCT STD.
4240I	\$\$BOIS01	LA	EXTENT OVERLAP ON ANOTHER
4740A	\$\$BODA01	BQ	
4241I	\$\$BOIS02	LD	EXTENT OVERLAP ON VTCC
4741A	\$\$BODA01	ВР	
42431	\$\$BORTV1	NE	INV EXTENT HI/LO LIMITS
4744A	\$\$BODA02	cc	OVERLAP ON UNEXPRD FILE
4245I	\$\$BOIS06	LM	TCO MANY EXTENTS
47451	\$\$BODA03	CE	
42461	\$\$BOIS07	MA	DISCONT INDEX EXTENTS
42491	\$\$BOIS05	LK	DATA TRACK LIMIT INVALID
4651I	\$\$BODAI1	BL	SYSUNITS NOT IN SEQUENCE
4751I	\$\$BODA04	СН	
42521	\$\$B0IS05	LH	DISCONT TYPE 1 EXTENTS

Figure 108. Message cross-reference list (2 of 3).

Message   Number	Issuing Phase	Chart Identification	Message
4254I	\$\$BOIS05 \$\$BORTV2	LK NF	DSK XTN ENTRY TABLE FULL
4255A	\$\$B0IS02 \$\$BORTV1	LC ND	WRONG PACK, MOUNT nnnnnn
4655A	\$\$BODAI1	ВК	
4755A	\$\$BODAO1	BN	
4 <b>7</b> 60I	\$\$BODAIN \$\$BODAO2 \$\$BODAO3	BJ CD CR	NO EXTENTS, ALL BYPASSED
4261I	\$\$BOIS01	LA	INVALID DLBL FUNCTION
42621	\$\$BOIS05	LK	NO PRIME DATA EXTENT
4263I	\$\$BOIS07	PB	LOAD FILE NOT CLOSED
42661	\$\$B0IS05	LJ	1 TRACK USER LABEL EXTENT
4766A	\$\$BODA01	ВÇ	
42691	\$\$BOIS07	MC	FILE IS OPEN FOR ADD
42 <b>7</b> 0I	\$\$BORTV2	NF	1ST XINT CD NOT INDX VOI
42 <b>7</b> 1I	\$\$BOIS01	LA	EXTENT INFO NEEDED
42 <b>7</b> 2I	\$\$BOIS08	ME	MOD AND DTF INCOMPATIBLE
42831	\$\$BOIS02 \$\$BORTV1	LC NC	INVALID LCGICAL UNIT
46831	\$\$BODAIN	BG	
4797I	\$\$BODA02	сс	OVLAP EXPIRED SECRD FILE
47981	\$\$BODA02	cc	OVLAP UNEXPRD SECRD FILE
4299D	\$\$BOIS06 \$\$BORTV1	LL ND	DATA SECURED FILE ACCESSED

Figure 108. Message cross-reference list (3 of 3).

channel program builder end-of-file add 115 normal add to prime data area 114 WAITF macro. 144 WAITF macro detail chart 237 WRITE NEWREY macro detail chart 237 adding records or afile 71 ADDRAF function to a file 71 ADDRAF function funct	ADD function (ISAM) add to overflow area 115	RANDOM RETRVE 133 SEONTL RETRVE 141
end-of-file add 115 normal add to prime data area 114 WAITF macro 114 WAITF macro 114 WAITF macro 115 WRITE NEWERY macro 115 WRITE NEWERY macro 115 DORTR function (ISAN) Channel program builder 155 energia add 153 SEELL macro 147 SET macro 147 SET macro 147 SET macro 148 SETL macro 15 WAITF macro 6detail chart 283 READ KEY macro 148 SETL macro 15 WAITF macro 6detail chart 284 SETL macro 15 SASSETL, detail chart 285 \$SESTL, detail chart 298 MAITF macro 15 WEKEY 153 NEWEY 153 NEWEY 153 NEWEY 153 SEEL 149 alteration factors 55 areas, work 76 asynchronous processing 102 relative addressing extensions 55 B-transients (see logical transients) buffering, double 103  B-transients (see logical transients) buffering, double 103  CCW chains 111, 130 CC		
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MAITF macro, detail chart 237 WRITE NEWREY macro, detail chart 234 add to the overflow area 115, 153 adding records to a file 71 ADDRET function (ISAM) Channel program builder 155 ESEIL macro 146 ESEIL macro, detail chart 278 GET macro 147 GET macro, detail chart 279 overflow area add 152 PUT macro 148 READ KEY macro, detail chart 283 READ KEY macro, detail chart 284 READ KEY macro, detail chart 284 SETL macro phase 1, \$\$BSEIL 188 READ KEY macro, detail chart 284 SETL macro phase 2, \$\$BSEIL, detail chart 285 \$\$SEIL, detail chart 283 WAITF macro, detail chart 283 WAITF macro, detail chart 283 WAITF macro, detail chart 283 WAITF macro, detail chart 283 WEY, detail chart 283 WEY, detail chart 293 WRITE macro SEY, detail chart 298 NEWKEY, 154 NEWKEY, 154 NEWKEY, 154 Seysberl, detail chart 298 alteration factors 55 areas, work 76 areas, wo	normal add to prime data area 114	
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