

9020 CE Handbook



9020 CE Handbook

Note

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Printing History

New editions of this manual will incorporate all material updated since the previous edition. Update packages may be issued between editions and contain replacement and additional pages to be merged into the manual by the user. Each updated page will be indicated by a revision date at the bottom of the page. A vertical bar in the margin indicates the changes on each page. Note that pages which are rearranged due to changes on a previous page are not considered revised.

The manual printing date and part number indicate its current edition. The printing date changes when a new edition is printed. (Minor corrections and updates which are incorporated at reprint do not cause the date to change.) The manual part number changes when extensive technical changes are incorporated.

August 1984...Edition 1. Replaced the 9020 CD Handbook, 09020-90039, and all updates. January 1984...Edition 1 with updates. April 1985...Edition 1 with update merged. July 1985...Update.

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WARRANTY

A copy of the specific warranty terms applicable to your Hewlett-Packard product and replacement parts can be obtained from your local Sales and Service Office.

9020 Product Information

Chapter 1

Product Information

Features

- 32-bit CPU and full 32-bit internal and external data paths.
- Add-on performance with multiple CPUs.
- Up to 10 Mbytes RAM.
- 36 Mbyte/second memory processor bus.
- Four internal HP-CIO slots expandable to 20.
- Virtual memory with 500 Mbyte address space.
- Single-user or multi-user system.
- HP-UX Operating System with FORTRAN 77, Pascal, or C languages; or BASIC Language System.
- Error correcting and self-healing memory.
- High-performance interactive graphics.
- Broad range of peripherals.

Central Processor Unit

- 32-bit single chip composed of 450,000 transistors.
- Direct address range of 500 Mbytes.
- Supports IEEE Floating Point Format.
- Instruction set of 230 operation codes.
- \bullet 18 MHz clock rate with micro-instruction cycle time of 55 ns and memory cycle time of 110 ns.
- Typical execution times:

10.34 microseconds
5.94 microseconds

(CPU with math chips)

Floating point math chips decrease program time by performing math functions in hardware chips. Improvement: 1.4 times faster (overall). Twice as fast on B1D program.

Memory

- 256 Kbyte RAM finstrates, 512K byte polystrates, or 1M byte polystrates.
- RAM memory expandable to 2.5 Mbytes.
- Single-bit error detection and correction.
- Double-bit error detection.

I/O Processor

- Supports 8 I/O channels with DMA capability on every channel.
- Two additional IOPs and their associated 97098A I/O Expanders are supported.
- Nominal IOP bandwidth of 900 Kbytes/second.
- Maximum IOP bandwidth of 5.1 Mbytes/second.

Real Time Clock

- Provides date and time of day.
- \bullet Accuracy to within 2 minutes/month within 0°C to 45°C.
- Battery-maintained up to 30 days nominal and 10 days worst case.

System Components

Component	9020A	9020B	9020C	9020AS	9020AT
CRT	Standard Color	Monochromatic	High Performance Color	Standard Color	Standard Color
Keyboard			ASCII S	Standard	
RAM (std.)		512K byte		1.0 Mbyte	1.5 Mbyte
RAM (opt.)		Up to 10 Megabytes (256K, 512K, and 1M Boards)			
Mass Storage	5¼" Flexible Disc		10M byte Fixed Disc 5¼" Flexible Disc	5¼" Flexible Disc (CS 80 Disc/Tape is Re- quired)	
Thermal Printer	Optional Stand			dard	
CPU	Single is standard, up to 2 additional are allowed				
IOP	Single is standard, up to 2 additional I/O Expanders are allowed				
System Software	O (sir H	ptional-BASIC or HI igle or multi-user HI IP BASIC is single-u	P-UX P-UX) ser)	HP BASIC and 2D-3D Graphics-Standard (HP BASIC is single-user)	HP-UX (single-user), FORTRAN 77, Pascal, Graphics, DGL, Graphics AGPstandard
HP-IB	Optional		Standard		

CRT Display Specifications

	Standard Color	High-Performance Monochromatic	High-Performance Color
Screen size	12.2 in.(310mm)	12.2 in. (310mm)	13 in. (330mm)
Screen brightness	50 Hz = 27 ftLamberts 60 Hz = 31 ftLamberts	To 30 ftLamberts	To 40 ftLamberts
X-Ray emission	<0.5 mR/hr.	<0.5mR/hr.	<0.5mR/hr.
Refresh rate	50 or 60 Hz	60 Hz	60 Hz
Maximum altitude	15,000 ft.	15,000 ft.	15,000 ft.
Screen capacity	26 lines x 80 characters	26 lines x 80 characters	26 lines x 80 characters
Dot spacing	.017 in. (.428mm)	.013 in. (.328mm)	.013 in. (.343mm)
Character matrix	7 x 9 characters in a 9 x 12 cell	7 x 9 character font in a 9 x 12 cell	7 x 9 character font in a 9 x 12 cell
Graphics			
No. of colors	16 displayed from 4,096	Monochrome	8 pure, 4,913
Raster size	8.5 in. x 6.4 in. (216 x 162.5mm)	7.24 in. x 5.86 in. (184 x 149mm)	7.55 in. x 6.14 in. (192 x 156mm)
Array size	512 x 390 dots	560 x 455 dots	560 x 455 dots
Dot resolution	.017 in. (.42mm)	.013 in. (.33mm)	.013 in. (.34mm)
Linearity	<2.5% full screen	1.5% full screen	<2% full screen
Cursor			
Plotting mode	Full screen or small crosshair	Full screen, small crosshair or blinking underline	Full screen or small crosshair
Letter mode	None	Blinking underline	Blinking underline
Character editing	Overstrike	Overstrike	Overstrike
Light Pen			
Min. intensity for pick of single pixel	N/A	10 ftLamberts (white, blue, or green)	10 ftLamberts (white, blue, or green)

Internal Thermal Graphics Printer Specifications

The internal printer offers the following features:

- True overprinting.
- Printing enhancements such as inverse (white characters on black), underline, overline and 150% tall in any combination.
- Capability to dump graphics from CRT (pixel-by-pixel). BASIC only.
- Seven user-definable characters.
- Standard character sets are: US ASCII and Line Drawing, HP Roman Extension or Katakana.
- Programmable vertical pitch, lines per page and top/bottom margin.

Line width) columns
Print speed	o to 450 lines/minute
Character sets	oman Extension or Katakana
Graphics resolution 56	0 dots/line, 77 dots/inch (vertical and horizontal)
Plot speed:	
Nominal plot 0.4	49 inches/second (12.5 millimetres/second)
Plot all pixels on0.1	15 inches/second (3.8 millimetres/second)
Character matrix 5 >	x 7 dots (7 x 12 field)
Paper dimensions 8.2	27 inches x 197 feet (210 millimetres x 60 metres)
8.5	5 inches x 200 feet (216 millimetres x 61 metres)
Paper types Bla	ack or blue print, perforated, fan fold, 330 sheets per package.

Internal Flexible Disc Specifications

Capacity
Average media life More than 2.5 million revolutions (140 hours rotating), stops when not
accessed
Tracks per disc
Sectors per track 16
Bytes per sector
Average access time 300 milliseconds
Maximum access time 425 milliseconds (assumes no data errors)
Average throughput 16 kbytes/second (interleave factor of 1)
Average throughput 16 kbytes/second (interleave factor of 1)

Internal Fixed Disc Specifications

Capacity	9.896 Mbytes (formatted), less directory file allocations
Number of platters	2
Number of tracks	1224 (306 cylinders x 4 heads); 1208 user available
Sectors per track	32
Bytes per sector	256
Average access time	85 milliseconds
Maximum access time	205 milliseconds (assumes no errors detected)
Average throughput	115 kbytes/second (interleave factor of 4)

Keyboard Options

ASCII (standard), French, German, Spanish, Katakana, and Swedish/Finnish.

System Software

HP BASIC

HP Product No.	Software
97050A	BASIC Language System (single-user)
97052A	BASIC 2D/3D Graphics
97053A	IMAGE/QUERY-9000 DBMS
97056A	BASIC Asynchronous Terminal Emulator
97058A	Shared Resource Management
98354	HP-FEM II Finite Element Modeling (Eur. only)
98355	HP-DESIGN Software (Eur. only)

HP-UX

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HP Prod Single-user	uct No. Multi-user	Software
97070A	97080A	HP-UX Operating System
97071A	97081A	FORTRAN 77 Compiler
97072A	97082A	HP Pascal Compiler
97073A	97083A	IMAGE-9000 DBMS
97074A	97084A	HP-UX GRAPHICS DGL
97075A	97085A	HP-UX GRAPHICS AGP
9707	76A	Asynchronous Terminal Emulator
97077A	97087A	RJE Communications Software
98163A	98183A	HPSPICE Circuit Simulation
228	5A	Local Area Network
9708	36A	Applications Migration Package

Accessories Supplied

The following items are supplied with the 9020:	
Installation and Test Manual	HP Part Number 09020-90013 2 each, 256K byte
Special Function Key Overlays System Functional Test Manual	2 blank, HP Part Number 7120-3107 HP Part Number 09020-11031
If 9020A, C, R or T is ordered, add:	
Fuse	2110-0051 for 100 - 120 Vac 2110-0056 for 220 - 240 Vac
If optional thermal printer is ordered, add:	
HP Part Number	
Paper Tray	. 09855-67951
For Opt. 590:	
Thermal Paper (8 ¹ / ₂ inch wide, black-on-white, 1 packag	e 9270 0640
For Opt 591:	. 9270-0040
Thermal Paper (210 millimetres wide, black-on-white, package of 330 sheets).	1 . 9270-0642

Accessories Available

Thermal Printer Paper	
(4 packs/box, 330 sheets/pack)	
$8\frac{1}{2}$ inch wide, black on white	9270-0640
$8\frac{1}{2}$ inch wide, blue on white	9270-0641
210 millimetres wide, black on white	9270-0642
210 millimetres wide, blue on white	9270-0643
5 ¹ / ₄ inch Flexible Discs (box of 10)	92190A
Flexible Disc Head Cleaner Kit.	92193A
Power Line Conditioner	35030A
Workstation Table	92213A

User Documentation

BASIC Manuals

HP Part No.	Description
97050-90000	BASIC Programming Techniques
97050-90005	BASIC Language Reference
97050-90015	BASIC Condensed Reference
97050-90045	BASIC Software Configuration
97050-90090	Where Do I Start With BASIC?
97052-90000	BASIC Graphics Programming Techniques
97050-80020	HP BASIC Manual Package (includes all above manuals)
97050-90102	BASIC Software Manual Catalog
97053-90000	IMAGE/Data Base Programming Techniques
97053-90001	QUERY User's Guide
97053-90002	Data Base Design Kit
97056-90000	HP BASIC Asynchronous Terminal Emulator User's Manual

HP-UX Manuals

HP Part No.	Description
09000-90007	HP-UX Reference
97073-90006	IMAGE HP-UX Reference Supplement
98680-90025	Introducing the UNIX System by McGilton & Morgan
97089-90004	HP-UX Concepts and Tutorials (4 Vols.)
97089-90048	HP-UX System Administrator's Manual
97080-90093	Unpacking Instructions for the HP 9000 Series 500 Computers
92836-90005	Structured FORTRAN 77 Programming by Pollack
97081-90001	FORTRAN/9000 Reference
92832-90002	Programming in Pascal by Grogono
97082-90001	Pascal/9000 Reference
97082-90002	Programming in Pascal with Pascal 9000
97084-90002	DGL/AGP Demonstration Instructions
97089-90000	The C Programming Language by Kernighan & Ritchie
97086-90001	Applications Migration Reference
97086-90002	Applications Migration Users Guide
97059-90000	HP-UX Local Area Network (LAN) User's Guide
97059-90001	HP-UX LAN Node Manager's Guide
97076-90001	HP-UX Asynchronous Communications User's Guide
97077-90011	RJE Synchronous Data Communications User's Guide
97084-90000	DGL Programmer Reference
97084-90001	DGL Supplement for the Series 500
97084-90026	Graphics/9000 Device Handlers Manual
97085-90000	AGP User's Guide
97085-90001	AGP Supplement for the Series 500
97085-90005	AGP Reference
98680-90021	Fortran Comparison Notes
98680-90045	HP-UX Portability Guide

09020-90035, rev:1/85

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HP Part No.	Description
09020-80038	Service Documentation Package (includes 09020-90013, 09020-90037, 09000-
	90040, Sales and Support Offices List (5955-6587), 98770A Color Graphics Display
	Service Manual (98770-90031), 98780A Monochromatic Display Service Manual
	(98780-90030), and 2-inch binder (9282-0989)).
09020-90013	Installation and Test
09020-90037	Service Manual
09020-90035	CE Handbook
09000-90040	Series 200/500 Site Preparation Manual
97060-90030	HP 97060A Graphics Processor Service
97062-90020	HP 97062A Color Output Interface Installation and Service
97098-90020	HP 97098A I/O Expander Installation and Service
27132-91001	HP 27132A HP-CIO Technical Reference Manual
09020-11031	System Functional Tests

Service Documentation

Tools List

HP Part No.	Description
8710-0899	#1 Pozidriv screwdriver
8710-0900	#2 Pozidriv screwdriver
8730-0001	Flat-blade screwdriver
8720-0015	5 16-inch wrench
8710-0881	1 8-inch Allen hex key
8710-1164	4-millimetre Allen hex kev
8720-0006	7 16-inch nutdriver
8710-0004	Longnose pliers
5040-7433	Keycap puller
09855-67004	Power supply discharge tool
9300-0794	Antistatic kit
09815-20602	Paper Spindle tool
09020-10010	Test Pack

Safety Considerations

WARNING

SWITCH POWER OFF AND UNPLUG POWER CORD FROM AC OUTLET BEFORE REMOVING ANY ASSEMBLY. LETHAL VOLTAGES ARE PRESENT INSIDE THE COMPUTER. OBSERVE ALL WARNING LABELS.

PRIMARY WIRING CHANGE WARNING

AFTER MAKING A PRIMARY WIRING CHANGE, PERFORM CON-TINUITY TEST BETWEEN POWER CORD GROUND AND METAL CHASSIS. RECORD RESULTS ON REPAIR ORDER.

POWER SUPPLY WARNING

WHEN POWER SUPPLY IS REMOVED FROM COMPUTER, YOU ARE EXPOSED TO LETHAL VOLTAGE FROM POWER SUPPLY CAPACITORS. WAIT AT LEAST 15 MINUTES AFTER POWER IS SWITCHED OFF BEFORE REMOVING SUPPLY, OR DISCHARGE SUPPLY WITH THE POWER SUPPLY DISCHARGE TOOL.

9020 Environmental/Installation/PM

Chapter 2

Environmental

Width	21.75 inches (55.2 centimetres)
Depth	29 inches (73.6 centimetres)
Height	24.5 inches (62.2 centimetres)
Net Weight:	
9020A	137 pounds (62.1 killograms)
9020B	121 pounds (55 killograms)
9020C	163 pounds (74 killograms)
Shipping Weight:	
9020A	168 pounds (76.2 killograms)
9020B	152 pounds (69 killograms)
9020C	194 pounds (88 killograms)
Temperature:	1) I pourido (eo funografilo)
Operating	10° to 40°C (with disc media)
Storage	-40° to 75°C (flexible disc media excluded)
Slew Rate (10-Mbute Winchester	10°C per hour
Humidity	20-80% RH non-condensing (maximum wet hulb
	25.5° machine operating
Altitude	15 000 feet (570 mbars barometric pressure)
	machine operating
Voltages	90-125 Vac or 189-250 Vac
Line Frequency Range	48-66 Hz
Current Requirements	12.0 A at 108 Vac
	8 0 A at 198 Vac
	15.0 A at 90 Vac (Japan)
Power Dissination	850 Wats (2900 BTU/br.)
Vibration (neak-to-neak amplitude deflection)	0.125 inches at 5 to 10 Hz
toration (peak-to-peak amplitude dellection)	0.120 inches at 0.1010 Hz
	0.000 menes at 10 to 25 Hz
	0.015 menes at 25 to 55 T IZ

Installation Procedure

- 1. Unpack the computer.
- 2. Position the computer. Leave about 6 inches of space at back of computer and 6 inches at top.
- 3. Install the display. Check display voltage selector switches and fuse.



Fuse for 9020A Display

Voltage Rating	Fuse Rating	Part Number
250V	5A NB	2110-0010

Fuse for 9020C Display

Voltage Rating	Fuse Rating	Part Number
100,120V	10A NB	2110-0051
220,240V	6A NB	2110-0056

4. Connect display power cord to power cord connector on back of display (9020A/C/R/T only), and connect mainframe and display power cords to power source.



NOTE: Plugs are viewed from connector end. Shape of molded plug may vary within country.

Power cords supplied by HP have polarities matched to the power-input socket on the computer: L = Line or Active Conductor (also called "live" or "hot")
 N = Neutral or Identified Conductor

- E = Earth or Safety Ground

Power Cords

- 5. Check switch settings of interface cards (Chapter 7).
- 6. Install interface cards and connect the cables.
- 7. Install and connect the peripheral devices.
- 8. Connect the HP 97098A I/O Expander(s) (If applicable).



9. Install paper in printer.

Printer Paper Part Numbers

Print Color	Paper Size	Part Number
Black	8.5 x 11 inch	9270-0640
Blue	8.5 x 11 inch	9270-0641
Black	210 x 290 mm	9270-0642
Blue	210 x 290 mm	9270-0643

Preventive Maintenance

There are no scheduled preventive maintenance procedures.

FINSTRATE INSTALLATION INSTRUCTIONS FOR HP-QUALIFIED PERSONNEL:

Start on page 2-6 (RAM/CPU), or 2-7 (IOP) and follow the instructions that apply to the installation you are performing. For example, if you are installing a 2nd IOP in a 520 computer you would start on page 2-7, and perform all steps that begin with: (ALL), (ALL 2nd IOP), (ALL EXCEPT 520-3rd IOP), (520, (520 ONLY), and (520-2nd IOP).

ALL RAM and CPU Instuctions start on 2-6. ALL IOP Instructions start on 2-7.

When completed with the installation of the finstrate, Insert the following pages in your CE Handbook (after page 2-4 of either the 9020 or 9030/9040 section).

RAM/CPU FINSTRATE INSTALLATION

1. (ALL)

TURN THE POWER OFF AND DISCONNECT THE POWER CORD.

(520) Open the left door. (530) Remove the front panel. (540) Remove the front bottom panel.

3. (530/540 ONLY)

From the front of the computer, remove the Radio Frequency Interference (RFI) shield by loosening the six thumbscrews.

4. (ALL)

Open processor stack door.

CAUTION

ELECTROSTATIC DISCHARGE DAMAGE CAN OCCUR IN THE FOLLOWING STEPS. FOLLOW THE PRECAUTIONS IN CHAPTER 4 OF THE SERVICE MANUAL.

DO NOT TOUCH EDGE CONNECTOR OR FINSTRATE PLANE. HOLD FINSTRATE BY EJECTORS OR SIDE EDGES ONLY. HAND-LING FINSTRATE INCORRECTLY COULD CAUSE ELECTROSTATIC DISCHARGE DAMAGE. WHEN INSTALLING FINSTRATE, HOLD BY EJECTORS AND MOVE AIR CONTROLLER OUT OF THE WAY WITH THE SIDE EDGE OF THE FINSTRATE.

5. (ALL)

Install the finstrate in the first unoccupied slot from the bottom. DO NOT LEAVE EMPTY SLOTS BETWEEN FINSTRATES.

6. (ALL)

Close the processor stack door. Firmly tighten thumbscrews to prevent RFI radiation. Replace the label that is used as a seal for the processor stack door (Part Number 5180-5201).

7. (530/540 ONLY) Replace RFI shield.

8. (520)

Close left door. (530) Replace front panel. (540) Replace the front better

Replace the front bottom panel.

9. (ALL)

Connect power cord to ac outlet.

IOP FINSTRATE INSTALLATION

WARNING

OBSERVE ALL WARNINGS AND SAFETY PROCEDURES IN THE COMPUTER SERVICE MANUAL. LETHAL VOLTAGES ARE PRE-SENT IN THE COMPUTER.

1. (ALL)

TURN THE POWER OFF AND DISCONNECT THE POWER CORD.

2. (520 - 2nd IOP)

Remove the left door.

(520 - 3rd IOP)

Open the left door. Remove the I/O expander cable from the I/O EXPANDER 1 slot on the processor stack door (if connected).

(530)

Remove front panel.

(540)

Remove both front panels and flip-top cover.

3. (520 - 2nd IOP)

Remove the trim piece on the left side of the computer by loosening the two #2 Pozidriv screws (Figure 1). The screws do not have to be completely removed to remove the trim piece.

(520 - 3rd IOP)

Continue with next step.

(530/540)

Remove top and bottom covers from System II enclosure. From the front of the computer remove the Radio Frequency Interference (RFI) shield by loosening 6 thumbscrews.



Figure 1. Model 520 Computer Stack.

4. (520 ONLY)

Remove the flat metal plate covering the appropriate I/O EXPANDER connector slot by removing the two #2 Pozidriv screws (Figure 2):

(2nd IOP) I/O EXPANDER 1.

(3rd IOP) I/O EXPANDER 2.

(530/540)

Continue with next step.



*****#2 POZIDRIV CAPTIVE SCREW

Figure 2. Series 500 Computer Stack Door Removal.

5. (ALL EXCEPT 520 - 3rd IOP)

Loosen the two #2 Pozidriv captive screws at the bottom of the processor stack door (Figure 2).

(520 - 3rd IOP) Continue with next step.

6. (ALL EXCEPT 520 - 3rd IOP) Remove the processor stack door by loosening the two captive thumbscrews (Figure 2). (520 - 3rd IOP)

Open processor stack door.

CAUTION

DO NOT TOUCH EDGE CONNECTOR OR FINSTRATE PLANE. HOLD FINSTRATE BY EJECTORS OR SIDE EDGES ONLY. HAND-LING FINSTRATE INCORRECTLY COULD CAUSE ELECTROSTA-TIC DISCHARGE DAMAGE. WHEN INSTALLING FINSTRATE, HOLD BY EJECTORS AND MOVE AIR CONTROLLER OUT OF THE WAY WITH THE SIDE EDGE OF THE FINSTRATE.

7. (ALL - 2nd IOP)

Move all finstrates above slot 2 up one slot. (ALL - 3rd IOP) Move all finstrates above slot 3 up one slot (Slo

Move all finstrates above slot 3 up one slot (Slot 3 is first accessible finstrate without removing door).

8. (520 - ONLY)

Remove cable clamp from processor stack door by removing two #1 Pozidriv screws.

9. (ALL)

Slide the IOP finstrate into the appropriate slot without seating finstrate into the motherboard. Open the connector gate (Figure 3):

(2nd IOP) Slot 3. (3rd IOP) Slot 4.



Figure 3. Series 500 Computer IOP Installation.

10. (520)

Place IOP cable door connector over studs on ends of appropriate I/O EXPANDER connector slot, and tighten connector to door with two nuts (Figure 3):

(2nd IOP) I/O EXPANDER 1 slot. (3rd IOP) I/O EXPANDER 2 slot.

(530/540)

Continue with step 12.

11. (520 ONLY)

Install cable clamp on door so that it holds cable(s) in position (Figure 4).



Figure 4. Series 500 Computer Stack Door.

12. (520)

Connect the IOP cable finstrate connector to the IOP finstrate with cable pointing down. Close the connector gate and seat the finstrate into the motherboard connector (Figure 4). (530/540)

Route IOP cable through base plate. Connect IOP cable finstrate connector to finstrate. Close connector gate and seat board into motherboard connector.

(ALL EXCEPT 520 - 3rd IOP) Install processor stack door with two captive #2 Pozidriv screws at bottom. (520 - 3rd IOP) Continue with next step.

14. (ALL)

Close processor stack door. Tighten thumbscrews to prevent Radio Frequency Interference (RFI) radiation. Replace the label that is used as a seal for the processor stack door (Part Number 5180-5201).

CAUTION ENSURE THE I/O EXPANDER CONNECTOR ON THE PROCESSOR STACK DOOR IS COVERED, AS EXPLAINED IN THE NEXT STEP, TO PREVENT RFI RADIATION.

15. (520)

Attach I/O expander cable(s) to the appropriate I/O EXPANDER connector (Figure 5), or cover connector with plastic connector cover (Figure 6).

(530/540 - 2nd IOP)

Route IOP cable along outside of base plate and attach IOP cable strain relief clamp to base with four #2 Pozidriv screws. Ensure cable is centered in clamp and is not pinched. (530/540 - 3rd IOP)

Remove I/O cable strain relief clamp which holds 2nd IOP cable in place on outside of base plate. Route IOP cable along outside of base plate and attach IOP cable strain relief clamp to base with four #2 Pozidriv screws. Ensure cable is centered in clamp and is not pinched.



Figure 5. IOP Connector Attached to Door.



Figure 6. Plastic IOP Connector Cover.

16. (530/540 ONLY)

Route cable between terminal block and base plate into enclosure.

17. (530/540 ONLY)

Remove cover plate from appropriate IOP expander slot in the computer rear panel. Insert IOP cable connector into the appropriate slot and secure in place with two nuts on the posts: (slots viewed from rear.)

- (530 2nd IOP) Upper slot.
- (530 3rd IOP) Lower slot.
- (540 2nd IOP) Right slot.
- (540 3rd IOP) Left slot.

CAUTION

ENSURE THE I/O EXPANDER CONNECTOR ON THE PROCESSOR STACK DOOR IS COVERED TO PREVENT RFI RADIATION.

18. (530/540 ONLY)

Attach I/O expander cable to connector on rear panel, or cover connector with plastic connector cover.

19. (520 - 2nd IOP)

Replace the trim piece and the left door.

(520 - 3rd IOP)

Close the left door.

(530)

Replace RFI shield, top and bottom covers of System II enclosure, and front panel.

(540)

Replace RFI shield, top and bottom covers of System II enclosure, front panels and flip top cover.

20. (ALL)

Plug the power cord into the ac outlet and switch on the power.

512K RAM Board Installation Information

Instructions For HP-Qualified Personnel:

Follow the instructions that apply to the installation you are performing. For example, if you are installing the RAM card in a 520 computer you would perform the steps that begin with: (ALL), and (520).

Load Board

Systems that are shipped from the Fort Collins Systems Division with 1 CPU, 1 IOP, and one 512K Byte RAM Board, will also have a Load Board in the slot that is adjacent to the RAM board (top occupied slot). If any other Finstrates, or RAM, is added to this configuration, the Load Board must be removed from the computer.

Any time the Processor Stack configuration is reduced to 1 CPU, 1 IOP, and one 512K Byte RAM Board, A Load Board (09855-66525) is required. Load Board (09855-66525) is a replaceable part.

CE Handbook

When completed with the installation, insert this page and the following page in your CE Handbook (after page 2-12 of either the 9020 or 9030/9040 section).

Part Numbers

512K Byte RAM (exchange) 97047-69805 (new) 5061-6805 Load Board 09855-66525

512K Byte RAM Board Installation

1. (ALL)

TURN THE POWER OFF AND DISCONNECT THE POWER CORD.

2. (520)

Open the left door.

(530)

Remove the front panel.

(540)

Remove the front bottom panel.

3. (530/540 ONLY)

From the front of the computer, remove the Radio Frequency Interference (RFI) shield by loosening the six thumbscrews.

4. (ALL)

Open processor stack door.

CAUTION

ELECTROSTATIC DISCHARGE DAMAGE CAN OCCUR IN THE FOLLOWING STEPS. FOLLOW THE PRECAUTIONS IN CHAPTER 4 OF THE SERVICE MANUAL.

DO NOT TOUCH EDGE CONNECTOR OR BOARD PLANE. HOLD BOARD BY EJECTORS OR SIDE EDGES ONLY. HANDLING THE RAM BOARD INCORRECTLY COULD CAUSE ELECTROSTATIC DISCHARGE DAMAGE. WHEN INSTALLING THE RAM BOARD, HOLD BY EJECTORS AND MOVE AIR CONTROLLER OUT OF THE WAY WITH THE SIDE EDGE OF THE BOARD.

5. (ALL)

Remove the Load Board (09855-66525) from the Processor Stack, if it is present and at least one RAM board is installed. The load board will no longer be required. It is the property of the customer.

6. (ALL)

Install the new RAM board in the first unoccupied slot from the bottom. DO NOT LEAVE EMPTY SLOTS BETWEEN BOARDS.

7. (ALL)

Close the processor stack door. Firmly tighten thumbscrews to prevent RFI radiation. Replace the label that is used as a seal for the processor stack door (Part Number 5180-5201).

8. (530/540 ONLY)

Replace RFI shield.

9. (520)

Close left door.

(530)

Replace front panel.

(540)

Replace the front bottom panel.

10. (ALL)

Connect power cord to ac outlet.

1 Megabyte RAM Board Installation

Instructions

Read the following information then follow the instructions that apply to the installation you are performing. For example, if you are installing the RAM Boards in a 520 computer you would perform all steps that begin with: (ALL), and (520).

RAM Boards

 $1\,Megabyte\,RAM$ Boards can only be installed in pairs. Any combination of 256K, 512K, and pairs of $1\,Megabyte$ boards can be used.

Load Board

If a system is shipped with 1 CPU, 1 IOP, and one 512K RAM Board, it will also have a Load Board (09855-66525) in the top occupied slot. When the 1 Megabyte RAM Boards are added to the stack, the Load Board is no longer required and must be removed (assuming the 512K RAM board remains in the system).

When the only RAM boards in the stack are 1 Megabyte RAM boards, a load board is required if there are six or less. The load board should be removed when there are more than six 1 Megabyte RAM boards in the stack, or if there is a mixture of 256K, 512K, and 1 Megabyte RAM boards in the stack.

Any time the Processor Stack configuration is changed so that it contains one of the above configurations, a Load Board is required. The load Board (09855-66525) is a replaceable part in spares. If the load board is used it must be in the top **OCCUPIED** slot of the Processor Stack. Do not leave any empty slots between finstates or boards.

Boot Loader ROM

When the Processor Stack contains 1 Megabyte RAM Boards, Boot Loader ROM Rev. B (09020-80001) must be used, and UNIX 4.0 or Basic 2.0 software must be used.

Boot Loader ROM 09020-80000 can be used with UNIX 4.0 or Basic 2.0 (or any previous software versions) as long as the stack **DOES NOT** contain a 1 Megabyte RAM Board.

Boot Loader ROM Rev. B (09020-80001) can be used with any RAM configuration but **MUST** use UNIX 4.0 or BASIC 2.0 software (any earlier versions of software cannot be used with this boot loader).

Access Times

When the 1 Megabyte RAM Boards are installed in a computer, the access times will be slower. The customer may notice this slower process time during operation.

System Functional Test

The previous SFT tests (Part Number 09020-10010 Rev. 2.0) are not compatible with the BASIC 2.0 Operating System. The updated version of the SFT must be used with this operating system.

The 4.0 HP-UX Operating System contains the same System Functional Tests (SFT) as the previous HP-UX. They are located in the CE utilities dictionary.

Part Numbers

1 Megabyte RAM Board (exchange)	97046-69704
(new)	5061-7704
Boot Loader ROM *		09020-80000 (Rev. A)
		09020-80001 (Rev. B)
Load Board		09855-66525

* See BOOT LOADER ROM on the previous page for part number applicability. When ordering the ROM, the serial number and model number of the computer **must** be given to the individual taking the order.

CE Handbook

When completed with the installation, insert these pages in your CE Handbook (after page 2-14 of either the 9020 or 9030/9040 section).

RAM Board Installation

1. (ALL)

TURN THE POWER OFF AND DISCONNECT THE POWER CORD.

2. (520)

Open the left door. (530) Remove the front panel. (540)

Remove both front panels and the fliptop cover.

3. (530/540 ONLY)

From the front of the computer, remove the Radio Frequency Interference (RFI) shield by loosening the six thumbscrews.

4. (ALL)

Open processor stack door.

CAUTION

ELECTROSTATIC DISCHARGE DAMAGE CAN OCCUR IN THE FOLLOWING STEPS. FOLLOW THE PRECAUTIONS IN CHAPTER 4 OF THE SERVICE MANUAL.

DO NOT TOUCH EDGE CONNECTOR OR BOARD PLANE. HOLD BOARD BY EJECTORS OR SIDE EDGES ONLY. HANDLING THE RAM BOARD INCORRECTLY COULD CAUSE ELECTROSTATIC DISCHARGE DAMAGE. WHEN INSTALLING THE RAM BOARD, HOLD BY EJECTORS AND MOVE AIR CONTROLLER OUT OF THE WAY WITH THE SIDE EDGE OF THE BOARD.

5. (ALL)

Remove the Load Board (09855-66525) from the Processor Stack, if it is present.

6. (ALL)

Install the RAM boards starting with the first unoccupied slot from the bottom. DO NOT LEAVE EMPTY SLOTS BETWEEN BOARDS. If a Load Board is required, install it in the next slot above the RAM. (see "Load Board" in the information at the front of the procedure.)

7. (ALL)

Close the processor stack door. Firmly tighten thumbscrews to prevent RFI radiation. Replace the label that is used as a seal for the processor stack door (Part Number 5180-5201).

- 8. (520) Close the left side door. (530/540) Replace the RFI shield.
- 9. (520) Remove right side door.

(530/540)

Remove the top cover of the System II enclosure. The cover has one captive screw at the back of the box. Loosen the screw and slide the cover back and away from the box.

10. (530/540 ONLY)

Disconnect the ac module cable and the service module cable.

11. **(520)**

Remove three #2 Pozidriv screws from the mass storage cover (Figure 1). (530/540)

Remove four #2 Pozidriv screws that attach the I/O lid. Remove the lid.



* #2 POZIDRIV MASS STORAGE COVER SCREW

★ I/O DOOR CAPTIVE THUMB SCREW

Figure 1. Removing Mass Storage Cover

12. (520 ONLY)

Loosen two captive thumbscrews on I/O door (Figure 1) and swing door open, allowing door to rest in open position.

13. (520 ONLY)

Slide mass storage cover towards front cover of computer, disengaging cover from slot in front card guide.

14. (520 ONLY)

Lift mass storage cover with attached I/O door up and away from computer.

2-18 9020 Environmental/Installation/PM

15. (520 With Bootstrap Loader Card)

Remove Bootstrap Loader card and replace the Bootstrap Loader ROM with the new ROM. Use tool 8710-0585. Reinstall the card in the I/O card cage.

(520 With Fixed Disc Drive Controller Assembly)

Remove both cable connectors from controller assembly. Remove controller assembly and replace the Bootstrap Loader ROM with the new one (Figure 2). Reinstall the card in the I/O card cage.

(530/540)

Remove the SCM and replace the Bootstrap Loader ROM with the new ROM (Figure 3). Use tool 8710-0585. Reinstall the card in the box.



Figure 2. Fixed Disc Controller Assembly



Bootstrap Loader ROM



16. **(520)**

Reinstall mass storage cover and right side door. (530/540) Reinstall I/O lid and connect the ac module cable and service module cable.

17. (530/540 ONLY)

Reinstall the top cover on the System II enclosure.

18. **(520)**

Close the right side door. (530) Replace front panel. (540) Replace the flip top cover and both front panels.

19. (ALL)

Connect power cord to ac outlet.

2-20 9020 Environmental/Installation/PM



3-2 9020 Configuration



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Supported Peripherals

Due to constant change of the list of supported peripherals, this information is published separately. The HP 9000 Series 500 Configuration Information and Order Guide or periodic publications of the FSD TSE NEWSLETTER will have this information.

Software Distribution Media

BASIC is always distributed on 5 1/4" Flexible Disc; no option is necessary. HP-UX is always distributed on 1/4" tape; Opt. 022 must be specified on 9020 bundled systems and on 5-digit product number orders.

Support S

- Software Sup
- Training
 Documentation
- Service/Maint
- Software Con
- Site Preparat

Options

Options are published in the HP 9000 Series 500 Configuration Information and Order Guide.



Peripherals

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Software Distribution Media

BASIC is always distributed on 5 1/4" Flexible Disc; no option is necessary. HP-UX is always distributed on 1/4" tape; Opt. 022 must be specified on 9020 bundled systems and on 5-digit product number orders.

Support Services

- Software Support
- Training
- Documentation
- Service/Maintenance Requirements
- Software Consulting
- Site Preparation and Installation









Live Unit Troubleshooting Flowchart



Dead Unit Troubleshooting Flowchart

9020 Diagnostics

Chapter 5

Error Messages

Indication	Cause	Cure
Fast beep (0.5-sec. repeat rate)	Major module failure during sys- tem load.	Troubleshoot displayed error messages.
Slow beep (2-sec. repeat rate)	Insufficient memory for system being loaded.	Check stack self-test LEDs for RAM finstrate failures and replace defective RAM, if necessary. Verify that memory configuration is large enough for system being loaded.
Clock and date not set	Contents of real-time clock and non-volatile memory have been lost.	Set time/date with BASIC state- ment SET TIME/TIMEDATE or HP-UX command <i>date</i> .
System halted due to double bit memory error on MC NN CCCCCCCC	Double bit error occurred. System halted.	Replace defective RAM.
NN=MC# counting from stack bottom		
CCCCCCCC = last healer content in hex for MC NN		
System halted: Incompatible IOPs	Illegal combination of IOPs was found at powerup. IOPs of Rev. 2.1 and earlier are not compatible with IOPs of Rev. 3.0 and later.	Reconfigure IOPs to legal com- bination.
System halted: Insufficient mem- ory to start system	Not enough memory for system.	Add RAM or reduce system.
System halted: System error	System fault.	Try new revision of operating sys- tem. Analyze system dump which has been displayed and/or printed.
Internal temperature approaching maximum; powerdown may occur without warning	Internal temperature above 51°C.	Protect programs and data be- cause shutdown occurs at 97°C. Find and remove cause of over- heating.
Indication	Cause	Cure
---	--	--
Self test error 1: IO address DA, SA STATUS: XXXXXXX DA = device address 0 = display alpha 1 = display graphics 6 = keyboard and printer 7 = internal mass storage	Tables follow that list all error codes by device address (DA) and subaddress (SA), and defines them.	Refer to following tables.
SA = subaddress With DA = 6 0 = printer 2 = keyboard With DA = 7 0 = fixed disc 1 = flexible disc		
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX		· · · · · · · · · · · · · · · · · · ·
Self test error 2: CHECKSUM for segment NN	An operating system failure.	Use different system discs or tape cartridge or replace defective
NN = the code segment in which the error occurred		RAM.
Self test error 3: XXXX NN	Finstrate failure.	System operation can continue
XXXX = CPU#, IOP#, or MC#		provided that the failed finstrate is
NN = the NNth of that finstrate type, from the bottom of stack		
Self test error 4: Memory reduced to: NNNNNNN bytes	Memory has been mapped out.	Replace defective RAM. System still runs with reduced RAM.
Self test error 5: Fewer finstrates were found than expected	The number of finstrates detected at powerup is smaller than the number recorded in NVM.	Ensure finstrate configuration meets system requirements. Change NVM as required. (If more finstrates were found than expected, NVM is automatically updated to match the actual con- figuration.)

DA	SA	Error Code	Definition	Probable Causes
0		00000001	Flag line not asserted	
0		00000002	Status line not asserted	Alpha/interface board,
0		00000003	Control register failed	1/O bus
0		00000004	Failed reset test	Alpha/interface board
0		00000005	Not responding to poll	Alpha/interface board,
				I/O bus
0		0000006	Status false during DMA transfer	Alpha/interface board
0		00000007	Timed out while waiting for DMA transfer	Alpha/interface board
0		00000008	Failed alpha memory test	Alpha/interface board
0		00000009	Channel end does not clear poll response	Alpha/interface board,
				I/O bus
1		00000001	Timed out while initializing GDC	Graphics/digital video board
1		00000002	Status line not asserted	Alpha/interface board,
				display modules
1		00000003	Control register fails readback	Alpha/interface board,
				I/O bus
1		00000004	Not responding to poll	Alpha/interface board,
				I/O bus
1		00000005	Channel end does not clear poll response	Alpha/interface board,
				I/O bus
1		00000006	Failed graphics memory test	Graphics/digital video board
1		00000007	Timeout while waiting for DMA transfer	Alpha/interface board,
				graphics/digital video board
1		00000008	Timeout while waiting for direct I/O transfer	Alpha/interface board,
				graphics/digital video board
1		00000009	GDC not functioning properly	Graphics/digital video board

98760A Display Self Test Error 1 Error Codes

DA	SA	Error Code	Definition	Probable Causes
0		00000001	Status line not asserted)
0		00000002	Flag line not asserted	DIM, I/O bus
0		0000003	FR0 failed loopback	
0		00000004	Weasel chip failed selftest	DIM
0		00000005	FR8 failed loopback	1
0		00000006	FR9 failed loopback	
0		00000007	FR10 failed loopback	DIM, I/O bus
0		0000008	FR12 failed loopback	
0		00000009	FR1 failed loopback	J
0		0000000A	Poll response asserted but not enabled	DIM
0		000000B	Poll response not asserted when enabled	DIM, I/O bus
0		000000C	Failed frame buffer memory test	n – – – – – – – – – – – – – – – – – – –
0		000000D	Failed space substitution test	
0		0000000E	Failed display interrupt test	
0		000000F	Failed synchronous startup test	
0		00000010	Failed window test	DIM
0		00000011	Failed reset test	
0		00000012	Timed out while waiting for interrupt	
0		00000013	Status false during frame buffer transfer	
0		00000014	Timed out while waiting for DMA transfer	J
1		00000001	Status line not asserted	1
1	.	0000002	Flag line asserted when graphics disabled	
1	1	0000003	Poll line asserted but not enabled	Graphics interface, DIM
1		00000004	Flag line not asserted when graphics enabled	
1		00000005	Poll line not asserted when graphics enabled	J
1		00000006	Graphics memory failure	n – – – – – – – – – – – – – – – – – – –
1		0000007	Rubber band memory failure	Graphics memory
1	1	00000008	Failed line drawing test	Crapiles memory
1		0000009	Erase memory failure	J
1		A0000000	Flag timeout while writing FR1	h
1		000000B	Flag timeout while writing FR0	Graphics interface DIM
1		000000C	Flag timeout while reading FR0	
1		0000000D	Timed out while waiting for DMA termination]]

98770A or 98780A Display Self Test Error 1 Error Codes

		Error		Probable			
DA	SA	Code	Definition	Causes			
6	0	0000XX00					
6	Ō	0000YY01	Timed out while waiting for printer to complete self test	h			
6	0	0000YY02	Flag did not go false after reading FR0				
6	0	0000YY03	Timed out while waiting for flag				
6	0	0000YY04	Failed loopback test	Logic board			
6	0	0000YY05	Printer not responding to poll	Logic board,			
6	0	0000YY06	Printer failed self test	10 003			
6	0	0000YY07	Printer responds to polls when disabled				
6	0	0000YY08	Printer status false				
6	0	00007709	Printer enabled for poll response after reset				
6	0	0000YY0A	Keyboard PFW interrupt cannot be cleared	J			
XX :	XX = Printer status byte (hex) in following binary format:						
PS	PSEL DSTE MSTE OOPP HDEL VHLO PTTH HDTH						
wh	ere: PS	FL = Power	supply failure	Motor drive board			
	DS	TF = Danger	rous self test failure; potential damage to printhead	Motor drive board			
	MS	TF = Don't	care				
	OC	OPP = Out of	paper	Out of paper			
	HDFL = Printhead control line failure; BURN and/or CLEAR line Logic board stuck high or low						
	VHLO = Printhead supply voltage too low Motor drive board						
	PT	Motor drive board					
	HDTH = Printhead too hot; temperature in printhead exceeded 70°C Motor drive board, External heat source						
YY =	= Don'i	t Care					

Printer Self Test Error 1 Error Codes

DA	SA	Error Code	Definition	Probable Causes
6	2	00000001	Failed loopback to FR15	
6	2	0000002	Keyboard not responding to poll	
6	2	0000003	Keyboard failed self test	
6	2	00000004	Battery low condition detected	Keyboard electronics
6	2	00000005	Keyboard not responding with SRQ bit	board, I/O bus
6	2	0000006	Keyboard not forcing status false	
6	2	00000007	Reading FR10 did not clear keyboard poll response	
6	2	0000008	Power fail warning bit set	J

Keyboard Self Test Error 1 Error Codes

Internal Disc Drives Self Test Error 1 Error Codes

DA	SA	Error Code	Definition	Probable Causes
7 7 7 7 7 7 7 7 7	X X X X X X X X X X X X X X X X	00000001 0000002 0000003 0000004 00000005 0000006 00000007 00000008 00000009 0 for fixed d 1 for flexible	Timeout waiting for self test Failed poll response Status false failure Timeout in loopback Failed loopback Qstat timeout Flag line stuck Poll response stuck Bad power on qstat isc drive disc drive	Drive electronics, Drive, I/O bus

Quick Checks

The following quick checks can be performed independently of module self tests and other diagnostics.

LED	Indication When Lit/Action Required
Green	Power is applied, and bias voltage is available from primary board. (LED visible through center exhaust fan.)
Yellow	No failures have been detected. Failure detection circuitry is enabled. (LED visible through center exhaust fan.)
DOORS OPEN	I/O card cage door or processor stack door is open. OV also lights. Close door.
STACK TEMP	Processor stack temperature has exceeded 100°C. Check fans. Remove heat source.
SEC BOARD	+12MM mass storage power supply has failed or temperature in power supply assembly has exceeded 97°C. Check $+12MM$ mass storage power supply. Check fans. Remove heat source.
PWR	Peak primary current exceeded 9A. Check power supply. Check for short circuits.
OV	Used in conjunction with voltage LEDs; "on" indicates an overvoltage condition on one or more of the supplies or door open, "off" indicates an undervoltage condition. Close door. Check power supply. Check for short or open circuits.
$ \begin{array}{r} -19 \\ -12 \\ -2 \\ 3 \\ 5 \\ 6 \\ 12 \\ 19 \\ \end{array} $	Fault condition exists on the indicated supply. If the OV LED is also lit, an overvol- tage condition is indicated. If the OV LED is not lit, an undervoltage condition is indicated. Check power supply. Check modules which use the faulty voltage (Chap- ter 9 - Power Distribution diagram). Check for short or open circuits.

Power Supply LEDs



Power Supply and Sweep Drive Circuitry LEDs

Keyboard

With an operating system installed in the computer, press the (CAPS) key several times. The LED at the left of the key should change state each time the (CAPS) key is pressed.

Printer

Test the optional internal thermal printer by simultaneously pressing the Paper Advance and Top of Form buttons on the printer. The output generated should match the following pattern (except the date).

```
      Printer Firmware: July 12, 1982.

      V$15654756

      V$15654756

      V$123456789:;<=>?@ABCDEFGHIJKLMNOPQRSTUVWXYZ[\]

      ^_abcdefghijklmnopqrstuvwxyz{}

      ^_abcdefghijklmnopqrstuvwxyz{}

      01776

      018776

      018776

      019876

      019877

      019877

      019877

      019877

      019877

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```

9020A Display

- a. Ensure that the green sweep drive circuitry LED is lit (visible through the top cover).
- b. Set the intensity adjustment dial for sufficient intensity.
- c. Set the 50/60 Hz CRT refresh rate switch to the correct position.

9020B or 9020C Display

- a. Press the self test switch located under the display. The pattern generated should match the following pattern. Ensure that the Insert Character mode is disabled when making this check.
- b. Set the intensity adjustment dial for sufficient intensity.



Display Switches and Intensity Adjustment Dial

2. 5 + 8	N.3+8	Na+8	Na+8	N = + 8	N = + 8	N.a+8	N = + *	N.à.+ 88	N 5 + 8
Ua⊥+ ∞ N ≟ + ∞	04.000 No.000	0 at 1 30	Nite	N 5 + 8	031.00 N 5.4 90	N 5 + 8	N 5 + 8	0- <u>1</u> + 38	Na+ #
\04 + ≋	Var s	0-a. ⊤ ≋		0a+**	0478	0a+*	0a+*	oa+≋ N÷+∞	04-8
5a+*	5a+8	0a+*	0a+®	0a+*	sa+s	0a+*	va+*	0a+*	0a+*
8a+8	0a+≋	8a+8	8a+8	0a+≋	69+*	3a+≋	Sa+≋	6a+*	8a+*
Na+⊗	8±+*	84+%)	Na+#	<i>47</i> +⊗	₩ <u>à</u> +≋	8a+≋	8a+≋	8a+≋	₩ <u>a</u> +≋
8a+*	8à+≋	8a+*	8a+*	8à+≋	8a+\$	8à+≋	8a+*	8a+*	Na+*
8a+%	8a+\$	84±8	8a+#	♥à+₩	84+%	8a+*	Na+*	8a+*	8a+*
번 à +豢	8a+≋	8a+*	Na+8	8a+≋	8a+≋	%a+≋	8a+*	%à+≋	8a+8
Và+₩	び点+翁	13年来	8a+*	8a+≋	8a+*	8a+≋	8a+#	8a+≋	8a+*
8à+\$	Na+*	8a+*	15年48	% à +≋	Na+%	8à+≋	13年業	8a+*	Na+%
5a+#	8a+*	8à+≋	5a+*	5a+≋	15年4	84+*	8a+*	8a+*	8a+*
8a+*	8a+8	8a+*	13年後	₹±	8à+*	8a+≋	8a+*	8à+≋	Na+≣
8a+*	8a+*	\a+#	Na+*	8a+8	8a+*	8a+*	18年来	8a+*	8a+#
%à+≋	13点+参	Nà+≋	Na+#	\$±+≋	84+8	8a+≋	ビュ+ ※	8a+*	8à+≋
8a+8	8a+*	Nà+≋	Nà+≋	8a+≋	Nà+⊗	*3.à+≋Î	84±8	%à+≋	Nà+≋
8a+#	Na+#	Na+#	Na+#	8a+*	Na+#	8a+#	8a+*	8a+#	Na+#
8a+#	Na+#	Nà+≋	Na+*	8a+*	10点+第	8a+*	Na+#	15a+*	Na+#
8a+*	Na+#	Na+*	Na+#	8a+*	8à+*	8a+*	8a+#	8a+*	Na+*
8a+#	Na+#	8a+*	Na+#	8a+8	Na+®	Sa+#	8à+#	8a+#	Na+8
N.a.+ #	Na+8	N.à + 8	NA+8	NA+*	N.á + #	N.à + 8	然本主義	NA+*	Na+*
N.à+88	N = + #	N.à + 8	N.à+#	N.à + #	Nà+8	N.a.+ #	N.à+#	Na+#	Na+#
N.3+8	Nà+#	N 5 + 8	N = + 8	N.à + %	N = + 8	Na+8	Na+*	N = + #	Na+*
N.3+8	Na+#	N 5 + 8	N 5 + 8	N 5 + 8	N 5 + 8	N 5 + 8	N 5 + 8	N.3+8	N 5 + 8
N 5.4 %	N - + 8	N 5 + 38	N 5 4 8	N 5 4 8	N 5 + 8	NA+8	N 5 + 8	N ÷ + ®	N A A &
0a.+∞ N÷+∞	Nate	NAAS	N = + 93	N ÷ + ∞	NA+ 8	N ÷ + %	Nà+≪	Nate	N ÷ + ®
Vat s	VAT 8	oa+≋	Vat-s.	0478	0.4.7 %	Var **	0478	Uat+≋ Ni⊥ m	0478
0478	Dar*	VA+*	va+*	va+*	UAT 8	04.4	DATE	va**	91
					\sim	-	\sim		1

Blinking Underline

Blinking Overline

Blinking Characters

Test Pattern for the 9020B Display

1916 N.L. 191	N	N ± • **	N ÷ + ∞	1 N L 1 60
Na+*	0a+*	03+% No+%	53+8 N 5+8	Va+**
No.+ 8	04.+ %	Na+®	Na+*	N.5+8
No+*	Na+*	Sales Na+*	Na+*	Na+8
beeds	Na+%	Na+®	Na+*	84+#
8++8	Na+®	Na+*	Na+#	Na+8
Na+#	Na+*	Sa+*	Na+*	Và+≋
Nà+8	Na+#	Na+#	Na+*	Va+*
₹4+%	Na+*	8a+8	Na+%	V.à+≋
8a+8	Na+8	8a+*	8a+*	8+£
8a+*	8a+*	8a+*	Nà+*	Va+*
8a+*	8a+#	Na+*	Va+*	% à +≋
₹	Na+8	Na+®	Na+*	8a+8
Na+8	Na+*	Na+*	Na+®	Sa+≋
Na+#	Na+*	Na+8	Sa+*	8 <u>a</u> +8
Sa+*	Va+*	Na+*	Va+*	<u>V⊒∓</u> ≋ N⇒+≫
Not 8	Nat*	NAT NAT	No.+ %	Val+**
Na+*	Nà+®	Na+S	Na+%	Na+®
Na+8	Na+8	Na+*	Na+*	Na+8
8a+#	Na+*	Na+8	Nà+*	Na+®
Na+*	Va+*	8a+8	84+8	Va+*
8a+*	Na+*	₹¥*	^{pans} ∀à+≋	V#**
Na+®	Na+8	Na+8	8a+*	Na+*
<u>∀a</u> +≋	Na+8	Na+®	Na+*	Na+*
% <u>à</u> +≋	t <u>∜á+</u> ≋	Na+%	Na+®	<u>Va+</u> *
		V		\sim

Blinking Underline

V Blinking Characters

Test Pattern for the 9020C Display

Module Self Tests

Module self tests are contained in the replaceable modules and are initiated on powerup. (The BASIC language system SCRATCH ALL or LOAD BIN command causes all module self tests except the processor stack tests to be executed. No equivalent HP-UX commands exist.) Successful completion of most tests is indicated by an LED turning on and then off. Because all modules are tested quickly and the LEDs are located in various places on the 9020, you must run the test for each module you wish to check.

Processor Stack

Each finstrate has its own self test. Results are indicated by twelve LEDs associated with card slots 1 through 12. The LEDs are visible by removing the left cosmetic door and looking through the window in the stack door. Normal operation is all LEDs on, followed one or two seconds later by all LEDs off. An LED that won't turn on or that turns on and won't turn off indicates a failure of the corresponding finstrate.



Processor Stack Self Test LEDs

Keyboard

This self test turns the CAPS LED on and off and turns the PRINT ALL LED on at powerup. The BASIC system then turns the PRINT ALL LED off and the CAPS LED back on. The HP-UX system turns both PRINT ALL and CAPS off.

Printer

An LED on the printer's logic board lights when its module self test detects a failure. The LED is visible through the air vents under the keyboard.



Printer and Disc Drive LEDs

Internal Flexible Disc Drive/Controller Card

The first part of the self test checks the drive, the drive board, and the controller card. The LED on the front of the drive indicates successful completion when it turns on.

The second part of the test requires that an initialized disc be stored in the drive. This part of the test thoroughly checks the drive. If the test passes, the LED on the controller card turns on and off once. With the right cosmetic door open, the controller card LED is visible through the upper hole in the I/O cover plate.

Internal Fixed Disc Drive/Controller Assembly

An LED on the drive is visible through the air vents under the keyboard. If this LED turns on, the controller assembly has successfully passed self test.

If the drive LED goes on, indicating the drive is being accessed, but the controller assembly LED fails to go off, a bad drive is indicated. With the right cosmetic door open, the controller assembly LED is visible through the middle hole in the I/O cover plate.

Bootstrap Loader Card

The LED on the bootstrap loader card is visible through the middle hole in the I/O cover plate with the right cosmetic door open. If the LED turns on and then off, the bootstrap loader code has been properly downloaded to the system RAM. If the LED fails to turn off, the code has not loaded properly.

Self Test Supervisory Code (STSC)

The STSC runs automatically after the module self tests on powerup. STSC verifies the integrity of the internal I/O bus, tests the interfaces on all of the internal modules, retests mapped-out memory blocks that previously failed the memory controller test, and reports the blocks that fail the retest. The code issues a message to the user in case of a failure.

The modules tested by the STSC are:

- Internal I/O bus
- Printer
- Keyboard
- Display interface module (9020B or 9020C)
- Alpha/interface and graphics/digital video boards (9020A)
- Internal flexible disc drive and controller card
- Internal fixed disc drive and controller assembly

System Functional Tests (SFT)

To Run HP-UX SFT:

- 1. If not previously done, install and verify the HP-UX operating system.
- 2. In response to the login: prompt, type: root and press **RETURN**. You are now the super-user.
- 3. Type: cd /usr/Ptests.
- 4. Type: start and press **RETURN**.
- 5. From the menu that appears on the system console, select the test you would like to run, enter its number, and press **RETURN**.

To Run BASIC SFT:

- 1. Load SFT TEST system boot discs.
- 2. Load SFT Mainframe Test Programs disc or SFT Peripheral Test Programs disc.
- 3. Type: LOAD "TEST: INTERNAL", 1 and press EXECUTE).

BASIC Mainframe System Functional Tests

Test Name	Pass/Fail Indicator	Description/Requirements
PRINTER		Hardware: 9020A, 9020B, or 9020C with built- in thermal printer
	Compare printed output with pattern that follows.	STANDARD - tests the printer hardware.
	Test fails if output contains vertical white lines.	SOLID - tests the print head. Vertical white lines appearing in the output indicate a non- functioning thermal element in the print head.
		REVERSE - press (8 24) to reverse motion of paper feed mechanism: use paper advance key to advance paper. This is useful for remov- ing paper which has become jammed in the printer.
DISPLAY	Check that characters and lines are sharp and clear.	STANDARD - shows a variety of displays which you should use to check that the display is in focus. When testing the 9020C and characters are not sharp. try running the CONVERGE and STANDARD options of CGRAPHICS test.
	Lines should appear straight. (+)s should be in straight lines and equal- ly spaced.	INTERACTIVE - verify that lines appear straight. The CRT Linearity Test uses a series of (+) characters to verify the display.
KEYBOARD	Message provided upon test failure only.	Verifies operation of the keyboard, real time clock, beeper, and non-volatile memory. Beeper test plays a musical scale when operating cor- rectly.
AGRAPHICS		Hardware: 9020A
	Message provided upon test failure only. If there is no display, check dis- play intensity, power cord, and fuse.	STANDARD - tests vector generation and graphics memory. No need to visually inspect display.
		INTERACTIVE - used to set the intensity of the three color guns of the display.

Test Name	Pass/Fail Indicator	Description/Requirements
BGRAPHICS		Hardware: 9020B
	Compare displays with outputs that follow. Graphics memory test pro- vides pass/fail message.	Tests graphics hardware such as: cursor types, vector generation, arc generation, area fill capa- bility, video mixer, LP bit (light pen bit), and graphics memory.
CGRAPHICS		Hardware: 9020C
	Compare displays with output that follows. Graphics memory test pro- vides pass/fail message.	STANDARD - degausses the color display, tests vector generation (compare display to Graphics output that follows), and tests graphics memory.
	Visually inspect for compliance with description.	INTERACTIVE - The linearity test consists of a series of lines of identical characters. Check that the lines are equally spaced vertically on the display, and that the characters in each line are equally spaced from left to right edges of the display area. During the cursor test, eight horizontal bands are displayed. The colors from top to bottom are: blue, white, yellow, magenta (purple), red, green, cyan (light blue), and black (not visible).
	If characters appear sharp and white, no convergence is needed.	CONVERGE - used to converge the three color guns of the display. Use when characters on the display appear to have colored (red, green, yel- low, etc) "shadows". Follow procedure detailed later in this chapter.
LTPEN		Hardware: 9020B or 9020C with Light Pen op- tion
	Error indicator described in test in- structions.	Verifies operation of the light pen and associated graphics hardware.
FLOPPY		Hardware: 9020A, 9020B, or 9020C with built- in flexible disc drive
	Test System Error number provided. Refer to Chapter 10.	STANDARD - tests ability of drive to create a file, write to a file, read from a file, and purge a file.
	Test System Error number provided. Refer to Chapter 10.	EXTENDED - tests ability of drive to write to and read from many locations on the disc. This test requires use of a blank, initialized flexible disc.
	Test System Error number provided. Refer to Chapter 10.	CLEANING - when used in conjunction with the HP 92193A Disc Cleaning Kit, cleans the read/ write head of the computer's built-in flexible disc drive.

BASIC Mainframe Functional Tests (Continue)

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Test Name	Pass/Fail Indicator	Description/Requirements
STATUS	Not a test; only information gener- ated.	Generates a description of the computer hard- ware, a list of the contents of Non-Volatile Mem- ory, a description of the memory/processor module configuration.
WINCHESTER		Hardware: 9020A, 9020B, or 9020C with built- in Winchester disc drive.
	Test System Error number provided. Refer to Chapter 10.	STANDARD - tests ability of drive to create a file, write to a file, read from a file and purge a file.
	Test System Error number provided. Refer to Chapter 10.	INITIALIZE - tests ability of drive to write to and read from many locations on the disc. After test- ing, the disc is initialized, destroying all pro- grams and data that exist on the disc. Use this test to recover from power down during previous initialize.

BASIC Mainframe System Functional Tests (Continued)

PRINTER Test Printout

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BGRAPHICS Line Type Test



BGRAPHICS Area Fill Test

CGRAPHICS Vector Test



Test Name	Pass/Fail Indicator	Description/Requirements	
CS80		Hardware: 9020A, 9020B, or 9020C with any CS/80 Disc Drive (and interface).	
	Test System Error number provided. Refer to Chapter 10.	Tests ability of HP disc drive to create a file, write to a file, read from a file and purge a file. Data or programs stored on the disc are not affected by this test.	
SS80	Test System Error number provided. Refer to Chapter 10.	This test is similar to the CS80 test, except that it interacts with disc drives that do not support all the features of CS/80 protocol (thus their identi- fication by SS/80, a subset of the command set 1980 protocol). SS/80 drives include: HP 9122, 9125, 9133, and 9134.	
9895		Hardware: 9020A, 9020B, or 9020C with HP 9895 Flexible Disc Drive (and inter- face).	
	Test System Error number provided. Refer to Chapter 10.	Tests ability of HP 9895 flexible disc drive to create a file, write to a file, read from a file and purge a file. Data or programs stored on the disc are not affected by this test.	
8290x		Hardware: 9020A, 9020B, or 9020C with HP 8290x Flexible Disc Drive (and inter- face).	
	Test System Error number provided. Refer to Chapter 10.	Tests ability of HP 8290x flexible disc drive to create a file, write to a file, read from a file and purge a file. Data or programs stored on the disc are not affected by this test.	
TAPE	Test System Error number provided. Refer to Chapter 10.	Test supported tape drives for file create, write, read, and purge operations. Test destroys any existing data on the tape .	
9885	Test System Error number provided. Refer to Chapter 10.	Tests HP 9885 M/S flexible disc drive using a dedicated GPIO interface. Except for HP-IB address not applying, test operation is similar to other tests. An initialized flexible disc is required for test.	
9121	Test System Error number provided. Refer to Chapter 10.	Tests HP 9121 disc drives. Test operation is simi- lar to other flexible disc drive tests. An initialized disc must be present in the drive 10. before the test is executed.	
SRM	Test System Error number provided. Refer to Chapter 10.	Tests SRM and interface by create a file, write, read, and purge. Provides more information ab- out current status during operation than most tests in package.	
HPIO	Test provides pass fail message upon completion.	Tests ability to write to and read from each I/O card. Does not perform comprehensive I/O card test.	
ASI		Hardware: 9020A, 9020B, or 9020C with HP 27128A Async. Serial Interface	
	Test provides pass fail message upon completion.	Tests the HP 27128A Asynchronous Serial Inter- face card by writing to and reading from the card. Provides a block of information describing the card and its configuration.	
HPIB		Hardware: 9020A, 9020B, or 9020C with HP 27110A HP-IB Interface Card	
	Test provides pass fail message upon completion.	Tests the HP 27110A HP-IB interface card by writing to and reading from the card. Provides a block of information describing the card and its configuration.	

BASIC Peripheral System Functional Tests

Test Name	Pass/Fail Indicator	Description/Requirements			
EXPRINTER		Hardware: 9020A, 9020B, or 9020C with any supported HP-IB printer.			
	Compare printer output with pattern that follows.	Prints character pattern to specified external printer.			
		HPIB – Formats test to match standard HP- IB printer protocol. Use this option for all standard HP-IB line printers.			
		CIPER – Formats test for CIPER printers that use HP-IB interfacing, but support a special protocol that helps reduce bus congestion and provides more efficient data transfer. CIPER printers include HP 2608, 2566, and 2567.			
		SERIAL – Formats data for printers that use standard RS-232C serial interfacing.			
PLOTTER		Hardware: 9020A, 9020B, or 9020C with any supported Hewlett-Packard HP-IB plotter.			
	Compare plotter output with pattern that follows.	Plots test pattern on specified plotter. If testing a multi-pen plotter, install pens such that: pen $#1$ = black, pen $#2$ = red, pen $#3$ = green and pen $#4$ = blue.			
DIGITIZER		Hardware: 9020A, 9020B, or 9020C with HP 9111 or HP 9874 Digitizer (and in- terface).			
	Test fails if display drawing fails to track stylus/puck movements.	Digitize (press stylus against the platen or press "D" on the 9874 puck) and move the stylus/ puck around on the platen. A line which tracks the stylus/puck movements should be drawn on the display. No line is drawn when not digitizing.			
		Press key $\begin{bmatrix} 24 \end{bmatrix}$ to clear the screen and repeat test, or $\begin{bmatrix} 0 & 16 \end{bmatrix}$ to exit.			
GPIO		Hardware: 9020A, 9020B, or 9020C with HP 27112A GPIO Interface Card and its HP 1251-8003 Test Connector.			
	Message provided upon test pass/fail.	Tests write/read to the HP 27112A GPIO Inter- face Card.			
97060		Hardware: 9020A, 9020B, or 9020C with HP 97060 Graphics Processor.			
	Message provided upon test pass/fail.	Tests the HP 97060 Graphics Processor for color synchronization.			
97062		Hardware: 9020A, 9020B, or 9020C with HP 97062 Color Output Interface Card.			
		Tests the HP 97062 Color Output Interface Card for color synchronization. If the colors and labels do not match check card connection.			
RGB ALIGN	Not a test. For Test System Error Codes, refer to Chapter 10.	Alignment aid for setting up color displays con- nected to the HP 97060 Graphics Processor or the HP 97062 color interface card. Supports convergence and RGB intensity and balance setup. 97060 – For graphics Processor HP 97060.			
		97062 – For RGB interface HP 97062.			

BASIC Periph	neral System	Functional Tests	(Continued)
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EXPRINTER Test Output

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0123456789:; <=>?@ABCDEFGHIJKLMNOPQRSTUVWXYZ[\]^_`	abcdefghijklmnopqrstuvwxyz(1)~#
0123456789:;<=>?@ABCDEFGHIJKLMNOPQRSTUVWXYZ[\]^_`	abcdefghijklmnopqrstuvwxyz(l)~#
0123456789:;<=>?@ABCDEFGHIJKLMNOPQRSTUVWXYZ[\]^_`	abcdefghijklmnopqrstuvwxyz()"#
0123456789:;<=>?@ABCDEFGHIJKLMNOPQRSTUVWXYZ[\]^_`	abcdefghijklmnopgrstuvwxyz(i)~#
0123456789:;<=>?@ABCDEFGHIJKLMNOPQRSTUVWXYZ[\]^_`	abcdefghijklmnopqrstuvwxyz[]~#
0123456789:;<=>?@ABCDEFGHIJKLMNOPQRSTUVWXYZ[\]^_`	abcdefghijklmnopqrstuvwxyz(}~₽
0123456789:;<=>?@ABCDEFGHIJKLMNOPQRSTUVWXYZ[\]^_`	abcdefghijklmnopqrstuvwxyz(1)~#
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0123456789:;<=>?@ABCDEFGHIJKLMNOPQRSTUVWXYZ[\]^_`	abcdefghijklmnopqrstuvwxyz(l)~#
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PLOTTER Test Output





Power-up Sequence Flowchart

System Loader Messages

Trailer NNNNN indicates Rev. A loader select codes.

Example:

Where: NNNNN is 21



Trailer SELECT CODE NN indicates Rev. B loader select codes; 0 through 7 = 1st IOP, 8 through 15 = 2nd IOP, and 16 through 24 = 3rd IOP.

Messages

Loader XXX – Informational message identifying the revision of the system loader. This message is usually followed by a single line message identifying the operating system the computer is attempting to load.

Testing Memory... – Informational message that follows the "Loader XXX" message indicating that the loader is performing memory tests and configuring memory. This can take up to 15 seconds.

Looking for System... – Informational message that follows the "Testing Memory..." message indicating that the loader is searching for an operating system.

Please mount next volume. – Informational message. The loader is ready to load another portion of the operating system. Mount the volume containing an unloaded portion of the operating system. Volumes may be mounted in any order without affecting the loading process.

SYSTEM NOT FOUND; WILL RETRY: XXX SYSTEM NOT FOUND; WILL RETRY IN XXX

– Unable to find an operating system on any mass storage device. The loader will attempt to find an operating system again in XXX seconds. Possible causes: mass storage device not powered up, no media in mass storage device, wrong disc in disc drive, computer or mass storage device hardware failure, media failure, incompatible loader/system revision numbers, etc.

BAD SYSTEM FILE: NNNNN BAD SYSTEM FILE: SELECT CODE NN

– Operating system loaded. However, an error has been detected in the operating system code during loading. Possible causes: corrupt system, media failure, mass storage hardware failure, or computer hardware failure.

INSUFFICIENT USABLE MEMORY: XXXX NOT ENOUGH USABLE MEMORY; TOTAL IS XXXX

- The amount of usable memory is too small to load the operating system. The total amount of good memory is "XXXX" bytes. However, the amount of memory available for the Rev. A operating system is "XXXX" minus 32K bytes. The amount of memory available for the Rev. B operating system is "XXXX" minus 98 304 bytes. Possible causes: corrupt system or hardware (memory) failure.

BAD CARD OR DEVICE: NNNNN BAD CARD OR DEVICE: SELECT CODE NN

- Informational message. A hardware failure has been detected (interface card or mass storage device did not pass the Module Self-Test). The loader continues searching for an operating system. Possible causes: bad interface card or mass storage device.

DEVICE NOT READY: NNNNN VOLUME NOT MOUNTED: NNNNN MEDIA/DEVICE NOT READY: SELECT CODE NN

- While loading. The media (Volume) was removed from the device (e.g., a floppy disc was pulled out of a disc drive), the device went offline, or a hardware problem caused the device to become "not ready".

DMA FAILED: NNNNN – Data did not transfer properly from the mass storage device to the computer. Possible cause: Mass storage device hardware failure or computer hardware failure.

UNRECOVERABLE DATA: NNNNN UNRECOVERABLE DATA: SELECT CODE NN

- Part of the operating system is not readable. Possible causes: media failure or mass storage device hardware failure.

END OF VOLUME: NNNNN END OF VOLUME: SELECT CODE NN

- Attempt to address or read past the end of a volume. Possible causes: corrupt system, media failure or mass storage device hardware failure.

CTRLR/UNIT FAULT: NNNNN CTRLR/UNIT FAULT: SELECT CODE NN

- Hardware passed initial self-test. However, it failed while being used to load the operating system. Possible causes: computer (interface card) hardware failure or mass storage device hardware failure.

IO TIMEOUT: NNNNN IO TIMEOUT: SELECT CODE NN

– Mass storage device failed to respond fast enough while attempting to load from it. Possible cause: computer hardware failure or mass storage device hardware failure.

CS80 DEVICE: NNNNN CS80 DEVICE: SELECT CODE NN

Indicates a mass storage device hardware failure.

TAPE DEVICE: SELECT CODE NN – Usually indicates a tape device (HP 7970, HP 7974, HP 7978) hardware failure. Can also indicate a failure on the HP 27110A HP-IB Interface (or the Internal HP-IB interface). Tape errors covered are: "Command Rejected", "Interface Busy", "Rewinding", "Tape Runaway", "Data Timing Error", and "Command Parity Error".

```
HPIB CARD: NNNNN
HPIB CARD: SELECT CODE NN
```

- Transaction to the indicated HPIB interface card was terminated due to a probable interface card failure.

KBD/SCM NOT FOUND. - Indicates a keyboard failure.

5-24 9020 Diagnostics

BAD IO BUS: NNNNN BAD IO BUS: SELECT CODE NN - Indicates a computer hardware failure on the computers first IOP.

BAD NVM: NNNNN BAD NVM: SELECT CODE NN

- Indicates that Non-Volatile Memory failed its self-test. Possible cause: computer hardware failure.

BAD RTC: NNNNN BAD RTC: SELECT CODE NN

- Indicates that the built in Real Time Clock is not operating correctly.

9020 Adjustments

Chapter 6

Print Quality

The internal thermal printer has two adjustment cams, one on each side of the printer. To obtain maximum print quality, loosen the cam lock bolts on both sides, adjust the cams while printer is operating for maximum print quality, and tighten the lock bolts.



Print Quality Adjustment

Keyboard Language Resistors

The keyboard can be configured to one of several languages. Reconfigurations can be done in the field. To reconfigure a keyboard, change resistors on keyboard electronics board and change keycaps.

To change resistors:

- 1. Turn off computer and unplug power cord from ac outlet.
- 2. Remove keyboard from computer and place keyboard upside down on an antistatic surface.
- 3. Determine current configuration by noting the placement of the language resistors. Determine desired configuration according to table. A "1" indicates a resistor is installed at that location; a "0" indicates there should be no resistor at that location. Add and/or remove resistor(s) to match the desired configuration. Resistor part number is 0698-3441.



Language Resistors

Keyboard Language Resistors

	R16	R17	R23	R24
English (ASCII)	1	1	1	1
Katakana (Japanese)	1	1	0	1
Svenska (Swedish)	1	0	1	1
Suomeks (Finnish)	1	0	1	1
Espanol (Spanish)	1	0	0	1
Deutsch (German)	1	1	1	0
Francais (French)	1	1	0	0

Keyboard Language Resistor Configurations

CRT Displays

Intensity Adjustment

The intensity adjustment dial is located in the same place on all 9020 CRT displays.



Intensity Adjustment Dial

Voltage Selection (9020A and 9020C)

The 9020A and 9020C color displays have voltage selector switches on the back. The switches can be set so that the display operates on one of two nominal line voltages, as indicated by the drawings.



9020A Display



CRT Refresh Switch (9020A)

The 9020A has a CRT refresh switch mounted underneath the display. The switch has two positions: 50 Hz and 60 Hz. The switch should be set to match the local line frequency. As you face the front of the display, 60 Hz is selected with the switch to the left and 50 Hz is selected with the switch to the right.



Display Self Test or Refresh Switch and Adjustment Dial

Self Test Switch (9020B and 9020C)

The 9020B and 9020C have a self test switch mounted underneath the display. Pressing the switch displays a test pattern. The pattern is displayed as long as the switch is pressed. When the switch is released, the pattern is no longer displayed.



Blinking Underline

Blinking Overline

Blinking Characters

Test Pattern for the 9020B Display

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MR. GR.	Sa+*	5.6.92.	Na+*	34	8a+*	- Exceleted	8a+*		8a+*
and op the	%à+≋		Nà+®		<u> 84+</u> *		Na+®		Na+*
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Blinking Underline

Blinking Characters

Test Pattern for the 9020C Display

6-6 9020 Adjustments

Aligning the 9020A Display

- 1. Load the TEST operating system using the System Functional Test discs.
- 2. Load the mainframe test disc and select AGRAPHICS, INTERACTIVE.
- 3. Step through the procedures as directed.

Note The video board contains all clamp pots, gain pots, and test points. The sweep board contains the screen grid adjustment, as well as several other adjustments.



9020A Video Board Pots and Test Points



9020A Sweep Board Adjustments

Converging the 9020C Display

- 1. Run the System Functional Tests using the MAINFRAME TEST PROGRAMS disc. Select the test for color graphics (CGRAPHICS) then select the CONVERGE option.
- 2. Open the door located just to the right of the screen (on the display case) to expose 39 alignment locations and the alignment tool; the 39 alignment locations are organized in rows by adjustment number (13 in all) and in columns by color (red, blue, and green). Remove the alignment tool from inside the door by pressing its top.
- 3. A small white plus sign (+) is displayed in one area. Additionally, the number 1 appears along the right hand edge of the display to remind you of the row of alignment locations you should use to adjust the convergence. Use the alignment tool to merge (if necessary) the red, blue, and green pluses by turning the appropriate control for the color you are moving. For example, adjusting the alignment location in row one, column one (the red column), moves the red plus.

Adjusting the alignment locations causes the pluses to move across the screen according to the following rules:

- a. the red plus moves along a line from lower left to upper right.
- b. the blue plus moves along a vertical line.
- c. the green plus moves along a line from upper left to lower right.

You may find that the easiest way to merge the pluses is to first merge the red and green pluses together (forming a yellow plus). Next merge the blue plus (which now lies directly above or below the yellow plus) into the yellow plus, forming a white plus.

Note

If the character displayed is a white plus with shaded (colored) edges rather than completely separated pluses, very little adjustment is needed. Even when the three colored pluses are correctly merged, a small amount of colored "fringing" may appear around the edges. This is normal.

- 4. After you are satisfied with the adjustment for alignment location one, press (a 24). Another white plus appears; this is for alignment location 2. Adjust locations 2 through 13 in the same manner as indicated in step 3 above.
- 5. If you are not satisfied that all pluses are properly merged, steps 3 through 4 may be repeated as many times as necessary. When you are completely satisfied that the display is converged, press (1-16) to exit the routine.

6-8 9020 Adjustments

9020 Peripherals

Chapter 7

CAUTION

DO NOT OVERLOAD THE POWER SUPPLY BY USING MORE HP 27123A, HP 27125A, OR HP 50961A INTERFACE CONNEC-TIONS THAN CAN BE SUPPORTED.

An HP 9020B with a 10-megabyte winchester disc drive must not have more than one of the following I/O cards installed in it if the MPB and I/O card cages are full. An additional card may be added for each MPB or I/O slot that is unoccupied. Because these interface cards can power external devices, they may exceed the per-slot power budget specified by the HP-CIO standard. Therefore, consider them to take the power of two interface cards. The I/O cards concerned are:

- HP 27123A when powering an HP 98028A SRM MUX.
- HP 27125A LAN/500 Link Interface.
- HP 50961A Option 500 (27123A with SRM Coax Adapter).

Select Code	Usage
0	Display Alphanumeric
1	Display Graphics
2	I/O Slot #2
3	I/O Slot #3
4	I/O Slot #4
5	I/O Slot #5
6	Keyboard, Printer, Real-Time Clock,
	Beeper, Optional Input Device
7	Internal Mass Storage

Select Codes

HP-CIO Interface Cards

HP 27110A/B Standard HP-IB

Features

L

- IEEE-488-1978 compatible.
- Supports DMA with two modes of performance: High Speed Mode for operation with fixed discs or other high-speed peripherals, and Standard Mode for instruments and slower peripherals.
- Supports up to 14 device loads.
- Selectable HP-IB controller or slave capabilities and parallel poll capabilities (BASIC Language System only).
- Built-in hardware self-test.

Configuration

The normal switch settings are shown in the next figure.

The switch functions are:

- S1-S5: Address 30 (decimal); S1 is Least Significant Bit
- S6: System Controller On
- S7: Normal Speed
- S8: Test Mode 1

High-speed devices can run on a normal-speed bus, but run slower than their capacity. Normalspeed devices cannot run on a high-speed bus. The following are examples of high-speed devices:

- disc drives
- 7971A tape drive
- 2608S and 2631B/G printers



Product Number	Length (in metres)
*92220R	0.3
10833D	0.5
10833A	1.0
*82977A	1.0
10833B	2.0
*82977B	2.0
10833C	4.0
8120-3448	6.0
8120-3449	8.0

HP-IB Cables

* Right Angle Connector

HP 27112A General Purpose I/O (GPIO) 16-Bit Parallel

Features

- Choice of programmable operating modes (clocked or transparent) for ease of use with instrumentation.
- Supports +5V level on all input and output signals, plus an optional +12V level on output signals.
- Programmed data detection for either positive true or ground true levels.
- Independent 16-bit input and output buses and storage registers.
- Two control and two status lines.

Configuration

The normal switch settings depend on which peripheral device is connected to the GPIO. The 9885M/S Flexible Disc Drive and the 97060A Graphics Processor require the GPIO switch settings shown in the next figure.

The switch setting functions are:

SW1

- S1: DIN Negative True
- S2: CTL and STS Negative True
- S3: PSET Negative True
- S4: PDIR Negative True
- S5: DOUT Negative True
- S6: PEND Negative True
- S7: PFLAG Negative True (97060A)
 - Positive True (9885M/S)
- S8: PCNTL Negative True

SW2

- S1: Bidirectional Bus Disabled
- S2: Internal Handshake Disabled
- S3: Full/Pulse Handshake Disabled (9885M/S)
 - Enabled (97060A)

S4 and S5: Data Clocked On Ready To Busy Edge Of PFLAG (Leading Edge)



HP 27112A General Purpose I/O (GPIO) Interface Card
7-6 9020 Peripherals

To configure the GPIO card:

- 1. Install jumpers in W1, W2, and W3, according to whether 5-volt or 12-volt logic levels are to be used.
- 2. Set the card's switches for proper operation.
- 3. If necessary, increase the delays on the card as follows:

Two one-shots (E15) on the GPIO card generate the write delay and the internal handshake delay. The write delay one-shot provides approximately 100 nsec for the output data to settle. When extra-long cables are used, or when the peripheral device requires additional settling time for the data, the delay can be increased by adding a capacitor between pins 1 and 4 of the socket at E16.

The formula for selecting the capacitor value is:

C = (T-100)/1.5

where

C = additional capacitance (in pf)

T =total time delay required in nsec

The internal delay one-shot provides a delay of approximately 3 usec between the assertion of PCNTL and the assertion of FLAG. The delay can be increased by adding a capacitor between pins 5 and 8 of the socket at E16.

The formula for the value of the capacitor is:

C = (T-3000)/3

where

C = additional capacitance (in pf)

T = total time delay required (in nsec)







Interconnecting the HP 97060A Graphics Processor

HP 27122A Remote Job Entry (RJE)

Features

- 1,200 to 19,200 baud rates.
- Compatible with EIA RS-232C and CCITT V.24 specifications.
- Supports Bell type 208B, 2096, and 212 data sets or equivalent.
- Supports Siemens MSV2 protocol.
- Works with full or half duplex modems, and supports AUTO ANSWER and ORIGINATE.
- Provides link control functions: line bid, normal and transparent data modes, all responses, and link termination.
- Assures data integrity with CRC error checking.
- EBCDIC character recognition.
- Space compression/truncation.



HP 27122A Remote Job Entry (RJE) Cabling

HP 27123A Shared Resources Manager (SRM)

Features

- Data transmission rate is 700 Kbits/sec.
- Access to the network through rotary polling on an HP 98028A Multiplexer (part of the SRM product).
- All transmissions are broadcast to all connections on the HP 98028A Multiplexer.
- Packets can contain up to 512 data bytes.
- Reception of packets is acknowledged.
- Remote file access to create/open/purge a file/directory, read/write bytes, set protection, and catalog.

Configuration

Ensure that the 8 switches are set to the binary equivalent of the assigned decimal node address. S1 is the MSB; S8 is the LSB.



HP 27123A Shared Resources Manager (SRM) Cabling

HP 27125A Local Area Network Interface Controller (LANIC)

Features

- Implementation of IEEE LAN standards 802.2 type 1 and 802.3.
- Provides LAN connection to HP 9000 Series 500 host computers.
- Operates with baseband networks using Carrier Sense Multiple Access with Collision Detect (CSMA/CD).
- Provides for connection and operation with Ethernet version 1.0.
- 10 Mbps transfer rate.
- 50 metres distance between LANIC and attachment unit.

Configuration

- There are no switches to be set.
- All node hardware must conform to the same standards.
- Remove and throw away jumper W3.
- Record the Link Address-08 00 09 (plus xx yy zz from the NOVRAM).



HP 27125A LANIC Cabling

HP 27128A Asynchronous Serial Interface (ASI)

Features

- Switch selectable and software programmable baud rate; up to 19,200 bits per second.
- EIA RS-232C, CCITT V.24, and CCITT V.28 compatibility.
- Asynchronous transmission in simplex, full duplex, and echoplex mode.
- Programmable format control and built-in framing error, overrun error, and parity checking.
- Break detection, support for X-ON/X-OFF and terminal emulation mode.

Configuration

The normal switch settings are as shown in the next figure.

S1: Single Text Termination;	Up - Single Text Termination. (Typical) Down - Not Single Text Termination.
S2: Hard wired;	Up - Device directly connected. Down - Device Not Directly Connected. (Mod- em use)
S3: No Parity;	Up - No Parity. (Typical) Down - Odd Parity.
S4: Bits Per Character;	Up - 8 Bits Per Character. (Typical) Down - 7 Bits Per Character.
S5 to S8:	Baud Rate: (See table)

ASI Baud Rate Switches

S 5	S6	S7	S8	BAUD Rate
Down	Down	Down	Down	50
Down	Down	Down	Up	75
Down	Down	Up	Down	110
Down	Down	Up	Up	134.5
Down	Up	Down	Down	150
Down	Up	Down	Up	300
Down	Up	Up	Down	600
Down	Up	Up	Up	900
Up	Down	Down	Down	*1200
Up	Down	Down	Up	1800
Up	Down	Up	Down	2400
Up	Down	Up	Up	3600
Up	Up	Down	Down	4800
Up	Up	Down	Up	7200
Up	Up	Up	Down	**9600
Up	Up	Up	Up	19200

* Typical modem

** Typical direct connection application





7-12 9020 Peripherals





HP 27130A/B Asynchronous 8-Channel Multiplex

Features

- CCITT V.28 and EIA RS-232C compatible.
- Supports simplex, echoplex, or full-duplex mode (asynchronous transmission only).
- Selection of data transmission attributes can be performed independently on each channel.
- Local intelligence reduces time consumed by the CPU during I/O transactions by offering edit functions, special character recognition, and handshake protocol control.
- Parity, overrun, and framing errors are sensed locally to detect transmission errors.
- X-ON/X-OFF (both directions) and ENQ/ACK (one direction, host sending ENQ) handshaking



HP 27130A/B Asynchronous 8-Channel Multiplex Cabling

HP 27140A Modem MUX Interface

Features

- Supports up to six EIA RS-232C/CCITT-V.22 compatible devices.
- Consists of interface card, cable, and connection panel.
- Provides control lines and handshaking for asynchronous modems and uucp networking.
- Direct DCE style connection.
- DTE style connection with HP 92219Q cable.

Configuration

There are no switches to set on the HP 27140A card.



HP 27140A Modem MUX Cabling

HP 97062A Color Video

Features

- Medium-resolution interface to 19-inch color monitor.
- Produces RS-343-compatible signals across three coaxial cables.
- Uses four memory planes to display 16 colors from 4096 available.
- Supports all Graphics/9000 plotter commands including area shading.



HP 97062A Color Video Interface Cabling

HP 2885A LAN 9000 Local Area Network

Features

- HP-IB interface to host CPU.
- Coax cable with baseband signaling.
- 10 Mbps data signaling rate.
- Minimum separation between nodes is 2.5 metres.
- Nodes can be up to 40 metres from the coax cable.
- Masterless protocol, Carrier-Sense Multiple Access with Collision Detection (CSMA/CD).
- Up to 500-metre segment coax length and up to 100 nodes per segment.
- Supports broadcast and multicast addressing.
- User-executable diagnostics which can be run simultaneously with other network services.

Configuration

Before you install the HP-IB card, ensure that the resistor pack is installed in socket U74 (normal speed) and that the switches are set in these positions:



Set the bus address of the LAN unit to 0 by setting the switches on the back of the unit to these positions:





HP 2885A LAN 9000 Local Area Network Cabling

9020 Replaceable Parts

Chapter 8

L

Fig. No.	Index No.	New Part Number	Rebuilt Part Number	Description
5-2	6	09855-67980	09855-69980	Power Supply Assembly
5-2	-	09855-66571	09855-69571	Display Interface Module
5-10	1	09855-66531	09855-69531	Keyboard Electronics Board
5-10	3	09855-67131	09855-69131	Keyswitch Board
5-3	5	09855-66552	09855-69552	Printer Motor Drive Board
5-6	8	09855-66561	09855-69561	9130K Controller Card
5-9	1	09130-67600	09130-69600	9130K Flexible Disc Drive
5-6	8	09855-66560	09855-69560	Winchester I/O Card
5-6	8	0950-0886	09855-69886	Winchester Disc Controller
5-8	1	09133-67102	09133-69102	Fixed Disc Mechanical Drive
5-8	2	09133-67101	09133-69101	Fixed Disc Electronics Board
5-11	1	09836-66550	09836-69550	98760A Power Supply Board
5-11	2	09836-66540	09836-69540	98760A Sweep Board
5-11	3	09836-66541	09836-69542	98760A Video Board
5-11	4	98760-66573	98760-69573	98760A Alpha/Interface Board
5-11	5	98760-66575	98760-69575	98760A Graphics/Digital Video Board, ASCII
5-11	5	98760-66576	98760-69576	98760A Graphics/Digital Video Board, Katakana
5-5	2	5061-6803	97043-69803	Floating Point CPU
5-5	2	5061-6806	97044-69806	IOP Finstrate (Rev. 3.1)
5-5	2	5461-4232	97040-69232	256K RAM Board
5-5	2	5061-6805	97047-69805	512K RAM Board
5-5	2	5061-7704	97046-69704	IM RAM Board

Exchange Parts

Non-Exchange Parts

Fig.	Index	Part Number	Description
5-5	3	5061-4224	Processor Stack Clock Board
5-5	4	5061-4263	Processor Stack Motherboard
5-5	2	5061-6806	IOP Board (Rev. 3.1)
5-5	2	5061-4228	IOP Buffer Assembly
5-5	2	97043-69235	CPU Board (5061-4235)
5-5	2	5061-6803	Floating Point CPU
5-5	2	5061-4232	256K RAM Board
5-5	2	5061-6805	512K RAM Board
5-5	2	5061-7704	1M RAM Board
5-6	8	09855-66562	Bootstrap Loader Card
5-3	4	09855-66551	Printer Logic Board
5-3	7	09845-67163	Printhead
5-3	8	09855-66555	Printhead Interconnect Board
5-4	10	09845-67161	Printhead Chip Module
5-2	28	09855-66500	Motherboard
5-6	1	09855-66501	I/O Backplane
5-2	15	3160-0377	Fan
5-11	7	98760-66500	98760A Display Motherboard

Product Support Tools

Part Number	Description
09855-67004	Power Supply Discharge Tool
5040-7433	Keycap Removal Tool
09020-10010	Test Pack
09020-80038	HP 9000 Model 520 Computer Service Manual Package
09815-20602	Paper Spindle Tool

Extender Board

Part Number		Description
27116A	HP-CIO Extender	

Product Support Documentation

Part Number	Description
09020-80038	Service Manual Package, which includes: 09020-90037 Service Manual for the HP 9000 Model 520 09020-90011 Installation and Test for the HP 9000 Model 520 09040-90040 Series 500 Site Preparation Manual 98770-90030 Service Manual for the HP 98770A Display 98780-90030 Service Manual for the HP 98780A Display 5955-6587 Sales and Support Offices 9282-0989 2" 3-Ring Binder 09020-90008 Manual Assembly Instructions 9211-3778 Package Shipping Carton

HP Printer Replacement Paper

Part Number	Description	
9270-0640	English-dimensioned, black print	
9270-0641	English-dimensioned, blue print	
9270-0642	Metric-dimensioned, black print	
9270-0643	Metric-dimensioned, blue print	



Covers

Index Number	HP Part Number	Quantity	Description
1	5041-3449	2	Exterior Door
2	5041-3456	1	Front Cover
	0403-0374	2	 Bumper, Square Self-Adhesive.
3	09855-60371	1	Rear Cover
	2190-0010	6	• Washer, Lock, Ext T.
1	3050-0071	6	• Washer, Flat, MTLC.
	0905-0960	6	• O-Ring.
4	5041-3455	1	Keyboard Bezel
	4040-2047	2	LED Lens.
	0905-0129	2	• O-Ring 0.145-Inch.
	1390-0064	4	Receptacle, Ball Stud.
	5041-3450	1	• Filler Key, Left.
	5041-3451	1	Filler Key, Right.
5	7200-1698	2	Door Trim
	Labels		
	5955-8036	1	FCC Computer Compliance Label
1	7121-3107	1	 Special Function Key Label
	7121-3493	1	Interlock Warning Label

Covers Parts List

Index	HP		
Number	Part Number	Quantity	Description
Miscellaneous Hardy			vare
	0515-0389		Screw, Machine, M3.5.
	0515-0653		Screw, Tapping, 3.6 x 1.34.
í	Compon	ents	
1	09855-67904	1	Complete Casting.
2	0050-2096	1	Partial Casting, Right Front (as seen from front).
3	0050-2095	1	Partial Casting, Rear.
4	0050-2094	1	Partial Casting, Left Front (as seen from front).
5	0403-0106	5	Rubber Foot.
6	09855-69980	1	Power Supply Assembly.
7	1390-0595	4	Captive Screw.
8	4040-2050	1	Power Supply Guide (bottom of power supply bucket).
9	1600-1235	1	Power Supply Bucket.
10	4040-2040	2	Power Supply Side Clamp.
11	0330-0359	1	Long Mounting Foam Support (inside power supply bucket).
12	1600-1216	4	Door Hinge.
13	1600-1217	1	Left Strap.
14	1600-1218	1	Right Strap.
15	3160-0377	3	9.5 VDC Fan.
16	0590-1453	8	Fan Clip.
17	8120-3551	1	Flexible Disc Drive Power Cable.
18	8120-3599	1	Ground Cable.
19	1600-1210	1	Alternate Cursor Control Bracket.
20	1600-1230	1	No Printer Cover.
21	5041-3457	1	Base.
22	8120-3553	1	Fan Cable.
23	8120-3558	1	Alternate Cursor Cable Assembly.
24	8120-3559	< 1 [×]	Keyboard Power Cable
25	8120-3562	1	Keyboard/IOP Bus Cable.
26	1531-0226	1	Roller/Bushing Assembly (under back of base).
27	1480-0051	2	Roller Axle.
28	09855-66500	1	Motherboard.
29	09855-60200	1	I/O Door Assembly.
30	1600-1226	1	Mass Memory Cover.
	98770A	and 98780A	Displays
	0050-2091	1	Casting
1	09855-69571	1	Display Interface Module (DIM).
	1600-1237	1	Lid.
	1531-0231	2	Pivot.
31	09855-67903	1	Display Connector Plate.
32	8120-3556	1	Display Data Cable (Long Cable).
33	8120-3563	1	Display Power Cable (Short Cable).

Chassis Components Parts List







Printer

Printhead

Index Number	HP Part Number	Quantity	Description
<u> </u>	09855-67950		Printer
1	0380-1502	4	PC Board Fastener
$\hat{2}$	0403-0268	2	4.50 inch PC Board Guide
3	0510-1196	1	Slider Clip
4	09855-66551	ī	Logic Board
5	09855-69552	l ī	Motor Drive Board
6	09855-66554	1	Paper Sense Board
7	09845-67163	1	Printhead
8	09855-66555	1	Printhead Interconnect Board
9	1205-0377	2	Printhead Heat Sink
10	09845-67161	7	Printhead Chip Module
11	1251-4819	28	Elastomate Connector, Long
12	1251-4820	7	Elastomate Connector, Short
13	1460-0636	2	Compression Spring
14	1500-0530	1	Drive Belt
15	1530-2025	1	Roller
16	1530-2026	1	Platen
17	1600-1201	4	Bezel Clamp
18	1600-1333	1	Right Side Plate
19	1600-1334	1	Left Side Plate
20	3101-2564	1	Switches Assembly
21	3140-0668	1	Stepper Motor and Connector
22	4040-2057	1	Paper Guide Slider
23	5040-8143	1	Roller Pulley
24	5040-8144	1	Motor Pulley
25	5041-1497	1	Lower Paper Guide
26	5041-1498	1	Tear Bar
27	5060-7463	1	Platen Assembly Pulley
28	8120-3560	1	Paper Sense Cable
29	8120-3564	1	Paper Advance Cable
30	7101-0689	1	Bezel
31	09855-67951	1	Paper Tray Assembly
32	1460-1948	1	Wire Paper Guide
33	4040-2038	2	Wire Guide Hinge
34	0380-1161	2	Hex Standoff
35	1818-1870	1	ROM
36	1600-1264	1	Belt Guard
37	4040-2079	1	Plastic Locator Guide, Right
38	4040-2080	1	Plastic Locator Guide, Left
39	8120-3994	1	Printhead Cable

Printer Parts List



Processor Stack

Index	HP		
Number	Part Number	Quantity	Description
1	97011-64403	1	Processor Cage (Without Cards or Clock)
2	Stack Re	eplacement	Boards
	97043-69235	1	• CPU Board (5061-4235)
	97043-69803	1	 Floating Point CPU (exchange)
	5061-6803	1	 Floating Point CPU (new)
1	97044-69806	1	 IOP Board (Rev. 3.1) (exchange)
	5061-6806	1	 IOP Board (Rev. 3.1) (new)
	5061-4228	1	IOP Buffer Assembly
	5061-4232	1	• 256K RAM Board
	5061-6805	1	 512K RAM Board
ļ	97047-69805	1	• 512K RAM Board (exchange)
	97046-69704	1	 1M RAM Board (exchange)
	5061-7904	1	 1M RAM Board (new)
	09855-66525	1	Load Board
3	5061-4224	1	Clock Board
4	5061-4263	1	Stack Motherboard
1	5041-3463	2	 Motherboard Support
5	1600-1353	1	Stack Door
	5180-5201	1	Label, Door Seal
6	1400-1179	1	IOP Cable Clamp
7	3160-0377	1	Fan
8	1600-1353	1	IOP Connector Plate, Aluminum
	5041-3468	1	IOP Connector Plate, Black
9	3101-2565	1	Processor Interlock Switch
10	8120-3600	1	Processor Interlock Cable
11	4040-2101	1	LED Window
12	5041-3464	2	Card Guide
13	4040-2114	1	Air Controller
14	4040-2115	1	Air Controller Pivot
15	1460-1981	1	Air Controller Spring
16	8120-3806	1	IOP #2 Cable
	8120-3807	1	IOP #3 Cable

Processor Stack Parts List

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I/O Cage

Index	HP	0	Duriniti
Number	Part Number	Quantity	Description
1	09855-66501	1	I/O Backplane.
2	8120-4017	1	I/O Interlock Cable and Switch.
3	0403-0434	1	I/O Front Card Guide.
4	0403-0435	1	I/O Rear Card Guide.
5	7121-3061	1	I/O Card Guide Label (Select Codes).
6	09855-60200	1	I/O Door Assembly.
7	1600-1226	1	Mass Memory Cover.
8*	I/O Card	s and Parts	
	09855-69560	1	Fixed Disc Drive I/O Card.
	8120-3801	1	 2 Pin Controller Interconnect Power Cable.
	8120-3789	1	 20 Pin Controller Interconnect Cable.
	8120-3788	1	 34 Pin Controller Interconnect Cable.
1	8120-3803	1	 50 Pin Controller Interconnect Cable.
	0380-1562	4	 Fixed Disc Controller Standoffs.
	09855-69886	1	Fixed Disc Drive Controller.
	1818-1989	1	 Fixed Disc Drive Program ROM.
1	09855-66562	1	Bootstrap Loader Card.
**	09020-80000	1	 Loader/ID ROM (for Fixed Disc Drive or Bootstrap Loader
			Card).
**	09020-80001	1	Loader/ID ROM (for Fixed Disc Drive or Bootstrap Loader
			Card).
	09855-69561	1	9130K Flexible Disc Drive Controller.
	1818-1897	1	Flexible Disc Controller ROM.

I/O Cage Parts List

* For Interface Card information, see the 520 Installation Manual, 09020-90011, Appendix A.

** When ordering the Loader/ID ROM, the serial number of the computer must be given to the individual taking the order. The serial number is programmed into the replacement ID ROM. Boot Loader ROM 09020-80001 is required for systems using 1 Megabyte RAM boards and UNIX 4.0 or BASIC 2.0. The Boot Loader ROM can be used with other RAM configurations if UNIX 4.0 or BASIC 2.0 is used. Boot Loader ROM can be used with other RAM configurations if UNIX 4.0 or BASIC 2.0 is used. Boot Loader ROM can be used with other RAM configurations if UNIX 4.0 or BASIC 2.0 is used. Boot Loader BOW can be used with other RAM configurations if UNIX 4.0 or BASIC 2.0 is used. Boot Loader BOW can be used with other RAM configurations if UNIX 4.0 or BASIC 2.0 is a 1 Megabyte board. The above part numbers are replacement part numbers. The ROM swill be labelled 1818-3467 (Rev. A) or 1818-3461 (Rev. B).



AC Module

AC Module I	Parts List
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Index Number	HP Part Number	Quantity	Description
1	09855-67901	1	110V Ac Module.
	09855-67902	1	220V Ac Module.
2	09855-67101	1	110V Ac Power Switch.
	09855-67102	1	220V Ac Power Switch.
3	8120-3554	1	110V Ac Module Cable.
	8120-3580	1	220V Ac Module Cable.
4	4040-2054	1	Switch Bezel.
5	9135-0163	1	Line Filter.
6	4040-2048	1	Ac Wiring Shield.
	7120-6157	1	Ac Danger Label.
	7121-3060	1	90-125V Line Voltage Label.
	7121-3062	1	220V Line Voltage Label.
	7121-2708	1	High Voltage Label.
7	Ac Powe	r Cords	
	09855-61600	1	United States. 110V.
	09855-61601	1	Australia.
	09855-61602	1	Europe.
	09855-61603	1	CSA and United States. 220V.
	09855-61604	1	Switzerland.
	09855-61605	1	Great Britain.
	09855-61606	1	Denmark.



Winchester Fixed Disc Drive

winchester i neu Disc Drive i arts List	Winchester	Fixed	Disc	Drive	Parts	List
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Index Number	HP Part Number	Quantity	Description
1	09133-69102	1	Fixed Disc Mechanical Drive.
2	09133-69101	1	Electronics Board.
3	1600-1306	2	Mounting Bracket.
4	1600-1311	1	Bottom Cover.
5	8120-3598	1	Ground Cable.
6	8120-3800	1	Drive Power Cable.
7	8120-3786	1	20 Pin Drive Cable.
8	8120-3802	1	34 Pin Drive Cable.



Flexible Disc Drive

	F	lexible	Disc	Drive	Parts	List
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Index Number	HP Part Number	Quantity	Description
1	09130-69600	1	9130K Flexible Disc Mechanical Drive.
2	09130-66501	1	Electronics Board.
*	09131-69600	1	9131G Flexible Disc Drive (with board).
3	1390-0596	3	Captive Screw.
4	1600-1212	1	Bucket Assembly.
5	1600-1228	1	Bucket Cover.
6	8120-3598	1	Ground Cable.
7	4040-2056	1	Appearance Bezel.
8	8120-3552	1	Power Cable.
9	8120-3555	1	Logic Cable.

 \ast 9020 serial number prefix 2422 uses this disc drive. The electronics board cannot be ordered separately.

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Keyboard

Keyboard Parts List

Index Number	HP Part Number	Quantity	Description
	09855-67930	1	Keyboard (complete)
1	09855-69531	1	Keyboard Electronics Board
2	1420-0302	1	4.8V RTC/NVM Battery Assembly.
3	09855-69131	1	Keyswitch Board.
4	1820-2707	1	Processor/ROM (8048 - Bottom of board).
5	3101-2595	_	Keyswitch, Micro.
	3101-2596		Keyswitch, Cortron.
6	1390-0448	4	Snap-in Fastener.
7	1440-0160	2	Pull Handle.
8	1600-1234	1	Structural Strip, Front.
9	1600-1236	1	Structural Strip, Rear.
10	1390-0448	4	Ball Stud.
11	3131-0494	2	Plastic Clip - Spacebar

Keycap Part Numbers

The last four digits of the keycap part number are provided for each keycap. The first four digits are always **0371**. For example, the "J" keycap is identified on the ASCII keyboard as 2632. To order the "J" keycap, order part number 0371-2632.

All keycap configurations except ASCII have some keycaps highlighted. The highlighted keycaps differ from the keycaps on the ASCII keyboard. The ASCII keyboard is the exchange assembly in case of keyboard failure. Before returning a failed non-ASCII keyboard, remove all non-ASCII keycaps from the failed board and place them on the replacement keyboard.



ASCII Keyboard



French Keyboard



Spanish Keyboard



German Keyboard



Katakana Keyboard



Swedish/Finnish Keyboard

8-20 9020 Replaceable Parts



Computer Block Diagram



9020 Diagrams

9020 Diagrams 9-1

Chapter

0

9-2 9020 Diagrams



Computer Interconnection Diagram



Power Supply Assembly Block Diagram



Power Distribution Diagram



Voltage Sensing Diagram


Safety Grounding Diagram



Processor Stack Block Diagram



256K RAM Finstrate Block Diagram

		Y ADDRESS								
MAPPER CAM	CS	SELECTED		X AND	Y	ADDRESS S	ELEC	CTS WORD		
REGISTER	SELECTED	BLOCK			WIT	HIN THE B	LOC	ĸ		
ADDRESS	ROW	NUMBER								
0	3	0	4K WORDS	4K BYTES		4K BYTES		4K BYTES		4K BYTES
	3	1	4K WORDS	4K BYTES		4K BYTES		4K BYTES		4K BYTES
2	3	2	4K WORDS	4K BYTES		4K BYTES		4K BYTES		4K BYTES
3	3	3	4K WORDS	4K BYTES		4K BYTES		4K BYTES		4K BYTES
4			l (
5			NOT							
6			USED							
7			<u> </u>	[
8		0	4K WORDS				_		_	
9	2	1	4K WORDS	16K BYTE		16K BYTE	_	16K BYTE	_	16K BYTE
10	-	2	4K WORDS	RAM		RAM	_	RAM	_	RAM
11		3	4K WORDS							
12) (
13			NOT J							
14			USED							
15			<u> </u>							
16		0	4K WORDS							
17	1	1	4K WORDS	16K BYTE		16K BYTE		16K BYTE	_	16K BYTE
18	'	2	4K WORDS	RAM		RAM		RAM		RAM
19		3	4K WORDS				_			
20			l r							
21			NOT							
22			USED							
23			<u> </u>	<u> </u>						
24		0	4K WORDS				_			
25	0	1	4K WORDS	16K BYTE		16K BYTE	_	16K BYTE	_	16K BYTE
26	Ū	2	4K WORDS	RAM	<u> </u>	RAM		RAM		RAM
27		3	4K WORDS							
28			<u> </u>							
29			NOT							
30			USED		_					
31			<u> </u>							

256K Memory Mapping Organization

9-10 9020 Diagrams



256K Memory Mapping Operation



X=DON'T CARE

256K Memory Healing



512K RAM Board Block Diagram

		Y ADDRESS								
MAPPER CAM	CS	SELECTED	X AND Y ADDRESS SELECTS WORD							
REGISTER	SELECTED	BLOCK	WITHIN THE BLOCK							
ADDRESS	ROW	NUMBER								
0	7	0	4K WORDS	4K BYTES		4K BYTES	1	4K BYTES		4K BYTES
1	7	1	4K WORDS	4K BYTES		4K BYTES		4K BYTES	_	4K BYTES
2	7	2	4K WORDS	4K BYTES		4K BYTES		4K BYTES	_	4K BYTES
3	7	3	4K WORDS	4K BYTES		4K BYTES		4K BYTES		4K BYTES
4		0	4K WORDS							
5		1	4K WORDS	16K BYTE		16K BYTE		16K BYTE		16K BYTE
6	0	2	4K WORDS	RAM		RAM		RAM		RAM
7		3	4K WORDS		_		-			
8		0	4K WORDS				_			
9		1	4K WORDS	16K BYTE		16K BYTE	_	16K BYTE		16K BYTE
10	Э	2	4K WORDS	RAM	_	RAM	_	RAM		RAM
11		3	4K WORDS						_	
12		0	4K WORDS							
13		1	4K WORDS	16K BYTE		16K BYTE		16K BYTE	_	16K BYTE
14	+	2	4K WORDS	RAM	_	RAM	_	RAM		RAM
15		3	4K WORDS				<u> </u>		_	
16		0	4K WORDS							
17		1	4K WORDS	16K BYTE	_	16K BYTE	_	16K BYTE	_	16K BYTE
18	3	2	4K WORDS	RAM		RAM		RAM		RAM
19		3	4K WORDS				<u> </u>			
20		0	4K WORDS				_		_	
21	•	1	4K WORDS	16K BYTE		16K BYTE	_	16K BYTE	_	16K BYTE
22	2	2	4K WORDS	RAM		RAM		RAM		RAM
23		3	4K WORDS		_		_			
24		0	4K WORDS				_		_	
25		1	4K WORDS	16K BYTE	_	16K BYTE	_	16K BYTE	—	16K BYTE
26	1	2	4K WORDS	RAM		RAM	_	RAM		RAM
27		3	4K WORDS		_				_	
28		0	4K WORDS				_			
29		1	4K WORDS	16K BYTE		16K BYTE		16K BYTE	_	16K BYTE
30	U	2	4K WORDS	RAM	-	RAM		RAM	_	RAM
31		3	4K WORDS							

512K Memory Mapping Organization







X-DON'T CARE

512K Memory Healing



RAM CHIPS ARE 256K X 1 BIT





1 Megabyte Memory Mapping Operation



1 Megabyte Memory Healing







Alpha/Interface Board Block Diagram (9020A Only)



Graphics/Digital Video Board Block Diagram (9020A Only)



Flexible Disc Drive Controller Card Block Diagram





Winchester Fixed Disc Drive I/O Card Block Diagram



Bootstrap Loader Card Block Diagram

Designator	Connecting Assembly				
J40	I/O Backplane				
J42	Power Supply				
J43	Power Supply				
J44	Power Supply				
J45	Power Supply				
J46	Power Supply				
J47	Power Supply				
J48	Processor Stack				
J49	Processor Stack				
J50	IOP #1, Printer, Keyboard, J40				
J51	Printer, Keyboard				
J52	Three Fans				
J53	Mass Storage Device				
.154	Mass Storage Device				

Motherboard Connectors



Motherboard Connectors Locator Drawing (Sheet 1 of 2)



Motherboard Connectors Locator Drawing (Sheet 2 of 2)

Motherboard Sign	al Definitions
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Signal	Definition
(KEY)	Keyed hole in connector plug.
-12	- 12 volt supply.
- 19	– 19 volt supply.
-2	 2 volt supply for processor stack back gate.
12	12 volt supply.
12MM	12 volt supply to mass storage devices only.
16	16 volt bias supply voltage.
10	Ground for bias supply.
2.95	19 Voit supply.
5.00	5.65 volt supply.
56	5 volt supply.
62	6 2 volt supply
CS - 12	Control sense for -12 volt supply
CS-2	Control sense for -2 volt supply
CS-2G	Control sense for -2 volt supply ground
CS12	Control sense for 12 volt supply.
CS3.85	Control sense for 3.85 volt supply.
CS5	Control sense for 5 volt supply.
CS5G	Control sense for 5 volt supply ground.
CS6.2	Control sense for 6.2 volt supply.
DGND	Dirty ground return.
FANCTL	Fan control. Connected to PST.
FANF	Power to processor stack fan (negative voltage).
FANGND	Fan ground return.
FANI	Power to I/O card cage fan (negative voltage).
FANP	Power to power supply fan (negative voltage).
GND	Ground plane of motherboard.
GUARDGND	Shields power supply supervisor board from primary and secondary boards.
LOLINE MS 19	Low line indication.
MS = 12 MS 10	Monitor sense for - 12 voit under/over voltage.
MS - 19 MS - 2	Monitor sense for - 19 volt under/over voltage.
MS12	Monitor sense for 12 volt under/over voltage.
MS12 MS19	Monitor sense for 19 volt under/over voltage
MS3 85	Monitor sense for 3.85 volt under/over voltage
MS5	Monitor sense for 5 volt under/over voltage
MS6.2	Monitor sense for 6.2 volt under/over voltage.
MSG	Monitor sense ground.
N – 2UNDER	-2 volt undervoltage (negative true).
NBR	I/O bus burst mode DMA request (negative true).
NDEND	I/O bus device end (negative true).
NDO	Door open (negative true). Wired OR of NFDO and NIDO.
NDOORLED	Door open (negative true).
NFANHI	Power supply fan at highest speed (negative true).
NFDO	Processor stack door open (negative true). OR'd with NIDO.
NFLG	I/O bus ready for data (negative true).
NIC1	I/O bus interface control bit 1 (negative true).
NIC2	I/O bus interface control bit 2 (negative true).
NIC3	I/O bus interface control bit 3 (negative true).
NIC4	I/O bus interface control bit 4 (negative true).
NIDO	I/O cage door open (negative true). OR'd with NFDO.
NICDO	I/O bus interface clear (negative true).
NIODI	1/O bus input/output data bit 0 (negative true).
NIOD2	100 bus input/output data bit 2 (negative true).
NIOD3	10 bus input/output data bit 3 (negative true).
NIOD4	I/O bus input/output data bit 4 (negative true)
	no ous inputourput uata on 4 (negative title).

L

Motherboard Signal	Definitions	(Continued)
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Signal	Definition
NIOD5	VO bus input/output data bit 5 (negative true).
NIOD6	I/O bus input/output data bit 6 (negative true).
NIOD7	I/O bus input/output data bit 7 (negative true).
NIOD8	I/O bus input/output data bit 8 (negative true).
NIOD9	I/O bus input/output data bit 9 (negative true).
NIOD10	I/O bus input/output data bit 10 (negative true).
NIOD11	I/O bus input/output data bit 11 (negative true).
NIOD12	I/O bus input/output data bit 12 (negative true).
NIOD13	I/O bus input/output data bit 13 (negative true).
NIOD14	I/O bus input/output data bit 14 (negative true).
NIOD15	I/O bus input/output data bit 15 (negative true).
NIOSB	I/O bus data transfer strobe (negative true).
NMI	Non-maskable interrupt.
NPA0	I/O bus peripheral address bit 0 (negative true).
NPA1	I/O bus peripheral address bit 1 (negative true).
NPA2	I/O bus peripheral address bit 2 (negative true).
NPFW	Power fail warning (negative true).
NPI	Not pop in (negative true). Resets the stack.
NPKPWR	Shut down command indicator due to peak power (negative true).
NPOLL	I/O bus interface poll (negative true).
NPSILED	Power supply overtemperature (negative true).
NEEL ETEST	I had power valid; all outputs in spec (negative true).
NSELF1ES1	(nogative true) Originates on keyboard
NSTLED	(negative true). Originales on keyboard.
NSTS	VΩ bus status (negative true). VΩ bus status (negative true). Driven bu keuboard
NSYSPII	Not sustem non unsurchronized (negative true). Used with NPI to cause stack to
101510	not system pop unsynchronized (negative inde). Osed with the r to cause stack to
NWAIT	I/O bus lengthen IOSB (negative true)
PFLG	Printer's flag line: driven by keyboard
PPON	Primary power on: all outputs in spec.
PSDGND	Power supply ground for 25 KHz and fan circuitry.
PST	Power supply temperature indicator; connected to FANCTL.
PSTS	Printer's status line.
PWMEN	Pulse width modulator enable.
READ	I/O bus data direction (positive true; high indicates data to IOP).
SAC +	25 KHz ac sine wave from power supply.
SAC -	25 KHz ac sine wave from power supply.
SECGND	Ground on power supply secondary board.
SIGGND	Ground for logic on supervisor board.
SPARE	Spare line; no connection.
ST1	Processor stack self-test from slot 1.
ST2	Processor stack self-test from slot 2.
513	Processor stack self-test from slot 3.
	Processor stack self-test from slot 4.
SI5	Processor stack self-test from slot 5.
510	Processor stack self-test from slot 6.
517 6T0	Processor stack self-test from slot 7.
518 STO	Processor stack self-test from slot 8.
ST10	Processor stack self-lest from slot 7.
ST11	Processor stack self-test from slot 11.
ST12	Processor stack self-test from slot 12
STACKTEMP	Processor stack temperature indicator
TEST81A	Test noint
TEST81B	Test point
TEST81C	Test point
TEST83A	Test point.
TON	5 volts from IOP.

Designator	Connecting Assembly
P1	Display Interface Module (Power)
P2	Display Interface Module (IOP)
P3	I/O Card
P4	I/O Card
P5	I/O Card
P6	I/O Card
P7	Reserved
P8	Reserved
P9	Reserved
P10	Mass Storage Controller Card
P11	Mass Storage Controller Card
J12	Motherboard
P13	Reserved
P14	I/O Cage Door Switch

I/O Backplane Connectors





I/O Backplane Connectors Locator Drawing (Sheet 1 of 3)

P5 TO I/O CARD SLOTS

	_	A 6	
	5-	40 4	
	5	39 3	9 L 5
	12	38 3	8 - 12
2	- 12	37 3	7 12
-	AC +	36 3	6 - AC +
-	AC	35 3	5 AC -
)	GND —	34 3	4 GND
N	PPON	33 3	3 PPON
A I	NPFW —	32 3	2 - NNMI
USED	NOT USED -	31 3	1 NOT USED
USED	NSW0 -	30 3	0 NOT USED
V1	NSW0 -	29 2	9 - NSW1
S	NDPA	28 2	8 - NSTS
)	GND —	27 2	7 GND
0	NMYPA —	26 2	6 NIFC
ND	NDBYT -	25 2	5 NDEND
10	NBR —	24 2	4 NARQ
)	GND	23 2	3 — GND
SB	CCLK —	22 2	2 – NIOSB
)	GND -	21 2	1 GND
DLL	SYNC -	20 2	0 - NPOLL
3	NIC2 —	19 1	9 — NIC3
4	NIC1	18 1	8 – NIC4
D	READ —	17 1	7 – NUAD
)	GND	16 1	6 – GND
1	NPA0 —	15 1	5 NPA1
3	NPA2	14 1	4 - NPA3
)	GND —	13 1	3 — GND
D1	NIOD0 -	12 1.	2 NIOD1
D3	NIOD2	11 1	
)	GND —	10 1	0 GND
D5	NIOD4 —	9	9 NIOD5
D7	NIOD6 -	8	
)	GND —	7	7 - GND
D9	NIOD8 -	6	6 NIOD9
D11	NIOD10	5	5 NIOD11
)	GND -	4	
013	NIOD12 -	3.	3 NIOD13
D15	NIOD14 -	2	
10	SGND -	1	- SGND

RESERVED							
	•	•	L				
GND	30	30					
	20	20					
DBIN	28	28	DBOUT				
GND -	27	27	GND				
DATA6 -	26	26	DATAZ				
DATA4 -	25	25	-DATA5				
DATA2	24	24	DATA3				
DATA0 -	23	23	DATA1				
GND -	22	22	GND				
CSB6 —	21	21	CSB7				
CSB4	20	20	CSB5				
CSB2 -	19	19	-CSB3				
CSB0	18	18	-CSB1				
GND -	17	17	- GND				
NOT USED	16	16	NOT USED				
CSA4 —	15	15	- SELO				
CSA2 -	14	14	-CSA3				
CSA0	13	13	-CSA1				
GND —	12	12	- GND				
CSRS -	11	11	-csws				
INTO	10	10	INT1				
NOT USED —	9	9	-NOT USED				
GND —	8	8	— GND				
RESET -	7	7	CLK				
GND —	6	6	GND				
12 —	5	5	- 12				
12	4	4	- 12				
- 12	3	3	- 12				
5	2	2	-5				
5 —	1	1	<u>⊢</u> ₅				

P8

	I/O Backplane Connectors Locator Drawing (Sheet 2 of 3)
1	- SGND
2	NIOD15
3	

P3			
TO I/O CARD SLOTS			
			1
_	A .	в	-
5	40	40	-5
5 —	39	39	-5
12	38	38	- 12
- 12	37	37	- 12
AC +	36	36	AC+
AC	35	35	AC -
GND —	34	34	GND
PPON —	33	33	- PPON
NPFW	32	32	- NNMI
NOT USED -	31	31	-NOT USED
NSW2 —	30	30	-NOT USED
NSW0 —	29	29	-NSW0
NDPA —	28	28	-NSTS
GND —	27	27	GND
NMYPA —	26	26	NIFC
NDBYT	25	25	- NDEND
NBR —	24	24	- NARQ
GND —	23	23	GND
CCLK —	22	22	- NIOSB
GND —	21	21	GND
SYNC -	20	20	- NPOLL
NIC2 —	19	19	- NIC3
NIC1	18	18	- NIC4
READ —	17	17	- NUAD
GND —	16	16	— GND
NPA0	15	15	-NPA1
NPA2 —	14	14	- NPA3
GND -	13	13	- GND
NIOD0 -	12	12	-NIOD1
NIOD2 -	11	11	- NIOD3
GND	10	10	- GND
NIOD4 —	9	9	- NIOD5
NIOD6 —	8	8	-NIOD7
GND	7	7	GND
NIOD8 —	6	6	-NIOD9
NIOD10 -	5	5	-NIOD11
GND —	4	4	GND
NIOD12 -	3	3	-NIOD13
NIOD14 -	2	2	NIOD15
SGND —	1	1	SGND

P6 TO I/O CARD SLOTS

		_	
	A	в	_
5	40	40	-5
5 —	39	39	- 5
12	38	38	- 12
- 12	37	37	- 12
AC +	36	36	AC +
AC	35	35	— AC –
GND —	34	34	- GND
PPON -	33	33	- PPON
NPFW	32	32	- NNMI
NOT USED -	31	31	- NOT USED
NSW2 -	30	30	-NOT USED
NSW2 —	29	29	-NSW2
NDPA —	28	28	- NSTS
GND	27	27	- GND
NMYPA —	26	26	NIFC
NDBYT	25	25	- NDEND
NBR —	24	24	- NARQ
GND —	23	23	GND
CCLK	22	22	- NIOSB
GND —	21	21	— GND
SYNC -	20	20	- NPOLL
NIC2 -	19	19	- NIC3
NIC1 —	18	18	-NIC4
READ	17	17	- NUAD
GND	16	16	- GND
NPA0 —	15	15	-NPA1
NPA2 —	14	14	- NPA3
GND —	13	13	— GND
NIOD0 -	12	12	- NIOD1
NIOD2 -	11	11	- NIOD3
GND -	10	10	— GND
NIOD4	9	9	- NIOD5
NIOD6 —	8	8	- NIOD7
GND -	7	7	— GND
NIOD8 —	6	6	- NIOD9
NIOD10 -	5	5	NIOD11
GND -	4	4	— GND
NIOD12	з	3	— NIOD13
NIOD14 -	2	2	- NIOD15
SGND -	1	1	SGND

P	'9
RESE	RVED
A GND 30 DSIN 29 DRIN 28 GND 27 DATA6 26 DATA4 25 DATA2 24 DATA2 23 GND 22 CS66 21 CS66 21 CS86 11 CS84 15 CS44 15 CS44 15 CS82 14 CS44 15 CS42 14 CS44 15 CS42 14 CS44 15 CS42 14 CS44 15 CS42 19 GND 17 CS67 11 INT 10 NOT USED 9 6 12 14 A 12 4 -12 3 CS 2 5 1 12	B
J	12
TO MOTH	IERBOARD
CONNEC	CTOR J40
- IB 1 - IB 1 1 - ID AD 1 1 - ID AD 1 1 - ID ID 1 1 - ID ID 1 1 - ID ID ID 1 - ID ID ID ID AC	- - - 18 2 - 18 - 18 3 - 18 - 18 4 - 18 - 16 6 DGND 8 - 5 10 - 5 11 - 5 12 - 5 11 - 5 14 - 5 16 12 17 - 12 18 - 12 18 - AC - 23 NIFC 23 NIFC 23 NIFC 24 - GND 25 NNDEND 27 NWAIT 28 - NICS 33 29 - NIC3 33 = GND 33 = GND 33 = NIC4 35 = NIC01 33 = GND 34 = NIC5 34 = NIC4 35 =

	P10	2	
TO MA	ASS S	TO	RAGE
CONTROL	LER (CAF	RD SLOTS
			1
_	A	в	-
5 —	35	35	-5
NOT USED -	34	34	- NOT USED
12	33	33	- 12
~ 12 —	32	32	- 12
AC +	31	31	AC +
AC	30	30	AC -
GND —	29	29	GND
PPON	28	28	- NOT USED
NPFW —	27	27	- NNMI
NOT USED -	26	26	- NOT USED
NOT USED	25	25	- NOT USED
NIFC —	24	24	GND
NDPA	23	23	-NSTS
NFLG -	22	22	- NDEND
NBR —	21	21	NWAIT
GND —	20	20	GND
GND —	19	19	NIOSB
GND —	18	18	GND
READ —	17	17	- NPOLL
NIC2 -	16	16	- NIC3
NIC1 -	15	15	-NIC4
NPA0	14	14	NPA1
NPA2 —	13	13	- PA3
GND —	12	12	GND
NIOD0 -	11	11	- NIOD1
NIOD2 —	10	10	- NIOD3
GND —	9	9	GND
NIOD4 —	8	8	- NIOD5
NIOD6 —	7	7	- NIOD7
GND —	6	6	GND
NIOD8 —	5	5	- NIOD9
NIOD10	4	4	-NIOD11
GND —	3	3	GND
NIOD12 -	2	2	-NIOD13
NIOD14	1	1	NIOD15
	P1	3	
в	ESEE		D.
п	LUCF		
GND -	50	49	

TO M/ CONTROL	P1 ASS S LER	1 510 CAI	RAGE RD SLOTS
5 NOT USED - - 12 AC GND PPON - NOT USED - NOT USED - NOT USED - NFLG	A 35 34 33 32 33 32 28 27 22 22 22 22 22 22 22 22 22	B 55 4 3 3 3 3 2 2 7 2 6 5 4 3 2 2 1 9 1 8 7 6 5 4 3 2 1	

P13 RESERVED GND 50 49 DSOUT DSIN 48 47 GND 44 43 DATA5 DATA4 40 39 GND 34 44 43 DATA5 DATA4 40 39 GND 34 43 CSB5 DATA0 DATA3 38 37 DATA4 40 39 GND 34 43 CSB5 DATA0 GND 34 33 CSB7 CSB6 32 31 CSB5 DATA0 GND 34 33 CSB7 CSB6 32 SEL0 22 21 CSA1 CSA2 19 GND 24 23 NOT USED SEL0 22 21 CSA1 CSA3 10 SEL0 22 21 CSA1 CSA5 INT 12 11 INTO USED RESET 4 3 GND 10 PNOT USED RESET 4 3 GND 12 CSB6 3 RESET 4 3 GND 12 CSB6 3

Signal	Definition
- 12	- 12 volt supply.
- 18	- 18 volt supply.
12	12 volt supply.
18	18 volt supply.
5	5 volt supply.
AC –	25 KHz ac sine wave from power supply.
AC+	25 KHz ac sine wave from power supply.
CLK	Clock.
CCLK	Common clock.
CSAU	Reserved
CSA1 CSA2	Reserved.
CSA2 CSA3	Reserved
CSA4	Reserved
CSB0	Reserved
CSB1	Reserved
CSB2	Reserved
CSB3	Reserved.
CSB4	Reserved.
CSB5	Reserved.
CSB6	Reserved.
CSB7	Reserved.
CSRS	Reserved.
CSWS	Reserved.
DATAO	Reserved.
DATA	Keserved.
DATA2	Reserved.
DATAA	Reserved.
DATA5	Reserved
DATA6	Reserved
DATA7	Reserved
DGND	Dirty ground return.
DRIN	Reserved.
DROUT	Reserved.
DSIN	Reserved.
DSOUT	Reserved.
GND	Ground plane of I/O backplane.
INTO	Reserved.
INTI	Reserved.
NAKQ	HP-IO card requests attention (negative true).
NDRVT	I/O bus burst mode DMA request (negative true).
NDEND	IO bus feady for data (negative true).
NDPA	Internal select code available (negative true)
NFLG	I/O bus ready for data (negative true).
NIC1	I/O bus interface control bit 1 (negative true).
NIC2	I/O bus interface control bit 2 (negative true).
NIC3	I/O bus interface control bit 3 (negative true).
NIC4	I/O bus interface control bit 4 (negative true).
NIDO	I/O cage door open (negative true).
NIFC	I/O bus interface clear (negative true).
NIODO	I/O bus input/output data bit 0 (negative true).
NIOD1	I/O bus input/output data bit 1 (negative true).
NIOD2	1/O bus input/output data bit 2 (negative true).
NIOD3	1/O bus input/output data bit 3 (negative true).
	VO bus input/output data bit 4 (negative true).
INIOD5	I/O bus input/output data bit 5 (negative true).

I/O Backplane Signal Definitions

Signal	Definition
NIOD6	I/O bus input/output data bit 6 (negative true).
NIOD7	I/O bus input/output data bit 7 (negative true).
NIOD8	I/O bus input/output data bit 8 (negative true).
NIOD9	I/O bus input/output data bit 9 (negative true).
NIOD10	I/O bus input/output data bit 10 (negative true).
NIOD11	I/O bus input/output data bit 11 (negative true).
NIOD12	I/O bus input/output data bit 12 (negative true).
NIOD13	I/O bus input/output data bit 13 (negative true).
NIOD14	I/O bus input/output data bit 14 (negative true).
NIOD15	I/O bus input/output data bit 15 (negative true).
NIOSB	I/O bus data transfer strobe (negative true).
NMYPA	HP-IO card recognized its address has been asserted (negative true).
NNMI	Non-maskable interrupt (negative true).
NPA0	I/O bus peripheral address bit 0 (negative true).
NPA1	I/O bus peripheral address bit 1 (negative true).
NPA2	1/O bus peripheral address bit 2 (negative true).
NPA3	I/O bus peripheral address bit 3 (negative true).
NPFW	Power fail warning (negative true).
NPOLL	I/O bus interface poll (negative true).
NSTS	I/O bus status (negative true).
NSW0	Select code switch 0 (negative true).
NSW1	Select code switch 1 (negative true).
NSW2	Select code switch 2 (negative true).
NUAD	Reserved.
NWAIT	I/O bus lengthen IOSB (negative true).
PA3	I/O bus peripheral address bit 3.
PPON	Primary power on: all outputs in spec.
READ	I/O bus data direction (positive true; high indicates data to IOP).
RESET	Reserved.
SELO	Reserved.
SGND	Safety ground.
SYNC	Synchronize.

I/O Backplane Signal Definitions (Continued)

9020 References

Chapter 10

BASIC Language Error Messages

1	Missing OPTION or configuration error	46	No binary to STORE BIN or no program to
2	Memory overflow		STORE or SAVE
3	Line not found or not in current program	47	COM declarations are inconsistent or incor- rect
4	Improper return	48	Direct recursion not allowed in a single line
5	Improper context terminator	10	function
6	Improper COR/NEXT matching	50	File number <1 or >10
7	Undefined function or subrouting	51	File not currently assigned
8	Improper parameter matching	52	Improper mass storage unit specifier had
Q I	Improper pumber of parameters	02	subaddress specified or bad driver name
10	String value required	53	Improper file name
11	Numeric value required	54	Duplicato filo namo
12	Attempt to redeclare variable	55	Directory overflow
12	Array dimensions not specified	56	File name is undefined
14	Multiple OPTION BASE statements or	57	SDF support missing
14	OPTION BASE after declaration	58	Improper file tupe
15	Invalid string or arrest hounds	50	Physical or logical End Of File/PLIEFER
15	Dimensiona are improved an inconsistent	39	found
10	Subscript out of reason	60	Dunia Dhusiaal on logical End of Descend found in
10	Subscript out of range	00	Physical of logical End of Record Jound in
10	Substring out of range or improper string	61	Defined around size is to a small foundate item
10	length	61	Defined record size is too small for data item
19	Improper value	02	File is protected, wrong PRUTECT code
20	INTEGER overflow	()	specified or PRUTECT not allowed
21	SHOKI overflow	03	Invalid record size
22	REAL overflow	64	Medium overflow – out of user storage
23	DOUBLE overflow	~	space, possibly due to tragmentation
24	SIN, COS, IAN argument too large for accu-	65	Incorrect data type
	rate evaluation	66	INITIALIZE failed – excessive bad tracks, or
25	Magnitude of ASN or ACS argument is >1	<i>(</i> -	can't spare dynamically
26	Zero to non-positive power	67	Mass storage parameter is incorrect
27	Negative base to non-integral power	68	Invalid line number or line did not parse dur-
28	LOG or LGT of non-positive number		ing GET
29	Illegal floating point number	69	Format switch on drive is off
30	Negative argument to SQR	73	Incorrect device type in mass storage unit
31	Division by zero, X MOD Y, or X MODULO		specifier
_	Y with $Y = 0$	77	File open on PURGE
32	String does not represent a valid number	78	Invalid volume label
33	Improper argument for NUM or RPT\$	79	File open on target device
34	Referenced line is not IMAGE	80	Door open, medium not in drive, medium
36	Out of DATA items		changed, or printer out of paper
37	EDIT string longer than 160 characters	81	Device/Interface hardware failure
39	Multi-line function not allowed here	82	Device/Interface not present
40	Improper COPYLINES, MOVELINES, DEL	83	Write protected
	or REN	84	Record not found; medium possibly unin-
41	First line number > second		itialized
42	Attempt to replace, modify or delete a busy	85	Mass storage medium is not initialized
	line or subprogram	86	Incorrect mass storage medium
43	Matrix not square	88	Read data error
44	Illegal operand in matrix transpose or matrix	89	Checkread error
	multiply	90	Mass storage system error

BASIC Language Error Messages (Continued)

91	Negative length field in BDAT unformatted	222
00	string	225
92	I YP not defined for this file type or device	226
100	sponding IMAGE is numeric	220
101	Item in USING list is numeric but the corre-	227
	sponding IMAGE is string	
102	Numeric field specifier is too large	229
103	Item in USING list has no corresponding	230
	IMAGE	231
108	Image is too long and/or complex	232
117	Too many nested structured statements	000
120	DEL SUB of missing or busy subprogram	233
136	REAL underflow	236
137	SHORT underflow	200
141	Variable already ALLOCATED or not allo-	244
142	Variable not ALL OCATED	247
143	Attempt to reference a missing OPTIONAL	248
	parameter	306
145	Too many COM blocks, or COM blocks are	315
	interleaved	316
150	Improper device specifier or select code	326
152	Parity error	327
155	Insufficient data to satisfy EINTER	330
157	No ENTER terminator found within 256	332
	characters of satisfying input	333
158	Improper IMAGE specifier	
159	Numeric data not received for numeric item	334
163	Driver or interface not present	
164	Illegal BYTE/WORD option	335
100	IMAGE specifier has count > size of variable	337
167	Interface status error	338
168	Device timeout occurred and ON TIMEOUT	340
	branch could not be taken	
170	I/O operation not allowed, or HP-IB improp-	341
	erly addressed	342
171	I/O error – illegal addressing sequence	949
172	I/O device or peripheral error	343
175	trol of the HP IB	J44
174	Concurrent I/O operation not allowed on ob-	345
175	ject – nested 1/O	247
175	Undefined I/O path name	347
208	Volume not mounted	353
209	SDF directory format required	
210	Bad status array	401
211	Improper data base specified	
212	Data set not found	402
213	Data base directory not found	402
214	Data base not created Operation left at least one data set commit	403
216	Maximum number of data bases already	404
	open	405
217	Data base definition incomplete	
220	Improper or illegal use of maintenance word	406
221	Data set not created	

Data base directory not created
Root file not compatible with current version
of IMAGE
Corrupt root file – must purge and redefine
Corrupt data base – some sets require era-
sure
Data base in use
Improper set list or duplicate sets in the set list
Improper record count specified
Root file cannot be purged until all data sets
have been purged
Root file not found
Referenced line not a PACKFMT statement
tion
Device was busy and could not handle re-
quest
Tape runaway - no data found on medium
Beginning/End of tape
Interface card failed self test
Missing clock from multiplexor pod
Link is down, clear to send false too long
Register address error
Register value error
Improper pointer array
Non-existent dimension specified
Pointer array contains out-of-range subscript
value
Pointer array length does not equal the num-
ber of records in the reorder dimension
Pointer array is not one-dimensional
Substring specifier extends beyond dimen-
sioned maximum length
Mode table too long or case table indicator is
improper
Improper mode indicator
Lexical table is not one dimensional or is not
of type INTEGER
Lexical mode section pointer is out of range
1 for 2 replacement list is either empty or too
long
Data type of expression in CASE does not
Improper matching of structured program.
ming construct
Remote node does not respond, data link
failure
Improper argument passed to system func-
tion or statement
MOVELINES could not completely delete
source lines after copying them
Line failed to copy; program modification
Specified SCREEN does not exist
Attempt to delete a SCREEN with sustem
function(s) attached
File specifier or BUFFER parameter not
allowed as a single line function parameter

BASIC Language Error Messages (Continued)

line(s) vice 406 Out of line numbers during LOAD or LOADSUB 778 Media failure 409 Attempt to load a non-BASIC intermediate code context 478 Media failure 410 STORE or STORESUB failed, all contexts reference missing options 480 Data operation aborted by an interface or device clear operation 411 Recordlength must be 256 for ES-AVE and RE-STORE KEY files 481 File already locked, or unlocked in exclusive mode 413 Variables must be explicitly declared in FORCE DECLARE ON mode 482 Cannot Move a directory via a RENAME 411 Eccordingth must be 256 for RE-SAVE and Resource Management controller is down 481 Headset is down 412 Line too complex 484 Password not found 487 413 Variables must he explicitly declared in range of 10 99 487 Requestionomable with previous requests or current state 413 Ballegal screen size in CREATE SCREEN 500 Partition anleredy exists 420 Cannot ASSIGN ROLL KEYS TO SCREEN 503 Partition is must be foreground partition subsystem 421 Bad key number in stored SFK definitions 507 EVENT LEVEL negative – cannot delete tor reanot delete to reanot delete to subsystem	407	SAVE failed; program contains unlistable	477	Only one driver may be attached to this de-
100 of the number of uning LOAD of the Loads and the L	409	line(s)	470	Vice Madia failuna
100 Attempt to load a non-BASIC intermediate code context. 100 100 STORE or STORESUB failed: all contexts reference missing options tected for the studies of the stu	400	LOADSUB	470	Operation incomplete due to user program
 and operation aborted by an interface or device clear operation STORE NEY Biles Record length must be 256 for RE SAVE and RE-STORE KEY likes Variables must be explicitly declared in acclusive mode Cannot Complex Complex Line too complex Line too complex Cannot COMPILE a TRACE or GET state ment SCREEN number is outside the allowed irrange of 1 to 99 SCREEN number is outside the allowed irrange of 1 to 99 SCREEN number is outside the allowed irrange of 1 to 99 Bilegal screen position in CREATE SCREEN Bilegal screen position in CREATE SCREEN Bilegal screen position in CREATE SCREEN Bilegal screen position in CREATE or MOVE SCREEN Bartition already exists Cannot ASSIGN ROLL KEYS TO SCREEN Attempt to ASSIGN KEYBOARD to a public SCREEN Attempt to ASSIGN KEYBOARD to a public SCREEN Too many chained SFK definition or suby another partition or subystem Ur socure in use by another partition or subystem Specified volume not found Specified volume not found File already locked, or nanot delete Directory format does not supported for this directory format Specified volume abels Passwords not supported for this directory format Directory format does not support this operation Specified directory format Directory format does not support this operation Passwords not allowed Passwords not support for this directory format Directory format does not support format Directory format does not support this operation Passwords not allowed Directory format does not support this directory format Directory format does not support CHECKREAD Directory for	409	Attempt to load a non-BASIC intermediate	477	med holdoff
410 STORE or STORESUB failed: all contexts reference missing options. 481 device clear operation 411 Record length must be 256 for RE-SAVE and RE-STORE KEY files 482 Cannot move a directory via a RENAME 413 Variables must be explicitly declared in FORCE DECLARE ON mode 481 Shared Resource Management controller is down 416 Cannot COMPILE a TRACE or GET state ment 484 Password not found 417 SCREEN number is outside the allowed range of 1 to 99 484 Password not found data Request incompatible with previous requests or current state 418 Ilegal screen size in CREATE SCREEN 500 Partition nateady exists 419 SCREEN number is stored SFK definition with on scrolling buffer 500 Partition in streed VEX SIGN KEYBOARD to a public. 421 Bad key number in stored SFK definition sub bit in the STOP state 507 EVENT not present 422 Too many chained SFK definition sub yastem 509 Attempt to COMPILE an empty program 425 Too many chained SFK definition sub yastem 509 Attempt to COMPILE an empty program 426 Directory format doe not match 510 PARCG file or PROG file directory oos mall a	,	code context	480	Data operation aborted by an interface or
reference missing options 481 File already locked, or unlocked in exclusive mode 411 Record length mustbe 256 for RE-SAVE and RE-STORE KEY files 482 Cannot move a directory via a RENAME 413 Variables must be explicitly declared in FORCE DECLARE ON mode 483 Shared Resource Management controller is down 416 Cannot COMPILE a TRACE or GET state ment 484 HB Bis econdary command seen 417 SCREEN number is outside the allowed range of 1 to 99 487 Request incompatible with previous requests or current state 418 lilegal screen position in CREATE SCREEN 500 Partition not present SCREEN 419 lilegal screen position in CREATE SCREEN 500 Partition not present SCREEN 420 Cannot ASSIGN ROLL KEYS TO SCREEN 500 Partition must be foreground to ATTACH with no scrolling buffer 504 421 Bad key number in stored SFK definition 505 EVENT already exists 507 522 Connot ASSIGN ROLL KEYS TO SCREEN 500 EVENT already exists 507 524 Insufficient I/O bandwidth to honor request 507 EVENT already exists 507 524 Insufficient I/O bandwidth to honor request 500 <th>410</th> <th>STORE or STORESUB failed; all contexts</th> <th></th> <th>device clear operation</th>	410	STORE or STORESUB failed; all contexts		device clear operation
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 413 Variables must be explicitly declared in FORCE DECLARE ON mode 416 Cannot COMPILE a TRACE or GET state ment 417 SCREED number is outside the allowed range of 1 to 99 418 Illegal screen size in CREATE SCREEN 419 Illegal screen size in CREATE SCREEN 419 Illegal screen size in CREATE SCREEN 420 Cannot ASSIGN ROLL KEYS TO SCREEN 421 Bad key number in stored SFK definition 422 Artempt to ASSIGN KEYBOARD to a public SCREEN 423 HP IB EOI assertion requires data 434 HP IB EOI assertion requires data 435 Prostible corrupt directory 436 Specified volume not found 437 Result array for INV is not SHORT or REAL 438 Ungassword so tupport for this directory 439 Directory format does not support for this directory 430 Directory format does not support for this directory 431 Directory format does not support frams 432 Artempt to SCREEN 433 File in use 434 Directory format does not support frams 434 Directory format does not support frams 434 Directory format does not support for this directory 434 Directory format does not support frams 435 Prostible corrupt directory 436 Directory format does not support frams 437 Passwords not support frams 438 Unsupported directory format 439 Directory format be at end of ATN 430 Directory not empty 431 Directory format a trae word and the ore modified once cognized 433 File in sub at end of ATN 434 Directory format at a et ond of this directory 434 Directory format at a directory 435 Prostible corrupt TRANSFER 436 Directory format at a trae word and the corrup at the set of SMytes exceeded 436 Directory format at a trae word address 437 Device does not support TRANSFER 438 Unsuported directory format 439 Directory format the at end o		RE-STORE KEY files	482	Cannot move a directory via a RENAME
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 111 Link too Complex and Complex	415	FORCE DECLARE ON mode	191	down Descuerd not found
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 417 SCREEN number is outside the allowed range of 1 to 99 418 Illegal screen position in CREATE SCREEN SCREEN SCREEN with no scrolling buffer 420 Cannot ASSIGN ROLL KEYS TO SCREEN with no scrolling buffer 421 Bad key number in stored SFK definition SCREEN with no scrolling buffer 422 Attempt to ASSIGN KEYBOARD to a public SCREEN to maximum buffer is stored SFK definition SCREEN to maximum buffer state of a subsystem 423 HP-IB EOI assertion requires data for nor equeres the subsystem 426 I/O resource in use by another partition or subsystem 426 I/O resource in use by another partition or subsystem 427 O partition alread SFK definition subsystem 428 II Subjecta volume not found 439 Specified volume hot found 431 Nortectory format doe not match 435 Ple in use 436 Directory format doe not match 437 Passwords not supported for this directory format 436 Directory format does not support format for max 437 Passwords not supported for this directory format 438 Unsupported directory format 439 Specified file is not a directory format 430 Directory normat be at end of ATN sequence 431 Duplicate volume attries 432 Povice folder the sen improperly inserted 433 File in use be improperly inserted 434 Directory format be at end of ATN sequence 434 Directory there are and bettimet size of 5 Mbytes exceeded 435 Poscified file is not a directory 436 Directory normat be at end of ATN sequence 437 Device does not support CHECKREAD range 438 Directary and be at alter coll and the red director format sequence 439 Synchronous data rate could not be metro complete this soperation mode not support CHECKREAD range 430 Device does not support CHECKREAD range 431 Device does not support CHECKREAD range 434 Directary nore data rate could not be met	110	ment	486	Write blocked due to unread inbound data
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 470 Device does not support CHECKREAD 702 Internal CRT has no graphics hardware, or malfunctioning hardware, or no graphics hardware, or malfunctioning hardware, or no graphics driver 472 Interface cannot be HP-IB active controller 473 Synchronous data rate could not be met to complete this operation 704 Upper bound <= lower bound complete this operation 705 Specified area is outside GDU limits Incompatible hardware and driver 475 HP-IB interface too slow for this device 707 Graphics device not initialized driver 709 Graphics device not selected 		sequence		PLOTTER drivers
 471 Device does not support TRANSFER 472 Interface cannot be HP-IB active controller 473 Synchronous data rate could not be met to complete this operation 474 Device failed its self test or diagnostic 475 HP-IB interface too slow for this device 476 Termination mode not supported by this driver 470 Graphics device not selected 470 malfunctioning hardware, or no graphics driver 471 Device failed its self test or diagnostic 472 HP-IB interface too slow for this device 473 Termination mode not supported by this driver 474 Device failed its and the support of the support	470	Device does not support CHECKREAD	702	Internal CRT has no graphics hardware, or
 472 Interface cannot be HP-IB active controller 473 Synchronous data rate could not be met to complete this operation 474 Device failed its self test or diagnostic 475 HP-IB interface too slow for this device 476 Termination mode not supported by this driver 470 Graphics device not initialized 470 Graphics device not selected 	471	Device does not support TRANSFER		malfunctioning hardware, or no graphics
 47.3 Synchronous data rate could not be met to complete this operation 47.4 Device failed its self test or diagnostic 47.5 HP-IB interface too slow for this device 47.6 Termination mode not supported by this driver 47.7 Termination mode not supported by this driver 47.8 Termination mode not supported by this driver 47.9 Termination mode not supported by	472	Interface cannot be HP-IB active controller	704	driver
474Device failed its self test or diagnostic705Specified area is outside GDU limits475HP-IB interface too slow for this device706Incompatible hardware and driver476Termination mode not supported by this driver708Graphics device not initialized709Graphics device not selected	4/3	Synchronous data rate could not be met to	704 705	Upper bound <= lower bound
 475 HP-IB interface too slow for this device 476 Termination mode not supported by this divice 477 Total and the sentence of the support of the sentence of the sen	474	Complete this operation Device failed its self test or diagnostic	705	Incompatible bardware and driver
476Termination mode not supported by this driver708Graphics device not initialized Graphics device not selected	475	HP-IB interface too slow for this device	707	Graphics device limits out of range
driver 709 Graphics device not selected	476	Termination mode not supported by this	708	Graphics device not initialized
		driver	709	Graphics device not selected

BASIC Language Error Messages (Continued)

710	Service request interrupt on HP-IB from un-	737	FONT stroke data invalid
	known origin	740	Parameters specify the zero length vector
711	Maximum number of graphics devices already initialized	741	Perspective image for the point is undefined – zero distance from center of projection
712	Memory overflow while attempting to report		along the view normal
	an overlapped I/O error	742	Front plane is not between the center of pro-
713	Request not supported by the device or		jection and the back plane
	driver	743	Parameters specify a point that is on the
714	An attribute value (PEN or LINE TYPE) is out of range for a graphics device		wrong side of the view plane as determined by the view normal
730	Array not in COM for array PLOTTER	744	Viewing matrix is not invertible
731	The specified TRACK operation has not been initiated	745	Two vectors specifying the viewing trans- formation are parallel
733	GESCAPE opcode not recognized	746	GDU z value too large - no point has a
734	FONT identifier out of range or not found		perspective image with that value
735	FONT identifier already in use	749	Graphics system error

736 FONT specification data invalid

IMAGE Status Error Messages

0	Successful execution - no error	11	Е
-1	Miscellaneous host system error, see status	12	N
	element 10 for error number	13	R
- 10	Maximum number of data bases already		fi
	open	14	В
-11	Bad data base reference or preceding blanks	15	E
	missing	16	Т
- 12	Not all necessary data sets are locked	17	N
- 14	DBPUT, DBDELETE, and DBUPDATE not		se
	allowed with DBOPEN mode 8	18	В
- 21	Improper or nonexistent data set, data item,	20	Α
	password, or volume specified		lc
- 22	Detail data set required	41	D
- 23	Write access to data set required	43	K
- 24	DBPUT, DBDELETE, or DBUPDATE not	44	С
	allowed on an Automatic master data set		D
- 31	Improper mode specified	50	S
- 32	DBOPEN mode conflicts with another user	51	V
- 52	Item specified is not a key item in the speci-		si
	fied set or bad List parameter	52	N
- 91	Root file not compatible with current version		n
	of IMAGE	53	A
- 92	Data base not created		b
- 93	Corrupt root file, must purge and redefine it	94	C
- 94	Corrupt data base, some sets require erasure		n
- 135	Wait lock not allowed while a lock is already	1xx	Т
	in effect	3xx	Т
10	Beginning of file encountered		

0
End of file encountered
Negative record number specified
Record number greater than capacity speci-
fied
Beginning of chain encountered
End of chain encountered
The data set is full
No current record, no chain head, or the
selected record is empty
Broken chain encountered
At least one requested data set is already
locked
DBUPDATE will not alter a key or sort item
Key value already exists in Master set
Can't delete a Master entry with non-empty
Detail chains
String buffer is too small for requested data
Variable size or type does not match the item
size or type
Number of variables specified does not
match the item list
Argument parameter type or size incompati-
ble with key item type or size
Corrupt data base opened in read-only
mode
There is no chain head for path xx

The Automatic Master for path xx is full

9020 Service Notes

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