

COBOL COMPILER H

Series 200 COBOL Compiler H is operational in the range from 32,768 characters of memory upward and can generate object programs up to 256K. It is a powerful data processing tool for sophisticated business applications which require the extended features of the language. COBOL H will be released in three phases; each phase will include all of the required features of the smaller COBOL D compiler, and will in addition contain elective design features necessary for faster, more efficient program translation in the larger, more extensive configurations.

COBOL LANGUAGE

The source-program reference format and the symbology adopted for the representation of language elements are identical to those implemented in other systems. COBOL H programs consist of the following four major divisions:

Identification Division — Identifies the source program and provides optional documentary information.

Environment Division — Specifies the processor on which the source program is to be compiled, the processor on which the object program is to be executed, and the relationships between data files and input/output media.

Data Division — Describes the data to be processed by the object program.

Procedure Division — Describes the procedures to be used in processing the data.

In keeping with the system-wide compatibility of Series 200 hardware, COBOL H is designed around a series of language modules. The extended features of COBOL H offer the user increased source language power and the ability to produce larger object programs.

Implementation Phases

COBOL H is implemented in three phases. The special features in each phase are extensions to the language and translation capability of the compiler.

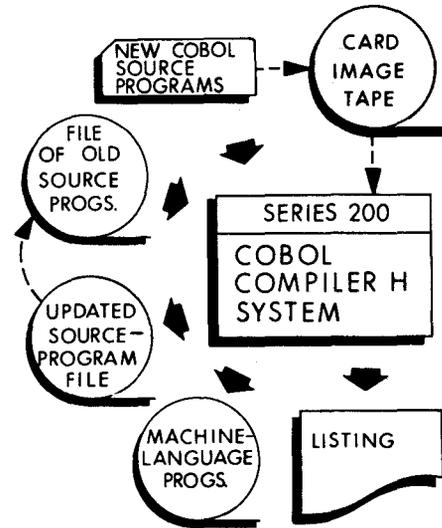
PHASE 1

- * **READ INTO Verb** — By means of a source statement, the user can cause a file to be read and an internal move order generated so that data can be read into several memory areas simultaneously.
- * **WRITE FROM Verb** — A source statement which allows the user to build output records and to generate move orders to output buffers and write simultaneously.
- * **WRITE AFTER ADVANCING Verb** — Permits the positioning of forms on the printer prior to a print (write) instruction.

PHASE 2

- * **Expansion of Name Table Limit Restrictions** — Permits the use of larger name tables and the use of additional memory by larger, more sophisticated programs.
- * **MULTIPLY AND DIVIDE Verb** — Facilitates these arithmetic operations using a machine instruction rather than a subroutine.

SERIES 200



TO: IMMEDIATE EXECUTION
OR
INTEGRATION WITH OTHER OBJECT TAPES

- * **GO TO DEPENDING Verb** — Enables the user to employ multibranch operations dependent on a value contained within an elementary item.
 - * **DUMP Verb** — Portions of memory can be written on the output device during object program execution.
 - * **RENAMES Verb** — Enables the user to create a tag name and assign it to another tagged field.
 - * **Additional I/O Trunks and Read/Write Channels Option** — Permits the user to make full use of additional trunks and channels during object program execution.
 - * **SORT Verb** — This verb is easily incorporated within source programs. The keys are specified for sorting in either ascending or descending order, and all I/O procedures are included.
- PHASE 3
- * **Three-level Subscripting** — Provides for 3-dimensional table references using the OCCURS clause.
 - * **TABLE-HANDLING Verb** — Facilitates handling of multilevel tables.
 - * **COMPUTE Verb** — Enables the user to express computations by symbols in a formula.

OPERATIONS

COBOL H has several important operating features. It will accept batched source programs and can operate in a batch-compile, load-and-go mode; or it can compile to a file of programs for operation under the control of the

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Honeywell
ELECTRONIC DATA PROCESSING

Series 200/Operating System. Source language programs can be maintained in magnetic tape libraries to facilitate program correction during checkout. A variety of testing and debugging aids are provided with COBOL H, such as dynamic and static dumping capability, English-language diagnostics, memory mapping, and test data distribution. In the interest of operational flexibility, the addresses of peripheral devices can be assigned at object time to allow the use of a single program with a variety of different peripheral arrays. In addition, dynamic assignment of

read/write channels at execution time enhances object-program efficiency.

EQUIPMENT REQUIREMENTS

- A minimum of 32,768 characters of memory
- 4 Magnetic Tape Units (Type 204B)
- 1 Card Reader
- 1 Printer
- Advanced Programming Instructions (Feature 011)
- Editing Instructions (Feature 013)