

FOR SUN STUDS, INC.

TAB NAME

SYSTEM DRAWINGS

SYSTEM DRAWINGS

TITLE SYSTEM MODEL LIST			FCF		GENERAL ELECTRIC PROCESS COMPUTER		PL 4DP0005ABDR		SECT		SH. NO. 1		CONT. ON SHEET		
ITEM NO.	REV CODE	IDENTIFICATION NO.	DESCRIPTION	ZONE	GROUP NO. AND QUANTITY										ROUTING
					73	77	73	77	73	77	73	77	73	77	
000A			FMF BDR												
001	B	70C182183	LOGIC SYS. CONFIGURATION	X											Z-2
002	B	70C182258G1	DOOR ASM	001											Z-5
002A			DOOR FOR AJR CABINET												N
CR1	B	4DP3000AE4201	CARD READER 200CPM	001											Z-5
CS1		70A124477G1	CABINET, CENTRAL SYSTEM	001											Z-5
DI1		71B100007P1	02-159 B01	001											501
PB1		4DP3010AE7401	MULTIPLEXER BUS SWITCH	001											Z-5
SCA		70X124473G1	SYSTEM CABLE INDEX	001											Z-5
TN1	D	70A121548P88	PUR SPEC TERMINET	001											Z-5

REVISIONS	A - 12/18/72	B - 01/30/73	C - 02/13/73	D - 04/06/73	E -	F -	G -	H -	I -	J -	K -	L -	M -	N -	O -	P -	Q -	R -	S -	T -	U -	V -	W -	X -	Y -	Z -
MADE BY	T		SHIFT		APPROVAL		DATE ISSUED		DISTRIBUTION CODE		PL 4DP0005ABDR		SECT		SECT		SH. NO. 1		C		ISSUED		1		16	

[illegible][illegible]

PROJECT OF THE
GENERAL ELECTRIC COMPANY

[illegible]

DESCRIPTION

SHEET

BLOCK DIAGRAM	2
CARD ARRANGEMENT	3
MOD 70 PROCESSOR CF1	4
15" EXPANSION CHASSIS CF2	5
MOD 70 PROCESSOR CF3	6
15" EXPANSION CHASSIS CF4	7
10" EXPANSION CHASSIS	7,1
COMMON BUS CABLING & PWBS	8,9
COMMON BUS SWITCHING	10
PROCESSOR SWITCHING	11
DIGITAL SUBSYSTEM	12
PERIPHERAL SWITCH	13
TELETYPE SUBSYSTEM	14
PAPER TAPE SUBSYSTEM	15
TERMINET SUBSYSTEM	16
CARD READER SUBSYSTEM	17
PAPER TAPE READER INTERFACE	18

REVISIONS

PARTS TO

B E

JULIE GUDIM
JAN 30 1973U & FAP
Phoenix, Ariz.

70C182183

2

1

LOGIC,
BLOCK DIAGRAM

NOT MADE FOR RDR

CSI

REAR	FRONT
	PERIPHERAL SWITCH
	PS1
26V POWER VX1	PROCESSOR W 32KB CF1
LOGIC POWER VL1	15" EXPANSION W 8KB CF2
LOGIC POWER VL2	PROCESSOR W 32KB CF3
LOGIC POWER VL3	15" EXPANSION W 8KB CF4
LOGIC POWER VL4	10" EXPANSION CF5
LOGIC POWER VLS	13 SLOT I/O CHASSIS CF6
AC ENTRY PE	BLOWER

70A124477

71B100007P1
128 PT
DIGITAL INPUT MODULE
DI1

CABLES

70C112738
DOOR
FOR
AJH
CABINET

DR1

KSR
TERMINET
70A121548P88

TN1

40P3010AE7401
MBS
PWB40P3000AE4201
200 CPM
CARD READER

CR1

REV NO. 1
B C D EJULIE GUDIM
JAN 30 1973U & PAP
PHOENIX, ARIZ.

7-0162183

LOGIC,
CARD ARRANGEMENT
NOT MADE FOR BDR

CONN 1 CONN 0

7		35-388
6		35-389
5		35-390
4		35-387
3	1ST 8KB	35-344
2	2ND 8KB	3010AC1402 35-344
1	3RD 8KB	3010AC1402 35-344
0	4TH 8KB	3010AC1402 35-344

CF1

CONN 1 CONN 0

7		35-388
6		35-389
5		35-390
4		35-387
3	1ST 8KB	35-344
2	2ND 8KB	3010AC1402 35-344
1	3RD 8KB	3010AC1402 35-344
0	4TH 8KB	3010AC1402 35-344

CF3

7	5TH 8KB	3010AC1402	35-344
6	SPARE MEMORY SLOT		
5	SPARE MEMORY SLOT		
4	SPARE MEMORY SLOT		
3	MEM PROTECT/CLOCK	3010AB3401 35-396	3010AF4201 35-398
2	SPARE		
1	ADAPTER EXTENDER	3010AE8001	3000AE7102 32-029
0	SPARE		

CF2

7	5TH 8KB	3010AC1402	35-344
6	SPARE MEMORY SLOT		
5	SPARE MEMORY SLOT		
4	SPARE MEMORY SLOT		
3	MEM PROTECT/CLOCK	3010AB3401 35-396	3010AF4201 35-398
2	ADAPTER CARD READER	3010AE8001	3000AE7101 32-084
1	SPARE		
0	SPARE		

CF4

3	PTR	718100027P1 32-072F03
4	TERMINET	3000AE2101 32-120
5	EXTENDER	3000AE7101 32-047
0	PTR	718100027P1 32-072F03
1	TERMINET	3000AE2101 32-120
2	EXTENDER	3000AE7101 32-047

25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
EXTENDER 32-047										DRIVER 35-050		DRIVER 35-050		DRIVER 35-050		INTERRUPT 32-019		INTERRUPT 32-019		INTERRUPT 32-019	MBS 7402 PX3000 MBSA1		MBS 7401 PX3000 MBSA1		

CF6

* THESE PWBS CALLED FOR AND USED ON SYSTEM AJH

25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	
CLM 35-050		CLM 35-050		CLM 35-050		CONTROL 35-050 LINE MOD.		DIGITAL 35-204		SLM 35-104		SLM 35-104		SLM 35-104		SLM 35-104		SLM 35-104		SENSELINE 35-104 MODULE		SPARE		PX3000 MBS7401 MBSA1		

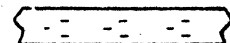
CF7

CF6 AND PWBS ARE PART OF SYSTEM AJH INSTALLED AT CUSTOMER SITE.

JULIE GUDIM
JAN 30 1973

U & PAP
PHOENIX, AZ 857

700182183

PLUG
ON PELOGIC POWER
SCHEM 71D102026
34-012D08AC
POWERCONTROL
TO CF2
70A123209G1

C1

C2

BASIC PROCESSOR
4DP3010AA7504
8KB W PARITY

CF1

SCHEM 71D102027
01-051R02D08

CONN

SLOT 0

CABLE
17-162TO CF2 SLOT 7
SEE SH. 5

CONN

SLOT 0

CABLE
17-163TO CF2 SLOT 7
SEE SH. 5

POWER

CABLE
17-152

DATA

SLOT 5

CONN 3

SLOT 5

CONN 4

TTY CABLE
17-180
71B100386TO SWITCH TTY
SEE SH. 13,14
ADDRESS X'02'DISPLAY
SCHEM 71D102024
09-051D08

ADDRESS X'01'

4DP3010A01402
(3) 8KB MEMORYSLOT 0,1,2
SCHEM 71D102025
02-211D08

2

3

5

GENERAL ELECTRIC

182183

6

5

LOGIC
LOGIC 15" CHASSIS

FIRST MADE FOR BDR

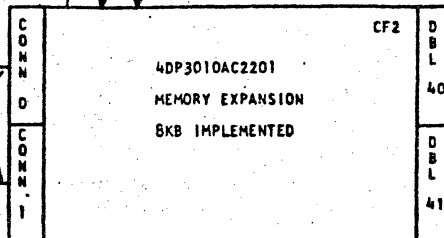
PLUG
ON PELOGIC POWER VL2
SCHEMATIC
710102026
34-012008CONTROL
FROM CF1
70A123209G1FROM CF1
SLOT 0

17-162

SLOT 7

FROM CF1
SLOT 0

17-163

17-016

TO CFS SLOT 2

SLOT 1

TO CFS SLOT 2

CHASSIS REAR
17-173
C1, C3

TO MEMORY CONTROL
35-387

CABLE
17-169
CF1 CSA

4DP3010AC1402
8KB MODULE

SLOT 7

SCHEM 710102025
02-211008

4DP3010AF4201
CLOCK

35-398
HALF SLOT 3

SCHEM 710102015
TEST SPEC 71A101311
PROD SPEC 71A101312
INSTL SPEC 71A101313
MAINT SPEC 71A101314
ADDRESS X'60'

4DP3010AB3401
MEMORY PROTECT

35-396
HALF SLOT 3

SCHEM 710102016
PROD SPEC 71A101317
TEST SPEC 71A101318
INSTL SPEC 71A101319
MAINT SPEC 71A101320
ADDRESS X'AE'

4DP3000AE7102
INTERNAL EXTENDER
32-029
SLOT 1

INSTL 70A124014
TEST 71A101049
PARTS 71B100019

40 41

17-016 CABLE

TO CFS SLOT 2

REVISIONS

REVISIONS

1 B C D E

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JAN 30 1973

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PHOENIX, ARIZ.

70C182183

6

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PLUG
ON PELOGIC POWER VL3
SCHEM 71D102026
34-012008

AC POWER

BASIC PROCESSOR CF3
4DP3010AA7504
8KB W PARITYCONTROL
TO CF4
70A123209G4

C1

C2

SCHEMATIC 71D102027
01-051R02D08CONN
0

SLOT 0

CABLE
17-162TO CF4 SLOT 7
SEE SH. 7CONN
1

SLOT 0

CABLE
17-163TO CF4 SLOT 7
SEE SH. 7

POWER

SLOT 5

SLOT 5

CONN 3

CONN 4

CABLE
17-152

DATA

TTY CABLE

17-180
71B100386TO SWITCH TTY
SEE SH. 13,14
ADDRESS X'02'

DISPLAY

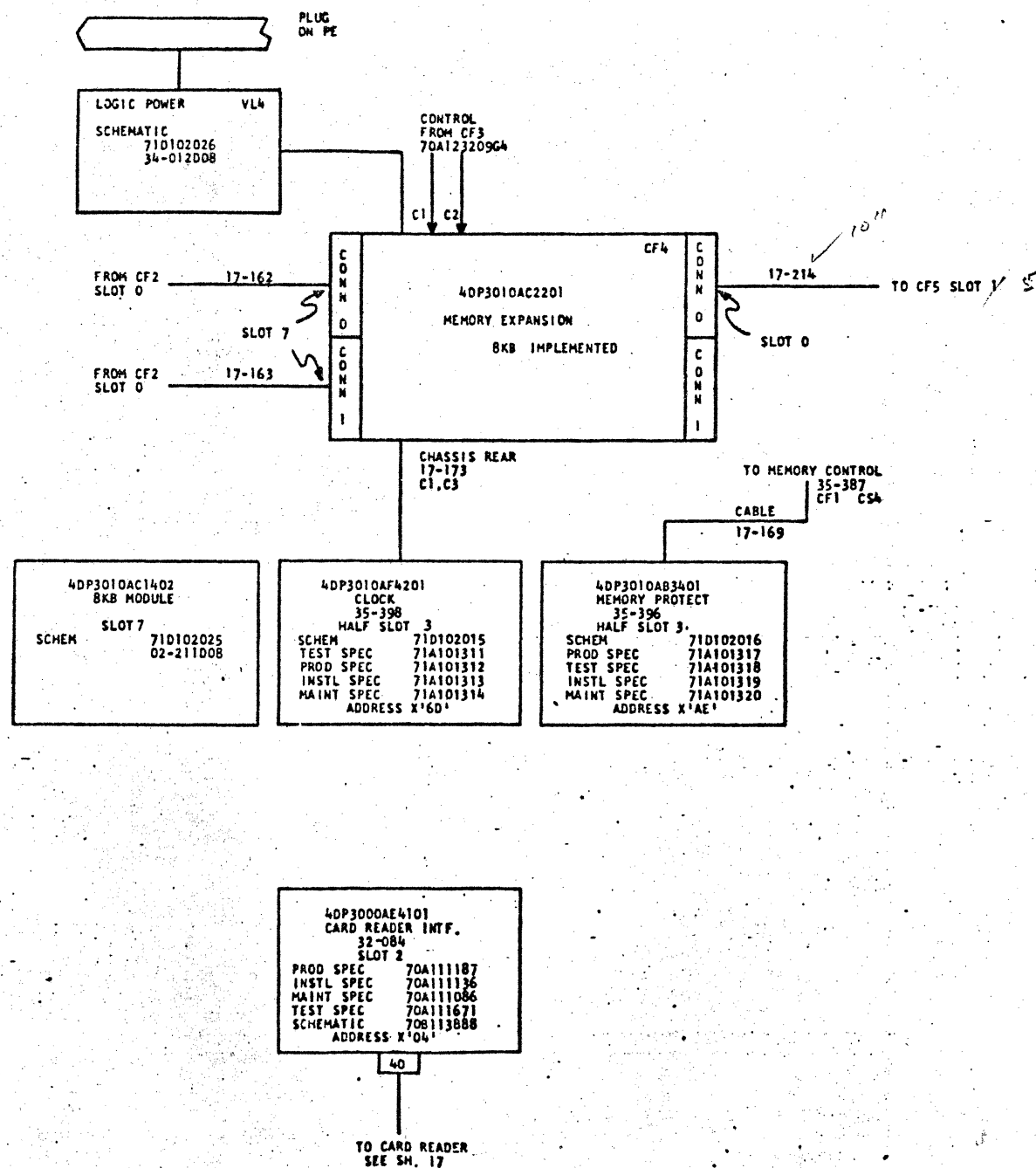
SCHEM 71D102024
09-051008

ADDRESS X'01'

4DP3010AC1402
(3) 8KB MEMORY
SLOT 0,1,2SCHEM 71D102025
02-211008

LOGIC.
LOGIC 15" CHASSIS

FIRST MADE FOR BDR

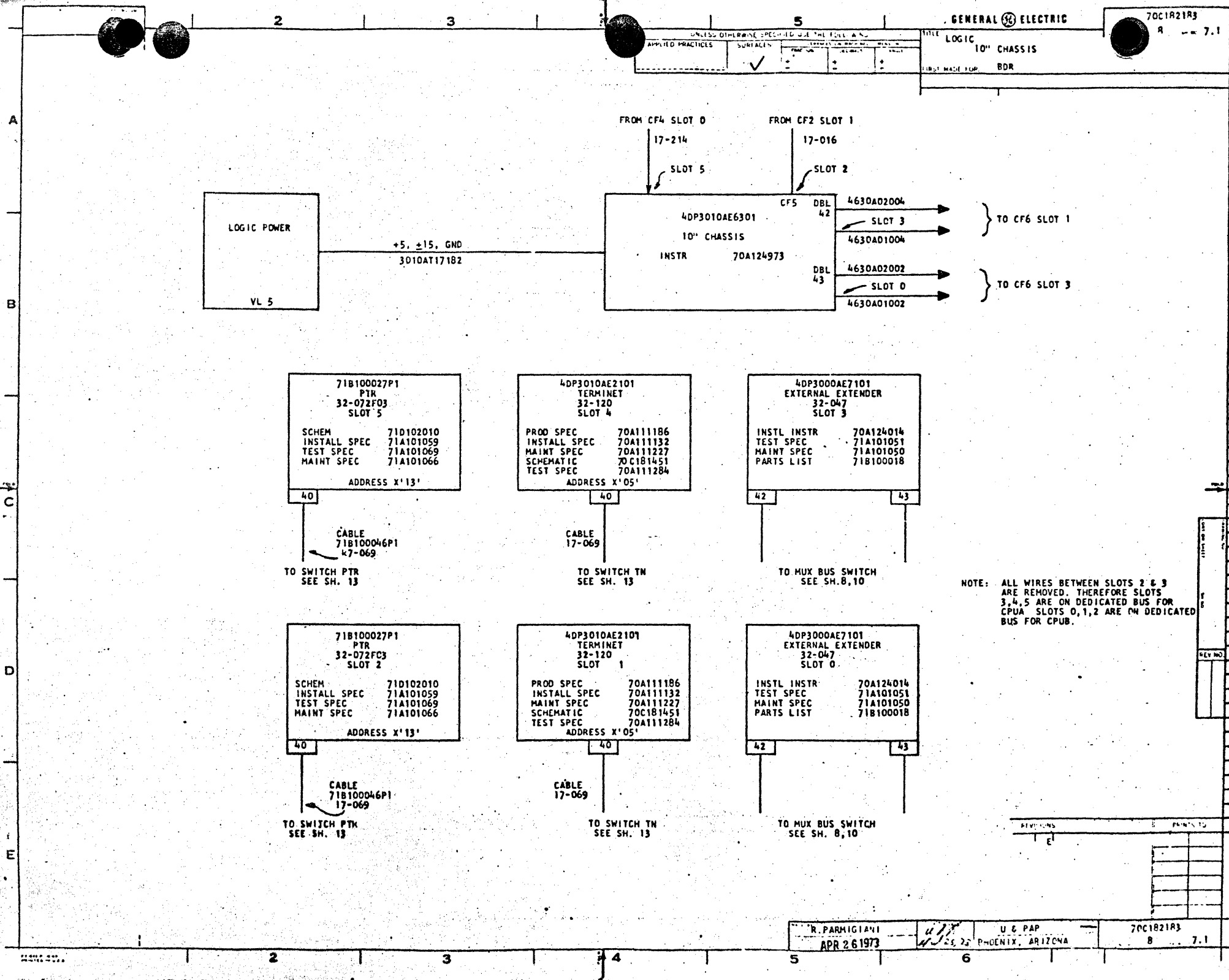


REVISION	PRINTS TO
B C D E	

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JAN 30 1973

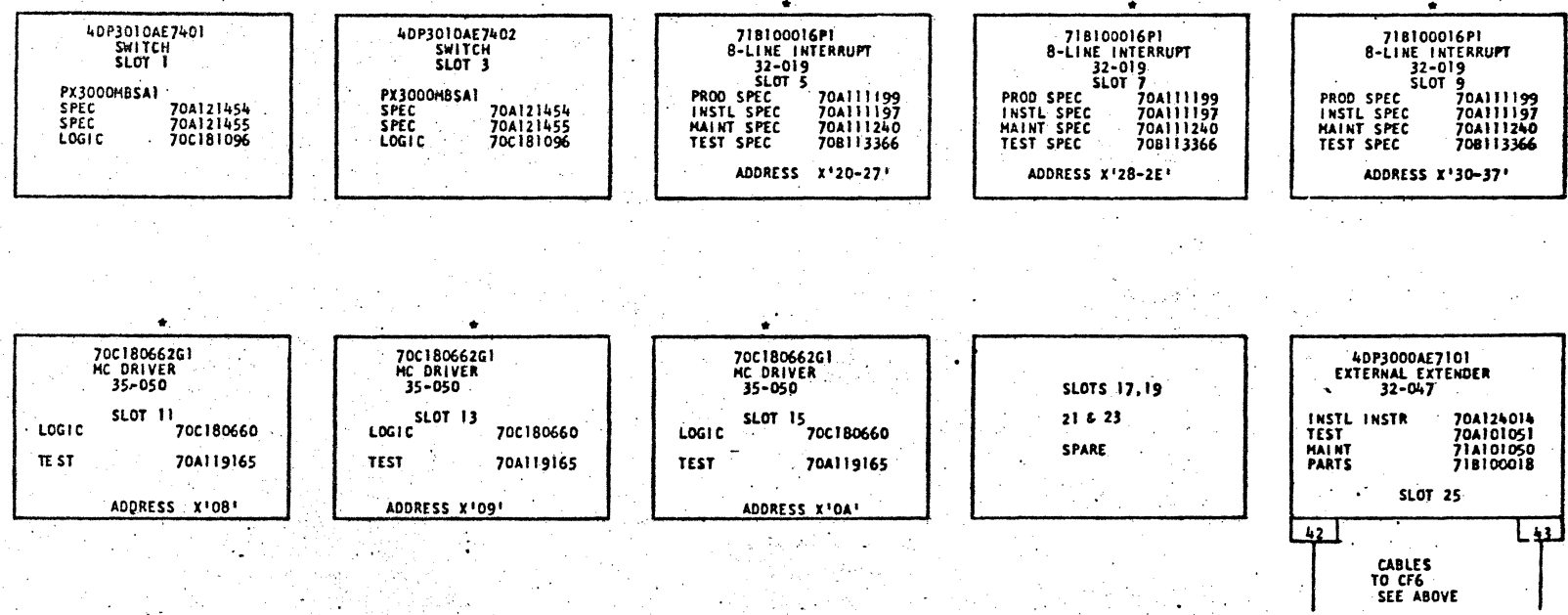
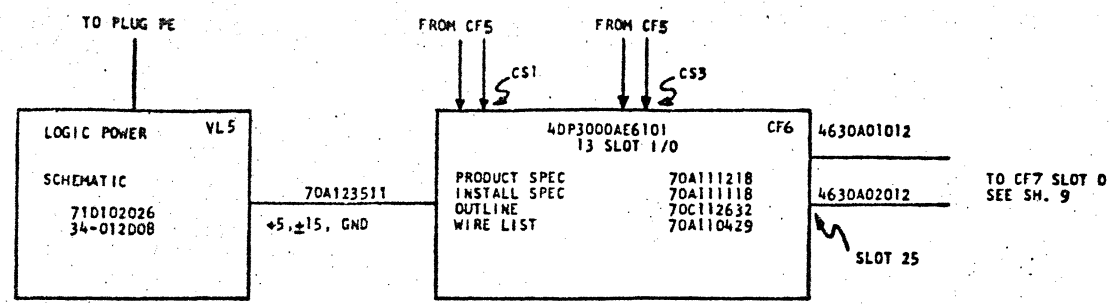
U. S. P. A. P.
PHOENIX, ARIZ.

70C182183
7.1 - 7



UNLESS OTHERWISE SPECIFIED USE THE FOLLOWING	
APPLIED PRACTICES	SURFACE
✓	✓

GENERAL ELECTRIC		182183
LOGIC, COMMON BUS CABLING & PWBS		9
MADE FOR BDR		



* THESE PWBS ARE PRESENTLY IN CUSTOMER EQUIPMENT(SEE 70C180190.) THEY WILL BE MOVED TO CFS IN THE FIELD.

REVISION	POINTS TO
1	
2	
3	
4	
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UNLESS OTHERWISE SPECIFIED USE THE FOLLOWING
APPLIED PRACTICES

SURFACE

FINISH

TOLERANCE

MATERIAL

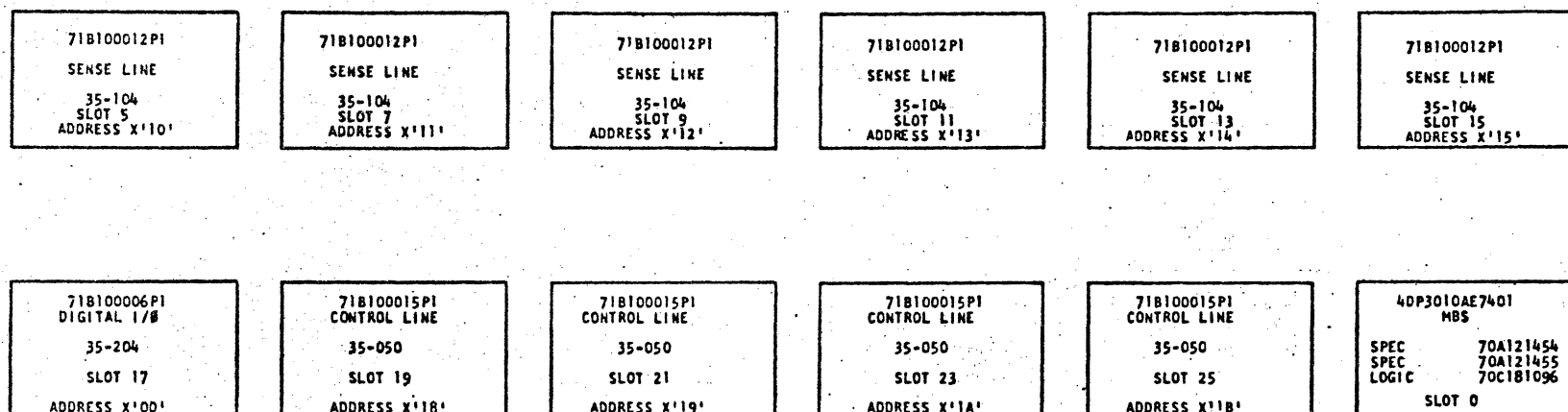
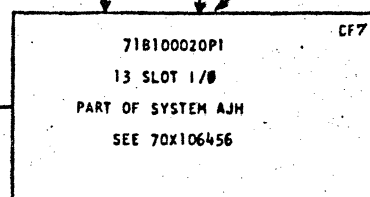
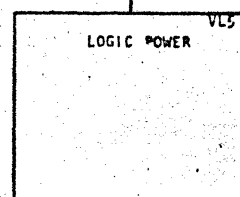
TITLE LOGIC

COMMON BUS CABLING & PWBS

FIRST MADE FOR BDR

TO PLUG PE

FROM CF6 SLOT 17

17-014 CABLE
CSO

ALL ITEMS THIS PAGE EXCEPT MBS, SLOT 0, ARE PART OF SYSTEM AJH. SEE 70C180190

DRAWINGS

	DIGITAL I/O	SENSE LINE	CONTROL LINE
SCHEMATIC	70D118011, 71D102001	70B113242	70B113243
TEST SPEC	71A101015	71A101026	71A101120
PRODUCT SPEC	71A101004	71A101018	71A101034
INSTALL SPEC	71A101014	71A101019	71A101035
MAINT SPEC	71A101009	71A101020	71A101036
PROG SPEC		71A101330	
APPL SPEC	71A101013	71A101027	

NOTE: WIRES FROM P5 BUS TO PINS 122-0 & 100-0 ON SLOTS 19-25 HAVE BEEN REMOVED. P5 PWR TO THESE SLOTS ROUTES THROUGH RELAY PROTECT CIRCUIT. SEE SHT. 12.

REVISIONS

REV NO

DATE

JULIE GUOIM
JAN 30 1973U & PAP
PHOENIX, ARIZ.

7 182183

10 9

APPLIED PRACTICES	CHECKS	DATE	INITIALS	REMARKS
	✓			

COMMON BUS SWITCHING

FIG. 1 MADE FOR BDR

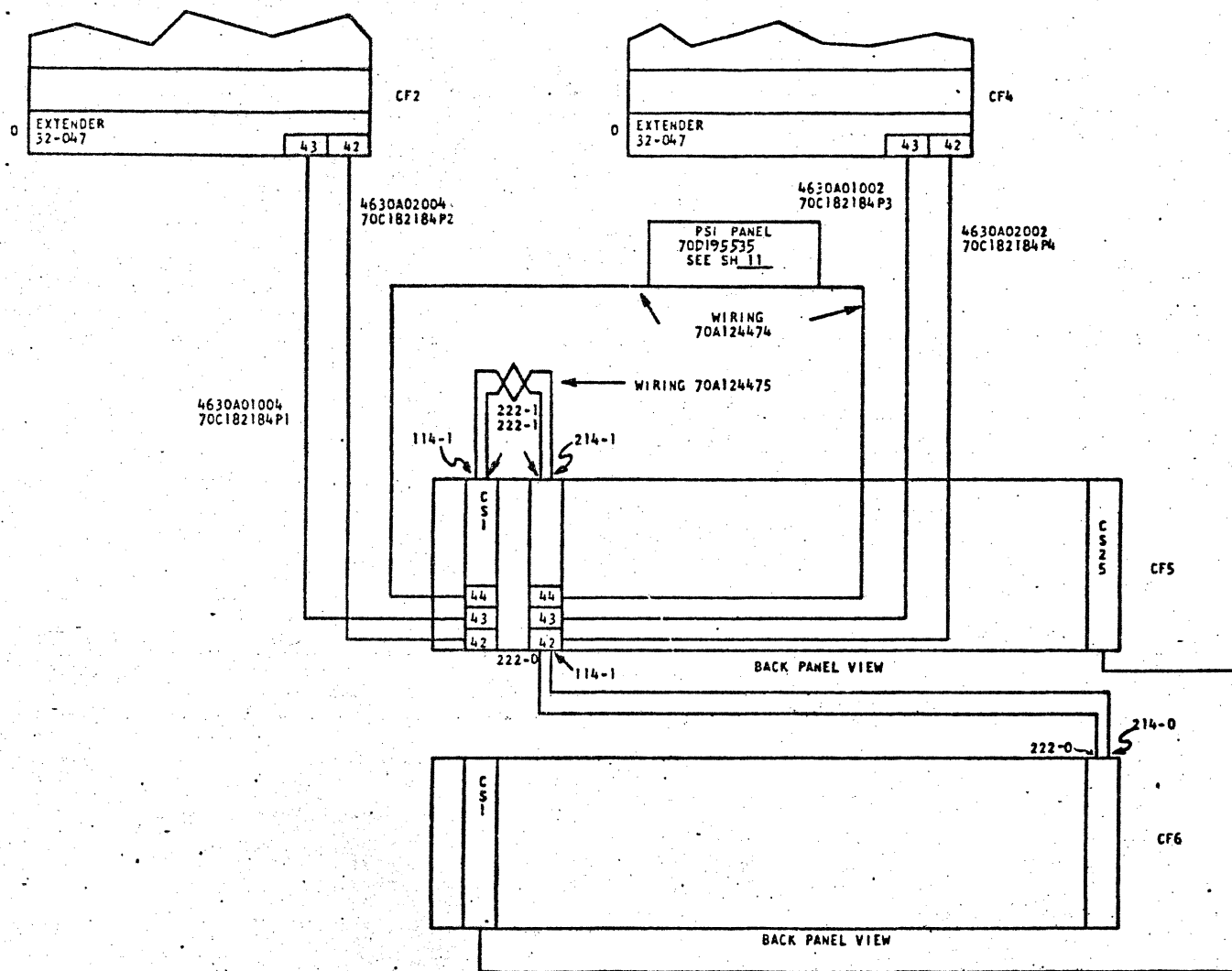
A

B

C

D

E



NOTE: CF6 IS PRESENTLY INSTALLED IN CUSTOMER CABINET

40P4630A01012
40P4630A02012

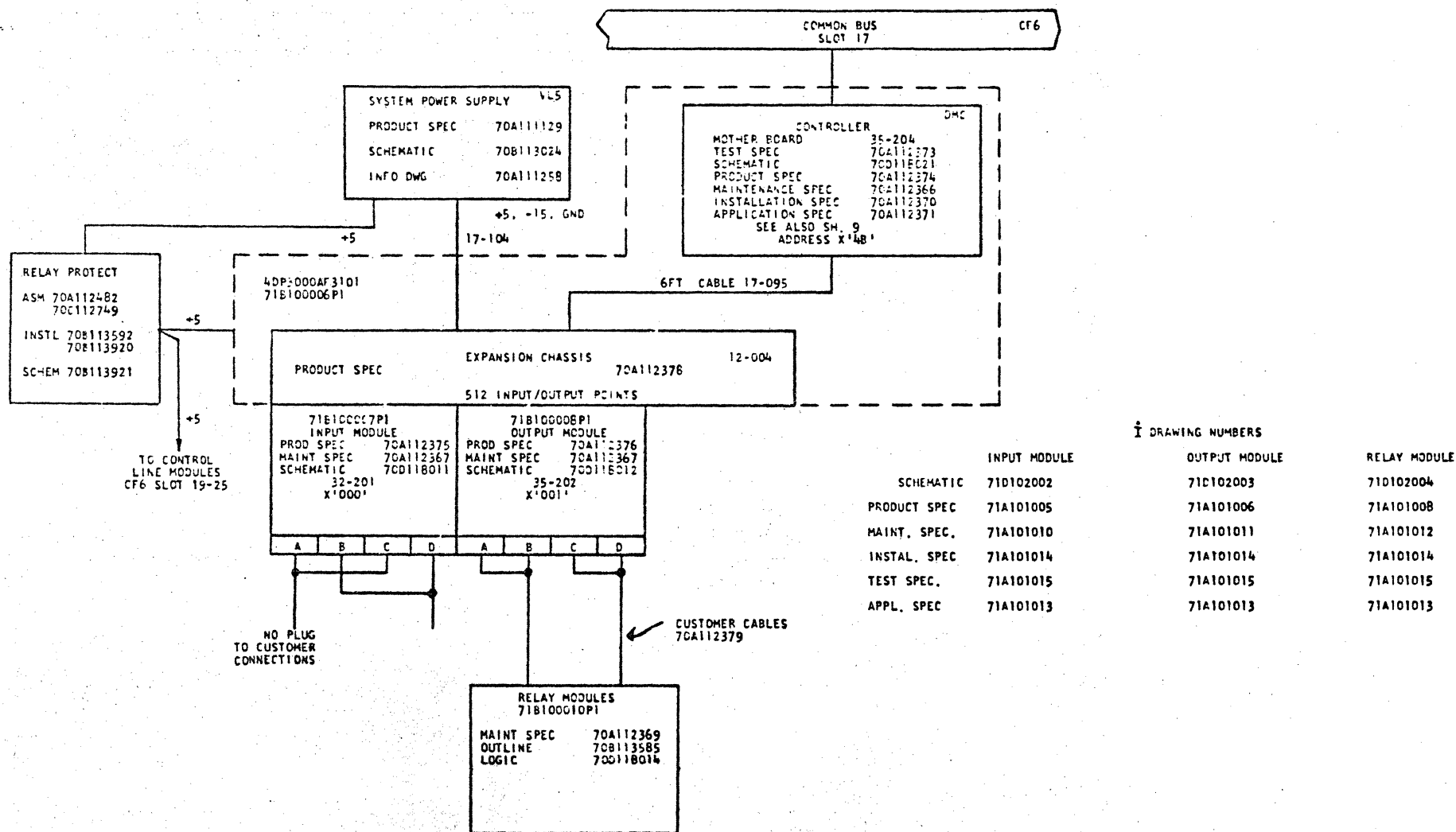
REV. NO.	DATE	BY	CHKD.
1			
2			
3			
4			
5			
6			

LOGIC

PROCESSOR SWITCHING
BDR



ASM	70D195535G8
LOGIC	70C182256
WIRING	70A124698



DRAWING NUMBERS

	INPUT MODULE	OUTPUT MODULE	RELAY MODULE
SCHEMATIC	71D102002	71D102003	71D102004
PRODUCT SPEC	71A101005	71A101006	71A101008
MAINT. SPEC.	71A101010	71A101011	71A101012
INSTAL. SPEC	71A101014	71A101014	71A101014
TEST SPEC.	71A101015	71A101015	71A101015
APPL. SPEC	71A101013	71A101013	71A101013

ALL MATERIAL ABOVE EXCEPT INPUT MODULE 71B100007P1 AND CABLE 4DP4628A ARE PART OF SYSTEM AJM.

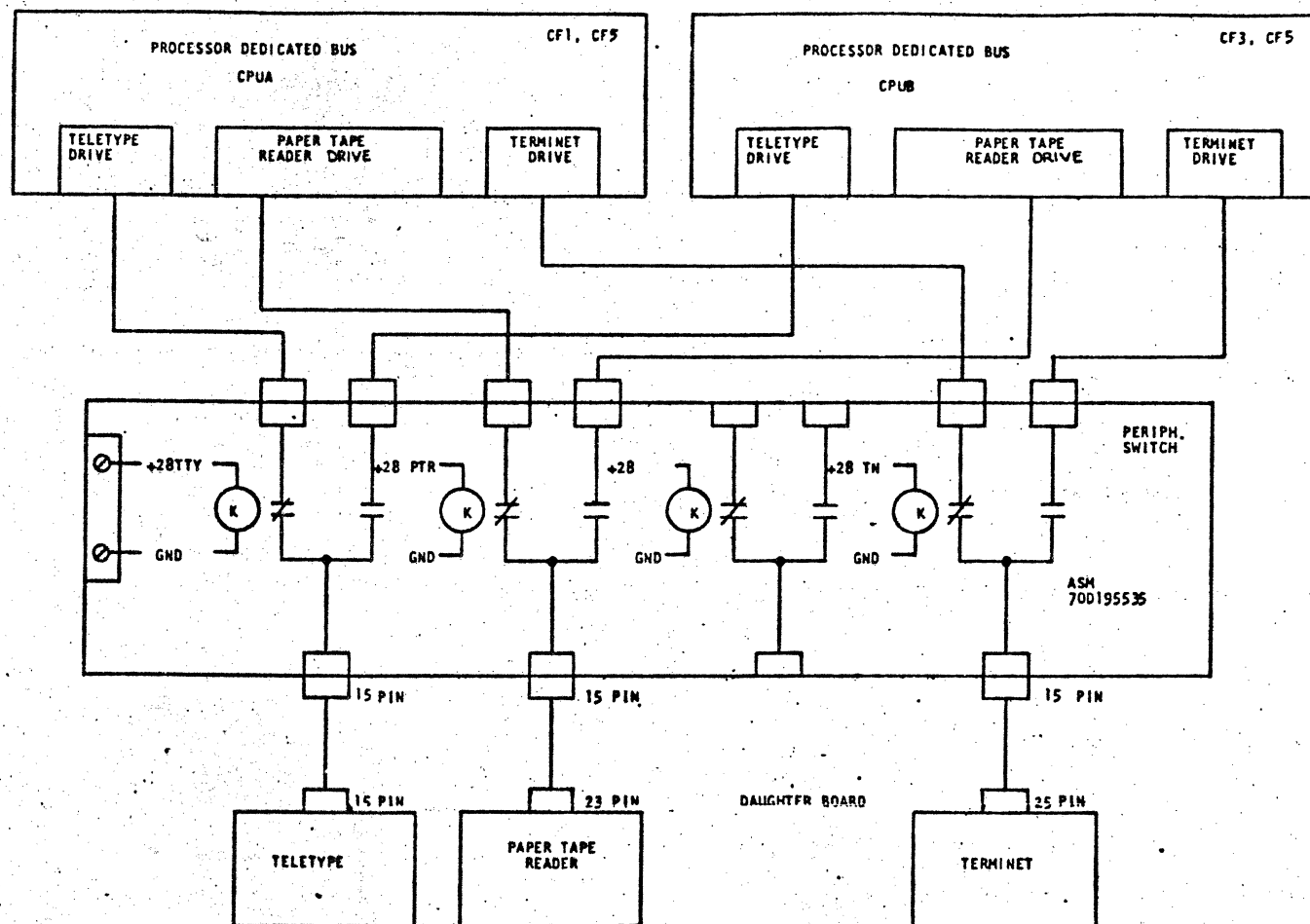
REVISIONS
 1 B C

JULIE GUDIM
 JAN 30 1973

U & PAP
 PHOENIX, ARIZ

71C1821E3

13 12



REV	DATE	BY	CHKD	APP'D
1	B	C	D	E

JULIE GUDIM
JAN 30 1973

U & PAP
PHOENIX, ARIZ.

700 182183

13



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GENERAL ELECTRIC

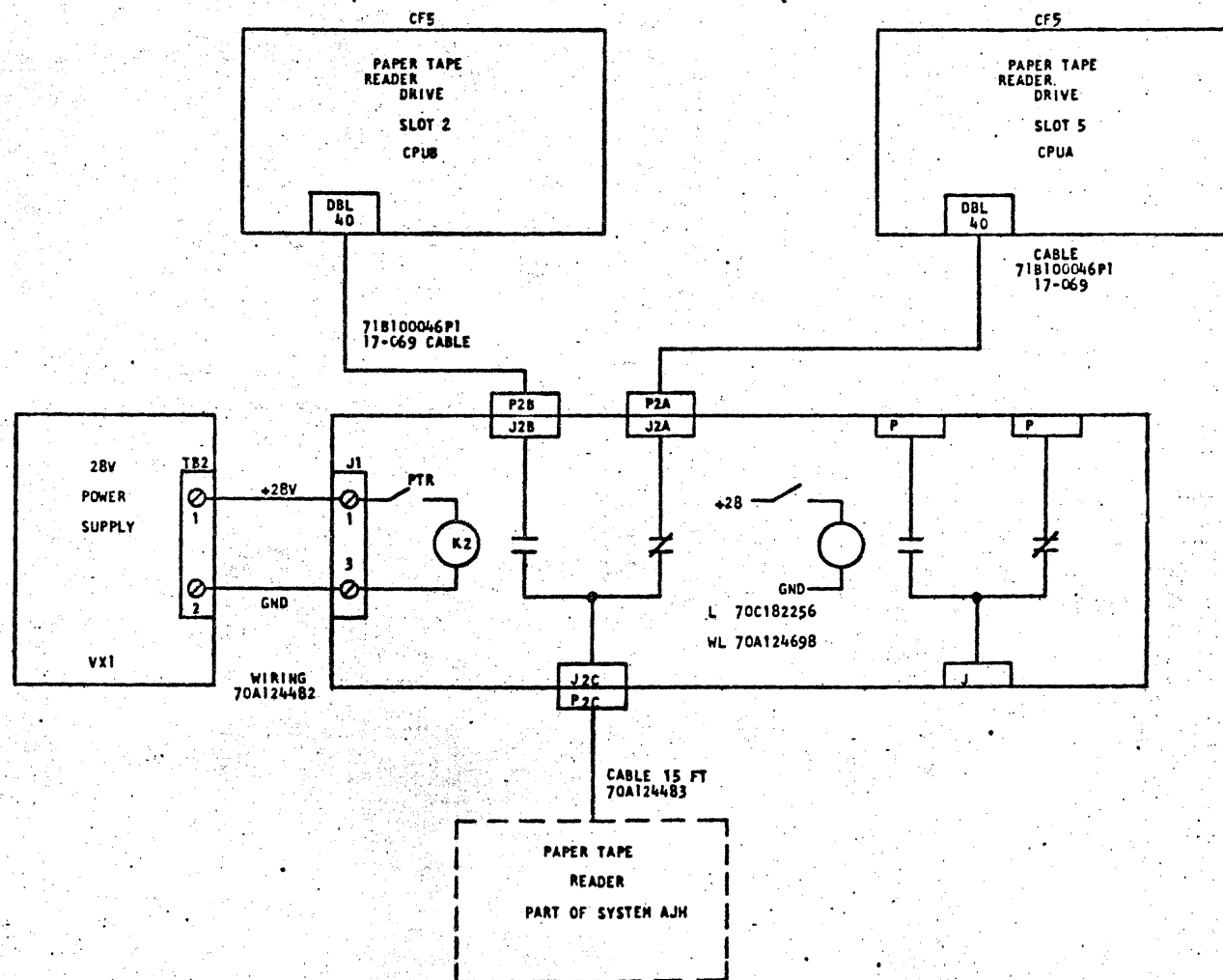
70C182183

16 15

UNLESS OTHERWISE SPECIFIED USE THE FOLLOWING			
APPLIED PRACTICES	SURFACES	TERMINALS AND WIRING	WELDING
	✓	+	+

LOGIC,
PAPER TAPE SUBSYSTEM

FIRST MADE 11M BDR



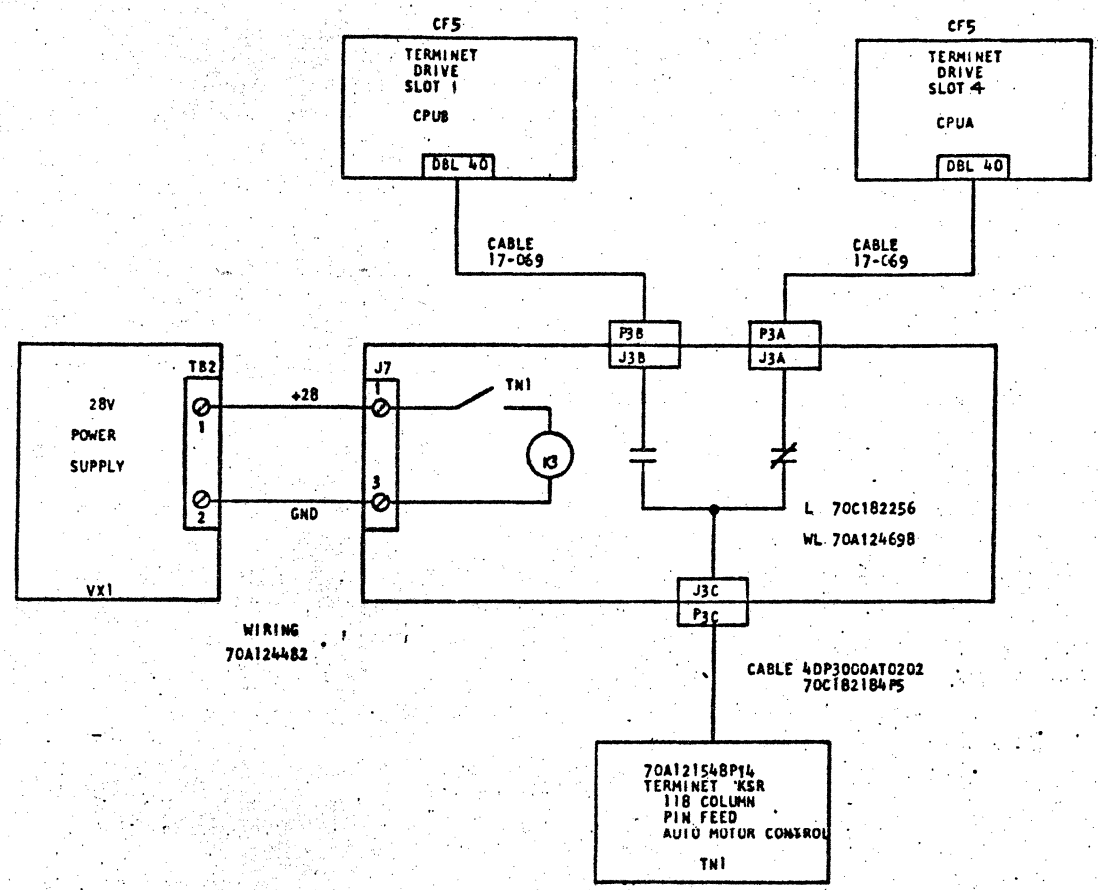
SEE 70C182183, SHT. 18.

REVISIONS	PRINTS TO
B D E	

JULIE GUD:M
JAN 30 1973

U & PAP
PHOENIX, ARIZ.

70C182183
16 15



REVISIONS	PRINTS TO
1	B
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GENERAL ELECTRIC

183

18

17

UNLESS OTHERWISE SPECIFIED USE THE FOLLOWING -
APPLIED PRACTICES

SURFACES

FINISH

TOLERANCE

WELD

TITLE

LOGIC
CARD READER PORTABLE MTG.

FIRST MADE FOR BDR

MUX BUS

4DP3000AE4101
CARD READER INTERFACE
SEE SH. 3
CFA
SLOT 2
ADDR. X104

CS1 CABINET

EXTENSION CABLE 15 FT.
17-053F02

115VAC
60HZ
300VA MAX

AC
POWER CORD

4DP3000AE4201
CARD READER 200 C/MIN
PRODUCT SPEC 70A111420
OUTLINE 70C112732
TEST PROG. 70A112458

REVISIONS

POINTS TO

1 B D

J. GUDIM

JAN 30 1977

U & PAP

PHOENIX, ARIZ.

70C182193

18

17

2

3

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GENERAL ELECTRIC

32183

F 18

UNLESS OTHERWISE SPECIFIED USE THE FOLLOWING			
APPLIED PRACTICES	SURFACES	STRENGTH	TEMPERATURE
	✓	+	+

TITLE
LOGIC,
PAPER TAPE READER INTERFACE
FIRST MADE FOR BDR

SIGNAL	INTERFACE LOCATION	PERIPH SWITCH J2A	PERIPH SWITCH J2B	PERIPH SWITCH J2C	PAPER TAPE READER
CH011	10-40	1	1	1	P1 - A
CH021	20-40	2	2	2	B
CH031	30-40	3	3	3	C
CH041	40-40	4	4	4	D
CH51	50-40	5	5	5	V
CH051	11-40	8	8	8	E
CH061	21-40	9	9	9	F
CH071	31-40	10	10	10	H
CH081	41-40	11	11	11	J
VCKA	51-40	12	12	12	Z
FWD1	71-40	14	14	14	P1 - K
GND	00-40	15	15	15	

SEE 70C182256 SHT. 4

REV. NO.	PRINTS TO
1	1
2	2
3	3
4	4
5	5
6	6

SUSAN O'DONNELL
FEB 23 1973

U&PAP
PHOENIX, ARIZONA

70C182183

F 18

2

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6