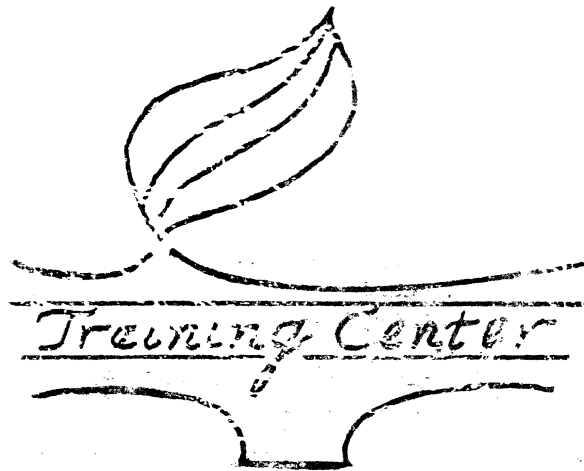


# FACTOR

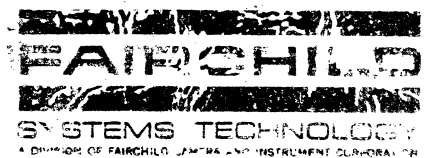
FOR  
SENTRY 600/610/611

A PROGRAMMED INSTRUCTION MANUAL



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FAIRCHILD SYSTEMS TECHNOLOGY  
1725 TECHNOLOGY DRIVE  
SAN JOSE, CALIFORNIA 95110



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## HOW TO USE THIS MANUAL

The text of this manual is self-instructional permitting the reader, as a student, to learn at his own rate. To accomplish this, the text is divided into information frames, reinforcement frames, and answer frames. The information frames, as might be expected, provide the reader with data relating to a particular topic. To reinforce the data presented and to verify your understanding of it, subsequent frame(s) will rephrase a statement of fact and require you to fill in a missing word or group of words, or to respond with an answer to a question. To validate your response, turn to the next page and check the corresponding numbered answer frame. Afterwards, return to the next sequential information frame and continue your study.

1. This is an "information" frame.	
2. A "reinforcement" frame is used to reinforce data or information presented in previous "information" frame.	
3. This is a <u>Reinforcement</u> frame.	
4. After validating your answer for frame 3, you should continue reading here.	

<p>5. This can be information or reinforcement frame.</p>	
<p>6. So can this one.</p>	
<p>7. This one too.</p>	<p>3. <u>Reinforcement</u> This is the "answer" frame for frame 3 on the previous page.</p>
<p>8. If you understand the difference between "information", "reinforcement", and "answer" frames then begin the lesson on Page 1, otherwise go back to frame 1 of this exercise.</p>	

LESSON 1

TOPIC:

AN INTRODUCTION TO FACTOR

GIVEN:

1. The Booklet  
AN INTRODUCTION TO FACTOR
2. Pencil and eraser.

PERFORMANCE:

Student proceeds through the numbered frames, sequentially.

Student writes short responses in the response frames, compares his responses with the data provided in the answer frames and corrects his responses to agree with the furnished answers.

STANDARD:

The student provides 100% correct responses within 20 minutes.

AN INTRODUCTION

TO

FACTOR\*

---

A COMPILER LANGUAGE

FOR THE

SENTRY 600

SENTRY 610

SENTRY II

---

\*Herein referred to as "S600 FACTOR"

1. FACTOR is a human language. People understand it, machines don't.

2. After you have learned to write a FACTOR program, it will have to be translated (compiled) before the Sentry System can react to the information in the program.

3. When the FACTOR program has been compiled (translated) the end result could be called machine language. Some people learn to read this (people are smarter than machines)

4. SO ARE YOU!!!!

(But you're probably not nearly as ornery, stubborn, mule-headed, cussed....etc.,etc., etc.,.....)!

You will learn how to have your FACTOR program translated (compiled) in another lesson. This lesson introduces you to the FACTOR language.



5.

EVERY

Factor statement ends with a semicolon.

;

6.

EVERY

program written with S600 Factor  
starts with a SET PAGE number;  
statement.

7. For example

SET PAGE 10;

is a valid opening statement for an  
S600 FACTOR program. Until you learn  
more FACTOR, don't use a number larger  
than 1024;

8. Try writing a statement that can start  
an S600 FACTOR program.

SET PAGE 200 ;

<p>9. Take a look at your answer to frame 8. Did you make any spelling errors? Did you leave at least one space after SET and after PAGE? Did you remember the semi-colon? Did you use solid capitals?</p>	
<p>10. If you wrote the statement correctly, the compiler would be able to <u>translate</u> (<u>compile</u>) it correctly.</p>	
<p>11. FACTOR is a compiler language using English language-type statements.</p>	
<p>12. FACTOR is <u>unique</u> to <u>SENTRY</u> test systems.</p>	<p>8. SET PAGE 1024;  (Any number between 1 and 1024 is satisfactory during this lesson.)</p>

12. Circle all true statements.

- ~~a.~~ FACTOR is a machine language.
- b. FACTOR is a compiler language.
- c. FACTOR is a programming language for Sentry test systems.
- ~~d.~~ FACTOR is a universal compiler language used by the semiconductor industry.

14. ALL FACTOR statements end with a ; . .

15. FACTOR provides two types of statement:

- 1. Arithmetic and logical control statements.
- 2. Test control statements, which set up and execute tests on electronic devices.

16. You can learn much about the arithmetic and control statements without even seeing a machine

Let's start right now.

<p>17.           A = 6 is algebra</p> <p>              A = 6; is a <u>FACTOR</u> statement.</p>	<p>13. b and c should be circled.</p>
<p>18. The <u>FACTOR</u> statement       <u>A = 6;</u> is <u>not a simple statement of equality.</u></p>	<p>14.           ;</p> <p>If you wrote the word "semicolon", erase it and replace it with ";".</p>
<p>19. <u>A = 6;</u> means store a value of <u>6</u> in a location named <u>A.</u></p>	
<p>20. <u>A = 6;</u>       is a <u>variable assignment statement.</u></p>	

21. You don't have to know where the location called A is. That is the Sentry's problem.

22. Once you have told (programmed) the system that a location, that you name, contains a value, the value is stored in the named location.

23.           A = 6;  
          is a FACTOR  
variable ASSIGNMENT statement.

24.           A = 6; means store 6  
in a location named A.

<p>25. <u>A + 1</u> is an <u>algebraic expression</u>. It can be used in <u>FACTOR</u> as an <u>algebraic expression</u>.</p>	
<p>26. <u>A = A + 1;</u> is a <u>legal FACTOR</u> statement, but it is <u>not an algebraic equation</u>.</p>	<p>22. <u>value</u> <u>location</u></p>
<p>27. <u>A = A + 1;</u> is <u>legal</u> in <u>FACTOR</u> because it is a <u>variable assignment statement</u>, <u>not an equation</u>.</p>	<p>23. assignment</p>
<p>28. <u>A = A + 1;</u> means <u>Fetch the present contents of A</u> <u>Add 1 to the number fetched</u> <u>Assign the sum to location A</u></p>	<p>24. (a value of) 6 <u>location</u> <u>A</u></p>

29. Interpret the following:

SET PAGE 1024;

A = 6;      A = 6

A = A + 1;    A = 6 + 1 = 7

END;

30. In frame 29, the statement

A = A + 1; means

fetch the value 6 from A (Location)

Add 1

Store the sum in "A" Location

31. In frame 29, the final value of A (Location)  
(before END;) is 7.

32. Only the system knows where A is.

<p>33. Is <math>A = A + 1;</math> an <u>algebraic equation</u>? <i>No!</i></p>	
<p>34. <u><math>A + 1 = -A;</math></u> is a <u>MISTAKE</u> (and the <u>compiler</u> will call it a <u>syntax error</u>).</p>	<p>30. <u>6 (location) A</u> <u>1</u> (location) A</p>
<p>35. <u>Location of a FACTOR variable</u> must be defined by a <u>single variable identifier</u>.</p>	<p>31. I have no idea, but I know that location A contains 7.</p>
<p>36. <u>Circle the legal FACTOR statements.</u></p> <p><u>C = 25;</u> <u>A = A + C;</u> 14 = D; C + 2 = 27;</p>	



<p>37. Perhaps you wondered about <u>14 = D;</u></p> <p>This is <u>incorrect</u> because the <u>variable identifier</u> must always be on the <u>left</u> of the <u>equal sign</u>.</p>	<p>33. NO</p>
<p>38. A <u>variable assignment statement</u> can generally be described by</p> <p style="text-align: center;"><u>variable = expression;</u></p>	
<p>39. In frame 34, because the words "<u>variable</u>" and "<u>expression</u>" are written in <u>lower case letters</u>, the <u>programmer is free to use his own variable identifier</u> and his own <u>expression</u>.</p>	
<p>40. <u>variable = expression;</u> is the <u>SYNTACTICAL</u> format of the <u>variable assignment statement</u>. <u>SYNTAX</u> is the <u>set of rules</u> for a language.</p>	<p>36.</p> <p>C = 25; A = A + C;</p>

41. Every S600 FACTOR program must start with

SET PAGE number;

Every FACTOR program ends with END;

42. The words supplied for the SET PAGE number; statement imply, syntactically, that you must write SET PAGE exactly as shown (because of the capitals) but that you may supply your own number.

43. Syntax is the set of rules for a language.

44. FACTOR is a (human/machine) HUMAN language.

<p>45. FACTOR is a (compiler/machine) <u>Compiler</u> language.</p>	<p>41.</p> <p><u>SET PAGE</u></p>
<p>46. FACTOR is a procedural test language used universally by the semiconductor industry.</p> <p>(True/false) <u>False</u>.</p>	
<p>47. Every FACTOR <u>statement</u> ends with a <u>;</u>.</p> <p>Every FACTOR program ends with <u>END</u>.</p>	<p>43.</p> <p><u>SYNTAX</u></p>
<p>48. X = 27; is a FACTOR <u>VARIABLE ASSIGNMENT</u> statement.</p>	<p>44.</p> <p><u>human</u></p>

<p>49. variable = expression; where variable must be a (<u>single variable identifier/any algebraic expression</u>) <u>single variable identifier</u>.</p>	<p>45. <u>compiler</u></p>
<p>50. <u>You may make up your own names (for variable identifiers) with very few exceptions.</u></p>	<p>46. <u>false</u></p>
<p>51. You <u>won't</u> have to <u>memorize the rules for naming variables</u> because:</p> <ol style="list-style-type: none"> <li>1. They are <u>simple</u>.</li> <li>2. The <u>compiler</u> will warn you if you <u>violate a rule</u>.</li> <li>3. As you learn more FACTOR statements the reasons for the rules tend to become obvious.</li> </ol>	<p>47. <u>i</u> <u>END</u></p>
<p>52. The <u>following are acceptable variable identifiers</u>:</p> <p>A CHISAUARE ALARGEIDENTIFIER A1B2C3D4 PHOENIX</p>	<p>48. <u>variable assignment</u></p>

<p>53. The <u>FACTOR</u> compiler <u>accepts only the first eight characters of a variable identifier, ignoring any additional characters.</u></p>	<p>49.</p> <p><u>single variable identifier</u></p>
<p>54. Thus</p> <p>ALARGEIDENTIFIER ALARGEID ALARGEIDEA ALARGEIDEAINDEED</p> <p>are all the same variable name that the compiler recognizes as <u>ALARGEID</u>.</p>	
<p>55. The <u>compiler will not warn you if you use more than eight characters to identify a variable.</u></p>	
<p>56. It is <u>good practice, since the usage of identifiers is totally determined by the programmer, to use names (identifiers) that represent the meaning or use of a variable.</u> TEMP, for instance, could be the name given to a working variable. COUNTER might be the name given a variable that is used as a general purpose counter, and so on. (This does <u>not imply, however, that FACTOR attaches any significance to these names.</u> They are purely artificial devices that aid the user's memory and make a program more intelligible.)</p>	

<p>57. If you use a <u>variable without first assigning a value to it, the <u>initial value becomes zero.</u></u></p> <p>An example is in frame 58.</p>	
<p>58. <u>B = A + 1;</u></p> <p>If this statement is the very first reference to A in the program, <u>zero (0) is stored in A.</u> The statement <u>B = A + 1;</u> results in <u>1 being stored in location B.</u></p>	<p>54.</p> <p><u>ALARGEID</u></p>
<p>59. SET PAGE 1024;        B = 9;        B = A + 4;     <i>A+4 ⇒ 0+4     B (location) CONTAINS 4</i>        END;</p>	
<p>60. Refer to frame 59. The last value stored in the location called B is <u>4</u>.</p>	

61. The following are not acceptable  
variable identifiers:

123 (Identifiers may not start with a  
digit)

AB C (Special characters, including blanks,  
are not allowed)

END (Reserved words are illegal)

62. You are not finished with this lesson  
until you look at the rules on the next page.

60.

4

(If you wrote down 9 or  
13, repeat frames 57-60)

## RULES FOR NAMING VARIABLES

1. A variable name can be of any length up to eight characters. Any additional legal characters are ignored.
2. The first character must be a  
Letter (English alphabet only)  
or  
# (often called a "pound sign").  
or  
\$ (The dollar sign)
3. The rest of the characters can be any combination of  
Any character allowed for the first character (letter, #, \$).  
or  
Numbers (digits)  
or  
. (Period).
4. The following words are reserved for the system and are, therefore, not acceptable variable identifiers.

AND	DCL	GOTO	PAUSE	WR
ASSIGN	DISABLE	GT	PGEN	WRITE
AT	DO	IF	PGM	XCON
BEGIN	ELSE	INSERT	RD	XCONF
BLOCK	ENABLE	LEQ	READ	XPMU
BRANCH	END	LT	REM	
BY	EOR	MEASURE	RESET	
CALL	EQ	NEG	SELECT	
CGEN	EXEC	NEQ	SET	
CONF	FOR	NOISE	SOCKET	
CONN	FORCE	NOT	SUBR	
CLEAR	FUNCT	ON	THEN	
CPMU	GE	OR	THRU	



## LESSON TWO

**TOPIC:**

FACTOR RELATIONAL LOGIC AND BRANCHING

**GIVEN:**

1. The Booklet

FACTOR RELATIONAL LOGIC AND BRANCHING

2. Pencil and eraser

**PREREQUISITE:** COMPLETION OF LESSON ONE

**PERFORMANCE:**

Student proceeds through the number frames responding to branching instructions.

Student writes short responses in the response frames, compares his responses with the data provided in answer frames and corrects his responses to agree with the furnished answers.

**Standard:** The student provides 100% correct responses within one hour.

F A C T O R  
RELATIONAL LOGIC  
AND  
BRANCHING

Special Notice to YOU  
the  
student

If a friend of yours offers to help you in a task, don't offend him by reminding him that the nails go into the board "pointy-end first."

On the other hand, when you are dealing with a computer, you often have to be ridiculously specific.

Some of the frames in this lesson might tend to aggravate you.

e. g.

Getting angry with a machine provides it with correct data <u>False</u> (true/false)
---

If this frame made you angry then programming might not be your bag.

1. The Sentry System, in response to FACTOR programs, can react as if making logical decisions.

2. A= expression is a variable Assignment statement.

3. The English statement "16 is greater than 4" is True (true/false).

$16 > 4$

4. The FACTOR variable assignment state-  
ment A=16 GT 4; stores the value of the  
expression in the location called A.

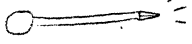
<p>5. GT is a relational operator meaning <u>greater than</u></p>															
<p>6. <u>A=16 GT 4</u>; requires that a value, indicating that the relation 16 GT 4 is true, be <u>in location A</u>.</p>	<p>2. <u>assignment</u></p>														
<p>7. The value stored when <u>variable = relational expression</u> is <u>1 for true</u> or <u>0 for false</u>.</p>	<p>3. true (Watch it!! STUPID was not one of the options)</p>														
<p>8. The relational operators are:</p> <table border="0"> <thead> <tr> <th><u>Symbol</u></th> <th><u>Operation</u></th> </tr> </thead> <tbody> <tr> <td>EQ</td> <td>equal</td> </tr> <tr> <td>GE</td> <td>greater than or equal</td> </tr> <tr> <td>GT</td> <td>greater than</td> </tr> <tr> <td>LT</td> <td>less than</td> </tr> <tr> <td>LEQ</td> <td>less than or equal</td> </tr> <tr> <td>NEQ</td> <td>not equal</td> </tr> </tbody> </table>	<u>Symbol</u>	<u>Operation</u>	EQ	equal	GE	greater than or equal	GT	greater than	LT	less than	LEQ	less than or equal	NEQ	not equal	
<u>Symbol</u>	<u>Operation</u>														
EQ	equal														
GE	greater than or equal														
GT	greater than														
LT	less than														
LEQ	less than or equal														
NEQ	not equal														

<p>9. Practice by completing frames 10 through 12</p>	<p>5. (is) <u>greater than</u></p>
<p>10. GIVEN: SET PAGE 1;  A=6;  B=1;  C=A LT B;      <i>A=6</i>  END;              <i>6 &gt; 1 False</i></p> <p>Location C contains <u>  <i>∅</i>  </u>.</p>	<p>6. <u>stored in (location) A</u></p>
<p>11. SET PAGE 1:  A=6; B=1;  C=A LT B;      <i>6 &gt; 1 False C=∅</i>  D=A GT B;      <i>6 &gt; 1 True D=1</i>  E=D GE C;      <i>1 ≥ 0 True 1</i>  END;</p> <p>Location E contains <u>  <i>1</i>  </u>.</p>	
<p>12. Circle the true relationships</p> <p><u>5 NEQ 6</u>      9 EQ 2  14 LEQ 13      <u>-4 LT 3</u>      <i>-4 &lt; 3</i>  <u>12 GE 6</u>  9 GT 9</p>	

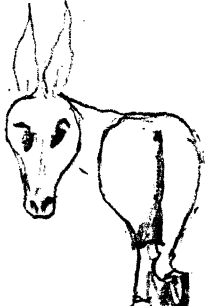
<p>13. If you had trouble with frame 11 <i>No Trouble</i> GOTO frame 15.</p>	
<p>14. GOTO frame 16</p>	<p>10. <u>0</u></p>
<p>15. In frame 11, C is 0 because 6 LT 1 is false, D is 1 because 6 GT 1 is true. Therefore E = D GE C; is E = 1 GE 0; True, therefore E=1.</p>	<p>11. <u>1</u></p>
<p>16. Fill in the blanks <u>GT</u> means greater than NEQ means <u>NOT Equal</u> <u>GE</u> means greater than or equal <u>EQ</u> means equal LT means <u>Less Than</u> <u>LEQ</u> means less than or equal</p>	<p>12. 5 NEQ 6 12 GE 6 -4 LT 3</p>

<p>17. = means <u>Assign To Variable A</u> Value</p>	
<p>18. IF 7 NEQ 28 then write 'true'. <u>True</u></p>	
<p>19. You can cause the <u>Sentry System</u> to respond to the statement in frame 18 if you program as shown in frame 20.</p>	
<p>20. <u>IF 7 NEQ 28 THEN WRITE 'TRUE';</u> <u>Execution of this statement causes the word</u> <u>TRUE to be written out on an output device.</u></p>	<p>16. GT not equal GE EQ less than LEQ</p>



<p>21. <u>IF relation THEN</u> <u>statement;</u></p>	<p>17. <u>assign a value to</u> or <u>store a value in</u></p>  <p>head                      point</p>
<p>22. According to FACTOR syntax you, the programmer, must fill in (refer to frame 21) the <u>relation</u> and a <u>statement</u>.</p>	<p>18. <u>true</u></p>
<p>23. IF 7 EQ 28 THEN WRITE 'TRUE';</p> <p>Is the relation (ship) true? <u>No</u></p>	
<p>24. Should a <u>computer</u> executing the statement in frame 23 write out anything at all? <u>No</u></p>	

<p>25. If 7 is less than 6 then write true in the blank _____.</p>	
<p>26. You didn't have any choice, but to ignore the blank in frame 25, and proceed to the next frame.</p>	<p>22. <u>relation</u> (ship) and a (FACTOR) <u>statement</u>.</p>
<p>27. If 2 EQ 2 then write true in the blank <u>True</u>.</p>	<p>23. NO</p>
<p>28. After you filled in the blank you still had no choice but to proceed to the next frame.</p>	<p>24. NO</p>

<p>29. If 2 EQ 2 then GOTO frame 33.</p>	<p>25.</p> 
<p>30. You must have a problem of some sort or you wouldn't be reading this frame. Go back to frame 29 and do what it says to do.</p>	
<p>31. The branch you just executed is (conditional/unconditional) <u>UNCONDITIONAL</u></p>	<p>27.</p> <p>true</p>
<p>32. GOTO frame 38.</p>	

<p>33. You <u>just</u> performed a <u>conditional branch</u>.  You <u>branched</u> from <u>frame 29</u> to <u>frame 33</u>  because the condition in <u>frame 29</u> is true.</p>	
<p>34. The <u>IF - THEN statement</u> is a form of  <u>conditional</u> branch.</p>	
<p>35. Perhaps you feel that a <u>conditional branch</u>  <u>requires a GOTO</u>.</p>	<p>31.  unconditional</p>
<p>36. <u>IF relation THEN</u>  A=6;  A=A+1;  Now, <u>if the relation is true</u>, <u>A=6</u> is <u>executed</u>.  If the <u>relation is false</u>, the program <u>branches</u>  around <u>A=6</u> and <u>does not execute it</u>. The branch  <u>is conditional</u>.</p>	

<p>37. GOTO frame 31.</p>	
<p>38. The simple GOTO is an <u>unconditional</u> branching statement.</p>	<p>34. conditional</p>
<p>39. The <u>simpliest</u> form of the GOTO statement is <u>GOTO label</u>;</p>	
<p>40. A <u>label</u> is a <u>symbolic statement identifier</u>.</p>	

<p>41. You can <u>make up label names using the same rules you learned for naming variables.</u></p>	
<p>42. <u>Once you have named a variable, that name cannot be used as a label and vice versa.</u></p> <p>Fortunately, the compiler knows this and issues warnings as shown in the next two frames.</p>	<p>38.</p> <p><u>unconditional</u></p>
<p>43.</p> <pre> 000001          SET PAGE 1; 000002          TURKEY=1; 000003          GOTO TURKEY; 000004  TURKEY: END;  TURKEY USE ERROR - DEFINED LABEL 0001B COMPILATION ERRS </pre>	
<p>44.</p> <pre> 000001          SET PAGE 1; 000002  DUCK:    A=2 000003          DUCK=4;  DUCK USE ERROR - DEFINED LABEL  000004          END;  0001B  COMPILATION ERRS </pre>	

<p>45. A <u>label</u> is a <u>symbolic statement identifier</u>.</p>	
<p>46. LOOP: WRITE X; Loop is a <u>symbolic</u> statement identifier which identifies the statement immediately following it (WRITE X;).</p>	
<p>47. Note (frame 46) that a colon was used to separate (<u>delimit</u>) LOOP from WRITE.</p>	
<p>48. But the <u>colon</u> is <u>not part of the label</u> and is <u>not used</u> when the <u>label is referenced</u>. E.g. LOOP: WRITE X; IF X LEQ 6 GOTO LOOP;</p>	

<p>49. A label is a <u>symbolic</u> statement <u>identifier</u>.</p>	
<p>50. A label is delimited by a <u>"."</u> but it does not contain a <u>:</u> <u>when referenced</u></p>	<p>46. symbolic</p>
<p>51. Label names follow the same rules as <u>VARIABLE</u> names.</p>	
<p>52. The same name can be used, in the same main program for both a label and a variable. (true/false) <u>False</u></p>	



<p>53. Now, if you are new to programming, you are probably anxious to see a practical use for all of this.</p>	<p>49. <u>symbolic</u> <u>identifier</u></p>
<p>54. Let us suppose that you wish to prepare a form with numbers printed at the left side of the page.</p>	<p>50. <u>:</u> <u>:</u></p>
<p>55. If you only wanted five numbered lines you would surely use a typewriter.</p> <pre> 1 2 3 4 5 </pre>	<p>51. variable</p>
<p>56. If you wanted to number 9999 lines, consider frame 57.</p>	<p>52. false</p>

```
57. SET PAGE 1;  
X = 1;  
LOOP: WRITE X;  
  
X = X + 1;  
IF X LEQ 9999 THEN  
GOTO LOOP;  
  
END;
```

58. Now that is easier than using a typewriter!  
(Naturally, economics might be a factor,  
too!)

59. You now have some programming power.  
With power you have responsibility also.

60. Careless programming can put a computer into  
a loop from which it can't escape.  
Such a loop is often called an infinite loop.  
An example is in the next frame.

61. SET PAGE 1;

LOOP: X=1;

WRITE X;

X=X + 1;

IF X LEQ 9999 THEN

GOTO LOOP;

END;

62. If you see why frame 61 is an infinite loop

GOTO frame 66.

63. On the first pass thru the program, X starts as

1 then increments to 2. The relation is true.

The program branches to loop which forces X  
to 1, again.

X is never assigned a value greater than 2.

64. If you don't see why 61 is an infinite loop THEN

BEGIN

SHAFT=1;

IF SHAFT LT 2 THEN

GOTO frame 60;

<p>65. I hope you lived through the last page.</p>	
<p>66. The simplest form of the IF-THEN statement is  IF relation THEN statement 1;  Statement 2;  (note: assume statement 1 is not a GOTO  label statement).</p>	
<p>67. Refer to frame 66  If the relation is true,  Statement 1 is executed (true/false) <u>True</u>  Statement 2 is executed (true/false) <u>True</u></p>	
<p>68. Refer to frame 66  If the relation is false,  Statement 1 is executed (true/false) <u>False</u>  Statement 2 is executed (true/false) <u>True</u></p>	

<p>69. Often, it is desirable to execute more than one statement if the relation is true.</p>	
<p>70. This can be accomplished by nesting the statements between  BEGIN  and  END;</p>	
<p>71. Look closely at frame 70.  Is there a semicolon after BEGIN?  <u>No</u>  Is there a semicolon after END? <u>yes</u></p>	<p>67.  <u>true</u>  <u>true</u></p>
<p>72. Past experience shows that many programmers forget the information in frame 71  IF N LT 2 THEN  BEGIN  N=N + 1;  GOTO frame 71;  END;</p>	<p>68.  <u>false</u>  <u>true</u></p>

<p>73. If the <u>relation</u> in frame 74 is true, statements <u>1 thru n are executed before statement X.</u>  If the <u>relation is false</u> <u>statements 1 thru n are not executed</u> (i. e. program branches directly to statement X.)</p>	
<p>74. IF relation THEN  BEGIN  Statement 1;  Statement 2;  :  :  Statement n;  END;  Statement X;</p>	
<p>75. If statement n in frame 74 is a GOTO label statement, Statement X (must, cannot, might) <u>might</u> be executed.</p>	<p>71. <u>no</u> <u>yes</u></p>
<p>76. The correct response for frame 75 requires you to realize that the location of the label could cause a branch that bypasses statement X;</p>	

<p>77. The <u>"END"</u>, statement that <u>closes a nest of statements started by "BEGIN"</u> does not <u>end the program.</u></p>	
<p>78. A FACTOR program may contain more than one "END;" statement. (true/false) <u>True</u></p>	
<p>79. If you <u>fail to close the group of statements started</u> with <u>"BEGIN"</u>, <u>all statements following "BEGIN"</u> are <u>seen</u> by the <u>compiler</u> as <u>inside the nested group.</u> The next <u>"END;"</u> card would <u>close</u> the group.</p>	<p>75. <u>might</u></p>
<p>80. Fortunately, the <u>compiler counts the number of "END;"</u> statements in your program. If it reads too <u>few</u>, it writes <u>END OF FILE INPUT</u> and points an arrow at the <u>last statement it read.</u></p>	

<pre> 81. 000001   SET PAGE 1;       000002   A=1; B=9;       000004   IF A LT B THEN       000005   BEGIN       000005       BUCKET =B;       000006       B=A;       000007       A=BUCKET;       000010       END;       END OF FILE INPUT </pre>	
<p>82. In frame <u>83</u> is an example of a <u>properly ended</u> program.</p> <p>Note the statement</p> <p>"0000<u>B</u> COMPILATION ERRS"</p> <p>(the "B" reminds you that the number of errors is printed in Octal)</p>	<p>78.</p> <p><u>true</u></p>
<pre> 83. 000001   SET PAGE 1;       000002   A=1; B=9;       000004   IF A LT B THEN       000005   BEGIN       000005       BUCKET =B,       000006       B=A;       000007       A=BUCKET;       000010       END;       000010       END;       0000B   COMPILATION ERRS </pre>	
<p>84. In the following frames are examples of coding.</p> <p>Each example is followed by a compiler listing.</p> <p>Try to spot the errors in each example before "peeking" at the compiler listing.</p>	



85.

```

SET PAGE 1;
A=1; B=9;
IF A LT B THEN
  BEGIN;
    BUCKET = B;
    B=A;
    A= BUCKET;
  END;
END;

```

*BEGIN does not HAVE A semicolon Ending it*

86.

```

000001 SET PAGE 1;
000002 A=1; B=9;
000004 IF A LT B THEN
000005 BEGIN;
INVALID TERMINATOR ^
000005 BUCKET = B;
000006 B=A;
000007 A= BUCKET;
000010 END;
000010 END;
0001B COMPILATION ERRS

```

87.

```

SET PAGE 1;
A=1; B=9;
IF A LT B THEN
  BEGIN
    BUCKET = B;
    B=A;
    A= BUCKET;
  END;
END;

```

*A=1 needs a semicolon to End the statement*

88.

```

000001 SET PAGE 1;
000002 A=1; B=9;
EXPRESSION SYNTAX ^
000003 IF A LT B THEN
000004 BEGIN
000004 BUCKET = B;
000005 B=A;
000006 A= BUCKET;
000007 END;
000007 END;
0001B COMPILATION ERRS

```

Note:

The compiler's interpretation of the error may differ from yours. i.e. | B = 9 is an improper expression.

89.

```

SET PAGE 1;
A=1; B=9;
IF A LT B NO THEN
  BEGIN
    BUCKET = B;
    B=A;
    A= BUCKET;
  END;
END;

```

90.

```

000001 SET PAGE 1;
000002 A=1; B=9;
000004 IF A LT B
000005 BEGIN
STATEMENT SYNTAX ^
000005 BUCKET = B;
000006 B=A;
000007 A= BUCKET;
000010 END;
000010 END;
0001B COMPILATION ERRS

```

Note:

The compiler does not detect that "THEN" is missing until it reads "B" of BEGIN.

91.

```

SET PAGE 1;
A=1; B=9;
IF A LT B THEN
  BEGIN
    BUCKET = B;
    B=A; is not semicolon
    A= BUCKET;
  END;
END;

```

92.

```

000001 SET PAGE 1;
000002 A=1; B=9;
000004 IF A LT B THEN
000005 BEGIN
000005 BUCKET = B;
000006 B=A;
EXPRESSION SYNTAX ^
000007 A= BUCKET;
000007 END;
000007 END;
0001B COMPILATION ERRS

```

You expected maybe 'improper semicolon' ?

93.

```
SET PAGE 1;  
A=1; B=9;  
IF A LT B THEN  
  BEGIN  
    BUKCET = B;  
    B=A;  
    A= BUCKET;  
  END;  
END;
```

*None*

94.

```
000001 SET PAGE 1;  
000002 A=1; B=9;  
000004 IF A LT B THEN  
000005 BEGIN  
000005 BUKCET = B;  
000006 B=A;  
000007 A= BUCKET;  
000010 END;  
000010 END;  
00000E COMPILATION ERRS
```

Quote:

"Compiler quite happy pounding  
on pointy end of nail".

— Fucius Kahn

95. The program in frame 94 executes ending up with zero stored in A. You see B is stored in BUKCET.

96.

E N D ;

### LESSON THREE

TOPIC: NUMBERS, COMPILED AND PRINTED

GIVEN:

1. The Booklet

NUMBERS, COMPILED AND PRINTED

2. Pencil and eraser

PREREQUISITE: COMPLETION OF LESSONS ONE AND TWO

PERFORMANCE:

Student proceeds through the number frames responding to branching instructions.

Student writes short responses in the response frames, compares his responses with the data provided in answer frames and corrects his responses to agree with the furnished answers.

Standard: The student provides 100% correct responses within 30 minutes.

NUMBERS,  
COMPILED  
AND  
PRINTED

1. Integers may be entered by variable assignment statements

For instance

DECINT=8;

2. The FACTOR compiler recognizes a number as Octal if it is immediately followed by B.

For instance

OCTINT=10B;

3. No imbedded characters are allowed in an integer or between an octal integer and B.

#### 4. INTEGERS

##### ACCEPTABLE

8  
10B  
+9  
+10B  
+71  
-77B

##### UNACCEPTABLE

8 0  
2 000  
27\_B  
9\_B

5. There is a limit to the magnitude of integer inputs.

It is

37777777B or, in

decimal form 8388607.

6. ACCEPTABLE

+8388607

8388607

-8388607

+37777777B

37777777B

-37777777B

UNACCEPTABLE

+8388608

8388608

-8388608

+40000000B

40000000B

-40000000B

7. There is no compiler warning if you enter an integer that is too large.

See frame 8.

8. This entry

A=8388608;

results in

A=-1;

G O T C H A !!!

9. Decimal numbers may be programmed with a fractional part preceded by a period. Octal numbers cannot. The string cannot exceed 8388607.

10.	<u>Acceptable</u>	<u>Unacceptable</u>
	20.2	37.1B
	838860.7	838860.8
	999.999	99999.99
	.00002	

11. No number can end with a period.

<u>Acceptable</u>	<u>Unacceptable</u>
88.0	88.

12. Circle the unacceptable input values.

100.1	8388607.1
37.77B	9.0
267.	40000000
34218B	1,024



<p>13. <u>Decimal</u> numbers may be followed by a <u>"power of ten"</u> multiplier <u>specified as E</u> followed by an <u>integer</u>. An example is in frame 14.</p>							
<p>14.                    <u>17 E6</u>                           means                           17 times 10<sup>6</sup></p>							
<p>15. <u>Numbers</u> expressed as <u>shown in frame 14</u> are often called <u>"exponentials"</u> or are said to be in <u>"engineering format"</u>.</p>							
<p>16. The <u>exponent</u> (<u>following the E</u>) <u>must be</u> a decimal <u>integer</u>. It <u>can</u> be <u>signed</u> or <u>unsigned</u>. FACTOR <u>accepts</u> an <u>unsigned</u> integer as <u>positive</u>.</p>	<p>12.                    UNACCEPTABLE</p> <table> <tr> <td>37.77B</td> <td>8388607.1</td> </tr> <tr> <td>267.</td> <td>40000000</td> </tr> <tr> <td>34218B</td> <td>1,024</td> </tr> </table>	37.77B	8388607.1	267.	40000000	34218B	1,024
37.77B	8388607.1						
267.	40000000						
34218B	1,024						

17.

ACCEPTABLE

2E10  
+2.4E-10  
-6E03  
-14E+2  
8388607E2

UNACCEPTABLE

2E10B  
+2.4E-10.1  
-6.E03  
-14BE+2  
8388608E2

18.

NOTE

8388607E2 is acceptable

838860700 is not !!!

19. Exponential values can exceed the limit  
for integers.

(true/false) True

20. There is a limit, even for exponentials.

The maximum range of FACTOR numbers is

(approximately)

2.7105E-20  $\leq$  /N/  $\leq$  9.2223E+8 where /N/ means  
the magnitude of N.

<p>21. FACTOR stores <u>all</u> numbers as <u>floating point numbers</u>.</p> <p>Thus    A= 100;           B= 144B;           C= 1E2;</p> <p>cause identical data to be stored in A, B and C.</p>	
<p>22. The <u>mantissa</u> of the <u>floating point format</u> is <u>16</u> (binary) <u>bits</u>. Therefore, the <u>resolution</u> of <u>numbers</u> stored in your FACTOR program is <u>1 part in 65535</u>.</p>	
<p>23. The resolution of the floating point is <u>much</u> more than you will need for <u>testing semiconductor devices</u>. But if you <u>insist</u> on trying to <u>add 65535</u> and 0.1, you can <u>succeed in wasting</u> the computer's (and your) <u>time</u>.</p>	<p>19.</p> <p><u>true</u></p>
<p>24. The <u>numerical</u> format <u>resulting</u> from <u>WRITE</u> statements is <u>different</u>, but <u>simpler</u>.</p>	

25. ONLY FACTOR WRITE STATEMENTS  
TO THE LINE PRINTER OR THE VIDEO  
SCREEN ARE WITHIN THE SCOPE OF  
THIS LESSON.

26. The general form for writing a numeric  
output is  
WRITE (LP) expression;  
to write to the line printer  
or  
WRITE (TTP) expression;  
to write to the video screen

27. TTP is actually a mnemonic for TeleType  
Printer. Most systems use a VKT (Video Key-  
board Terminal) which is a compatible alternative  
to the teletype. Hence TTP can mean the video  
screen of the VKT.

28. WRITE (LP) expression;  
or  
WRITE (TTP) expression;  
always  
writes a decimal number  
on the appropriate device

29. consider:  
SET PAGE 1;  
A = 10B;  
WRITE (LP) A;  
END;

30. The program in frame 29 prints

8  
on the line printer

31. consider:  
SET PAGE 1;  
B = -8;  
WRITE (LP) B;  
END;

32. The program in frame 31 writes

-008  
on the line printer

33. The plus sign is suppressed for positive integer printouts. The minus sign is not. Integer printouts are limited to four characters.

34. The maximum positive number that the LP or TTP can print out is 9999.  
The maximum negative is -999.

35. Consider:  
SET PAGE 1;  
X= 16384;  
WRITE (TTP) X;  
END;

36. The program in frame 35  
writes  
+1.638E+04  
on the TTP.

<p>37. Is 16384 an integer? <u>yes</u>  Does it print as an integer? <u>No</u></p>	
<p>38. How many characters were printed out in frame 36?    10</p>	<p>34.  <u>9999</u>  <u>-999</u></p>
<p>39. <u>Non-integer printouts always appear as a signed four-digit decimal number between 1.000 and 9.999 with a signed two digit exponent. Plus signs are not suppressed.</u></p>	
<p>40. Consider:  SET PAGE 1;  WRITE (LP) 409.6;  END;  This program prints <u>4.096 E+02</u>  on the line printer.</p>	

<p>41. In frame 42, write the given number as it would appear on the line printer or video screen.</p>	<p>37.</p> <p><u>yes</u></p> <p><u>no</u></p>										
<p>42.</p> <table border="1" data-bbox="321 533 802 716"> <thead> <tr> <th>Given</th> <th>Output</th> </tr> </thead> <tbody> <tr> <td>-27B</td> <td>-023</td> </tr> <tr> <td>+21E1</td> <td>210</td> </tr> <tr> <td>13000</td> <td>1.300E+04</td> </tr> <tr> <td>-6.07</td> <td>-6.070E-00</td> </tr> </tbody> </table>	Given	Output	-27B	-023	+21E1	210	13000	1.300E+04	-6.07	-6.070E-00	<p>38.</p> <p><u>10</u></p>
Given	Output										
-27B	-023										
+21E1	210										
13000	1.300E+04										
-6.07	-6.070E-00										
<p>43. Numbers <u>printed out</u> are <u>"rounded off"</u>, <u>not truncated</u>.</p>											
<p>44.</p> <p style="text-align: center;">E N D</p> <p>(The answer to frame 42 is on the next sheet).</p>	<p>40.</p> <p>+4.096E+02</p>										



	<p>42.</p> <table border="1"> <thead> <tr> <th>Given</th> <th>Output</th> </tr> </thead> <tbody> <tr> <td>-27 B</td> <td>-023</td> </tr> <tr> <td>+21E1</td> <td>210</td> </tr> <tr> <td>13000</td> <td>+1.300E+04</td> </tr> <tr> <td>-6.07</td> <td>-6.070E-00</td> </tr> </tbody> </table>	Given	Output	-27 B	-023	+21E1	210	13000	+1.300E+04	-6.07	-6.070E-00
Given	Output										
-27 B	-023										
+21E1	210										
13000	+1.300E+04										
-6.07	-6.070E-00										

## LESSON FOUR

TOPIC:

FACTOR ARITHMETIC EXPRESSIONS

GIVEN:

1. The Booklet  
FACTOR ARITHMETIC EXPRESSIONS
2. Pencil and eraser

PREREQUISITE: COMPLETION OF LESSONS ONE, TWO AND THREE

PERFORMANCE:

Student proceeds through the number frames responding to branching instructions.

Student writes short responses in the response frames, compares his responses with the data provided in answer frames and corrects his responses to agree with the furnished answers.

Standard: The student provides 100% correct responses within 40 minutes.

FACTOR  
ARITHMETIC  
EXPRESSIONS

1. Somewhere, at some time, you probably learned that XY is the product of a variable named X and a variable named Y. In other words, XY means X times Y.

2. The FACTOR compiler recognizes XY as the name of a VARIABLE location or of a symbolic statement identifier called a LABEL.

3. The FACTOR multiplication operator is the asterisk (\*).  
Thus X\*Y means X times Y to the FACTOR compiler.

4. Write the FACTOR statement that stores the product of R and T in location D.

D=R\*T;

<p>5. Write the FACTOR statement equivalent to the algebraic equation <math>C = 2\pi r</math></p> <p><math>A = 3.14;</math>  <math>C = 2 * A * R;</math></p>	
<p>6. Frame 5 illustrates a very practical point. There is no point in forcing a machine to look up (access) a number unless you have a good reason for it. Accessing takes a finite amount of time.</p>	<p>2.</p> <p><u>variable</u> <u>label</u></p>
<p>7. Which is faster?</p> <p>example a. <math>C = 6.28 * R;</math>  example b. <math>PI = 3.14;</math>  <math>C = 2 * PI * R;</math></p> <p>example <u>A. <math>C = 6.28 * R;</math></u></p>	
<p>8. Circle the incorrect FACTOR statements</p> <p><math>A = B * C;</math>  <math>Z = MN;</math>  <math>Q = 9T;</math></p>	<p>4.</p> <p><math>D = R * T;</math></p>

<p>9. HOLD IT!!!</p> <p>If you circled <math>Z=MN</math> in frame 8, you jumped to the conclusion that M and N were different variables. MN is a legitimate variable name. Look at the compiler's opinion.</p>	<p>5.</p> $\frac{C = 2 * 3.14 * R;}{\text{or}}$ $\frac{C = 6.28 * R;}{\text{or}}$ <p>PI = 3.14; - C = 2 * PI * R; etc.</p>
<p>10.</p> <pre> 000001      SET PAGE 1; 000002      A = B * C; 000003      Z = MN; 000004      Q = 9T; EXPRESSION SYNTA^ 000005      END; 0001B      COMPILATION ERRS </pre>	
<p>11. FACTOR uses the traditional <u>+</u> for an <u>addition operator</u> and <u>-</u> for <u>subtraction</u>.</p>	<p>7.</p> <p><u>a</u></p>
<p>12. The algebraic equation <math>A = B + C + D + E</math> is easily programmed as</p> $A = B + C + D + E;$ <p>Similarly <math>F = G - H + I - K</math> is programmed as</p> $F = G - H + I - K;$	<p>8.</p> <p><u>Q = 9T;</u></p>

13. The compiler scans an expression  
from left to right just as you (usually) do.

14. The compiler  
ALWAYS  
scans an expression from left to right.

15. Consider:

$A = P + PRT$ , where P, R and T are separate  
variables.

You probably would "group" this equation like  
this

$$A = P (1 + RT)$$

16. But you wouldn't try to program that as

$$A = P * 1 + R * T;$$

No! No! No! No! No!

17. The FACTOR compiler recognizes parentheses as grouping symbols.

18. Write a FACTOR statement equivalent to

$$A = P(1 + RT).$$

$$\underline{A = P * (1 + R * T);}$$

19. The FACTOR operator for division is the slash (/).

That is, Percent =  $N \div 100$

can be programmed as

$$\text{PERCENT} = N/100;$$

20. Write the FACTOR statement equivalent to

$$X = \frac{A+B}{C+D}$$

$$\underline{X = (A + B) / (C + D);}$$



<p>21. The compiler <u>ALWAYS</u> scans an expression from <u>LEFT</u> to <u>RIGHT</u></p>	
<p>22. The FACTOR operator for division is <u>" / " A slash</u></p>	<p>18. A = P*(I+R*T);</p>
<p>23. Since the FACTOR operator for <u>division cannot group terms</u> you <u>must use parentheses</u> to program a fraction where the numerator or denominator is a polynomial.</p>	
<p>24. <math>\frac{A + B}{C + D}</math> <del>X</del> A + B/C + D</p> <p>You know it. I know it.</p> <p>BUT</p> <p>The FACTOR compiler will never know what that "thing" on the left is.</p>	<p>20. X = (A + B) / (C + D);</p>

<p>25. The FACTOR operator for <u>exponentiation</u> is the up arrow (<math>\uparrow</math>).</p> <p>That is</p> <p>Given: <math>X = \sqrt{y}</math></p> <p>Program: <math>X = Y \uparrow (\frac{1}{2})</math>;</p>	<p>21.</p> <p><u>always</u></p> <p><u>left</u></p> <p><u>right</u></p>
<p>26. Since <u>NEGATION</u> takes <u>precedence over EXPONENTIATION</u> never try to <u>raise a negative number to a power</u>. IF <u>Y</u> in frame 25 were <u>negative</u>, X would be <u>"imaginary"</u>. FACTOR handles only <u>real numbers</u>.</p>	<p>22.</p> <p>/</p>
<p>27. Given: <math>N = 3 * 4 \uparrow 2</math>;</p> <p>Result: 48 (not 144) is stored in N.</p> <p>Because <u>exponentiation</u> has <u>precedence over multiplication</u></p>	
<p>28. The FACTOR compiler ALWAYS scans an expression from left to right but <u>performs operations in order of precedence</u>.</p> <p>Read the rules on the next page.</p>	

RULES FOR EVALUATION OF FACTOR  
ARITHMETIC EXPRESSIONS

Arithmetic expressions are evaluated left-to-right according to the following rules:

- 1.) Parenthesized expressions are evaluated first. If parenthesized expressions are nested, the innermost expression is evaluated, then the next innermost until the entire expression has been evaluated.
- 2.) Within parenthesis and/or whenever parenthesis do not govern the order or evaluation, the hierarchy of operations in order of precedence is
  - a.) Negation (NEG)
  - b.) Exponentiation ( $\uparrow$ )
  - c.) Multiplication or division (\*, /),
  - d.) Addition or subtraction (+, -).

Example:

The expression

$$A * (Z - (B \uparrow C) / T) + VAL$$

is evaluated in the following sequence:

$$B \uparrow C \rightarrow e_1 \quad \text{NOTE: That is, } B \uparrow C \text{ is temporarily held as partial solution } e_1.$$

$$e_1 / T \rightarrow e_2$$

$$Z - e_2 \rightarrow e_3$$

$$e_3 * A \rightarrow e_4$$

$$e_4 + VAL \rightarrow e_5$$

29. Program the equation

$$A = P (1 + RT)^N$$

where R is rate and T is time

$$A = P * ((1 + R * T) \uparrow N);$$

30. CAUTION

The Sentry's computer does not handle "imaginary" numbers. Therefore B↑C is not allowed when B is negative, since the result could be "imaginary".

31. Circle the incorrect FACTOR statements

$$W = 5 \uparrow 2;$$

$$X = -5 \uparrow 2;$$

$$Y = -(5 \uparrow 2);$$

$$Z = 5 \uparrow (-2);$$

32. Negation takes highest precedence! The minus sign in front of the 5 in the value assigned to X (frame 31) is a negation not a subtraction.

<p>33. Compare frame 34 with frame 32.</p>	<p>29.</p> $A = P * (1 + R * T) \uparrow N;$
<p>34. <math>Q = 15 - 3 \uparrow 2;</math>  <u>is just fine.</u> Plus 9 is <u>subtracted</u> from 15.  This time the <u>minus sign</u> is a <u>subtraction</u>  operator.</p>	
<p>35. <u>Negation</u> can be <u>programmed</u> with the symbol "<u>NEG</u>"  <u>or with the minus sign</u> in a <u>context</u> other than <u>sub-</u>  <u>traction.</u>  Note examples in frame 36.</p>	<p>31.</p> $X = -5 \uparrow 2$
<p>36. <math>A = -6;</math>  <math>A = \text{NEG } 6;</math>  <math>C = D * \text{NEG } E;</math>  <math>C = D * - E;</math>  <math>C = D * (-E);</math></p>	<p>All these examples are permis-  sable but most programmers use  only the <u>first</u> and last forms, a-  voiding the others as unnecessar-  ily confusing.</p>

## LESSON FIVE

TOPIC:

PIN DEFINITIONS

GIVEN:

1. The booklet: Pin Definitions
2. Pencil and eraser

PREREQUISITE:

Completion of Lesson Four

PERFORMANCE:

Student proceeds through the number frames responding to branching instructions.

Student writes short responses in the response frames, compares his responses with the data provided in answer frames and corrects his responses to agree with the furnished answers.

Standard: The student provides 100% correct responses within one hour.

PIN DEFINITIONS

A PROGRAMMED STUDY AID

SENTRY 600 PROGRAMMING

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1725 Technology Drive  
San Jose, California 95110

## PIN DEFINITIONS

1. In this lesson, a pin refers to a land on a pin electronics card that mates with a connector on the performance board.

2. A pin is a LAND on an electronics card.

3. There are two sets of identical pin electronics per card; hence, two electronically identical pins per card.

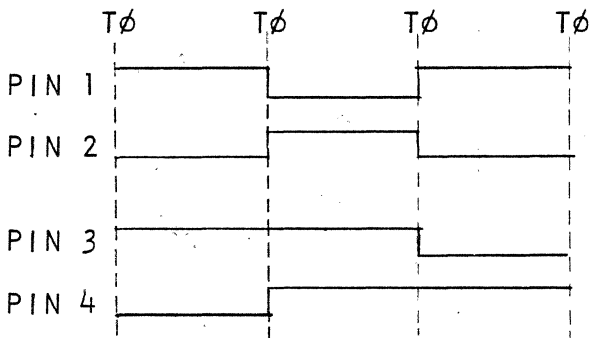
4. Therefore, the pin electronics of:  
A "30-PIN" Sentry 600 requires 15 cards.  
A "60-PIN" Sentry 600 requires 30 cards.



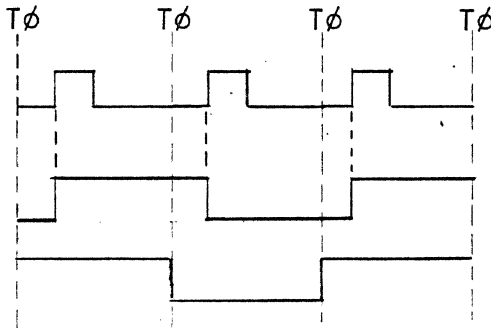
<p>5. Since <u>all</u> pins are <u>electronically identical</u>, <u>any</u> pin can be programmed as either <u>an input</u> or an <u>output</u> pin.</p>	
<p>6. The <u>terms input</u> and <u>output</u> are relative to the <u>DUT</u>; an <u>input</u> pin connects an <u>input</u> to the <u>DUT</u> from the Sentry 600.</p>	<p>2.</p> <p>land</p>
<p>7. An input pin connects an input <u>To</u> the DUT <u>From</u> the Sentry 600.</p>	
<p>8. <u>Input</u> pins may be used to <u>apply power</u>, <u>clocks</u> or <u>data</u> to the <u>DUT</u>.</p>	<p>4.</p> <p><u>15</u></p> <p><u>30</u></p>

<p>9. Input pins may be used to apply <u>power</u>, <u>CLOCK</u> or <u>data</u> to the DUT.</p>	
<p>10. <u>Power</u> pins <u>provide</u> a <u>steady DC</u> voltage to the <u>DUT</u>.</p>	
<p>11. <u>Data</u> pins normally apply <u>NRZ (non-return-to-zero)</u> <u>data</u> to the <u>DUT</u>.</p>	<p>7.</p> <p><u>to</u></p> <p><u>from</u></p>
<p>12. Data pins normally apply NRZ (<u>Non - return -</u> <u>To - zero</u>) data to the DUT.</p>	

<p>13. The <u>source of the data</u> is <u>local memory</u> in the <u>high speed test station controller</u>.</p>	<p>9.</p> <p><u>power</u> <u>clocks</u> <u>data</u></p>
<p>14. The <u>source</u> of the data is <u>Local Memory</u> in the high speed test station controller.</p>	
<p>15. <u>NRZ</u> data stays at the <u>programmed level</u> for a <u>period equal</u> to that programmed by the <u>SET PERIOD</u> statement.</p>	
<p>16. NRZ data, stays at the programmed level for a period <u>Equal</u> to that programmed by the SET PERIOD statement.</p>	<p>12.</p> <p>non-return-to-zero</p>

<p>17. NRZ data stays at the <u>PROGRAMED</u> <u>LEVEL</u> for a period <u>EQUAL</u> to that programmed by the <u>SET PERIOD</u> statement.</p>	
<p>18. Compare the statements in frame 19 with the waveforms in frame 20.</p>	<p>14.</p> <p><u>source</u></p> <p><u>local memory</u></p>
<p>19. SET DA 1111 . . . ;  SET F 1010 . . . ; REM 1st PERIOD DATA;  SET F 0111 . . . ; REM 2nd PERIOD DATA;  SET F 1001 . . . ; REM 3rd PERIOD DATA;  SET PERIOD 500E-9, RNG0;  ENABLE TEST;</p>	
<p>20. Assume positive logic</p> 	<p>16.</p> <p><u>equal</u></p>

<p>21. The simplest programming for a data pin, <u>syncs</u> the <u>data</u> with the <u>programmed period</u>. Review frames 19 and 20, noting the data transition points, then go on to frame 22.</p>	<p>17.</p> <p><u>programmed level</u> <u>equal</u> <u>SET PERIOD</u></p>
<p>22. The simplest programming case for a data pin applies (NRZ/RZ) <u>NRZ</u> data to the pin. This data is in synchronization with the <u>Programmed</u> period.</p>	
<p>23. <u>NRZ data</u> can be <u>synchronized</u> with a <u>timing generator</u> by <u>connecting a timing generator</u> to the <u>pin</u>.</p>	
<p>24. NRZ data can be synchronized with a <u>Timing Generator</u> by connecting it to the pin.</p>	

<p>25. Frame 26 shows the same data on pins 1 and 2 but pin 1 is connected to a timing generator while pin 2 is not. Both pins are NRZ.</p>	
<p>26.</p>  <p>Timing Gen.</p> <p>PIN 1</p> <p>PIN 2</p>	<p>22.</p> <p><u>NRZ</u></p> <p><u>programmed</u></p>
<p>27. Note that the data on pins 1 and 2 have the same period but different transition points.</p>	
<p>28. Transitions on pin 1 are synchronized by the leading edge of the <u>Timing Generator</u> pulse. Transitions on pin 2 occur at <u><math>T\phi</math></u>.</p>	<p>24.</p> <p><u>timing</u></p> <p><u>generator</u></p>

<p>29. If no timing generator is connected to a data pin, data transitions occur at <u><math>T\phi</math></u>.</p>	
<p>30. If a timing generator is connected to an NRZ data pin, data transitions occur at <u>positive edge of timing pulse</u>.</p>	
<p>31. NRZ data stays at the programmed level for a period equal to that programmed by the <u>SET PERIOD</u> statement.</p>	
<p>32. Machines with the "<u>1-nanosecond option</u>" provide a special case that can violate the statement in frame 31. This case is covered in the <u>OPTIONS</u> lesson.</p>	<p>28. <u>timing generator</u> <u><math>T\phi</math></u></p>

<p>33. Clock pins normally provide <u>RZ</u> (<u>return-to-zero</u>) waveforms to the DUT.</p>	<p>29. <u>T<sub>0</sub></u></p>
<p>34. Clock pins normally provide RZ (<u>return-to-zero</u>) waveforms to the DUT.</p>	<p>30. the <u>leading edge of the timing generator pulse.</u></p>
<p>35. Data can drive an RZ waveform to a "<u>1</u>" level <u>only</u> during the <u>timing generator pulse</u>. When the <u>pulse ends</u>, the <u>waveform returns to zero</u>.</p>	<p>31. <u>SET PERIOD</u></p>
<p>36. Data can drive an RZ waveform to a "<u>1</u>" <u>One</u> level only during the <u>Timing Generator</u> pulse. When the pulse ends, the waveform returns to <u>zero</u>.</p>	



<p>37. Since a clock pin normally applies RZ data to the DUT and RZ data requires a timing generator, a <u>Timing Generator</u> must be connected to a clock pin.</p>	
<p>38. An input pin becomes a clock pin when its number appears in a CONN CLK pin list; statement.</p>	<p>34. <u>return-to-zero</u></p>
<p>39. Input pins can provide <u>Power</u>, <u>DATA</u>, or <u>Clock</u> to the DUT.</p>	
<p>40. An input pin becomes a <u>clock</u> pin when its number appears in a CONN CLK pin list; statement.</p>	<p>36. <u>"1"</u> <u>timing generator</u> <u>zero</u></p>

<p>41. An <u>input</u> pin becomes a <u>power</u> pin when its number appears in a <u>.CONN [DPS1/DPS2/DPS3/TCOM]</u> pin list; statement.</p>	<p>37.  timing generator</p>
<p>42. A "<u>1</u>" must appear in the <u>n<sup>th</sup></u> position of <u>either</u> a <u>SET DA</u> or a <u>SET DB pin-pattern</u>; statement <u>before</u> pin <u>n</u> can be an <u>input</u> pin.</p>	
<p>43. SET DA 1001001; SET DB 0000111; Pins <u>1</u>, <u>4</u>, <u>5</u>, <u>6</u> and <u>7</u> can be input pins.</p>	<p>39.  <u>power</u> <u>clocks</u> <u>data</u></p>
<p>44. Study frame 45 then respond to frame 46.</p>	<p>40.  <u>clock</u></p>

<p>45. SET DA <u>10011011</u>;  CONN CLK <u>1, 2, 4, 5</u>;  CONN DPS1 <u>7</u>;  CONN TCOM <u>8</u>;</p> <p>(Assume no SET DB statement)</p>	
<p>46. Pins <u>1, 4, and 5</u> are clock pins.  Pins <u>7 and 8</u> are power pins.  Pin <u>2</u> is incorrectly programmed.</p>	
<p>47. <u>Data pins require more definition.</u>  Pin <u>n</u> can be a <u>data pin only</u> if a "<u>1</u>" appears in the <u>n<sup>th</sup></u> position of a SET [<u>DA/DB</u>] pin pattern; statement and the <u>DA/DB</u> register is <u>enabled</u>.</p>	<p>43.</p> <p><u>1, 4, 5, 6</u>  and <u>7</u></p>
<p>48. Pin n can be a data pin only if a <u>1</u> appears in the n<sup>th</sup> position of a SET [<u>DA/DB</u>] pin pattern; statement and the DA/DB register is enabled.</p>	

<p>49. In order for an input pin to be a data pin, the DA/DB register defining the input pin must be <u>ENABLED</u>.</p>	
<p>50. The DA register is <u>enabled</u> by the <u>ENABLE DA;</u> statement. The DB register is enabled by the <u>ENABLE DB;</u> statement. The enable statement applies to the <u>memory loads</u> that follow it.</p>	<p>46.</p> <p><u>1, 4, 5</u></p> <p><u>7, 8</u></p> <p><u>2</u></p>
<p>51. If <u>no ENABLE [DA/DB]</u> ; statement <u>appears</u> in a FACTOR program, <u>DA</u> is <u>enabled by default</u>. If an <u>ENABLE [DA/DB]</u> statement <u>appears</u>, it <u>prevails until the next one appears</u>.</p>	
<p>52. <u>No ENABLE DA;</u> statement is <u>required</u> to <u>enable the DA</u> register unless an <u>ENABLE</u> <u>DB;</u> statement is currently programmed.</p>	<p>48.</p> <p><u>"1"</u></p> <p><u>DA/DB</u></p>

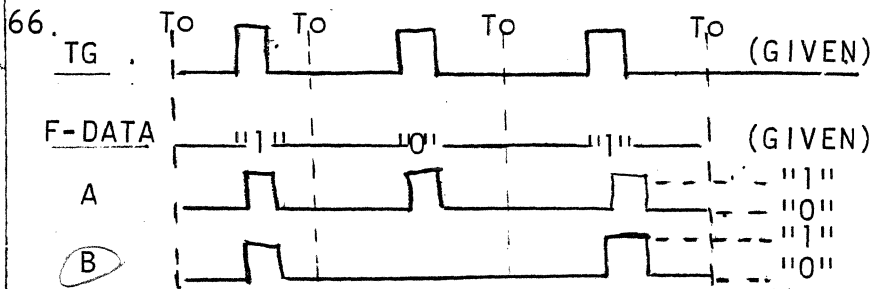
<p>53. A <u>power</u> or <u>clock</u> pin must have a "1" in the pin pattern of either the <u>DA</u> or the <u>DB</u> register but a <u>data</u> pin <u>must</u> have a "1" in the pin pattern of the <u>ENABLED</u> D register.</p>	<p>49. <u>enabled</u></p>
<p>54. A power or clock pin must have a "1" in the pin pattern of <u>either</u> the DA <u>OR</u> the DB register, but a <u>DATA</u> pin must have a "1" in the pin pattern of the <u>ENABLED</u> D register.</p>	
<p>55. <u>Power, clock and data pins are mutually exclusive. The last definition in the FACTOR program prevails.</u></p>	
<p>56. Therefore, if a <u>pin</u> number appears in the pin list of a CONN [<u>DPS1/DPS2/DPS3/TCOM/CLK</u>] pinlist; <u>statement it cannot be a data pin.</u></p>	<p>52. <u>ENABLE DB</u></p>

<p>57. The data shown on pin 4 in frame 58 is (RZ/NRZ) <u>RZ</u> data.</p>	<p>53. <u>enabled</u></p>
<p>58.</p> <p>DATA IN Local Memory</p> <p>PIN 4</p>	<p>54. <u>either</u> <u>or</u> <u>data</u> <u>enabled</u></p>
<p>59. Pin 4 could be either a <u>DATA</u> or a <u>Clock</u> pin depending upon whether or not 4 appears in a CONN CLK pinlist; statement.</p>	
<p>60. An RZ pulse can appear during a data period only if the data for that period is a logical <u>1</u>.</p>	

<p>61. Since <u>data</u> pins are <u>normally</u> provided with <u>NRZ</u> waveforms, a <u>SET RZ</u> pin pattern; statement is <u>necessary</u> to apply <u>RZ</u> waveforms to a <u>data</u> pin.</p>	<p>57.</p> <p style="text-align: center;"><u>RZ</u></p>
<p>62. A logical <u>"1"</u> in the <u>pin</u> pattern of a <u>SET RZ</u> statement <u>defines</u> a <u>pin</u> as <u>RZ</u>, a <u>"0"</u> defines the pin as <u>NRZ</u>.</p>	
<p>63. SET RZ 0011 . . . . ; The above statement defines pins <u>1</u> and <u>2</u> as NRZ and pins <u>3</u> and <u>4</u> as <u>RZ</u>.</p>	<p>59.</p> <p style="text-align: center;"><u>clock</u> <u>data</u></p>
<p>64. An <u>RZ</u> pin receives a <u>pulse</u> only during a period where <u>F=data</u> is a (<u>one/zero</u>) <u>1</u>.</p>	<p>60.</p> <p style="text-align: center;"><u>1</u></p>

65. Choose the waveform in frame 66 that is correct for an RZ pin (i.e. choose A or B).

B



67. An RZ pin receives a pulse only during a period where F-data is a 1.

63.

1 and 2  
3 and 4 as RZ

68. Circle A, B, or C.

The width of an RZ pulse is determined by:

- A. The programmed period.
- B. The width of the timing generator pulse.
- C. The F-data

64.

one



<p>69. A data pin IS NRZ unless a "1" is programmed for the pin in SET <u>BZ</u> pinlist.</p>	
<p>70. A clock pin is normally (RZ/NRZ) <u>BZ</u>.</p>	<p>66.</p> <p>(An RZ pulse<sup>B</sup> can appear during a data period only if the data for that period is a logical one.)</p>
<p>71. The <u>CONN CLK pinlist</u>; statement <u>auto-</u> <u>matically</u> loads a "1" into the <u>RZ register</u> for <u>every pin specified</u> in the <u>pinlist</u>.</p>	<p>67.</p> <p><u>one</u></p>
<p>72. Data pins are normally (RZ/NRZ) <u>NRZ</u> but clock pins are normally (RZ/NRZ) <u>BZ</u>.</p>	<p>68.</p> <p>B. The width of the timing generator pulse.</p>

<p>73. <del>Clock pins can be redefined as NRZ by programming a SET RZ pinlist; statement after the <u>CONN CLK</u> pinlist; statement.</del></p>	<p>69.</p> <p style="text-align: center;"><u>RZ</u></p>
<p>74. Clock pins can be redefined as NRZ by programming a (one/zero) <u>zero</u> into the SET RZ pinlist; statement (<u>before/after</u>) <u>AFTER</u> the appropriate CONN CLK pinlist; statement.</p>	<p>70.</p> <p style="text-align: center;"><u>RZ</u></p>
<p>75. Data transitions for NRZ pins, with no timing generator connected to them, are at <u>T<sub>0</sub></u>.</p>	
<p>76. Data transitions for NRZ pins, with a timing generator connected to them, are at the <u>Leading Edge</u> of the <u>Timing generator pulse</u>.</p>	<p>72.</p> <p style="text-align: center;"><u>NRZ</u></p> <p style="text-align: center;"><u>RZ</u></p>

<p>77. Pulses are applied to RZ pins only during the programmed <u>width</u> of a timing generator pulse and only if the F-data for the pin is a logical <u>1</u>.</p>	
<p>78. Each pin can be programmed as either an input or an <u>output</u> pin.</p>	<p>74.</p> <p><u>zero</u></p> <p><u>after</u></p>
<p>79. Each <u>pin</u> is <u>connected</u> to a <u>functional test comparator</u> during <u>functional testing</u>.</p>	<p>75.</p> <p>T<math>\emptyset</math></p>
<p>80. The functional test <u>comparator</u> can be <u>programmed</u> to perform a <u>pass/fail</u> test on <u>each</u> pin during <u>each</u> <u>period</u> of a functional test.</p>	<p>76.</p> <p><u>leading edge</u> timing generator pulse</p>

<p>81. Usually test data is desired <u>only</u> for <u>output pins</u>.</p>	<p>77.</p> <p><u>width</u></p> <p><u>one</u></p>
<p>82. Any pin that is not an <u>input pin</u> can be <u>considered</u> an <u>output pin</u>.</p>	<p>78. output</p>
<p>83. A pin that is neither a <u>power pin</u> nor a <u>clock pin</u> and that does not have a "<u>1</u>" assigned in the pin pattern of an <u>enabled D</u> register is an <u>output</u> pin.</p>	
<p>84. <u>Functional</u> test data for a <u>pin</u> is <u>ignored</u> unless a "<u>1</u>" appears for that pin in an <u>enabled MA</u> or <u>MB</u> register.</p>	

<p>85. If <u>no</u> ENABLE [MA/MB]; statement is programmed, the MA register is <u>enabled</u> by default. Once programmed, an ENABLE [MA/MB]; statement <u>prevails</u> for <u>subsequent</u> memory loads <u>until</u> replaced by another ENABLE [MA/MB]</p>													
<p>86. Definition [DA/DB] and Mask [MA/MB]; registers can be <u>enabled</u> together in <u>one</u> statement or <u>separately</u>. See frame 87.</p>													
<p>87. Possible valid enable statements</p> <table data-bbox="282 1104 961 1297"> <tr> <td>ENABLE MA;</td> <td>ENABLE DA</td> </tr> <tr> <td>ENABLE MA, DA;</td> <td>ENABLE DA, MA;</td> </tr> <tr> <td>ENABLE MA, DB;</td> <td>ENABLE DA, MB;</td> </tr> <tr> <td>ENABLE MB;</td> <td>ENABLE DB;</td> </tr> <tr> <td>ENABLE MB, DA;</td> <td>ENABLE DB, MA;</td> </tr> <tr> <td>ENABLE MB, DB;</td> <td>ENABLE DB, MB;</td> </tr> </table>	ENABLE MA;	ENABLE DA	ENABLE MA, DA;	ENABLE DA, MA;	ENABLE MA, DB;	ENABLE DA, MB;	ENABLE MB;	ENABLE DB;	ENABLE MB, DA;	ENABLE DB, MA;	ENABLE MB, DB;	ENABLE DB, MB;	<p>83.</p> <p><u>output</u></p>
ENABLE MA;	ENABLE DA												
ENABLE MA, DA;	ENABLE DA, MA;												
ENABLE MA, DB;	ENABLE DA, MB;												
ENABLE MB;	ENABLE DB;												
ENABLE MB, DA;	ENABLE DB, MA;												
ENABLE MB, DB;	ENABLE DB, MB;												
<p>88. Only the mask register that is <u>Enabled</u> specifies the pins to be tested.</p>													

	88. <u>enabled</u>

## LESSON SIX

TOPIC:

LOCAL MEMORY MANAGEMENT

GIVEN:

1. The booklet: Local Memory Management
2. Pencil and eraser

PREREQUISITE:

Completion of Lesson Five

PERFORMANCE:

Student proceeds through the number frames responding to branching instructions.

Student writes short responses in the response frames, compares his responses with the data provided in answer frames and corrects his responses to agree with the furnished answers.

Standard: The student provides 100% correct responses within one hour.

LOCAL MEMORY  
MANAGEMENT

A PROGRAMMED STUDY AID

SENTRY 600 PROGRAMMING

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1725 Technology Drive  
San Jose, California 95110



## LOCAL MEMORY MANAGEMENT

1. Local memory locations are loaded sequentially by a series of SET F statements.

(REF: FACTOR MANUAL paragraph  
11.4.5)

2. A series of SET F statements load local memory Locations (how?)

*↓ sequentially*

3. Each memory load starts at address 0, unless modified by an AT statement.

4. Unless modified by an AT statement, each memory load starts at address 0.

<p>5. A memory load <u>consisting of 16 SET F</u> statements loads local memory locations <u>0</u> through <u>15</u>.</p>	
<p>6. The <u>first SET F (or SET FI)</u> following an <u>ENABLE TEST</u> statement <u>starts a new</u> memory load.</p>	<p>2. <u>sequentially</u></p>
<p>7. If <u>sixteen</u> memory locations are programmed followed by <u>ENABLE TEST</u>; the next SET F statement loads address <u>0</u>.</p>	
<p>8. Unless modified by an <u>AT</u> statement, each memory load starts at address <u>0</u>.</p>	<p>4. 0</p>

<p>9. An <u>AT</u> statement can specify an address to be loaded. For example <u>AT 11; SET F</u> pin pattern; loads memory location eleven (decimal).</p> <p>(REF: FACTOR MANUAL paragraph <u>11.4.23</u>)</p>	<p>5.</p> <p style="text-align: center;"><u>0</u> <u>15</u> -</p>
<p>10. The <u>AT</u> statement can be <u>used</u> to start a <u>new memory load</u> at an address <u>other than 0</u>. <u>Subsequent loads are sequential</u>. Note the programming and remarks in frame 11.</p>	
<p>11. <u>ENABLE TEST</u>; REM THE PREVIOUS TEST LOADED 16 LOCATIONS;</p> <p><u>AT 11</u>;</p> <p><u>SET F</u> pin pattern; REM THIS LOADS LOCATION <u>11</u>;</p> <p><u>SET F</u> pin pattern; REM THIS LOADS LOCATION <u>12</u>;</p> <p><u>SET F</u> pin pattern; REM THIS LOADS LOCATION <u>13</u>;</p> <p><u>ENABLE TEST</u>;</p>	<p>7.</p> <p style="text-align: center;"><u>0</u></p>
<p>12. The programming in frame 11 modified the contents of locations <u>11</u>, <u>12</u> and <u>13</u> but <u>did not change locations 0 through 10, 14, and 15</u>.</p>	<p style="text-align: center;"><u>0</u></p>

13. The first SET F statement of a memory load or following an AT statement programs all pins to zero that are not specified as logical ones. Examples are given in frame 14.

14. SET PAGE 100; REM 30-PIN SYSTEM;  
\* .... set-up instructions ....  
ENABLE DA, MA;  
SET F 0010111; REM 3, 5, 6 and 7 PRO-  
TO ONES, THE OTHER 26  
PINS ARE PROGRAMMED TO  
ZEROES;  
ENABLE TEST;  
AT 10; SET F 1; REM F-DATA IS ONE  
FOLLOWED BY 29 ZEROES;

15. The first SET F statement of a memory load or after an AT statement programs all pins to zero that are not specified as logical 1's.

16. Subsequent SET F statements within one memory load need specify only the change from one location to the next. Note the example in frame 17.

17. Sequential SET F's	LOC	F-DATA (30 PINS)
SET F 00001;	0	00001 00000 00000 00000 00000 00000 00000
SET F 0001;	1	00011 00000 00000 00000 00000 00000 00000
SET F 001;	2	00111 00000 00000 00000 00000 00000 00000
SET F 01;	3	01111 00000 00000 00000 00000 00000 00000
SET F 1;	4	11111 00000 00000 00000 00000 00000 00000

18. During compilation of a FACTOR program, each statement that controls the tester is converted into a code that can be executed by the Sentry 600.

19. During execution, this executable code causes the pin pattern of each SET F statement to be loaded into local memory as F-Data.

15.

AT

ones

20. Two enable bits are loaded into the local memory along with the F-Data. One enables a D register. The other enables an M register.

<p>21. Two enable bits are loaded into local memory along with the <u>DATA</u>. One enables a <u>D</u> register. The other enables an <u>M</u> register.</p>	
<p>22. The <u>state</u> of these <u>two enable bits</u> is determined, at <u>compile time</u>, <u>ENABLE</u> <u>[MA/MB/DA/DB]</u> (<u>,MA/MB/DA/DB</u>); statement.</p>	
<p>23. The state of these two enable bits is determined at <u>compile time</u>.</p>	
<p>24. Thus, the <u>ENABLE</u> MA etc. statement does not appear as <u>executable</u> code in the <u>compiled</u> program. It <u>has already</u> caused the <u>compiler</u> to <u>modify</u> the <u>code</u> that <u>loads local memory</u>.</p>	

<p>25. Since <u>ENABLE MA etc.</u> produces <u>no executable code</u> it <u>does not exist</u> in the <u>executable DATA file</u>.</p>	<p>21. <u>F-Data</u> <u>D</u> <u>M</u></p>
<p>26. Since it is <u>impossible to branch to a statement that does not exist at execution time</u>, an <u>ENABLE MA etc. statement</u> should <u>not be labelled</u>.</p>	
<p>27. An <u>ENABLE MA etc</u> statement should <u>NOT</u> be labelled.</p>	<p>23. Compile</p>
<p>28. Any <u>executable statement written within a string of SET F statements</u> starts a <u>new memory load</u>.</p>	

29. Any executable statement written within a string of SET F statements starts a new local memory load.

30. Note the remarks *Delete*

```
SET F .....; REM LOAD LOC 0;  
SET F .....; REM LOAD LOC 1;  
SET F .....; REM LOAD LOC 2;  
  
If A EQ 2  
  
    THEN SET F .....  
    ELSE SET F .....;  
ENABLE TEST;
```

31. In frame 30, note that the first location of local memory is changed if A equals 2 but the second location (LOC 1) is changed if A does not equal 2.

27.

not

*Delete*

32. BUT

ENABLE [MA/MB/DA/DB] (,MA/MB/DA/DB/)  
generates no executable code

Therefore it CAN  
be nested within a string of SET F statements  
without starting a new load.



<p>33. A <u>D</u> or an <u>M</u> enable <u>CAN</u> (can/cannot) be loaded within a string of SET.F statements without starting a new memory load.</p>	<p>29.</p> <p><u>new</u></p>
<p>34. This characteristic of the <u>D</u> or <u>M</u> enable allows <u>changing pin definition (data/output)</u> and mask definition (<u>care/ don't care</u>) during execution of a functional test.</p>	
<p>35. When the <u>Test Operating System</u> executes the <u>ENABLE TEST;</u> statement the <u>F-Data</u> are executed at the <u>high-speed test rate.</u></p>	
<p>36. F-Data are executed at the <u>high-speed</u> test rate.</p>	

37. The high-speed test rate is programmed by the SET PERIOD statement. This statement must appear before the first ENABLE TEST; in a program.

(REF: FACTOR MANUAL paragraph 11.4.17)

33. Can

38. The functional test rate is programmed by the SET PERIOD statement.

39. The shortest period is determined by the system capability. Thus, minimum period for a 10 MHz system is 100 nanoseconds; minimum period for a 5 MHz system is 200 nanoseconds.

40. Rate (frequency) and period are reciprocals, i.e.

$$\text{frequency} = \frac{1}{\text{period}} \quad ; \quad \text{period} = \frac{1}{\text{frequency}}$$

$$\text{HERTZ} = \frac{1}{\text{seconds}} \quad ; \quad \text{seconds} = \frac{1}{\text{Hertz}}$$

36.

high-speed

41. A test rate of 2 MHz requires a programmed period of 500 ns.

$$\frac{1}{2 \times 10^6} = .5 \times 10^{-6} = 500 \text{ ns}$$

42. When the functional test takes place, the first local memory word executed is that in location 0, unless a SET START statement appeared since the last ENABLE TEST; statement.

38.

SET PERIOD

43. During functional test execution the first F-data is from memory location 0 unless a SET START statement appeared since the last ENABLE TEST; statement.  
(REF: FACTOR MANUAL paragraph 11.4.22)

44. A SET START statement does not "carry-over" from one functional test to the next. That is, if the first functional test has a SET START statement but the second does not, the second test starts at location zero.

<p>45. A SET START statement <u>does not</u> (does/ does not) carry over from one functional test to the next..</p>	<p>41.</p> <p><u>500 nanoseconds</u></p>
<p>46. Note the difference between the <u>AT</u> and the <u>SET START</u> statements. An <u>AT</u> statement may <u>control</u> the <u>first location loaded</u>. A <u>SET START</u> statement <u>controls</u> the <u>first location executed</u>.</p>	
<p>47. An <u>AT</u> statement may control the first location <u>loaded</u>. A <u>SET START</u> statement controls the <u>first statement executed</u>.</p>	<p>43.</p> <p><u>0</u></p>
<p>48. It is <u>possible</u> to <u>modify</u> part of <u>local memory</u> using the <u>AT</u> statement but execute all of <u>local memory</u> by not using <u>SET START</u>. Consider the <u>remarks</u> in frame <u>49</u>.</p>	

<p>49. ENABLE TEST; REM THIS TEST USED LOCATIONS 0 THRU 15;</p> <p>AT 12;  SET F .....;  SET F .....;  SET F .....;  SET F .....;</p> <p>ENABLE TEST; REM TEST STARTS AT LOCATION 0;</p>	<p>45.</p> <p><u>does not</u></p>
<p>50. If frame <u>49</u> were modified by a <u>SET START 12;</u> statement placed just before the AT or just before the last <u>ENABLE TEST;</u> the test would execute locations <u>12 through 15</u> without using or changing locations <u>0 thru 11.</u></p>	
<p>51. A functional test starts at location 0 unless modified by a <u>SET START</u> statement.</p>	<p>47.</p> <p><u>loaded</u></p>
<p>52. Unless a <u>SET MAJOR</u> statement is programmed, the <u>last local memory location</u> executed for a specific load is the <u>highest location loaded</u> during that load.</p> <p>(REF: FACTOR MANUAL paragraph 6.5.2.2)</p>	

53. Unless a SET MAJOR statement is programmed, the last memory location executed for a specific load is the highest location loaded during that load.

54. Unless a SET MAJOR statement is programmed, the last memory location executed for a specific load is the highest location loaded during THAT load.

51.

SET START

	53.  <u>highest</u>
	54.  <u>SET MAJOR</u> <u>highest.</u> <u>that</u>