Digital Scientific

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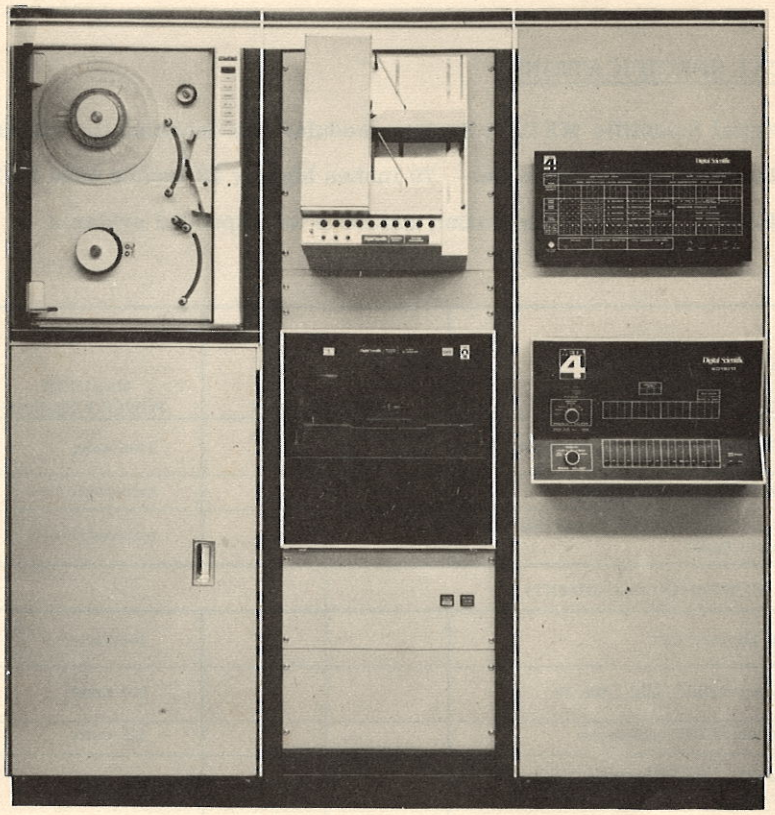
META 4™

COMPUTER SYSTEM
DATA SHEET

The Digital Scientific META 4 is a flexible, logical processor controlled by a random-access Read-Only Memory (ROM). ROM firmware (control memory program) can be custom-tailored to user specifications, either by us or by you; it will process special instructions or other computers' instruction sets at speeds sufficient to emulate all operations of a computer . . . . including I/O.

The control memory program executes at less than 90 nanoseconds per step. ROM firmware programs can be field changed or modified by either Digital Scientific or user personnel, easily and quickly. This makes META 4 a true offspring of the fourth generation. The META 4's firmware Microassembler and software models that are written in IBM 1130/1800 language are offered to facilitate firmware programming.

ROM programs and options also provide capability for handling data communications jobs; high-speed, floating-point hardware requirements; sophisticated controller interface replacements; or more — all at high speed and low cost.



EXPANSION CAPABILITIES

• ROM (35-nanosecond access):	up to 4096 16-bit words
• Internal Registers, directly addressable:	up to 31 16-bit words
• Integrated Circuit Scratch-Pad Memory:	up to 256 16-bit words
• Core Memory (400-nanosecond access, 900-nanosecond cycle):	up to 65,536 18-bit words per memory I/O register up to a maximum 458,753 words
• Multiple Overlapping Core Memory Banks:	up to 8 8192-word banks per memory I/O register
• Multiple Memory Ports:	4 ports per bank
• Options:	Real-Time Clock, Storage Protect, Stall Alarm

SOFTWARE

The Digital Scientific Model 4001 META 4 Processor's 1130/1800 Emulator Versions are software-compatible with the IBM 1130/1800 computer systems.