



```

LL          IIIIII  BBBB8888  AAAAAA  SSSSSSSS  SSSSSSSS  IIIIII  GGGGGGGG  NN  NN
LL          IIIIII  BBBB8888  AAAAAA  SSSSSSSS  SSSSSSSS  IIIIII  GGGGGGGG  NN  NN
LL          II      BB      BB  AA      AA  SS      SS      II      GG      NN  NN
LL          II      BB      BB  AA      AA  SS      SS      II      GG      NN  NN
LL          II      BB      BB  AA      AA  SS      SS      II      GG      NNNN  NN
LL          II      BB      BB  AA      AA  SS      SS      II      GG      NNNN  NN
LL          II      BBBB8888  AA      AA  SSSSSS  SSSSSS  II      GG      NN  NN
LL          II      BBBB8888  AA      AA  SSSSSS  SSSSSS  II      GG      NN  NN
LL          II      BB      BB  AAAAAAAAAA  SS      SS      II      GG  GGGGGG  NN  NNNN
LL          II      BB      BB  AAAAAAAAAA  SS      SS      II      GG  GGGGGG  NN  NNNN
LL          II      BB      BB  AA      AA  SS      SS      II      GG      NN  NN
LL          II      BB      BB  AA      AA  SS      SS      II      GG      NN  NN
LLLLLLLLLLL IIIIII  BBBB8888  AA      AA  SSSSSSSS  SSSSSSSS  IIIIII  GGGGGG  NN  NN
LLLLLLLLLLL IIIIII  BBBB8888  AA      AA  SSSSSSSS  SSSSSSSS  IIIIII  GGGGGG  NN  NN

```

```

LL          IIIIII  SSSSSSSS
LL          IIIIII  SSSSSSSS
LL          II      SS
LL          II      SS
LL          II      SS
LL          II      SS
LL          II      SSSSSS
LL          II      SSSSSS
LL          II      SS
LL          II      SS
LL          II      SS
LL          II      SS
LLLLLLLLLLL IIIIII  SSSSSSSS
LLLLLLLLLLL IIIIII  SSSSSSSS

```

(2) 44
(3) 50
(4) 76

HISTORY ; Detailed Current Edit History
DECLARATIONS
LIB\$ASSIGN -



```

0000 1 .TITLE LIB$ASSIGN ; Assign channel to device
0000 2 .IDENT 'V04-000'
0000 3
0000 4 *****
0000 5 *****
0000 6 * COPYRIGHT (c) 1978, 1980, 1982, 1984 BY *
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0000 22 * SOFTWARE ON EQUIPMENT WHICH IS NOT SUPPLIED BY DIGITAL. *
0000 23 *
0000 24 *
0000 25 *****
0000 26 *****
0000 27
0000 28 FACILITY: Language-independent support library
0000 29 ++
0000 30 ABSTRACT:
0000 31
0000 32
0000 33 --
0000 34
0000 35 VERSION: 1
0000 36
0000 37 HISTORY:
0000 38
0000 39 AUTHOR:
0000 40
0000 41 Herb Jacobs August 1981
0000 42

```

LIBSASSIGN
V04-000

J 7
: Assign channel to device 16-SEP-1984 02:17:54 VAX/VMS Macro V04-00 Page 2
HISTORY ; Detailed Current Edit History 5-SEP-1984 04:40:09 [VMSLIB.SRC]LIBSASSIGN.MAR;1 (2)

0000 44 .SBTTL HISTORY ; Detailed Current Edit History
0000 45
0000 46 ; EDIT HISTORY:
0000 47 ;
0000 48 : 1-001 -

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```
0000 50          .SBTTL DECLARATIONS
0000 51
0000 52
0000 53  : INCLUDE FILES:
0000 54  :
0000 55          $FABDEF
0000 56          $NAMDEF
0000 57  :
0000 58  : EXTERNAL SYMBOLS:
0000 59  :
0000 60
0000 61  :
0000 62  : MACROS:
0000 63  :
0000 64
0000 65  :
0000 66  : PSECT DECLARATIONS:
0000 67  :
0000 68
0000 69          .PSECT _LIB$CODE          PIC, SHR, LONG, EXE, NOWRT
0000 70
0000 71  :
0000 72  : EQUATED SYMBOLS:
0000 73  :
0000 74
```

.....

```

0000 76      .SBTTL LIB$ASSIGN -
0000 77
0000 78 :++
0000 79 : FUNCTIONAL DESCRIPTION:
0000 80 :
0000 81 :     These routines provide a general mechanism to get a channel assigned
0000 82 :     to the associated device with a file specification. The channel
0000 83 :     assigned is always to the device, never to the file. The purpose
0000 84 :     of these routines are to successfully accomplish up cased recursive
0000 85 :     logical name translation and succeed with process permanent files.
0000 86 :     Since these routines use RMS as a basis, compatibility is assured
0000 87 :     with all working programs.
0000 88
0000 89 : CALLING SEQUENCE:
0000 90 :
0000 91 :     ret = LIB$ASSIGN      ( filedesc, chan)
0000 92 :     ret = LIB$ASSIGN_FILE ( filedesc, chan)
0000 93 :     ret = LIB$ASSIGN_DEV  ( filedesc, chan)
0000 94
0000 95 : INPUT PARAMETERS:
0000 96 :
0000 97 :     FILEDESC - Address of character string descriptor describing input
0000 98
0000 99 : IMPLICIT INPUTS:
0000 100 :
0000 101 :     NONE
0000 102
0000 103 : OUTPUT PARAMETERS:
0000 104 :
0000 105 :     CHAN - Address of word to receive channel number assigned
0000 106
0000 107 : IMPLICIT OUTPUTS:
0000 108 :
0000 109 :     NONE
0000 110
0000 111 : COMPLETION CODES:
0000 112 :
0000 113 :     $$$_NORMAL - success
0000 114 :     All other completion codes are generated as RMS or SS codes.
0000 115
0000 116 : SIDE EFFECTS:
0000 117 :
0000 118 :     NONE
0000 119
0000 120 :--
0000 121 :.ENTRY -
0000 122 LIB$ASSIGN, ^M<>
16 50 41'AF 6C FA 0002 123 CALLG (AP) B^LIB$ASSIGN_DEV ;ATTEMPT DEVICE ASSIGNMENT
16 50 16 50 EB 0006 124 BLBS R0,10$ ;BRANCH IF SUCCESSFUL
0009 125 :
0009 126 : PROCESS LIST OF RECOGNIZABLE FAILURES AS A DEVICE NAME THAT SHOULD BE
0009 127 : RETURNED AS AN ERROR RATHER THAN ATTEMPTING AS A FILE NAME
0009 128 :
0009 129 :     CMPL #RMSS_CHN,R0 ;DEVICE ALLOCATED
50 00000000'8F D1 0009 130 BEQL 10$
0009 131 :     CMPL #RMSS_PRIV,R0 ;NO PRIVILEGE FOR DEVICE ACCESS
50 00000000'8F D1 0012 131 BEQL 10$
04 13 0019 132

```

```

20'AF 6C FA 001B 133 CALLG (AP),B^LIB$ASSIGN_FILE ;ATTEMPT AS GENERAL FILE SPEC
04 001F 134 10$: RET ;RETURN STATUS FROM SUBROUTINE
0020 135
0020 136
0020 137
0020 138 :: GET CHANNEL ASSOCIATED WITH FILE SPEC, ASSUME NO SPECIAL PUNCTUATION
0020 139
0020 140
007C 0020 141 .ENTRY -
SE 000000B0 8F C2 0022 142 LIB$ASSIGN FILE, ^M<R2,R3,R4,R5,R6>
56 5E D0 0029 143 SUBL #FAB$C_BLN+NAM$C_BLN,SP ;ALLOCATE ROOM FOR FAB AND NAM
007C 30 002C 144 MOVL SP,R6 ;GET ADDRESS OF FAB
05 50 E9 0038 145 BSBW ASSIGN_SETUP ;PERFORM COMMON SETUP
00F1'CF 6C FA 003B 146 10$: $PARSE FAB=<(R6)> ;GET THE DEVICE INFORMATION
04 0040 147 20$: BLBC R0,20$ ;BRANCH IF ERROR TO RETURN CODE
0041 148 CALLG (AP),W^ASSIGN_CHAN ;CALL FOR STACK CLEANUP
0041 149 RET ;CHANNEL
0041 150
0041 151 :: GET CHANNEL ASSOCIATED WITH FILE SPEC, FORCE TO LOOK LIKE DEVICE NAME AND
0041 152 :: ONLY SUCCEED IF INPUT REPRESENTS DEVICE NAME.
0041 153
0041 154 .ENTRY -
SE 000000B0 8F C2 0043 155 LIB$ASSIGN DEV, ^M<R2,R3,R4,R5,R6>
56 5E D0 004A 156 SUBL #FAB$C_BLN+NAM$C_BLN,SP ;ALLOCATE ROOM FOR FAB AND NAM
005B 30 004D 157 MOVL SP,R6 ;GET ADDRESS OF FAB
61 50 3A 3A 0050 158 BSBW ASSIGN_SETUP ;PERFORM COMMON SETUP
11 13 0054 159 10$: LOCC #^A/;/,R0,(R1) ;DOES STRING CONTAIN A COLON?
50 D7 0056 160 BEQL 20$ ;BRANCH IF NO
2A 13 0058 161 DECL R0 ;WAS THIS LAST CHARACTER?
01 A1 3A 91 005A 162 BEQL 30$ ;BRANCH IF YES
24 12 005E 163 CMPB #^A/;/,1(R1) ;IS THIS A NODE SPECIFIER?
51 02 C0 0060 164 BNEQ 30$ ;BRANCH IF NOT, EXPLICIT ":"
50 D7 0063 165 ADDL #2,R1 ;BUMP PAST ":"
E9 11 0065 166 DECL R0 ;ONE LESS CHARACTER IN COUNT
50 62 9A 0067 167 BRB 10$ ;TRY FOR REAL ":"
50 04 C0 006A 168 20$: MOVZBL (R2),R0 ;GET SIZE OF STRING
50 03 CA 006D 169 ADDL #4,R0 ;ROUND UP FOR STACK ALIGNMENT
5E 50 C2 0070 170 BICL #3,R0 ;SIZE NEEDED ON STACK
6E 50 3A 04 B2 62 2C 0073 171 SUBL R0,SP ;ALLOCATE SPACE FOR COPY
34 A6 04 BC 01 81 007A 172 MOVCS (R2),@4(R2),#^A/;/,R0,(SP) ;MAKE A COPY WITH A ":" AT END
00 04 A6 5E D0 0080 173 ADDB3 #1,@4(AP),FAB$B_FNS(R6) ;SET SIZE WITH COLON
15 50 E9 0084 174 MOVL SP,FAB$L_FNA(R6) ;SET ADDRESS OF COPY
50 0000'8F 3C 0089 175 30$: BBSS #FAB$V_NFS,FAB$L_FOP(R6),35$ ;SET NON FILE STRUCTURE ACCESS
D3 009A 176 35$: $PARSE FAB=<(R6)> ;GET THE DEVICE INFORMATION
009B 177 40$: BLBC R0,50$ ;BRANCH IF ERROR TO RETURN CODE
009B 178 MOVZWL #$$ IVDEVNAM,R0 ;ASSUME FAILURE
009B 179 BITL #<NAM$M_EXP DIR!NAM$M_EXP NAME!- ;CHECK FOR ANYTHING BUT DEVICE
009B 180 NAM$M_EXP_TYPE!NAM$M_EXP_VER!-
009B 181 NAM$M_GRP_MBR!NAM$M_WILD<CARD>,-
0084 C6 00080147 8F 009B 182 NAM$L_FNB^FAB$C_BLN(R6)
05 12 00A3 183 BNEQ 50$ ;BRANCH IF ANY OTHERS SET
00F1'CF 6C FA 00A5 184 CALLG (AP),W^ASSIGN_CHAN ;CALL FOR STACK CLEANUP
04 00AA 185 50$: RET
00AB 186
00AB 187
00AB 188
00AB 189 ::
:: Routine used for common setup between ASSIGN_FILE and ASSIGN_DEV

```



```

50 00000000'8F D0 00AB 190 ASSIGN_SETUP:
      6C 02 91 00AB 191      MOVL #LIB$ INVARG,R0      ;ASSUME FAILURE
      39 12 12 00B5 192      CMPB #2,(AP)      ;ARE THERE 2 ARGUMENTS?
04 BC 00FF 8F B1 00B7 193      BNEQ 10$      ;BRANCH IF NOT
      31 19 19 00BD 194      CMPW #255,@4(AP)      ;IS SIZE OF DEVNAME REASONABLE?
66 00B0 8F 00 6E 00 2C 00BF 195      BLSS 10$      ;BRANCH IF SIZE TOO BIG
      66 03 90 00C7 196      MOVCS #0,(SP),#0,#FAB$C_BLN+NAM$C_BLN,(R6) ;ZERO THEM
01 A6 50 8F 90 00CA 198      MOVB #FAB$C_BID,FAB$B_BID(R6) ;SET BLOCK ID TO FAB
      50 A6 02 90 00CF 199      MOVB #FAB$C_BLN,FAB$B_BLN(R6) ;SET LENGTH OF FAB
51 A6 60 8F 90 00D3 200      MOVB #NAM$C_BID,NAM$B_BID+FAB$C_BLN(R6) ;SET BLOCK ID TO NAME BLOCK
28 A6 50 A6 9E 00D8 201      MOVB #NAM$C_BLN,NAM$B_BLN+FAB$C_BLN(R6) ;SET LENGTH OF NAME BLOCK
      52 04 AC D0 00DD 202      MOVAB FAB$C_BLN(R6),FAB$B_BLN(R6) ;INDICATE NAME BLOCK EXISTS
      50 62 9A 00E1 203      MOVL 4(AP),R2 ;GET ADDRESS OF DESCRIPTOR
      51 04 A2 D0 00E4 204      MOVZBL (R2),R0 ;GET SIZE OF STRING
      34 A6 50 90 00E8 205      MOVL 4(R2),R1 ;GET ADDRESS OF STRING
      2C A6 51 D0 00EC 206      MOVB R0,FAB$B_FNS(R6) ;SET SIZE OF DEVICE NAME
      05 00F0 207 10$: RSB R1,FAB$B_FNA(R6) ;SET ADDRESS OF DEVICE NAME
      00F1 208 ;RETURN
      00F1 209 ;
      00F1 210 ; Routine used for common closure for both ASSIGN_FILE and ASSIGN_DEV
      00F1 211 ;
      00F1 212 ASSIGN_CHAN:
54 65 A6 0000 00F1 213      .WORD 0 ;USE COMMON REGISTER SET
      54 54 DD 00F3 214      MOVAB NAM$T_DVI+1+FAB$C_BLN(R6),R4 ;SET UP ADDRESS OF DEVICE NAME FIELD
7E FF A4 9A 00F7 215      PUSHL R4 ;FORM A DESCRIPTOR
55 5E D0 00FD 216      MOVZBL -1(R4),-(SP) ;GET SIZE OF FIELD
64 5F 8F 91 0100 217      MOVL SP,R5 ;R5 IS INPUT DESC FOR $TRNLOG
      30 13 0104 218      CMPB #'A\_(R4) ;DO WE ALREADY HAVE A PHYSICAL NAME?
      5E 32 C2 0106 219      BEQL 10$ ;BRANCH IF YES TO ASSIGN THE CHANNEL
      5E DD 0109 220      SUBL #50,SP ;ALLOCATE A BUFFER
      32 DD 010B 221      PUSHL SP ;SET UP THE ADDRESS
53 5E D0 010D 222      PUSHL #50 ;AND SIZE
      29 50 E9 0123 223      MOVL SP,R3 ;R3 IS OUTPUT DESC FOR $TRNLOG
      55 53 D0 0126 224      $TRNLOG_S (R5),(R3),(R3) ;TRANSLATE THE STRING
04 B5 1B 91 0129 225      BLBC R0,20$ ;BRANCH IF FAILURE
      07 12 012D 226      MOVL R3,R5 ;SET UP TO USE TRANSLATED STRING
      65 04 C2 012F 227      CMPB #27,@4(R5) ;DOES OUTPUT START WITH ESCAPE?
04 A5 04 C0 0132 228      BNEQ 10$ ;BRANCH IF NOT
      52 5E D0 0136 229      SUBL #4,(R5) ;REDUCE SIZE BY PROCESS PERM STUFF
      04 50 E9 0138 230      ADDL #4,4(R5) ;POINT PAST IT
      52 5E D0 0138 231 10$: PUSHL #0 ;AREA TO GET CHANNEL BACK IN
      04 50 E9 0138 232      MOVL SP,R2 ;GET ADDRESS OF AREA
      04 50 E9 0138 233      $ASSIGN_S (R5),(R2) ;ASSIGN A CHANNEL
08 BC 62 B0 0148 234      BLBC R0,20$ ;BRANCH IF THIS METHOD FAILS ALSO
      04 04 B0 014B 235      MOVW (R2),@8(AP) ;STORE THE CHANNEL FOR THE USER
      04 04 B0 014F 236 20$: RET ;RETURN
      0150 237 ;
      0150 238 .END

```

LIB\$ASSIGN
Symbol table

: Assign channel to device

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16-SEP-1984 02:17:54 VAX/VMS Macro V04-00
5-SEP-1984 04:40:09 [VMSLIB.SRC]LIB\$ASSIGN.MAR;1

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```

$$TMP1      = 00000001
$$TMP2      = 00000066
$$T1        = 00000001
ASSIGN_CHAN = 000000F1 R    02
ASSIGN_SETUP = 000000AB R    02
FABS$BID    = 00000000
FABS$BLN    = 00000001
FABS$FNS    = 00000034
FABS$C_BID  = 00000003
FABS$C_BLN  = 00000050
FABS$L_FNA  = 0000002C
FABS$L_FOP  = 00000004
FABS$L_NAM  = 00000028
FABS$V_NFS  = 00000010
LIB$ASSIGN  = 00000000 RG   02
LIB$ASSIGN_DEV = 00000041 RG   02
LIB$ASSIGN_FILE = 00000020 RG   02
LIB$INVARG  = ***** X   02
NAMS$BID    = 00000000
NAMS$BLN    = 00000001
NAMS$C_BID  = 00000002
NAMS$C_BLN  = 00000060
NAMS$L_FNB  = 00000034
NAMS$M_EXP_DIR = 00000040
NAMS$M_EXP_NAME = 00000004
NAMS$M_EXP_TYPE = 00000002
NAMS$M_EXP_VER = 00000001
NAMS$M_GRP_MBR = 00080000
NAMS$M_WILDCARD = 00000100
NAM$T_DVI   = 00000014
RMS$CHN     = ***** X   02
RMS$PRV     = ***** X   02
SS$IVDEVNAM = ***** X   02
SYS$ASSIGN  = ***** GX  02
SYS$PARSE   = ***** GX  02
SYS$TRNLOG  = ***** GX  02
    
```

! Psect synopsis !

PSECT name	Allocation	PSECT No.	Attributes
. ABS .	00000000 (0.)	00 (0.)	NOPIC USR CON ABS LCL NOSHR NOEXE NORD NOWRT NOVEC BYTE
\$ABSS	00000000 (0.)	01 (1.)	NOPIC USR CON ABS LCL NOSHR EXE RD WRT NOVEC BYTE
_LIB\$CODE	00000150 (336.)	02 (2.)	PIC USR CON REL LCL SHR EXE RD NOWRT NOVEC LONG

! Performance indicators !

Phase	Page faults	CPU Time	Elapsed Time
Initialization	29	00:00:00.09	00:00:00.78
Command processing	107	00:00:00.47	00:00:01.61
Pass 1	171	00:00:03.96	00:00:08.75
Symbol table sort	0	00:00:00.36	00:00:00.61

LIB\$ASSIGN
VAX-11 Macro Run Statistics

; Assign channel to device

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5-SEP-1984 04:40:09 [VMSLIB.SRC]LIBASSIGN.MAR;1

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Pass 2	57	00:00:00.90	00:00:01.86
Symbol table output	6	00:00:00.04	00:00:00.04
Psect synopsis output	2	00:00:00.02	00:00:00.02
Cross-reference output	0	00:00:00.00	00:00:00.00
Assembler run totals	374	00:00:05.85	00:00:13.69

The working set limit was 1050 pages.

21183 bytes (42 pages) of virtual memory were used to buffer the intermediate code.

There were 20 pages of symbol table space allocated to hold 351 non-local and 12 local symbols.

238 source lines were read in Pass 1, producing 19 object records in Pass 2.

16 pages of virtual memory were used to define 14 macros.

! Macro library statistics !

Macro library name

Macros defined

_ \$255\$DUA28:[SYSLIB]STARLET.MLB;2

11

486 GETS were required to define 11 macros.

There were no errors, warnings or information messages.

MACRO/DISA=TRACE/LIS=LIS\$:LIBASSIGN/OBJ=OBJ\$:LIBASSIGN MSRC\$:LIBASSIGN/UPDATE=(ENH\$:LIBASSIGN)

