


```

AAAAAA DDDDDDD PP PPPPPPP SSSSSSSS UU UU BBBB8888 77777777 888888 000000
AAAAAA DDDDDDD PP PPPPPPP SSSSSSSS UU UU BBBB8888 77777777 888888 000000
AA AA DD DD PP PP SS SS UU UU BB BB 77 88 88 00 00 00
AA AA DD DD PP PP SS SS UU UU BB BB 77 88 88 00 00 00
AA AA DD DD PP PP SS SS UU UU BB BB 77 88 88 00 00 00
AA AA DD DD PP PP SS SS UU UU BB BB 77 88 88 00 00 00
AA AA DD DD PPPPPPP SSSSSS UU UU BBBB8888 77 888888 00 00 00
AA AA DD DD PPPPPPP SSSSSS UU UU BBBB8888 77 888888 00 00 00
AAAAAAAAA DD DD PP SS UU UU BB BB 77 88 88 0000 00
AAAAAAAAA DD DD PP SS UU UU BB BB 77 88 88 0000 00
AA AA DU DD PP SS UU UU BB BB 77 88 88 00 00 00
AA AA DD DD PP SS UU UU BB BB 77 88 88 00 00 00
AA AA DDDDDDD PP SSSSSSS UUUUUUUUU BBBB8888 77 888888 000000
AA AA DDDDDDD PP SSSSSSS UUUUUUUUU BBBB8888 77 888888 000000

```

```

LL JIIIII SSSSSSSS
LL IIIIII SSSSSSSS
LL II SS
LL II SS
LL II SS
LL II SS
LL II SSSSSS
LL II SSSSSS
LL II SS
LL II SS
LL II SS
LLLLLLLLLL IIIIII SSSSSSSS
LLLLLLLLLL IIIIII SSSSSSSS

```

(3)	148	CISINT - CI INTERRUPT HANDLER
(4)	237	DR\$INT - DR INTERRUPT HANDLER
(5)	337	UBASINITIAL - CPU-DEPENDENT UNIBUS ADAPTER INITIALIZATION
(5)	418	MASSBUS ADAPTER INTERRUPT DISPATCHER
(5)	535	MASSBUS ADAPTER INITIALIZATION
(6)	567	INISMPMADP - BUILD ADP AND INITIALIZE MULTI-PORT MEMORY
(6)	661	MASINITIAL - INITIALIZE MULTI-PORT MEMORY ADAPTER
(6)	730	INTER-PROCESSOR REQUEST HANDLER
(6)	847	REPORT RESOURCE AVAILABILITY TO INTERESTED PORTS
(6)	891	INTER-PORT INTERRUPT DISPATCHER
(6)	1002	INTER-PROCESSOR REQUEST DISPATCHER
(6)	1070	INTERRUPT PORTS - ROUTINE TO INTERRUPT SELECTED PORTS
(6)	1090	UPDATE LOCAL COPY OF EVENT FLAG CLUSTER
(6)	1139	REPORT RESOURCE AVAILABILITY TO LOCAL SYSTEM

```
0000 1 .NOSHOW CONDITIONALS
0000 3 .TITLE ADPSUB780 - ADAPTER SUBROUTINES FOR VAX 11/780
0000 5
0000 9
0000 13
0000 17
0000 21
0000 22 .IDENT 'V04-000'
0000 23
0000 24 :*****
0000 25 :*
0000 26 :* COPYRIGHT (c) 1978, 1980, 1982, 1984 BY
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0000 42 :* SOFTWARE ON EQUIPMENT WHICH IS NOT SUPPLIED BY DIGITAL.
0000 43 :*
0000 44 :*
0000 45 :*****
0000 46 :
0000 47 : Facility: System bootstrapping and initialization
0000 48 :
0000 49 : Abstract: This module contains initialization routines that are loaded
0000 50 : during system initialization (rather than linked into the system).
0000 51 :
0000 52 : Environment: Mode = KERNEL, Executing on INTERRUPT stack, IPL=31
0000 53 :
0000 54 : Author: Kerbey T. Altmann Creation date: 30-Oct-1982
0000 55 :
0000 56 : Modification history:
0000 57 :
0000 58 : V03-007 TCM0002 Trudy C. Matthews 04-Jun-1984
0000 59 : Include more 780-specific code for the 11/790 version of
0000 60 : this routine.
0000 61 :
0000 62 : V03-006 KPL0001 Peter Lieberwirth 12-Apr-1984
0000 63 : Init ADP$S_SHB properly again; V03-004 ASSUMEd this field
0000 64 : was at a certain constant offset, and a change to the ADP
0000 65 : moved it. Note - this is a 780 change only.
0000 66 :
0000 67 : V03-005 KDM0081 Kathleen D. Morse 13-Sep-1983
0000 68 : Create version for Micro-VAX I.
0000 69 :
0000 70 : V03-004 ROW0196 Ralph O. Weber 27-JUL-1983
0000 71 : Correct INISMPMADP so the ADP$S_SHB is correctly initialized
```

```
0000 72 :           to zero.
0000 73 :
0000 74 : V03-003 MSH0001      Maryann Hinden      06-Dec-1982
0000 75 :           Add initialization for DW750.
0000 76 :
0000 77 : V03-002 ROW0142      Ralph O. Weber      23-NOV-1982
0000 78 :           Correct JMP in multiport memory interrupt dispatching code
0000 79 :           prototype, MPMINTD, to a JSB. MASINT expects to receive
0000 80 :           control via a JSB.
0000 81 :
0000 82 : V03-001 TCM0001      Trudy C. Matthews      8-Nov-1982
0000 83 :           Initialize field ADPSL_AVECTOR in IPISMPMADP.
0000 84 :
0000 85 :--
```

```
00000001 0000 88          C780_LIKE = 1
          0000 90
          0000 94
          0000 98
          0000 102
          0000 106
          0000 107      : MACRO LIBRARY CALLS
          0000 108      :
          0000 109      $ADPDEF      : Define ADP offsets.
          0000 110      $CRBDEF      : Define CRB offsets.
          0000 111      $DCDEF       : Define AI codes.
          0000 112      $DDBDEF      : Define DDB offsets.
          0000 113      $DDTDEF      : Define DDT offsets.
          0000 114      $DYNDEF      : Define data structure type codes.
          0000 115      $IDBDEF      : Define interrupt dispatcher offsets.
          0000 116      $MBADEF      : Define MASSBUS registers.
          0000 117      $MCHKDEF     : Define machine check masks.
          0000 118      $MPMDEF      : Define multi-port memory.
          0000 119      $NDTDEF      : Define nexus device types.
          0000 120      $PRDEF       : Define IPR numbers.
          0000 121      $PTEDEF      : Define Page Table Entry bits.
          0000 122      $RPBDEF      : Define Restart Parameter Block fields.
          0000 123      $SSDEF       : Define system service codes.
          0000 124      $UBADEF      : Define UBA register offsets.
          0000 125      $UBIDEF      : Define UNIBUS interconnect
          0000 126      : register offsets.
          0000 127      $UCBDEF      : Define unit control block.
          0000 128      $VADEF       : Define virtual address fields.
          0000 129      $VECDEF      : Define vec offsets.
          0000 130
          0000 132      $CEBDEF      : COMMON EVENT BLOCK
          0000 133      $FKBDEF      : FORK BLOCK
          0000 134      $IPLDEF      : INTERRUPT PRIORITY LEVELS
          0000 135      $PRIDEF      : PRIORITY INCREMENT DEFINITIONS
          0000 136      $PRQDEF      : INTER-PROCESSOR REQUEST
          0000 137      $RSNDEF      : RESOURCE NUMBER DEFINITIONS
          0000 138      $SHBDEF      : SHARED MEMORY CONTROL BLOCK
          0000 139      $SHDDEF      : SHARED MEMORY DATAPAGE
          0000 141
          0000 145
00000000 0000 146          .PSECT SYSLOA, LONG
```

```

0000 148      .SBTTL  CISINT - CI INTERRUPT HANDLER
0000 149      :
0000 150      :+ CISINT - CI INTERRUPT HANDLER
0000 151      :
0000 152      :   THIS MODULE IS A DUMMY CI32 INTERRUPT HANDLER WHICH IS USED
0000 153      :   UNTIL THE REAL CI DRIVER (PADRIVER) IS LOADED. IT ALSO CONTAINS
0000 154      :   A DUMMY CI32 CONTROLLER INITIALIZATION ENTRY POINT.
0000 155      :
0000 156      : INPUTS:
0000 157      :
0000 158      :   THE STACK ON ENTRY IS AS FOLLOWS:
0000 159      :
0000 160      :           0(SP)          ADDRESS OF IDB ADDRESS
0000 161      :   4(SP) - 16(SP)       SAVED R2 - R5
0000 162      :           20(SP)       INTERRUPT PC
0000 163      :           24(SP)       INTERRUPT PSL
0000 164      :
0000 165      : OUTPUTS:
0000 166      :
0000 167      :   NONE
0000 168      :
0000 169      : SIDE EFFECTS:
0000 170      :
0000 171      :   INTERRUPTS ARE DISABLED ON THE CI32
0000 172      : -
0000 173      :
0000 174      :
0000 175      :
0000 176      :
0000 177      :
0000 178      : $SPAREGDEF -- Define offsets to CI registers and fields in the registers.
0000 179      :
0000 180      :
0000 181      :   $DEFINI PAREG
0000 182      :
0000 183      :   $DEF  PA_CNF  .BLKL  1          ; Configuration register
0000 184      :
0000 185      :   _VIELD PA_CNF,0,<-          ; Define config register fields:
0000 186      :   <ZADPTYP,,M>,-            ; Adapter type code
0000 187      :   <PFD,,M>,-                ; Powerfail disable
0000 188      :   <TDEAD,,M>,-              ; Transmit dead
0000 189      :   <TFAIL,,M>,-              ; Transmit fail
0000 190      :   <,5>,-                    ; 5 unused bits
0000 191      :   <CRD,,M>,-                ; CRD on port init'd read
0000 192      :   <RDS,,M>,-                ; RDS on port init'd read
0000 193      :   <CXTER,,M>,-              ; SBI error confirm
0000 194      :   <RDTO,,M>,-               ; Port init'd read timeout on SBI
0000 195      :   <CSTMO,,M>,-              ; Port init'd command xmit timeout
0000 196      :   <,1>,-                    ; 1 unused bit
0000 197      :   <PUP,,M>,-                ; Adapter power up
0000 198      :   <PDN,,M>,-                ; Adaptor power down
0000 199      :   >
0000 200      :
0000 201      :   $DEF  PA_PMC  .BLKL  1          ; Port maint control/status register
0000 202      :
0000 203      :   _VIELD PA_PMC,0,<-          ; Define register fields:
0000 204      :   <ZMIN,,M>,-                ; Maint initialized
0000 205      :   <MTD,,M>,-                ; Maint timer disable
0000 206      :   <MIE,,M>,-                ; Maint interrupt enable

```

```

0008 207      <MIF,,M>,-
0008 208      >
0008 209
0008 210      $DEFEND PAREG
0000 211
0000 212  CISINT::
64    53 9E D0 0000 213      MOVL      @(SP)+,R3      ; GET ADDRESS OF IDB
64    54 63 D0 0003 214      MOVL      IDB$ (SR(R3),R4 ; GET ADDRESS OF FIRST CSR
00400000 8F D0 0006 215      MOVL      #PA_CNF_M_PUP,PA_CNF(R4) ; CLEAR POWER UP
00800000 8F D0 000D 216      MOVL      #PA_CNF_M_PDN,PA_CNF(R4) ; CLEAR POWER DOWN
04 A4 01 D0 0014 217      MOVL      #PA_PMC_M_MIN,PA_PMC(R4) ; SET MAINTENCE INITIALIZE
52 8E 7D 0018 218      MOVQ     (SP)+,R2
54 8E 7D 001B 219      MOVQ     (SP)+,R4 ; RESTORE REGISTERS
02 001E 220      REI
001F 221
001F 222
001F 223
001F 224  CISINITIAL:: ; CONTROLLER INITIALIZATION
001F 225  CISHUTDOWN:: ; CONTROLLER SHUTDOWN
001F 226
001F 227
04 A4 01 D0 001F 230
001F 231      MOVL      #PA_PMC_M_MIN,PA_PMC(R4) ; SET MAINTENCE INITIALIZE
0023 232
05 0023 233      RSB
0023 234
0023 235

```



```

0024 237 .SBTTL DR$INT - DR INTERRUPT HANDLER
0024 238 :+
0024 239 : DR$INT - DR INTERRUPT HANDLER
0024 240 :
0024 241 : THIS MODULE IS A DUMMY DR32 INTERRUPT HANDLER WHICH IS USED
0024 242 : UNTIL THE REAL DR DRIVER (XFDRIVER) IS LOADED. IT ALSO CONTAINS
0024 243 : A DUMMY DR32 CONTROLLER INITIALIZATION ENTRY POINT.
0024 244 :
0024 245 : INPUTS:
0024 246 :
0024 247 : THE STACK ON ENTRY IS AS FOLLOWS:
0024 248 :
0024 249 :         0(SP)          ADDRESS OF IDB ADDRESS
0024 250 :     4(SP) - 16(SP)    SAVED R2 - R5
0024 251 :         20(SP)        INTERRUPT PC
0024 252 :         24(SP)        INTERRUPT PSL
0024 253 :
0024 254 : OUTPUTS:
0024 255 :
0024 256 :     NONE
0024 257 :
0024 258 : SIDE EFFECTS:
0024 259 :
0024 260 :     INTERRUPTS ARE DISABLED ON THE DR32
0024 261 : -
0024 262 :
0024 263 :
0024 264 :
0024 265 :
0024 266 :
0024 267 : DR32 DCR REGISTER DEFINITIONS
0024 268 : -
0024 269 :
0024 270 $DEFINI DR
0000 271 $DEF DR_DCR .BLKL 1 ; DR32 CONTROL REGISTER
0004 272 _VIELD DR_DCR,0 <- ; ADAPTER TYPE
0004 273 <ADPTYP,8>,- ; ID2 ERROR
0004 274 <ID2ERR,,M>,- ; ID2 TIME-OUT STATUS
0004 275 <ID2TOS,2>,- ; RESERVED
0004 276 <,1>,- ; ID1 ERROR
0004 277 <ID1ERR,,M>,- ; ID1 TIME-OUT STATUS
0004 278 <ID1TOS,2>,- ; READ DATA SUBSTITUTE
0004 279 <RDS,,M>,- ; CORRECTED READ DATA
0004 280 <CRD,,M>,- ; DCR HALT
0004 281 <DCRHLT,,M>,- ; DCR ABORT INTERRUPT
0004 282 <DCRABT,,M>,- ; PACKET INTERRUPT
0004 283 <PKTINT,,M>,- ; INTERRUPT ENABLE
0004 284 <INTENB,,M>,- ; RESERVED
0004 285 <,1>,- ; ADAPTER POWER UP
0004 286 <PWR_UP,,M>,- ; ADAPTER POWER DOWN
0004 287 <PWR_DN,,M>,- ; EXTERNAL ABORT
0004 288 <EXTABT,,M>,- ; RESERVED
0004 289 <,1>,- ; IMPLEMENTATION DEPENDENT BITS
0004 290 <IMPDEP,6>,-
0004 291 >
0004 292
0004 293 : DCR CONTROL FIELD A CODES (USED WHEN WRITING TO DCR)
0004 294
00000100 0004 295 DCR_K_CLRPOWERUP=^X100

```

```

00000200 0004 296          DCR_K_CLRPRDN=^X200          ; CLEAR POWER DOWN
00000300 0004 297          DCR_K_CLREXTABT=^X300        ; CLEAR EXTERNAL ABORT
00000400 0004 298          DCR_K_CLRABTINT=^X400        ; CLEAR ABORT INTERRUPT
00000500 0004 299          DCR_K_CLRINTENB=^X500        ; CLEAR INTERRUPT ENABLE
00000600 0004 300          DCR_K_SETINTENB=^X600        ; SET INTERRUPT ENABLE
00000700 0004 301          DCR_K_CLRHLT=^X700          ; CLEAR HALT
          0004 302
          0004 303 ; DCR CONTROL FIELD B CODES (USED WHEN WRITING TO DCR)
          0004 304
00001000 0004 305          DCR_K_CLRCRD=^X1000        ; CLEAR CRD
00002000 0004 306          DCR_K_SETEXTABT=^X2000        ; SET EXTERNAL ABORT
00003000 0004 307          DCR_K CLRPKTINT=^X3000        ; CLEAR PACKET INTERRUPT
00004000 0004 308          DCR_K RESET=^X4000          ; RESET
00005000 0004 309          DCR_K SETOSQTST=^X5000        ; SET OSEQ TEST
00006000 0004 310          DCR_K CLROSQTST=^X6000        ; CLEAR OSEQ TEST
          0004 311
          0024 312
          0024 313 DR$INT::
64      53  9E  D0 0024 314      MOVL      @(SP)+,R3          ; GET ADDRESS OF IDB
64      54  63  D0 0027 315      MOVL      IDB$L(CSR(R3),R4      ; GET ADDRESS OF FIRST CSR
          8F  D0 002A 316      MOVL      #DCR_R_CLRPRUP,DR_DCR(R4) ; CLEAR POWER UP
          8F  D0 0031 317      MOVL      #DCR_K_CLRPRDN,DR_DCR(R4) ; CLEAR POWER DOWN
          52  8E  7D 0038 318      MOVQ     (SP)+,R2          ; RESTORE REGISTERS
          54  8E  7D 003B 319      MOVQ     (SP)+,R4
          02  003E 320      REI
          003F 321
          003F 324
          003F 325 DR$INITIAL::          ; CONTROLLER INITIALIZATION
          003F 326 DR$SHUTDOWN::        ; CONTROLLER SHUTDOWN
          003F 327
          003F 330
64      4000 8F  3C 003F 331      MOVZWL  #DCR_K_RESET,(R4)      ; RESET DR (R4 POINTS TO CSR)
          0044 334
          05  0044 335      RSB

```


ADPSUB780
V04-000

M 16
- ADAPTER SUBROUTINES FOR VAX 11/780 16-SEP-1984 00:41:08 VAX/VMS Macro V04-00
UBAS\$INITIAL - CPU-DEPENDENT UNIBUS ADAPT 5-SEP-1984 04:06:45 [SYSLOA.SRC]ADPSUB.MAR;1

Page 9
(5)

		0068	405				
		0068	407				
		0068	408				
3F	BA	0068	409	POPR	#^M<R0,R1,R2,R3,R4,R5>		
		006A	412				
	02	006A	414	REI			

: FOR 780-LIKE PROCESSORS, RESTORE
: SAVED REGISTERS
:
: FOR 11/750, NO REGISTERS SAVED
: IGNORE INTERRUPT

```

006B 418 .SBTTL MASSBUS ADAPTER INTERRUPT DISPATCHER
006B 419 :+
006B 420 : MBASINT - MASSBUS ADAPTER INTERRUPT DISPATCHER
006B 421 :
006B 422 : THIS ROUTINE IS ENTERED VIA A JSB INSTRUCTION WHEN AN INTERRUPT OCCURS
006B 423 : ON A MASSBUS ADAPTER. THE STATE OF THE STACK ON ENTRY IS:
006B 424 :
006B 425 : 00(SP) = ADDRESS OF IDB ADDRESS.
006B 426 : 04(SP) = SAVED R2.
006B 427 : 08(SP) = SAVED R3.
006B 428 : 12(SP) = SAVED R4.
006B 429 : 16(SP) = SAVED R5.
006B 430 : 20(SP) = INTERRUPT PC.
006B 431 : 24(SP) = INTERRUPT PSL.
006B 432 :
006B 433 : INTERRUPT DISPATCHING OCCURS AS FOLLOWS:
006B 434 :
006B 435 : IF THE INTERRUPTING ADAPTER IS CURRENTLY OWNED AND THE OWNER UNIT
006B 436 : IS EXPECTING AN INTERRUPT, THEN THAT UNIT IS DISPATCHED FIRST. ALL
006B 437 : OTHER UNITS ARE DISPATCHED BY READING THE ATTENTION SUMMARY REG-
006B 438 : ISTER AND SCANNING FOR UNITS THAT HAVE ATTENTION SET. AS EACH UNIT
006B 439 : IS FOUND, ITS ATTENTION SUMMARY BIT IS CLEARED AND THEN A TEST IS
006B 440 : MADE TO DETERMINE IF AN INTERRUPT IS EXPECTED ON THE UNIT. IF YES,
006B 441 : THEN THE DRIVER IS CALLED AT ITS INTERRUPT RETURN ADDRESS. ELSE
006B 442 : THE DRIVER IS CALLED AT ITS UNSOLICITED INTERRUPT ADDRESS. AS EACH
006B 443 : CALL TO THE DRIVER RETURNS, THE ATTENTION SUMMARY REGISTER IS RE-
006B 444 : READ AND AN ATTEMPT IS MADE TO FIND ANOTHER UNIT TO DISPATCH. WHEN
006B 445 : NO UNITS REQUESTING ATTENTION REMAIN, THE INTERRUPT IS DISMISSED.
006B 446 :-
006B 447 :
006B 448 .ALIGN LONG
006C 449
006C 450 MBASINT:: :MASSBUS ADAPTER INTERRUPT DISPATCHER
53 00 BE D0 006C 451 MOVL @ (SP),R3 :GET ADDRESS OF IDB
54 63 D0 0070 452 MOVL IDB$$_CSR(R3),R4 :GET ADDRESS OF CONFIGURATION STATUS REGISTE
0073 453
0073 455
00800000 8F D3 0073 456 BITL #MBASM_CSR_PD,-
64 0079 457 MBASL_CSR(R4) :CHECK FOR MBA POWER DOWN
61 12 007A 458 BNEQ 45$ :BRANCH IF POWERFAIL
007C 459
007C 467
55 04 A3 D0 007C 468 MOVL IDB$$_OWNER(R3),R5 :GET OWNER UNIT UCB ADDRESS
0A 13 0090 469 BEQL 10$ :IF EQL NO OWNER
52 0090 C5 9A 0082 470 MOVZBL UCBSB_SLAVE(R5),R2 :GET OWNER SLAVE CONTROLLER NUMBER
21 64 A5 01 E0 0087 471 BBS #UCBSB_INT,UCBSW_STS(R5),20$ :IF SET, INTERRUPT EXPECTED
53 00 BE D0 008C 472 10$: MOVL @ (SP),R3 :RETRIEVE ADDRESS OF IDB
54 63 D0 0090 473 MOVL IDB$$_CSR(R3),R4 :RETRIEVE MBA CONFIGURATION REGISTER ADDRESS
08 A4 00 D2 0093 474 MCOML #0,MBASL_SR(R4) :CLEAR ALL MBA STATUS BITS
52 0410 C4 D0 0097 475 MOVL MBASL_ASTR4),R2 :READ ATTENTION SUMMARY REGISTER
52 52 08 00 EA 009C 476 FFS #0,#8,R2,R2 :FIND FIRST UNIT REQUESTING ATTENTION
0A 12 00A1 477 BNEQ 20$ :IF NEQ UNIT FOUND
5E 04 C0 00A3 478 ADDL #4,SP :REMOVE IDB ADDRESS FROM STACK
52 8E 7D 00A6 479 MOVQ (SP)+,R2 :RESTORE REGISTERS
54 8E 7D 00A9 480 MOVQ (SP)+,R4
02 00AC 481 REI
00AD 482

```

```

55 18 A342 D0 00AD 483 20$: MOVL IDB$UCBLST(R3)[R2],R5 ;GET ADDRESS OF UCB OR INTERRUPT DISPATCHER
    22 55 E8 00B2 484 BLBS R5,40$ ;IF LBS INTERRUPT DISPATCHER FOR MULTI-
    00B5 485 ; DEVICE CONTROLLER
0410 C4 01 52 78 00B5 486 ASHL R2,#1,MBASL_AS(R4) ;CLEAR ATTENTION SUMMARY BIT
    55 D5 00BB 487 TSTL R5 ;SEE IF UCB DEFINED
    CD 13 00BD 488 BEQL 10$ ;IF EQL NONE DEFINED
09 64 A5 01 E5 00BF 489 BBCC #UCBSV_INT,UCBSW_STS(R5),30$ ;IF CLR, INTERRUPT NOT EXPECTED
    53 10 A5 7D 00C4 490 MOVQ UCB$FR3(R5),R3 ;RESTORE DRIVER CONTEXT
    OC B5 16 00C8 491 JSB @UCB$FPC(R5) ;CALL DRIVER AT INTERRUPT RETURN ADDRESS
    BF 11 00CB 492 BRB 10$ ;
    00CD 493 ;
53 0088 C5 D0 00CD 494 30$: MOVL UCB$DDT(R5),R3 ;GET ADDRESS OF DDT
    04 B3 16 00D2 495 JSB @DDT$E_UNSOINT(R3) ;CALL UNSOLICITED INTERRUPT ROUTINE
    B5 11 00D5 496 BRB 10$ ;
    00D7 497 ;
    7E DC 00D7 498 40$: MOVPSL -(SP) ;READ CURRENT PSL
    75 16 00D9 499 JSB -(R5) ;CALL SLAVE CONTROLLER INTERRUPT DISPATCHER
    AF 11 00DB 500 BRB 10$ ;
    00DD 501 ;
    00DD 503 ;
    00DD 504 ;
    00DD 505 ; IN CASE OF ADAPTER POWER DOWN BIT ASSERTED, RETRIEVE ADP ADDRESS AND JUMP
    00DD 506 ; TO ADAPTER ERROR ROUTINE IN SYSLOA780.
    00DD 507 ;
    00DD 508 ;
54 14 A3 D0 00DD 509 45$: MOVL IDB$ADP(R3),R4 ;GET ADP ADDRESS
    FF1C' 31 00E1 510 BRW EXE$RR780_INT ;JUMP TO ERROR ROUTINE
    00E4 511 ;
    00E4 533 ;

```

```

00E4 535      .SBTTL  MASSBUS ADAPTER INITIALIZATION
00E4 536      :+
00E4 537      : MBASINITIAL - MASSBUS ADAPTER INITIALIZATION
00E4 538      :
00E4 539      : THIS ROUTINE IS CALLED VIA A JSB INSTRUCTION AT SYSTEM STARTUP AND AFTER
00E4 540      : A POWER RECOVERY RESTART TO ALLOW INITIALIZATION OF MASSBUS ADAPTERS.
00E4 541      :
00E4 542      : INPUTS:
00E4 543      :
00E4 544      :     R4 = CSR ADDRESS OF MASSBUS ADAPTER.
00E4 545      :     R5 = ADDRESS OF ADAPTER IDB.
00E4 546      :
00E4 547      :     ALL INTERRUPTS ARE LOCKED OUT.
00E4 548      :
00E4 549      : OUTPUTS:
00E4 550      :
00E4 551      :     THE MASSBUS ADAPTER IS INITIALIZED AND INTERRUPTS ARE ENABLED.
00E4 552      : -
00E4 553      :
00E4 554      MBASINITIAL::                                ;MASSBUS ADAPTER INITIALIZATION
00E4 555      :
04 01  D0 00E4 558      MOVL  #MBASM_CR_INIT,-
00E4 559      MBASL_CR(R4)                                ;INITIALIZE MASSBUS ADAPTER
04 04  D0 00E8 560      MOVL  #MBASM_CR_IE,-
00E4 561      MBASL_CR(R4)                                ;ENABLE INTERRUPTS
00E4 562      :
00EC 564      :
05 00EC 565      RSB

```

```

00ED 567      .SBTTL INISMPMADP - BUILD ADP AND INITIALIZE MULTI-PORT MEMORY
00ED 568      :+
00ED 569      : INISMPMADP IS CALLED AFTER MAPPING THE REGISTERS FOR A MULTI-PORT
00ED 570      : MEMORY ADAPTER. AN ADAPTER CONTROL BLOCK IS ALLOCATED AND FILLED.
00ED 571      : THE HARDWARE ADAPTER IS THEN INITIALIZED BY CALLING MPM$INITIAL.
00ED 572      :
00ED 573      : NOTE: THIS ROUTINE HAS BEEN LOCATED HERE IN SYSLOAXXX.EXE INSTEAD OF
00ED 574      : INILOA.EXE BECAUSE IT CAN BE CALLED WHILE THE SYSTEM IS RUNNING
00ED 575      : LONG AFTER INILOA.EXE HAS BEEN DELETED!!!
00ED 576      :
00ED 577      : INPUT:
00ED 578      : R4 - nexus identification number of this nexus
00ED 579      :
00ED 580      : OUTPUTS:
00ED 581      : ALL REGISTERS PRESERVED
00ED 582      :-
00ED 583      :
00000010 00ED 584 NUMMPMVEC = 16 ; NUMBER OF INTER-PORT INTERRUPT VECTORS
00ED 585      :
00ED 586 INISMPMADP:: ; INITIALIZE MPM DATA STRUCTURES
00ED 587      :
00ED 592      PUSH  #^M<R0,R1,R2,R3,R4,R5,R6,R7,R8,R9,R10> ; SAVE REGISTERS
00F1 593      :
00F1 594      : Allocate and initialize Adapter Control Block (ADP).
00F1 595      :
00F1 596      MOVZWL #ADP$C MPMADPLEN+- ; GET SIZE OF ADP PLUS VECTOR
00F6 597      <NUMMPMVEC * 4>,R1 ; DISPATCH TABLE
00F6 598      JSB @#EXE$ALONONPAGED ; ALLOCATE ADP FOR ADAPTER
58 00000000'9F 16 00FC 599      MOVL R2,R3 ; COPY ADDRESS
00000000'9F 53 52 D0 00FF 600      MOVL @#MMG$GL SBICONF,R8 ; GET SYSTEM ADDRESS OF CONFIG ARRAY
83 6844 D0 0106 601      ASSUME ADP$C_CSR EQ 0
0106 602      MOVL (R8)[R4],(R3)+ ; SET ADDRESS OF CONFIG REGISTER
010A 603      ASSUME ADP$C_LINK EQ 4
83 D4 010A 604      CLRL (R3)+ ; CLEAR LINK FIELD
010C 605      ASSUME ADP$W_SIZE EQ 8
83 51 B0 010C 606      MOVW R1,(R3)+ ; SET SIZE OF STRUCTURE
83 01 9B 010F 607      MOVZBW #DYN$C ADP,(R3)+ ; SET TYPE OF STRUCTURE
0112 608      ASSUME ADP$W_TR EQ 12
83 54 B0 0112 609      MOVW R4,(R3)+ ; SET NEXUS NUMBER OF ADAPTER
0115 610      ASSUME ADP$W_ADPTYPE EQ 14
83 03 B0 0115 611      MOVW #ATS MPM,(R3)+ ; SET THE ADAPTER TYPE
0118 612      ASSUME ADP$C_VECTOR EQ 16
51 40 A2 DE 0118 613      MOVAL ADP$C_VECTOR+8(R2),R1 ; GET ADDRESS OF DISPATCH TABLE
83 51 D0 011C 614      MOVL R1,(R3)+ ; SET ADDRESS OF DISPATCH TABLE
011F 615      ASSUME ADP$C_PRQQFL EQ 20
011F 616      ASSUME ADP$C_PRQQBL EQ 24
83 83 53 D0 011F 617      MOVL R3,(R3)+ ; INIT PRQ WAIT QUEUE FORWARD PTR.
83 14 A2 DE 0122 618      MOVAL ADP$C_PRQQFL(R2),(R3)+ ; INIT PRQ WAIT QUEUE BACKWARD PTR.
30 A2 7C 0126 619      CLRQ ADP$C_SHB(R2) ; CLEAR SHB FIELD
0129 620      :
0129 621      : Initialize adapter interrupt vectors in System Control Block.
0129 622      :
50 00000000'9F D0 0129 623      MOVL @#EXE$GL_SCB,R0 ; GET ADDRESS OF SCB
54 54 04 00 EF 0130 624      EXTZV #0,#4,R4,R4 ; Get low 4 bits of nexus number.
50 0100 C044 DE 0135 625      MOVAL ^X100(R0)[R4],R0 ; COMPUTE ADDR OF 1ST INT VECTOR
1C A2 50 D0 013B 626      MOVL R0,ADP$C_AVECTOR(R2) ; SAVE ADDRESS OF ADAPTER'S SCB VECTORS
013F 627      : *** VECTORS WITHOUT JUMPER ***

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40 A0 60 39 A2 9E 013F 628        MOVAB    ADPSL_INTD+1(R2),(R0)    ; CONNECT IPL 20 TO DISPATCHER
                    9E 0143 629        MOVAB    W^EXESINT58+1,64(R0)        ; CONNECT IPL 21 TO ERROR LOGGER
                    0149 630                                                  ; *** VECTORS WITH JUMPER ***
0080 C0 39 A2 9E 0149 631        MOVAB    ADPSL_INTD+1(R2),128(R0) ; CONNECT IPL 22 TO DISPATCHER
00C0 C0 0001'CF 9E 014F 632        MOVAB    W^EXESINT58+1,192(R0) ; CONNECT IPL 23 TO ERROR LOGGER
                    0156 633                                           ; ***
                    0156 634                                           ;
                    0156 635                                           ;
                    0156 636                                           ;
                    10S:
38 A2 82'AF D0 0156 637        MOVL    B^MPMINTD,ADPSL_INTD(R2) ; INIT INTER-PORT DISPATCHER
3C A2 02CE'CF 9E 015B 638        MOVAB    W^MASINT,ADPSL_INTD+4(R2) ; AND ADDRESS
                    50 10 D0 0161 639        MOVL    #NUMMPMVEC,R0            ; GET NUMBER OF VECTORS IN TABLE
                    10S:
                    81 D4 0164 641        CLRL    (R1)+                    ; SET VECTOR TO 'NOT IN USE''
                    FB 50 F5 0166 642        SOBGTR    RO,10S                    ; DECREMENT COUNT AND LOOP
                    54 62 D0 0169 643        MOVL    ADPSL_CSR(R2),R4        ; GET ADAPTER CSR ADDRESS
                    50 64 D0 016C 644        MOVL    MPMSL_CSR(R4),R0        ; GET CSR VALUE
                    00 EF 016F 645        EXTZV    #MPMSV_CSR_PORT,-            ; GET PORT NUMBER
50 50 02        0171 646               #MPMSS_CSR_PORT,R0,R0        ;
34 A2 50 90 0174 647        MOVB    RO,ADPSB_PORT(R2)       ; SAVE PORT NUMBER
                    FE85' 30 0178 648        BSBW    ADPLINK                    ; LINK ADP TO END OF CHAIN
                    017B 649                                           ;
                    017B 650                                           ;
                    017B 651                                           ;
                    0D 10 017B 652        BSBB    MASINITIAL                ; INITIALIZE THE ADAPTER
                    07FF 8F BA 017D 653        POPR    #^M<R0,R1,R2,R3,R4,R5,R6,R7,R8,R9,R10> ; RESTORE REGS
                    05 0181 654        RSB                                    ;
                    0182 655                                           ;
                    0182 656 MPMINTD:                                    ; MULTI-PORT INTERRUPT DISPATCHER
                    3F BB 0182 657        PUSHR    #^M<R0,R1,R2,R3,R4,R5>       ;
00000000 9F 16 0184 658        JSB    @#0                        ;

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EXE
EXE
FKB
FKB
FKB
IDB
IDB
IDB
INI
INT


```

24 A4 0F 50 78 01EE 722 ASHL RO,#^XF,MPMSL_IIE(R4) ; ENABLE INTERPORT INTERRUPT
00 01F3 723 MOVL #MPMSM_CR_MIE;- ; ENABLE ALL INTERRUPTS
04 A4 03 01F4 724 MPMSM_CR_EIE,- ;
01F4 725 MPMSL_CR(R4) ;
05 01F7 726 ; FROM ALL PORTS
RSB ; RETURN

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01F8 730      .SBTTL INTER-PROCESSOR REQUEST HANDLER
01F8 731      :
01F8 732      :
01F8 733      : FUNCTIONAL DESCRIPTION:
01F8 734      :
01F8 735      : THIS ROUTINE IS CALLED BY A DRIVER OR AN EXEC FUNCTION TO
01F8 736      : EITHER SEND A REQUEST TO OR JUST INTERRUPT ANOTHER PROCESSOR
01F8 737      : THAT IS CONNECTED TO A PORT OF THE MULTIPOINT MEMORY.
01F8 738      :
01F8 739      : INPUTS:
01F8 740      :
01F8 741      : R4 = ADAPTER CONTROL BLOCK ADDRESS.
01F8 742      : R5 = IF LSS 0 - ADDRESS OF A FORK BLOCK TO USE IF REQUEST
01F8 743      :       BLOCK IS NOT AVAILABLE.
01F8 744      :       IF GEQ 0 - PORT NUMBER OF PROCESSOR TO JUST INTERRUPT.
01F8 745      :
01F8 746      : OUTPUTS:
01F8 747      :
01F8 748      : WHEN THIS ROUTINE IS CALLED WITH A FORK BLOCK ADDRESS, IT WILL
01F8 749      : ATTEMPT TO ALLOCATE A REQUEST BLOCK. IF THE REQUEST FAILS,
01F8 750      : THE CONTEXT OF THE CALLER WILL BE SAVED IN THE FORK BLOCK, THE
01F8 751      : FORK BLOCK WILL BE INSERTED IN THE REQUEST BLOCK WAIT
01F8 752      : QUEUE AND A RETURN TO THE CALLER'S CALLER IS EXECUTED.
01F8 753      :
01F8 754      : IF A REQUEST BLOCK IS ALLOCATED SUCCESSFULLY, CONTROL WILL
01F8 755      : RETURN TO THE CALLER VIA A CO-ROUTINE CALL SO THE CALLER CAN
01F8 756      : FILL-IN THE REQUEST BLOCK.
01F8 757      :
01F8 758      : THE CALLER WILL THEN PERFORM ANOTHER CO-ROUTINE CALL TO RETURN
01F8 759      : TO THIS ROUTINE SO THE BLOCK CAN BE INSERTED IN THE DESIRED
01F8 760      : PROCESSOR'S INTER-PROCESSOR REQUEST QUEUE. IF IT IS THE
01F8 761      : FIRST REQUEST IN THE QUEUE AN INTER-PORT INTERRUPT WILL
01F8 762      : ALSO BE REQUESTED TO WAKE-UP THE DISPATCHER ON THE PORT.
01F8 763      :
01F8 764      :
01F8 765      : IF THIS ROUTINE IS CALLED WITH A PORT NUMBER INSTEAD OF A
01F8 766      : FORK BLOCK ADDRESS, IT WILL JUST REQUEST AN INTERRUPT FOR
01F8 767      : THE PROCESSOR ON THE SPECIFIED PORT. IT IS THEN UP TO THE
01F8 768      : INTERRUPTED PROCESSOR TO DETERMINE WHAT THE INTERRUPT WAS
01F8 769      : FOR.
01F8 770      :
01F8 771      : R0 = SUCCESS OR FAILURE OF OPERATION. THIS SHOULD BE CHECKED
01F8 772      : BY THE CALLER BOTH TIMES THIS ROUTINE RETURNS.
01F8 773      :
01F8 774      : R3,R4,R5 ARE PRESERVED.
01F8 775      :
01F8 776      :--
01F8 777      :
01F8 778      MAS$REQUEST:: ; REQUEST HANDLER
01F8 779      :
01F8 780      :
01F8 781      :
01F8 782      :
01F8 783      :
01F8 784      :
01F8 785      :
01F8 786      :
01F8 787      :
01F8 788      :
01F8 789      :
01F8 790      :
01F8 784      MOVL R5,R1 ; FORK BLOCK ADDRESS SPECIFIED?
01F8 785      BGEQ REQ_INTERRUPT ; IF GEQ, NO - JUST AN INTERRUPT
01F8 786      MOVL ADP$S_SHB(R4),R1 ; GET SHB ADDRESS
01F8 787      MOVL SHB$S_DATAPAGE(R1),R1 ; GET DATAPAGE ADDRESS
01F8 788      MOVZBL ADP$B_PORT(R4),R0 ; GET OUR PORT NUMBER
01F8 789      BBSSI R0,SHB$W_PRQWAIT(R1),58 ; ASSUME FAILURE (AVOID MISSING NOTIFICATION
01F8 790      QRETRY SUCCESS=TOS- ; GET A REQUEST BLOCK

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51 55 D0
51 30 A4 D0
51 04 A1 D0
50 34 A4 9A
00 00A4 C1 50 E6

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50 0394 8F 3C 020F 791 REMQHI SHD$Q PRQ(R1),R2 ;
05 0220 792 MOVZWL #SS$_BADQUEUEHDR,R0 ; SET FAILURE STATUS CODE
05 0225 793 RSB ;
05 0226 794 10$: ;
50 00A4 C1 5B 1D 0226 795 BVS NOBLOCKS ; IF V-SET, NO BLOCKS LEFT
34 A4 9A 0228 796 MOVZBL ADP$B_PORT(R4),R0 ; GET OUR PORT NUMBER
00 00A4 C1 50 E7 022C 797 BBCCI R0,SHD$W_PRQWAIT(R1),RETURN_BLOCK ; CLEAR WAITING FLAG
0232 798 ;
0232 799 ; RETURN ADDRESS OF REQUEST BLOCK TO CALLER
0232 800 ;
0232 801 RETURN_BLOCK: ; RETURN ADDRESS OF BLOCK
50 01 D0 0232 802 MOVL #SS$_NORMAL,R0 ; SET SUCCESS
9E 16 0235 803 JSB @($PT)+ ; CO-ROUTINE CALL CALLER BACK
0237 804 ;
0237 805 ; INSERT BLOCK IN DESIRED PORT'S REQUEST QUEUE
0237 806 ;
51 18 54 DD 0237 807 PUSHL R4 ; SAVE REGISTER
50 30 A4 D0 0239 808 MOVZWL PRQ$W_TO_PORT(R2),R1 ; GET DESIRED PORT NUMBER
54 04 A0 D0 023D 809 MOVL ADP$B_SHB(R4),R0 ; GET SHB ADDRESS
0241 810 MOVL SHB$B_D_TAPAGE(R0),R4 ; GET DATAPAGE ADDRESS
0245 811 QRETRY SUCCESS=10$- ; INSERT REQUEST IN PORT'S WORK QUEUE
50 0394 8F 3C 0245 812 INSQTI (R2),SHD$Q_PROQRK(R4)[R1] ;
05 05 11 0257 813 MOVZWL #SS$_BADQUEUEHDR,R0 ; SET FAILURE STATUS CODE
07 13 025C 814 BRB 20$ ;
50 01 D0 025E 815 10$: BEQL 30$ ; IF EQL, FIRST ENTRY IN QUEUE
54 8ED0 0260 816 MOVL #SS$_NORMAL,R0 ; SET SUCCESS
05 0263 817 20$: POPL R4 ; RESTORE REGISTER
0266 818 RSB ;
0267 819 ;
54 8ED0 0267 820 30$: POPL R4 ; RESTORE REGISTER
026A 821 ;
026A 822 ; REQUEST AN INTER-PORT INTERRUPT TO WAKE-UP PROCESSOR ON DESIRED
026A 823 ; PORT.
026A 824 ;
50 34 A4 9A 026A 825 REQ_INTERRUPT: ; REQUEST AN INTER-PORT INTERRUPT
50 04 C4 026A 826 MOVZBL ADP$B_PORT(R4),R0 ; GET OUR PORT NUMBER
50 51 C0 026E 827 MULL #MPM$C_PORTS,R0 ; COMPUTE INTERRUPT REQUEST BIT #
50 10 C0 0271 828 ADDL R1,R0 ; ...
51 64 D0 0274 829 ADDL #MPM$V_IIR_CTL,R0 ; ...
20 A1 01 50 78 0277 830 MOVL ADP$B_CSR(R4),R1 ; GET ADAPTER CSR ADDRESS
50 01 D0 027A 831 ASHL R0,#1,MPM$B_IIR(R1) ; SET PORT INTERRUPT REQUEST BIT
05 027F 832 MOVL #SS$_NORMAL,R0 ; SET SUCCESS
0282 833 RSB ;
0283 834 ;
0283 835 ; NO BLOCKS ARE AVAILABLE. SAVE THE CALLER'S CONTEXT IN THE FORK
0283 836 ; BLOCK, INSERT THE FORK BLOCK IN THE REQUEST BLOCK WAIT QUEUE, AND
0283 837 ; RETURN TO THE CALLER'S CALLER.
0283 838 ;
0283 839 NOBLOCKS: ; NO REQUEST BLOCKS AVAILABLE
10 A5 53 7D 0283 840 MOVQ R3,FKB$B_FR3(R5) ; SAVE REGISTERS
18 B4 0C A5 8ED0 0287 841 POPL FKB$B_FPC(R5) ; SAVE RETURN ADDRESS
50 65 0E 028B 842 INSQUE (R5),ADP$B_PROQBL(R4) ; INSERT FORK BLOCK IN WAIT QUEUE
50 01 D0 028F 843 MOVL #SS$_NORMAL,R0 ; SET SUCCESS
05 0292 844 RSB ; RETURN TO CALLER'S CALLER

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0293 847      .SBTTL REPORT RESOURCE AVAILABILITY TO INTERESTED PORTS
0293 848      :++
0293 849      :
0293 850      : FUNCTIONAL DESCRIPTION:
0293 851      :
0293 852      : THIS ROUTINE IS CALLED TO REPORT TO ANY PROCESSORS THAT A RESOURCE
0293 853      : HAS BEEN MADE AVAILABLE.
0293 854      :
0293 855      : INPUTS:
0293 856      :
0293 857      : R0 = RESOURCE NUMBER OF RESOURCE MADE AVAILABLE.
0293 858      : R1 = SHARED MEMORY CONTROL BLOCK (SHB) ADDRESS.
0293 859      :
0293 860      : OUTPUTS:
0293 861      :
0293 862      : ANY PROCESSORS WAITING FOR THE SPECIFIED RESOURCE ARE INTERRUPTED
0293 863      : TO NOTIFY THEM THE RESOURCE IS AVAILABLE.
0293 864      :
0293 865      : R0,R1,R2,R3 ARE NOT PRESERVED.
0293 866      :--
0293 867      :
0293 868      MASRAVAIL::
0293 869      :
0293 874      MOVL   SHB$$_DATAPAGE(R1),R2      ; GET ADDRESS OF DATAPAGE
0293 875      TSTW   SHD$$_RESWAIT(R2)[R0]    ; ANYONE WAITING FOR THE RESOURCE?
0293 876      BEQL   30$                               ; IF EQL, NO
0293 877      PUSHL  SHB$$_ADP(R1)              ; SAVE ADDRESS OF ADAPTER CONTROL BLOCK
0293 878      CLRL   R3                          ; INIT PORT NUMBER
0293 879      :
0293 880      10$:  MOVAW  SHD$$_RESWAIT(R2)[R0],R1 ; GET ADDRESS OF RESOURCE WAIT MASK
0293 881      BBC     R3,(RT),20$                  ; IF CLR, NO ONE WAITING AT PORT
0293 882      MOVAW  SHD$$_RESAVAIL(R2)[R0],R1 ; GET ADDRESS OF AVAILABLE MASK
0293 883      BBSSI  R3,(RT),15$                  ; SET PORT'S RESOURCE AVAIL BIT
0293 884      15$:  BBSSI  R3,SHD$$_RESSUM(R2),20$ ; SET PORT'S RESOURCE AVAIL SUMMARY BIT
0293 885      20$:  AOBLS  #MPMSC_PORTS,R3,10$ ; INCREMENT PORT NUMBER AND LOOP
0293 886      POPL   R3                          ; GET ADDRESS OF ADAPTER CONTROL BLOCK
0293 887      MOVZWL SHD$$_RESWAIT(R2)[R0],R1 ; GET RESOURCE WAIT MASK
0293 888      BSBW   INTERRUPT_PORTS              ; INTERRUPT WAITING PORTS
0293 889      30$:  RSB                               ;

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02CE 891      .SBTTL INTER-PORT INTERRUPT DISPATCHER
02CE 892      :++
02CE 893      :
02CE 894      : FUNCTIONAL DESCRIPTION:
02CE 895      :
02CE 896      : THIS ROUTINE IS ENTERED V.A A JSB INSTRUCTION WHEN AN INTERRUPT
02CE 897      : OCCURS ON A MULTI-PORT MEMORY. THE STATE OF THE STACK ON ENTRY IS:
02CE 898      :
02CE 899      : 00(SP) = ADDRESS OF BYTE FOLLOWING 'JSB @#MASINT' IN ADPSL_INTD.
02CE 900      : 04(SP) - 24(SP) = SAVED R0 - R5.
02CE 901      : 28(SP) = INTERRUPT PC.
02CE 902      : 32(SP) = INTERRUPT PSL.
02CE 903      :
02CE 904      : INTERRUPT DISPATCHING OCCURS AS FOLLOWS:
02CE 905      :
02CE 906      : THE FIRST REQUEST BLOCK IN THIS PORT'S INTER-PROCESSOR REQUEST
02CE 907      : QUEUE IS DEQUEUED. THE REQUEST BLOCK IS THEN USED AS A FORK
02CE 908      : BLOCK WHICH IS THEN ENTERED INTO THE LOWEST IPL DEVICE DRIVER
02CE 909      : FORK QUEUE. IF THE FORK BLOCK WAS THE FIRST IN THE FORK QUEUE,
02CE 910      : A SOFTWARE INTERRUPT IS POSTED TO DISPATCH THE FORK PROCESS WHEN
02CE 911      : THE IPL IS LOW ENOUGH. WHEN ALL THE REQUEST BLOCKS HAVE BEEN
02CE 912      : DEQUEUED FROM THE PORT'S INTER-PROCESSOR REQUEST QUEUE, AND
02CE 913      : REQUEUED TO THE FORK QUEUE, THE INTERRUPT IS DISMISSED.
02CE 914      :--
02CE 915      :
02CE 916      MASINT:                                ; MA780 INTERRUPT DISPATCHER
02CE 917
02CE 918      MOVL   (SP)+,R3                          ; GET ADDRESS OF BYTE IN ADP
13 53 8E D0 02D1 919      MOVAB  -<ADPSL_INTD+8>(R3),R3      ; COMPUTE ADDRESS OF ADP
02CE 920      MOVL   ADPSL_CSR(R3),R0                  ; GET CSR ADDRESS
02CE 921      MOVZBL ADPSB_PORT(R3),R2                 ; GET PORT NUMBER
20 51 52 04 C5 02DC 922      MULL3  #MPM$C_PORTS,R2,R1          ; COMPUTE INTERRUPT REQUEST BIT #
02CE 923      ASHL  R1,#^XF,MPM$S_IIR(R0)            ; CLEAR ANY INTERRUPT REQUESTS
02CE 924      MOVL  ADPSL_SHB(R3),R4                  ; GET ADDRESS OF SHB
02CE 925      BNEQ  10$                                ; IF NEQ, MEMORY CONNECTED
02CE 926      BRW   INT_EXIT                          ; ELSE, IGNORE INTERRUPT
02CE 927 10$:  MOVL  SHB$S_DATAPAGE(R4),R4           ; GET ADDRESS OF DATAPAGE
02CE 928      :                                       ; R2 = PORT NUMBER
02CE 929      :                                       ; R3 = ADP ADDRESS
02CE 930      :                                       ; R4 = DATAPAGE ADDRESS
00 00A6 C4 52 BB 02F2 931      PUSHR  #^M<R3,R4>                ; SAVE R3-R4
02CE 932      BBSSI R2,SHD$W_POLL(R4),DEQUEUE_BLOCK ; INDICATE THIS PROCESSOR ACTIVE
02CE 933      :
02CE 934      : DEQUEUE THE NEXT REQUEST BLOCK IN OUR WORK QUEUE AND REQUEUE TO THE
02CE 935      : APPROPRIATE FORK QUEUE.
02CE 936      :
02CE 937      DEQUEUE_BLOCK:                          ; DEQUEUE NEXT REQUEST BLOCK
02CE 938      MOVA  (SP),R3                          ; RESTORE R3-R4
02CE 939      QRETRY SUCCESS=10$-                    ; DEQUEUE THE NEXT REQUEST BLOCK
02CE 940      REMQHI SHD$Q_PRQWRK(R4)[R2],R5         ;
02CE 941      BUG_CHECK BADQHDR                      ; REMQHI FAILED - BAD QUEUE HEADER
02CE 942 10$:  BVS  BLOCK_AVAIL                      ; IF V-SET, NO BLOCKS LEFT
02CE 943      PUSHAB DEQUEUE_BLOCK                  ; SET RETURN PC
02CE 944      PUSHAB W^REQUEST_DISP                 ; SET FORK PC
02CE 945      JMP   G^EXES$FORK                     ; INSERT BLOCK IN FORK QUEUE
02CE 946      :
02CE 947      : IF THERE ARE ANY FORK PROCESSES ON THIS PROCESSOR WAITING FOR INTER-PROCESSOR

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0322 948 ; REQUEST BLOCKS, AND IF ANY BLOCKS ARE NOW AVAILABLE, GIVE THEM TO THE
0322 949 ; PROCESSES AND RESTART THEM.
0322 950 ;
0322 951 BLOCK_AVAIL: ; CHECK IF ANY BLOCKS AVAILABLE
50 00A4 C4 52 E1 0322 952 BBC R2,SHD$W_PRQWAIT(R4),RESOURCE_AVAIL ; IF CLR, NO PROCESSES WAITING
53 6E 7D 0328 953 10$: MOVQ (SP),R3 ; RESTORE R3-R4
032B 954 QRETRY SUCCESS=20$- ; ATTEMPT TO ALLOCATE A FREE BLOCK
032B 955 REMQHI SHD$Q_PRQ(R4),R5 ;
033C 956 BUG_CHECK BADQHDR ; REMQHI FAILED - BAD QUEUE HEADER
51 14 B3 1D 0340 957 20$: BVS RESOURCE_AVAIL ; IF V-SET, NO BLOCK AVAILABLE
06 12 0342 958 REMQUE @ADP$L_PRQFL(R3),R1 ; GET NEXT WAITING FORK BLOCK
00 00A4 C4 52 E7 0346 959 BNEQ 30$ ; IF NEQ, NOT LAST ENTRY
53 51 D0 0348 960 BBCCI R2,SHD$W_PRQWAIT(R4),30$ ; ELSE LAST, CLEAR WAITING FLAG
OB A5 53 51 D0 0350 961 30$: BVS 50$ ; IF SET, NO PROCESSES LEFT
OB A5 OB A3 90 0353 962 MOVL R1,R3 ; SET ADDRESS OF FORK BLOCK
CD AF 9F 0358 963 MOVB FKBSB_FIPL(R3),FKBSB_FIPL(R5) ; SET FORK IPL
64 AF 9F 035B 964 PUSHAB B^10$ ; SET RETURN PC
0000000 GF 17 035E 965 PUSHAB B^40$ ; SET FORK PC
0364 966 JMP G^EXE$FORK ; INSERT BLOCK IN FORK QUEUE
52 55 D0 0364 967 40$: MOVL R5,R2 ; SET ADDRESS OF REQUEST BLOCK
5 53 D0 0367 968 MOVL R3,R5 ; SET ADDRESS OF DRIVER FORK BLOCK
53 10 A5 7D 036A 969 MOVQ FKBSL_FR3(R5),R3 ; RESTORE REGISTERS
OC A5 DD 036E 970 PUSHL FKBSL_FPC(R5) ; SET RETURN ADDRESS OF HANDLER CALLER
FEFE 31 0371 971 BRW RETURN_BLOCK ; RETURN TO HANDLER
05 0374 972 RSB ;
0375 973 50$: BSBW DEALLOC_BLOCK ; DEALLOCATE UNEEDED BLOCK
0076 30 0375 974 ;
0378 975 ;
0378 976 ;
0378 977 ; IF THERE IS A RESOURCE NOW AVAILABLE THAT PROCESS(S) ON THIS PROCESSOR
0378 978 ; ARE WAITING FOR, CREATE A FORK PROCESS TO REPORT THE AVAILABILITY TO
0378 979 ; THE SCHEDULER.
0378 980 ;
0378 981 RESOURCE_AVAIL:
42 00E8 C4 52 E1 0378 982 BBC R2,SHD$W_RESSUM(R4),50$ ; IF CLR, NONE TO REPORT
3C 00A4 C4 52 E6 037E 983 BBSSI R2,SHD$W_PRQWAIT(R4),50$ ; ASSUME NO BLOCKS AVAILABLE
0384 984 ; AND IF THERE ALREADY AREN'T, EXIT
0384 985 QRETRY SUCCESS=20$- ; ATTEMPT TO ALLOCATE A BLOCK
0384 986 REMQHI SHD$Q_PRQ(R4),R5 ;
0395 987 BUG_CHECK BADQHDR ; REMQHI FAILED - BAD QUEUE HEADER
00 00A4 C4 25 1D 0399 988 20$: BVS 50$ ; IF V-SET, NO BLOCK AVAILABLE
00 00E8 C4 52 E7 039B 989 BBCCI R2,SHD$W_PRQWAIT(R4),30$ ; CLEAR WAIT FLAG
OB A5 06 90 03A1 990 30$: BBCCI R2,SHD$W_RESSUM(R4),40$ ; CLEAR RESOURCE REPORT SUMMARY
1C A5 00 B0 03A7 991 40$: MOVB #IPL$_QUEUEAST,FKBSB_FIPL(R5) ; SET FORK IPL
20 A5 01 B0 03AB 992 MOVW #PRQ$C_EXEC,PRQ$W_DISPATCH(R5) ; SET EXEC DISPATCHER ID
CO AF 9F 03AF 993 MOVW #PRQ$C_RESAVL,PRQ$W_REQTYPE(R5) ; SET RESOURCE AVAILABLE TYPE
03C6 CF 9F 03B3 994 PUSHAB B^50$ ; SET RETURN PC
0000000 GF 17 03B6 995 PUSHAB W^REQUEST_DISP ; SET FORK PC
5E 08 C0 03BA 996 JMP G^EXE$FORK ; CREATE FORK PROCESS
3F BA 03C0 997 50$: ADDL #8,SP ; REMOVE SAVED R3-R4
02 03C3 998 INT_EXIT: ; EXIT INTERRUPT
03C3 999 POPR #*M<R0,R1,R2,R3,R4,R5> ; RESTORE REGISTERS
03C5 1000 REI ;

```



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03C6 1002      .SBTTL INTER-PROCESSOR REQUEST DISPATCHER
03C6 1003      :++
03C6 1004      :
03C6 1005      : FUNCTIONAL DESCRIPTION:
03C6 1006      :
03C6 1007      : THIS ROUTINE IS CALLED BY THE FORK PROCESS DISPATCHER WHEN
03C6 1008      : IT DISPATCHES A FORK BLOCK THAT IS AN INTER-PROCESSOR REQUEST
03C6 1009      : BLOCK.
03C6 1010      :
03C6 1011      : INPUTS:
03C6 1012      :
03C6 1013      : R0-R2 = SCRATCH.
03C6 1014      : R3 = ADAPTER CONTROL BLOCK ADDRESS.
03C6 1015      : R4 = SHARED MEMORY DATAPAGE ADDRESS.
03C6 1016      : R5 = INTER-PROCESSOR REQUEST BLOCK ADDRESS.
03C6 1017      :
03C6 1018      : DISPATCHING OCCURS AS FOLLOWS:
03C6 1019      :
03C6 1020      : THE REQUEST DISPATCHER ID CODE IS EXAMINED AND IF IT IS
03C6 1021      : AN EXECUTIVE REQUEST (PROSC EXEC) THEN THE EXEC REQUEST HANDLER
03C6 1022      : IS CALLED. IF IT IS NOT THE EXECUTIVE REQUEST ID, THE ID
03C6 1023      : CODE IS USED AS AN INDEX INTO THE DRIVER DISPATCHER VECTOR
03C6 1024      : TABLE TO CALL THE DRIVER INTER-PROCESSOR REQUEST DISPATCHER.
03C6 1025      :
03C6 1026      : WHEN THE CALLED DISPATCHER RETURNS, THE REQUEST BLOCK IS
03C6 1027      : DEALLOCATED TO THE SHARED MEMORY REQUEST QUEUE AND A RETURN
03C6 1028      : TO THE FORK PROCESS DISPATCHER IS EXECUTED.
03C6 1029      :
03C6 1030      :--
03C6 1031      REQUEST_DISP:
51 38 BB 03C6 1032      PUSHR  #^M<R3,R4,R5>          : PROCESSOR REQUEST DISPATCHER
03C8 1033      ASSUME  PROSC_EXEC EQ 0          : SAVE REGISTERS
03C8 1034      MOVZWL PROSW_DISPATCH(R5),R1    : GET DISPATCHER ID
03CC 1035      BNEQ   10$                      : IF NEQ, NOT EXECUTIVE REQUEST
03CE 1036      :
03CE 1037      : CALL APPROPRIATE EXECUTIVE INTER-PROCESSOR REQUEST HANDLER
03CE 1038      :
03CE 1039      EC'AF 9F 03CE 1039      PUSHAB B^15$          : SET RETURN ADDRESS
03D1 1040      CASE   PROSW_REQTYPE(R5),<-    : CALL REQUEST HANDLER
03D1 1041      SETEF,-                          : UPDATE EVENT FLAGS
03D1 1042      RESAVL-                          : REPORT RESOURCE AVAILABLE
03D1 1043      >
03DA 1044      5$:  BUG_CHECK UNKNPRQ          : UNKNOWN REQUEST ID
03DE 1045      BRB   15$
03E0 1046      :
03E0 1047      : CALL CLASS DRIVER INTER-PROCESSOR REQUEST DISPATCHER
03E0 1048      :
03E0 1049      10$:
50 10 A3 D0 03E0 1050      MOVL  ADP$ VECTOR(R3),R0    : GET ADDRESS OF VECTOR TABLE
50 50 6041 D0 03E4 1051      MOVL  (R0)[R1],R0        : GET ADDRESS OF DISPATCHER
03E8 1052      BEQL  15$                          : IF EQL, UNUSED VECTOR
03EA 1053      : (CAN OCCUR AFTER CRASH IF REQUESTS ARE LEFT)
03EA 1054      60 16 03EA 1054      JSB   (R0)          : CALL DRIVER DISPATCHER
03EC 1055      15$:
03EC 1056      38 BA 03EC 1056      POPR  #^M<R3,R4,R5>    : RESTORE REGISTERS
03EE 1057      :
03EE 1058      : DEALLOCATE THE REQUEST BLOCK

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03EE 1059 :  
03EE 1060 DEALLOC_BLOCK: ; DEALLOCATE THE REQUEST BLOCK  
03EE 1061 QRETRY SUCCESS=10$- ; DEALLOCATE REQUEST BLOCK  
03EE 1062 INSQTI (R5),SHD$Q_PRQ(R4) ;  
03FF 1063 BUG_CHECK BADQHDR ; INSQTI FAILED - BAD QUEUE HEADER  
51 00A4 C4 3C 0403 1064 10$: ;  
02 13 0403 1065 MOVZWL SHD$W_PRQWAIT(R4),R1 ; AN PROCESSORS WAITING FOR A BLOCK?  
01 10 0408 1066 BEQL 20$ ; IF EQL, NO  
05 040A 1067 BSBB INTERRUPT_PORTS ; NOTIFY WAITING PORTS  
040C 1068 20$: ;  
RSB ;
```

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040D 1070      .SBTTL INTERRUPT_PORTS - ROUTINE TO INTERRUPT SELECTED PORTS
040D 1071      :++
040D 1072      : FUNCTIONAL DESCRIPTION:
040D 1073      :
040D 1074      : THIS ROUTINE SETS THE INTERRUPT REQUEST BITS FOR THE SELECTED PORTS.
040D 1075      :
040D 1076      : INPUTS:
040D 1077      :
040D 1078      : R1 = MASK OF PORTS TO BE INTERRUPTED.
040D 1079      : R3 = ADAPTER CONTROL BLOCK ADDRESS
040D 1080      :--
040D 1081      : INTERRUPT_PORTS:
50 34 A3 9A 040D 1082      MOVZBL ADPSB_PORT(R3),R0      : GET OUR PORT NUMBER
50 50 04 C4 0411 1083      MULL   #MPMSI_PORTS,R0      : COMPUTE INTERRUPT REQUEST BIT #
50 50 10 C0 0414 1084      ADDL   #MPMSV_IIR_CTL,R0      : ...
50 51 50 78 0417 1085      ASHL   R0,R1,R0      :
20 A1 63 D0 041B 1086      MOVL   ADPSL_CSR(R3),R1      : GET ADAPTER CSR ADDRESS
20 A1 50 D0 041E 1087      MOVL   R0,MPMSI_IIR(R1)      : SET PORTS' INTERRUPT REQUEST BIT(S)
05 0422 1088      RSB
  
```

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0423 1090      .SBTTL UPDATE LOCAL COPY OF EVENT FLAG CLUSTER
0423 1091      :++
0423 1092      :
0423 1093      : FUNCTIONAL DESCRIPTION:
0423 1094      :
0423 1095      : THIS ROUTINE HANDLES THE INTER-PROCESSOR REQUEST TO COPY THE MASTER
0423 1096      : COMMON EVENT FLAGS INTO THE SLAVE COMMON EVENT BLOCK.  SOME PRQS
0423 1097      : MAY BE DELIVERED AFTER THE SLAVE COMMON EVENT BLOCK HAS BEEN
0423 1098      : DELETED.  THIS HAPPENS FREQUENTLY AFTER A PROCESSOR CRASHES
0423 1099      : AND REBOOTS.  THE LOGIC HANDLES THIS BY IGNORING THE PRQ.
0423 1100      :
0423 1101      : INPUTS:
0423 1102      :
0423 1103      : R4 = SHARED MEMORY DATA PAGE ADDRESS
0423 1104      : R5 = INTER-PROCESSOR REQUEST BLOCK ADDRESS
0423 1105      :
0423 1106      : OUTPUTS:
0423 1107      :
0423 1108      : R0 = SSS_NORMAL - SUCCESSFUL RETURN
0423 1109      :--
0423 1110      SETEF:
0423 1111      DSBINT #IPL$_SYNCH ; RAISE TO SYNCH FOR REFCNT CHANGE
53 50 24 A5 3C 0429 1112 MOVZWL PRQ$ _PARAM(R5),R0 ; GET INDEX TO MASTER CEB
54 54 08 A4 C1 042D 1113 ADDL3 SHD$ _CEFPTR(R4),R4,R3 ; GET ADR OF 1ST MASTER CEB
52 52 08 A3 3C 0432 1114 MOVZWL CEB$ _SIZE(R3),R2 ; GET THE SIZE OF ONE MASTER CEB
52 52 50 C4 0436 1115 MULL2 R0,R2 ; GET BYTE OFFSET TO THIS MASTER
53 53 52 C0 0439 1116 ADDL2 R2,R3 ; R3=ADR OF MASTER CEB
51 51 18 A5 3C 043C 1117 MOVZWL PRQ$ _TO_PORT(R5),R1 ; RECEIVER PORT #
51 51 38 A341 D0 0440 1118 MOVL CEB$ _VASLAVE1(R3)[R1],R1 ; R1=ADR OF SLAVE CEB, OR 0
10 A1 10 A3 D0 0445 1119 BEQL 40$ ; BR IF SLAVE NO LONGER EXISTS
56 56 14 A1 9E 0447 1120 MOVL CEB$ _EFC(R3),CEB$ _EFC(R1) ; COPY FLAGS FROM MASTER
56 56 01 9A 044C 1121 PUSHR #*M<R3,R4,R5,R6> ; SAVE REGISTERS
56 56 01 9A 0450 1122 MOVAB CEB$ _WQFL(R1),R6 ; GET HEAD OF WAIT QUEUE FOR CEFC
56 56 01 9A 0454 1123 MOVZBL #PRI$ _IOCOM,R2 ; SET PRIORITY INCREMENT
56 56 66 D0 0457 1124 MOVL (R6),R4 ; GET FIRST PCB IN WAIT QUEUE
56 56 54 D1 045A 1125 20$: CMLP R4,R6 ; IS THIS THE END OF THE QUEUE?
55 55 12 13 045D 1126 BEQL 30$ ; BR IF END OF QUEUE
50 50 FC A6 DE 045F 1127 MOVL (R4),R5 ; REMEMBER NEXT PCB IN QUEUE
00000000 GF 16 0462 1128 MOVAL <CEB$ _EFC-CEB$ _WQFL>(R6),R0 ; POINT TO EVENT FLAG MASK
54 54 55 D0 0466 1129 JSB G^EXE$CHKWAIT2 ; CHECK IF THE PROCESS CAN RUN NOW
54 54 E9 11 046C 1130 MOVL R5,R4 ; GET NEXT PCB IN WAIT QUEUE
0078 8F BA 046F 1131 BRB 20$ ; CONTINUE LOOPING THROUGH ALL OF QUEUE
0078 8F BA 0471 1132 30$: ; NO MORE PCB'S IN WAIT QUEUE
50 50 01 9A 0471 1133 40$: POPR #*M<R3,R4,R5,R6> ; RESTORE REGISTERS
0475 1134 ; NO SLAVE CEB EXISTED, SO NO WAITERS
0475 1135 MOVZBL #SS$ _NORMAL,R0 ; RETURN SUCCESS STATUS
0478 1136 ENBINT ; LOWER FROM SYNCH
05 05 047B 1137 RSB ; RETURN

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047C 1139      .SBTTL REPORT RESOURCE AVAILABILITY TO LOCAL SYSTEM
047C 1140      :++
047C 1141      :
047C 1142      : FUNCTIONAL DESCRIPTION:
047C 1143      :
047C 1144      : THIS ROUTINE HANDLES THE INTERPROCESSOR REQUEST TO REPORT
047C 1145      : THAT A RESOURCE IS AVAILABLE TO THE LOCAL SYSTEM.
047C 1146      :
047C 1147      : INPUTS:
047C 1148      :
047C 1149      : R3 = ADAPTER CONTROL BLOCK ADDRESS.
047C 1150      : R4 = SHARED MEMORY DATAPAGE ADDRESS.
047C 1151      : R5 = INTER-PROCESSOR REQUEST BLOCK ADDRESS.
047C 1152      :
047C 1153      : OUTPUTS:
047C 1154      :
047C 1155      : RESOURCE AVAILABILITY IS REPORTED, THEREBY UNBLOCKING ANY PROCESSES
047C 1156      : THAT ARE WAITING FOR THE RESOURCE.
047C 1157      :--
047C 1158      : RESAVL:
047C 1159      :
52  34  A3  9A  0482 1160      DSBINT  #IPL$ SYNCH          : SYNCHRONIZE DATABASE ACCESS
      50  01  BB  0486 1161      MOVZBL  ADP$B_PORT(R3),R2    : GET OUR PORT NUMBER
51  00C8 C440 3E  0488 1162      PUSHR  #*M<R2,R3>         : SAVE REGISTERS
      10  61  6E  E7  0488 1163      MOVL   #1,R0              : INIT RESOURCE NUMBER
51  00A8 C440 3E  0488 1164 10$: MOVAW  SHD$W_RESAVAIL(R4)[R0],R1 : GET ADDRESS OF AVAILABLE MASK
      06  61  6E  E7  0491 1165      BBCCI  (SP),(R1),20$      : IF CLR, RESOURCE NOT AVAILABLE
      00000000'GF 16  0495 1166      MOVAW  SHD$W_RESWAIT(R4)[R0],R1 : GET ADDRESS OF WAIT MASK
      E2  50  0F  BA  0498 1167      BBCCI  (SP),(R1),20$      : IF CLR, NO PROCESSES WAITING
      0C  05  04  049F 1168      JSB    G^SCH$RAVAIL       : REPORT RESOURCE AVAILABLE
      04A5 1169 20$: AOBLS$ #RSN$ MAX,R0,10$ : INCREMENT RESOURCE NUMBER AND LOOP
      04A9 1170      POPR   #*M<R2,R3>         : RESTORE REGISTERS
      04AB 1171      ENBINT                    : RESTORE IPL
      04AE 1172      RSB
      04AF 1173
      04AF 1175      .END

```

ADPSUB780
Symbol table

- ADAPTER SUBROUTINES FOR VAX 11/780²

16-SEP-1984 00:41:08 VAX/VMS Macro V04-00
5-SEP-1984 04:06:45 [SYSLOA.SRC]ADPSUB.MAR;1

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ADP
V04

ADPSB_PORT	= 00000034			INT_EXIT	000003C3	R	02
ADPSC_MPMADPLEN	= 00000044			IOSGL_UBA_INT0	*****	X	02
ADPSL_AVECTOR	= 0000001C			IPLS_QUEUEAST	= 00000006		
ADPSL_CSR	= 00000000			IPLS_SYNCH	= 00000008		
ADPSL_INTD	= 00000038			MASINITIAL	0000018A	RG	02
ADPSL_LINK	= 00000004			MASINT	000002CE	R	02
ADPSL_PRQOBL	= 00000018			MASRAVAIL	00000293	RG	02
ADPSL_PRQOFL	= 00000014			MASREQUEST	000001F8	RG	02
ADPSL_SHB	= 00000030			MBASINITIAL	000000E4	RG	02
ADPSL_VECTOR	= 00000010			MBASINT	0000006C	RG	02
ADPSW_ADPTYPE	= 0000000E			MBASL_AS	= 00000410		
ADPSW_SIZE	= 00000008			MBASL_CR	= 00000004		
ADPSW_TR	= 0000000C			MBASL_CSR	= 00000000		
ADPSW_UMR_DIS	= 00000256			MBASL_SR	= 00000008		
ADPLINK	*****	X	02	MBASH_CR_IE	= 00000004		
ATS_MPM	= 00000003			MBASH_CR_INIT	= 00000001		
BLOCK_AVAIL	00000322	R	02	MBASH_CSR_PD	= 00800000		
BUGS_BADQHDR	*****	X	02	MMGSGC_SBTCONF	*****	X	02
BUGS_UNKNPRQ	*****	X	02	MPMEC_PORTS	= 00000004		
C780_LIKE	= 00000001			MPMSL_CR	= 00000004		
CEBSC_EFC	= 00000010			MPMSL_CSR	= 00000000		
CEBSL_VASLAVE1	= 00000038			MPMSL_CSR1	= 00000C18		
CEBSL_WQFL	= 00000014			MPMSL_ERR	= 00000010		
CEBSW_SIZE	= 00000008			MPMSL_IIE	= 00000024		
CISINITIAL	0000001F	RG	02	MPMSL_IIR	= 00000020		
CISINT	00000000	RG	02	MPMSL_INV	= 0000000C		
CISSHUTDOWN	0000001F	RG	02	MPMSL_MR	= 0000001C		
CPU_TYPE	= 00000001			MPMSL_SR	= 00000008		
DCR_K_CLRPWRDN	= 00000200			MPMSM_CR_EIE	= 00000002		
DCR_K_CLRPWRUP	= 00000100			MPMSM_CR_ERRS	= FF000000		
DCR_K_RESET	= 00004000			MPMSM_CR_MIE	= 00000001		
DDTSL_UNSOINT	= 00000004			MPMSM_CSR1_MIA	= 00000400		
DEALLOC_BLOCK	000003EE	R	02	MPMSM_CSR_PU	= 00400000		
DEQUEUE_BLOCK	000002FA	R	02	MPMSM_ERR_ELR	= 10000000		
DR\$INITIAL	0000003F	RG	02	MPMSM_ERR_ICRD	= 40000000		
DR\$INT	00000024	RG	02	MPMSM_ERR_IMP	= 80000000		
DR\$SHUTDOWN	0000003F	RG	02	MPMSM_INV_STADR	= 7FF00000		
DR_DCR	00000000			MPMSM_SR_ACA	= 80000000		
DYN\$C_ADP	= 00000001			MPMSM_SR_AGP	= 10000000		
EXESALONONPAGED	*****	X	02	MPMSM_SR_EIE	= 00000002		
EXESCHKWAIT2	*****	X	02	MPMSM_SR_IDL	= 000040C0		
EXESFORK	*****	X	02	MPMSM_SR_IT	= 00008000		
EXESGL_DEFFLAGS	*****	X	02	MPMSM_SR_MXF	= 40000000		
EXESGL_LOCKRTRY	*****	X	02	MPMSM_SR_SS	= 00002000		
EXESGL_SCB	*****	X	02	MPMSM_CSR_PORT	= 00000002		
EXESINT58	*****	X	02	MPMSV_CSR_PORT	= 00000000		
EXESRH780_INT	*****	X	02	MPMSV_IIE_CTL	= 00000010		
EXESV_CRDENABL	*****	X	02	MPMSV_IIR_CTL	= 00000010		
FKBSB_FIPL	= 0000000B			MPMSV_INV_ID	= 0000C000		
FKBSL_FPC	= 0000000C			MPMINTD	00000182	R	02
FKBSL_FR3	= 00000010			NOBLOCKS	00000283	R	02
IDBSL_ADP	= 00000014			NUMPMVEC	= 00000010		
IDBSL_CSR	= 00000000			PA_CNF	= 00000000		
IDBSL_OWNER	= 00000004			PA_CNF_M_PDN	= 00800000		
IDBSL_UCBLST	= 00000018			PA_CNF_M_PUP	= 00400000		
INISMPMADP	000000ED	RG	02	PA_PMC	= 00000004		
INTERRUPT_PORTS	0000040D	R	02	PA_PMC_M_MIN	= 00000001		

ADPSUB780
Symbol table

- ADAPTER SUBROUTINES FOR VAX 11/780²

16-SEP-1984 00:41:08
5-SEP-1984 04:06:45

VAX/VMS Macro V04-00
[SYSLOA.SRC]ADPSUB.MAR;1

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ADP
V04

PRS_IPL	=	00000012		
PRS_SID_TYP730	=	00000003		
PRS_SID_TYP750	=	00000002		
PRS_SID_TYP780	=	00000001		
PRS_SID_TYP790	=	00000004		
PRS_SID_TYPUV1	=	00000007		
PRIS_IOCOM	=	00000001		
PROSC_EXEC	=	00000000		
PROSC_RESAVL	=	00000001		
PROSL_PARAM	=	00000024		
PROSW_DISPATCH	=	0000001C		
PROSW_REQTYPE	=	00000020		
PROSW_TU_PORT	=	00000018		
REQUEST_DISP		000003C6	R	02
REQ_INTERRUPT		0000C26A	R	C2
RESAVL		0000047C	R	02
RESOURCE_AVAIL		00000378	R	02
RETURN_BLOCK		00000232	R	02
RSNS_MAX	=	0000000F		
SCHSRVAVAIL		*****	X	02
SETEF		00000423	R	02
SHBSL_ADP	=	0000001C		
SHBSL_DATAPAGE	=	00000004		
SHDSL_CFPTR	=	00000008		
SHDSQ_PRO	=	000000F0		
SHDSQ_PROWRK	=	00000100		
SHDSW_POLL	=	000000A6		
SHDSW_PRQWAIT	=	000000A4		
SHDSW_RESAVAIL	=	000000C8		
SHDSW_RESSUM	=	000000E8		
SHDSW_RESWAIT	=	000000A8		
SIZ...	=	00000006		
SSS_BADQUEUEHDR	=	00000394		
SSS_NORMAL	=	00000001		
UBAS_INITIAL		00000045	RG	02
UBAS_INT0		00000060	RG	02
UBASL_CR	=	00000004		
UBASL_CSR	=	00000000		
UBASL_SR	=	00000008		
UBASM_CR_BRIE	=	00000020		
UBASM_CR_CNFIG	=	00000004		
UBASM_CR_IFSIE	=	00000040		
UBASM_CR_SUEFIE	=	00000008		
UBASM_CR_USEFIE	=	00000010		
UBASV_CR_MRDSB	=	0000001A		
UBA_UREXINT		00000068	RG	02
UCBSB_SLAVE	=	00000090		
UCBSL_DDT	=	00000088		
UCBSL_FPC	=	0000000C		
UCBSL_FR3	=	00000010		
UCBSV_INT	=	0000C001		
UCBSW_STS	=	00000064		

! Psect synopsis !

PSECT name	Allocation	PSECT No.	Attributes
. ABS .	00000000 (0.)	00 (0.)	NOPIC USR CON ABS LCL NOSHR NOEXE NORD NOWRT NOVEC BYTE
\$AB\$\$	00000008 (8.)	01 (1.)	NOPIC USR CON ABS LCL NOSHR EXE RD WRT NOVEC BYTE
SYSLOA	000004AF (1199.)	02 (2.)	NOPIC USR CON REL LCL NOSHR EXE RD WRT NOVEC LONG

! Performance indicators !

Phase	Page faults	CPU Time	Elapsed Time
Initialization	30	00:00:00.03	00:00:01.95
Command processing	106	00:00:00.44	00:00:04.39
Pass 1	554	00:00:14.85	00:00:56.01
Symbol table sort	0	00:00:02.25	00:00:07.96
Pass 2	196	00:00:03.19	00:00:12.12
Symbol table output	20	00:00:00.13	00:00:01.04
Psect synopsis output	2	00:00:00.01	00:00:00.01
Cross-reference output	0	00:00:00.00	00:00:00.00
Assembler run totals	910	00:00:20.90	00:01:23.49

The working set limit was 1800 pages.
138898 bytes (272 pages) of virtual memory were used to buffer the intermediate code.
There were 120 pages of symbol table space allocated to hold 2165 non-local and 52 local symbols.
1179 source lines were read in Pass 1, producing 17 object records in Pass 2.
44 pages of virtual memory were used to define 43 macros.

! Macro library statistics !

Macro library name	Macros defined
\$_\$255\$DUA28:[SYS.OBJ]LIB.MLB;1	30
\$_\$255\$DUA28:[SYSLIB]STARLET.MLB;2	8
TOTALS (all libraries)	38

2268 GETS were required to define 38 macros.

There were no errors, warnings or information messages.

MACRO/LIS=LIS\$:ADPSUB780/OBJ=OBJ\$:ADPSUB780 MSRC\$:CPUSW780/UPDATE=(ENH\$:CPUSW780)+MSRC\$:ADPSUB/UPDATE=(ENH\$:ADPSUB)+EXECMLS/LIB

