


```
MM      MM      CCCCCCCC DDDDDDDD EEEEEEEEE EEEEEEEEE FFFFFFFF
MM      MM      CCCCCCCC DDDDDDDD EEEEEEEEE EEEEEEEEE FFFFFFFF
MMMM    MMMM    CC        DD        DD        EE        FF
MMMM    MMMM    CC        DD        DD        EE        FF
MM      MM      CC        DD        DD        EE        FF
MM      MM      CC        DD        DD        EE        FF
MM      MM      CC        DD        DD        EEEEEEEE FFFFFFFF
MM      MM      CC        DD        DD        EEEEEEEE FFFFFFFF
MM      MM      CC        DD        DD        EE        FF
MM      MM      CC        DD        DD        EE        FF
MM      MM      CC        DD        DD        EE        FF
MM      MM      CC        DD        DD        EE        FF
MM      MM      CCCCCCCC DDDDDDDD EEEEEEEEE EEEEEEEEE FF
MM      MM      CCCCCCCC DDDDDDDD EEEEEEEEE EEEEEEEEE FF
```

```
MM      MM      DDDDDDDD LL
MM      MM      DDDDDDDD LL
MMMM    MMMM    DD        DD LL
MMMM    MMMM    DD        DD LL
MM      MM      DD        DD LL
MM      MM      DD        DD LL
MM      MM      DD        DD LL
MM      MM      DD        DD LL
MM      MM      DD        DD LL
MM      MM      DD        DD LL
MM      MM      DD        DD LL
MM      MM      DDDDDDDD LLLLLLLLLL
MM      MM      DDDDDDDD LLLLLLLLLL
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Version: 'V04-000'

```

*****
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```

Define Machine check stack parameters.

```

$STRUCT MC
  F      BYTCNT,L
  F      TYPECODE,L
  F      P1,L
  F      P2,L
  F      PC,L
  F      PSL,L
  E

```

Define memory controller CSRs.

```

$STRUCT MEM
  F      CSRO,L
  F      CSR1,L
  F      CSR2,L
  E

```

```

$STRUCT CSRO
  V<M
  ECCSYND,7      ; ECC error syndrome.
  2
  ERRPFN,15      ; PFN of error.
  >
  E

```

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```
$STRUCT CSR1
V<M
ECCMAINT,7      ; ECC maintenance check bits.
,18             ;
ECCDISAB,      ; Disable ECC.
DIAG,          ; Diagnostic mode.
MME,          ; Memory mapping enable.
CRDENAB,      ; Enable CRD interrupts.
TBPAR,        ; Force translation buffer parity.
CRD,          ; Corrected Read Data error.
,1
>
E
```

```
$STRUCT CSR2
V<M
MEMMAP,16      ; One bit for each 128K/512K (depending on
,8             ; chip size).
CHIPSIZE,1    ; 0 => 128 Kbytes/bit, 1 => 512 Kbytes/bit.
,7
>
E
```

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/*
CL

en
/*
CL

en
/*
CL

en
/*
CL

