

A large, stylized letter 'A' is formed using the characters 'S' and 'Y'. The letter is symmetrical and composed of multiple rows of these characters. The top of the 'A' is formed by a row of 'S's, followed by rows of 'Y's and 'S's. The middle section consists of a row of 'Y's, followed by rows of 'S's and 'Y's. The bottom of the 'A' is formed by a row of 'S's, followed by rows of 'Y's and 'S's. The overall shape is a wide, triangular 'A' with a central vertical stem.

SCI
VO4

```

SSSSSSSS  CCCCCCCC  BBBB BBBB  VV      VV  EEEEEEEEEEE  CCCCCCCC  TTTTTTTTTT  000000  RRRRRRRR
SSSSSSSS  CCCCCCCC  BBBB BBBB  VV      VV  EEEEEEEEEEE  CCCCCCCC  TTTTTTTTTT  000000  RRRRRRRR
SS      CC  BB      BB  VV      VV  EE      CC  TT      00      00  RR      RR
SS      CC  BB      BB  VV      VV  EE      CC  TT      00      00  RR      RR
SS      CC  BB      BB  VV      VV  EE      CC  TT      00      00  RR      RR
SS      CC  BB      BB  VV      VV  EE      CC  TT      00      00  RR      RR
SSSSSSS  CC  BBBB BBBB  VV      VV  EEEEEEEEE  CC  TT      00      00  RRRRRRRR
SSSSSSS  CC  BBBB BBBB  VV      VV  EEEEEEEEE  CC  TT      00      00  RRRRRRRR
SS      CC  BB      BB  VV      VV  EE      CC  TT      00      00  RR      RR
SS      CC  BB      BB  VV      VV  EE      CC  TT      00      00  RR      RR
SS      CC  BB      BB  VV      VV  EE      CC  TT      00      00  RR      RR
SS      CC  BB      BB  VV      VV  EE      CC  TT      00      00  RR      RR
SSSSSSSS  CCCCCCCC  BBBB BBBB  VV      VV  EEEEEEEEEEE  CCCCCCCC  TT      000000  RR      RR
SSSSSSSS  CCCCCCCC  BBBB BBBB  VV      VV  EEEEEEEEEEE  CCCCCCCC  TT      000000  RR      RR

```

```

LL          IIIIII          SSSSSSSS
LL          IIIIII          SSSSSSSS
LL          II             SS
LL          II             SS
LL          II             SS
LL          II             SS
LL          II             SSSSSS
LL          II             SSSSSS
LL          II             SS
LL          II             SS
LL          II             SS
LL          II             SS
LLLLLLLLLLLL IIIIII          SSSSSSSS
LLLLLLLLLLLL IIIIII          SSSSSSSS

```


(1)	55	SCB TEMPLATE
-----	----	--------------

SCI
Syn

BU
DY
IP
IP
MPI
MPI
PC
PC
PC
PC
PC
PC
PR
PR
QE
SCI
SCI
SCI
SCI
SCI
SCI
SCI
SCI
SCI
SCI

PS

SA
AE

Ph

In
Co
Pa
Sy
Pa
Sy
Ps
Cr
As

Th
26
Th
12
15


```
0000 1 .TITLE SCBVECTOR
0000 2 .IDENT 'V04-000'
0000 3
0000 4 *****
0000 5
0000 6 *
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0000 24 *
0000 25 *****
0000 26
0000 27 ++
0000 28 -- VMS SCB
0000 29
0000 30
0000 31 VERSION X01 (BASE LEVEL 0)
0000 32
0000 33 AUTHOR: R. HEINEN 9-AUG-76 (X01)
0000 34
0000 35 MODIFIED BY:
0000 36
0000 37 V02-010 LMP0009 L. Mark Pilant 5-Feb-1982 18:25
0000 38 Add support for IPL 12 interrupts.
0000 39
0000 40 V07 NPKCOMET N. KRONENBERG 19-FEB-1979
0000 41 REMOVED GLOBAL LABELS FOR SPECIFIC VECTORS;
0000 42 REMOVED MASTERWAKE TO INIT AND UNEXP TO ERRORLOG.
0000 43 CHANGED UNUSED VECTOR NAMES FROM LOCAL TO GLOBAL.
0000 44
0000 45 V08 NPKCOMET N. KRONENBERG 12-APR-1979
0000 46 GAVE CPU-INDEPENDENT NAMES TO VECTORS
0000 47 ^X54,58,5C,60.
0000 48
0000 49 V09 STJ0130 STEVEN T. JEFFREYS 27-OCT-1981
0000 50 PLUGGED THE FORK INTERRUPT SERVICE ROUTINE VECTORS
0000 51 WITH THE ADDRESS OF THE NEW ENTRY POINTS. SEE THE
0000 52 MODULE "FORKCNTRL" FOR DETAILS.
0000 53
```


SCB TEMPLATE

```
0000 55 .SBTTL SCB TEMPLATE
0000 56 :++
0000 57 : SCB TEMPLATE COPIED INTO PHYSICALLY CONTIGUOUS PAGES
0000 58 : ALLOCATED AND MAPPED BY SYSBOOT.
0000 59 :--
0000 60 $IPLDEF ; DEFINE IPL LEVELS
0000 61 $PRDEF ; DEFINE PROCESSOR REGISTERS
0000 62
0000 63 :
0000 64 : LOCAL MACROS
0000 65 :
0000 66 : MACRO DEFVEC DEFINES AN UNUSED VECTOR.
0000 67 :
0000 68
0000 69 .MACRO DEFVEC,VNUM
0000 70 .LONG ERL$VEC,VNUM+INTSTK
0000 71 .ENDM DEFVEC
0000 72
00000000 73 .PSECT $$$00SCB, LONG ;
00000001 0000 74 INTSTK=1 ; DEFINITION FOR INTERRUPT STACK
00000001 0000 75 SCB$AL_BASE:: ; SCB BASE ADDRESS
00000001 0000 76 .LONG ERL$VECO+INTSTK ; UNUSED, RESERVED TO DIGITAL
00000001 0004 77 .LONG EXE$MCHK+INTSTK ; MACHINE CHECK HANDLER. ^X004
00000001 0008 78 .LONG EXE$KERSTKNV+INTSTK ; KERNEL STACK NOT VALID HALT. ^X008
00000001 000C 79 .LONG EXE$POWERFAIL+INTSTK ; POWER FAIL INTERUPT. ^X00C
00000000 0010 80 .LONG EXE$OPCDEC ; RESERVED/PRIVILEGED INSTRUCTION FAULT. ^X010
00000000 0014 81 .LONG EXE$OPCCUS ; CUSTOMER RESERVED INSTRUCTION FAULT. ^X014
00000000 0018 82 .LONG EXE$ROPRAND ; RESERVED OPERAND FAULT/HALT. ^X018
00000000 001C 83 .LONG EXE$RADRMOD ; RESERVED ADDRESSING MODE FAULT. ^X01C
00000000 0020 84 .LONG EXE$ACVIOLAT ; ACCESS CONTROL VIOLATION FAULT. ^X020
00000000 0024 85 .LONG MMG$PAGEFAULT ; TRANSLATION NOT VALID FAULT. ^X024
00000000 0028 86 .LONG EXE$TBIT ; TBIT FAULT. ^X028
00000000 002C 87 .LONG EXE$BREAK ; BREAKPOINT FAULT. ^X02C
00000000 0030 88 .LONG EXE$COMPAT ; COMPATIBILITY FAULT. ^X030
00000000 0034 89 .LONG EXE$ARITH ; ARITHMETIC TRAP. ^X034
00000000 0038 90 .LONG ERL$VEC56 ; UNUSED ^X038
00000000 003C 91 .LONG ERL$VEC60 ; UNUSED ^X03C
00000000 0040 92 .LONG EXE$CMODKRNL ; CHMK TRAP. ^X040
00000000 0044 93 .LONG EXE$CMODEXEC ; CHME TRAP. ^X044
00000000 0048 94 .LONG EXE$CMODSUPR ; CHMS TRAP. ^X048
00000000 004C 95 .LONG EXE$CMODUSER ; CHMU TRAP. ^X04C
00000000 0050 96 .LONG ERL$VEC80 ; UNUSED. ^X050
00000001 0054 97 .LONG EXE$INT54+INTSTK ; CPU-DEPENDENT FAULT ^X054
00000001 0058 98 .LONG EXE$INT58+INTSTK ; CPU-DEPENDENT FAULT ^X058
00000001 005C 99 .LONG EXE$INT5C+INTSTK ; CPU-DEPENDENT FAULT ^X05C
00000001 0060 100 .LONG EXE$INT60+INTSTK ; CPU-DEPENDENT FAULT ^X060
00000000 0064 101 .LONG ERL$VEC100 ; UNUSED.
00000000 0068 102 .LONG ERL$VEC104 ; UNUSED.
00000000 006C 103 .LONG ERL$VEC108 ; UNUSED.
00000000 0070 104 .LONG ERL$VEC112 ; UNUSED.
00000000 0074 105 .LONG ERL$VEC116 ; UNUSED.
00000000 0078 106 .LONG ERL$VEC120 ; UNUSED.
00000000 007C 107 .LONG ERL$VEC124 ; UNUSED.
00000000 0080 108 .LONG ERL$VEC128 ; UNUSED.
00000001 0084 109 .LONG ERL$VEC132+INTSTK ; SOFTWARE LEVEL 1 INTERUPT.
00000000 0088 110 .LONG SCH$ASTDEL ; SOFTWARE LEVEL 2 INTERUPT. ^X088
00000000 008C 111 .LONG SCH$RESCHED ; SOFTWARE LEVEL 3 INTERUPT. ^X08C
```


SCB TEMPLATE

```
00000001: 0090 112 .LONG IOC$IOPST+INTSTK; SOFTWARE LEVEL 4 INTERRUPT. ^X090
00000001: 0094 113 .LONG INIS$MASTERWAKE+INTSTK; SOFTWARE LEVEL 5 INTERRUPT. ^X094
00000001: 0098 114 .LONG EXE$FRKIPL6DSP+INTSTK; SOFTWARE LEVEL 6 INTERRUPT. ^X098
00000001: 009C 115 .LONG EXE$SWTIMINT+INTSTK; SOFTWARE LEVEL 7 INTERRUPT. ^X09C
00000001: 00A0 116 .LONG EXE$FRKIPL8DSP+INTSTK; SOFTWARE LEVEL 8 INTERRUPT. ^X0A0
00000001: 00A4 117 .LONG EXE$FRKIPL9DSP+INTSTK; SOFTWARE LEVEL 9 INTERRUPT. ^X0A4
00000001: 00A8 118 .LONG EXE$FRKIPL10DSP+INTSTK; SOFTWARE LEVEL 10 INTERRUPT. ^X0A8
00000001: 00AC 119 .LONG EXE$FRKIPL11DSP+INTSTK; SOFTWARE LEVEL 11 INTERRUPT. ^X0AC
00000001: 00B0 120 .LONG EXE$I$PCONTROL+INTSTK ; SOFTWARE LEVEL 12 INTERRUPT. ^X0B0
00000001: 00B4 121 .LONG ERL$VEC180+INTSTK ; SOFTWARE LEVEL 13 INTERRUPT. ^X0B4
00000001: 00B8 122 .LONG ERL$VEC184+INTSTK ; SOFTWARE LEVEL 14 INTERRUPT. ^X0B8
00000001: 00BC 123 .LONG ERL$VEC188+INTSTK ; SOFTWARE LEVEL 15 INTERRUPT. (^XBC)
00000001: 00C0 124 .LONG EXE$HWCLKINT+INTSTK; INTERVAL TIMER (^XC0)
00000001: 00C4 125 .LONG ERL$VEC196+INTSTK ; UNUSED. (^XC4)
00000001: 00C8 126 .LONG ERL$VEC200+INTSTK ; UNUSED. (^XC8)
00000001: 00CC 127 .LONG ERL$VEC204+INTSTK ; UNUSED. (^XCC)
00000001: 00D0 128 .LONG ERL$VEC208+INTSTK ; UNUSED. (^XD0)
00000001: 00D4 129 .LONG ERL$VEC212+INTSTK ; UNUSED. (^XD4)
00000001: 00D8 130 .LONG ERL$VEC216+INTSTK ; UNUSED. (^XD8)
00000001: 00DC 131 .LONG ERL$VEC220+INTSTK ; UNUSED. (^XDC)
00000001: 00E0 132 .LONG ERL$VEC224+INTSTK ; UNUSED. (^XE0)
00000001: 00E4 133 .LONG ERL$VEC228+INTSTK ; UNUSED. (^XE4)
00000001: 00E8 134 .LONG ERL$VEC232+INTSTK ; UNUSED. (^XE8)
00000001: 00EC 135 .LONG ERL$VEC236+INTSTK ; UNUSED. (^XEC)
00000001: 00F0 136 .LONG ERL$VEC240+INTSTK ; UNUSED. (^XF0)
00000001: 00F4 137 .LONG ERL$VEC244+INTSTK ; UNUSED. (^XF4)
00000001: 00F8 138 .LONG CON$INTDISI+INTSTK; CONSOLE INTERRUPT FOR INPUT (^XF8)
00000001: 00FC 139 .LONG CON$INTDISO+INTSTK; CONSOLE INTERRUPT FOR OUTPUT (^XFC)
0100 140
00000100 0100 141 VNUM=256 ; VECTORS ^X100-^X1FC FOR NEXUS DEVICES
0100 142 ; NEXUS VECTORS, IPL ^X14
0100 143 .REPT 16
0100 144 DEFVEC \VNUM
0100 145 VNUM=VNUM+4
0100 146 .ENDR
0140 147
0140 148 .REPT 16 ; NEXUS VECTORS, IPL ^X15
0140 149 DEFVEC \VNUM
0140 150 VNUM=VNUM+4
0140 151 .ENDR
0180 152
0180 153 .REPT 16 ; NEXUS VECTORS, IPL ^X16
0180 154 DEFVEC \VNUM
0180 155 VNUM=VNUM+4
0180 156 .ENDR
01C0 157
01C0 158 .REPT 16 ; NEXUS VECTORS, IPL ^X17
01C0 159 DEFVEC \VNUM
01C0 160 VNUM=VNUM+4
01C0 161 .ENDR
0200 162
0200 163 .END
```


SCBVECTOR
Symbol table

J 4

16-SEP-1984 01:07:37
5-SEP-1984 03:47:15VAX/VMS Macro V04-00
[SYS.SRC]SCBVECTOR.MAR;1Page 4
(1)

CONSINTDISI	*****	X	02	ERLSVEC372	*****	X	02	EXESKERSTKNV	*****	X	02
CONSINTDISO	*****	X	02	ERLSVEC376	*****	X	02	EXESMCHK	*****	X	02
ERLSVECO	*****	X	02	ERLSVEC380	*****	X	02	EXESOPCCUS	*****	X	02
ERLSVEC100	*****	X	02	ERLSVEC384	*****	X	02	EXESOPCDEC	*****	X	02
ERLSVEC104	*****	X	02	ERLSVEC388	*****	X	02	EXESPOWERFAIL	*****	X	02
ERLSVEC108	*****	X	02	ERLSVEC392	*****	X	02	EXESRADRMOD	*****	X	02
ERLSVEC112	*****	X	02	ERLSVEC396	*****	X	02	EXESROPRAND	*****	X	02
ERLSVEC116	*****	X	02	ERLSVEC400	*****	X	02	EXESSWTIMINT	*****	X	02
ERLSVEC120	*****	X	02	ERLSVEC404	*****	X	02	EXESTBIT	*****	X	02
ERLSVEC124	*****	X	02	ERLSVEC408	*****	X	02	INISMASTERWAKE	*****	X	02
ERLSVEC128	*****	X	02	ERLSVEC412	*****	X	02	INTSTK	= 00000001		
ERLSVEC132	*****	X	02	ERLSVEC416	*****	X	02	IOC\$IOPOST	*****	X	02
ERLSVEC180	*****	X	02	ERLSVEC420	*****	X	02	MMG\$PAGEFAULT	*****	X	02
ERLSVEC184	*****	X	02	ERLSVEC424	*****	X	02	OPS_ACBD	= 0000006F		
ERLSVEC188	*****	X	02	ERLSVEC428	*****	X	02	OPS_ACBF	= 0000004F		
ERLSVEC196	*****	X	02	ERLSVEC432	*****	X	02	OPS_ACBG	= 00004FFD		
ERLSVEC200	*****	X	02	ERLSVEC436	*****	X	02	OPS_ACBH	= 00006FFD		
ERLSVEC204	*****	X	02	ERLSVEC440	*****	X	02	OPS_ADDD2	= 00000060		
ERLSVEC208	*****	X	02	ERLSVEC444	*****	X	02	OPS_ADDD3	= 00000061		
ERLSVEC212	*****	X	02	ERLSVEC448	*****	X	02	OPS_ADDF2	= 00000040		
ERLSVEC216	*****	X	02	ERLSVEC452	*****	X	02	OPS_ADDF3	= 00000041		
ERLSVEC220	*****	X	02	ERLSVEC456	*****	X	02	OPS_ADDG2	= 000040FD		
ERLSVEC224	*****	X	02	ERLSVEC460	*****	X	02	OPS_ADDG3	= 000041FD		
ERLSVEC228	*****	X	02	ERLSVEC464	*****	X	02	OPS_ADDH2	= 000060FD		
ERLSVEC232	*****	X	02	ERLSVEC468	*****	X	02	OPS_ADDH3	= 000061FD		
ERLSVEC236	*****	X	02	ERLSVEC472	*****	X	02	OPS_ADDP4	= 00000020		
ERLSVEC240	*****	X	02	ERLSVEC476	*****	X	02	OPS_ADDP6	= 00000021		
ERLSVEC244	*****	X	02	ERLSVEC480	*****	X	02	OPS_ASHP	= 000000F8		
ERLSVEC256	*****	X	02	ERLSVEC484	*****	X	02	OPS_CLRD	= 0000007C		
ERLSVEC260	*****	X	02	ERLSVEC488	*****	X	02	OPS_CLRF	= 000000D4		
ERLSVEC264	*****	X	02	ERLSVEC492	*****	X	02	OPS_CLRG	= 0000007C		
ERLSVEC268	*****	X	02	ERLSVEC496	*****	X	02	OPS_CLRH	= 00007CFD		
ERLSVEC272	*****	X	02	ERLSVEC500	*****	X	02	OPS_CMPD	= 00000071		
ERLSVEC276	*****	X	02	ERLSVEC504	*****	X	02	OPS_CMPF	= 00000051		
ERLSVEC280	*****	X	02	ERLSVEC508	*****	X	02	OPS_CMPG	= 000051FD		
ERLSVEC284	*****	X	02	ERLSVEC56	*****	X	02	OPS_CMPH	= 000071FD		
ERLSVEC288	*****	X	02	ERLSVEC60	*****	X	02	OPS_CMPP3	= 00000035		
ERLSVEC292	*****	X	02	ERLSVEC80	*****	X	02	OPS_CMPP4	= 00000037		
ERLSVEC296	*****	X	02	EXESACVIOLAT	*****	X	02	OPS_CRC	= 0000000B		
ERLSVEC300	*****	X	02	EXESARITH	*****	X	02	OPS_CVTBD	= 0000006C		
ERLSVEC304	*****	X	02	EXESBREAK	*****	X	02	OPS_CVTBF	= 0000004C		
ERLSVEC308	*****	X	02	EXESCMODEXEC	*****	X	02	OPS_CVTBG	= 00004CFD		
ERLSVEC312	*****	X	02	EXESCMODKRN	*****	X	02	OPS_CVTBH	= 00006CFD		
ERLSVEC316	*****	X	02	EXESCMODSUPR	*****	X	02	OPS_CVTDB	= 00000068		
ERLSVEC320	*****	X	02	EXESCMODUSER	*****	X	02	OPS_CVTDF	= 00000076		
ERLSVEC324	*****	X	02	EXESCOMPAT	*****	X	02	OPS_CVTDH	= 000032FD		
ERLSVEC328	*****	X	02	EXESFRKIPL10DSP	*****	X	02	OPS_CVTDL	= 0000006A		
ERLSVEC332	*****	X	02	EXESFRKIPL11DSP	*****	X	02	OPS_CVTDW	= 00000069		
ERLSVEC336	*****	X	02	EXESFRKIPL6DSP	*****	X	02	OPS_CVTFB	= 00000048		
ERLSVEC340	*****	X	02	EXESFRKIPL8DSP	*****	X	02	OPS_CVTFD	= 00000056		
ERLSVEC344	*****	X	02	EXESFRKIPL9DSP	*****	X	02	OPS_CVTFG	= 000099FD		
ERLSVEC348	*****	X	02	EXESHWCLKINT	*****	X	02	OPS_CVTFH	= 000098FD		
ERLSVEC352	*****	X	02	EXESINT54	*****	X	02	OPS_CVTFL	= 0000004A		
ERLSVEC356	*****	X	02	EXESINT58	*****	X	02	OPS_CVTFW	= 00000049		
ERLSVEC360	*****	X	02	EXESINT5C	*****	X	02	OPS_CVTGB	= 000048FD		
ERLSVEC364	*****	X	02	EXESINT60	*****	X	02	OPS_CVTGF	= 000033FD		
ERLSVEC368	*****	X	02	EXESIPCONTROL	*****	X	02	OPS_CVTGH	= 000056FD		

SCBVECTOR
Symbol table

K 4

16-SEP-1984 01:07:37 VAX/VMS Macro V04-00
5-SEP-1984 03:47:15 [SYS.SRC]SCBVECTOR.MAR;1

Page 5
(1)

OP\$ _CVTGL = 00004AFD
OP\$ _CVTGW = 000049FD
OP\$ _CVTHB = 000068FD
OP\$ _CVTHD = 0000F7FD
OP\$ _CVTHF = 0000F6FD
OP\$ _CVTHG = 000076FD
OP\$ _CVTHL = 00006AFD
OP\$ _CVTHW = 000069FD
OP\$ _CVTLD = 0000006E
OP\$ _CVTLF = 0000004E
OP\$ _CVTLG = 00004EFD
OP\$ _CVTLH = 00006EFD
OP\$ _CVTLP = 000000F9
OP\$ _CVTPL = 00000036
OP\$ _CVTPS = 00000008
OP\$ _CVTPT = 00000024
OP\$ _CVTRDL = 0000006B
OP\$ _CVTRFL = 0000004B
OP\$ _CVTRGL = 00004BFD
OP\$ _CVTRHL = 00006BFD
OP\$ _CVTSP = 00000009
OP\$ _CVTTP = 00000026
OP\$ _CVTWD = 0000006D
OP\$ _CVTWF = 0000004D
OP\$ _CVTWG = 00004DFD
OP\$ _CVTWH = 00006DFD
OP\$ _DIVD2 = 00000066
OP\$ _DIVD3 = 00000067
OP\$ _DIVF2 = 00000046
OP\$ _DIVF3 = 00000047
OP\$ _DIVG2 = 000046FD
OP\$ _DIVG3 = 000047FD
OP\$ _DIVH2 = 000066FD
OP\$ _DIVH3 = 000067FD
OP\$ _DIVP = 00000027
OP\$ _EDITPC = 00000038
OP\$ _EMODD = 00000074
OP\$ _EMODF = 00000054
OP\$ _EMODG = 000054FD
OP\$ _EMODH = 000074FD
OP\$ _MATCHC = 00000039
OP\$ _MNEGD = 00000072
OP\$ _MNEGF = 00000052
OP\$ _MNEGG = 000052FD
OP\$ _MNEGH = 000072FD
OP\$ _MOVD = 00000070
OP\$ _MOVF = 00000050
OP\$ _MOVG = 000050FD
OP\$ _MOVH = 000070FD
OP\$ _MOV P = 00000034
OP\$ _MOVTC = 0000002E
OP\$ _MOVTUC = 0000002F
OP\$ _MULD2 = 00000064
OP\$ _MULD3 = 00000065
OP\$ _MULF2 = 00000044
OP\$ _MULF3 = 00000045
OP\$ _MULG2 = 000044FD

OP\$ _MULG3 = 000045FD
OP\$ _MULH2 = 000064FD
OP\$ _MULH3 = 000065FD
OP\$ _MULP = 00000025
OP\$ _POLYD = 00000075
OP\$ _POLYF = 00000055
OP\$ _POLYG = 000055FD
OP\$ _POLYH = 000075FD
OP\$ _SCANC = 0000002A
OP\$ _SKPC = 0000003B
OP\$ _SPANC = 0000002B
OP\$ _SUBD2 = 00000062
OP\$ _SUBD3 = 00000063
OP\$ _SUBF2 = 00000042
OP\$ _SUBF3 = 00000043
OP\$ _SUBG2 = 000042FD
OP\$ _SUBG3 = 000043FD
OP\$ _SUBH2 = 000062FD
OP\$ _SUBH3 = 000063FD
OP\$ _SUBP4 = 00000022
OP\$ _SUBP6 = 00000023
OP\$ _TSTD = 00000073
OP\$ _TSTF = 00000053
OP\$ _TSTG = 000053FD
OP\$ _TSTH = 000073FD
SCB\$AL BASE 00000000
SCH\$ASTDEL *****
SCH\$RESCHED *****
VNUM = 00000200

RG 02
X 02
X 02

+-----+
! Psect synopsis !
+-----+

PSECT name	Allocation	PSECT No.	Attributes
ABS	00000000 (0.)	00 (0.)	NOPIC USR CON ABS LCL NOSHR NOEXE NORD NOWRT NOVEC BYTE
\$AB\$\$	00000000 (0.)	01 (1.)	NOPIC USR CON ABS LCL NOSHR EXE RD WRT NOVEC BYTE
\$\$\$00SCB	00000200 (512.)	02 (2.)	NOPIC USR CON REL LCL NOSHR EXE RD WRT NOVEC LONG

+-----+
! Performance indicators !
+-----+

Phase	Page faults	CPU Time	Elapsed Time
Initialization	29	00:00:00.03	00:00:01.90
Command processing	121	00:00:00.60	00:00:05.67
Pass 1	375	00:00:10.23	00:00:36.96
Symbol table sort	0	00:00:00.72	00:00:01.92
Pass 2	51	00:00:03.24	00:00:13.04
Symbol table output	22	00:00:00.15	00:00:00.15
Psect synopsis output	2	00:00:00.03	00:00:00.02
Cross-reference output	0	00:00:00.00	00:00:00.00
Assembler run totals	602	00:00:15.00	00:00:59.68

The working set limit was 1650 pages.
45545 bytes (89 pages) of virtual memory were used to buffer the intermediate code.
There were 40 pages of symbol table space allocated to hold 584 non-local and 0 local symbols.
2915 source lines were read in Pass 1, producing 21 object records in Pass 2.
137 pages of virtual memory were used to define 136 macros.

+-----+
! Macro library statistics !
+-----+

Macro library name	Macros defined
_\$255\$DUA28:[SYS.OBJ]LIB.MLB;1	1
_\$255\$DUA28:[SYSLIB]STARLET.MLB;2	5
TOTALS (all libraries)	6

520 GETS were required to define 6 macros.

There were no errors, warnings or information messages.

MACRO/LIS=LISS\$:SCBVECTOR/OBJ=OBJ\$:SCBVECTOR MASD\$:[EMULAT.SRC]MISSING/UPDATE=(MASD\$:[EMULAT.ENH]MISSING)+MASD\$:[SYS.SRC]SCBVECTOR/UP

0380 AH-BT13A-SE
VAX/VMS V4.0

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