```
SSSSSSSSSSSS  YYY        YYY   SSSSSSSSSSSS
SSSSSSSSSSSS  YYY        YYY   SSSSSSSSSSSS
SSSSSSSSSSSS  YYY        YYY   SSSSSSSSSSSS
SSS           YYY        YYY   SSS
SSS           YYY        YYY   SSS
SSS           YYY        YYY   SSS
SSS              YYY  YYY      SSS
SSS              YYY  YYY      SSS
SSS              YYY  YYY      SSS
   SSSSSSSSS        YYY           SSSSSSSSS
   SSSSSSSSS        YYY           SSSSSSSSS
   SSSSSSSSS        YYY           SSSSSSSSS
         SSS        YYY                 SSS
         SSS        YYY                 SSS
         SSS        YYY                 SSS
         SSS        YYY                 SSS
         SSS        YYY                 SSS
         SSS        YYY                 SSS
SSSSSSSSSSSS        YYY        SSSSSSSSSSSS
SSSSSSSSSSSS        YYY        SSSSSSSSSSSS
SSSSSSSSSSSS        YYY        SSSSSSSSSSSS
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_S

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```
SSSSSSSS    CCCCCCCC  BBBBBBBB   VV      VV  EEEEEEEEEE   CCCCCCC  TTTTTTTTTT   000000   RRRRRRRR
SSSSSSSS    CCCCCCCC  BBBBBBBB   VV      VV  EEEEEEEEEE   CCCCCCC  TTTTTTTTTT   000000   RRRRRRRR
SS            CC      BB      BB  VV      VV  EE          CC           TT       00    00  RR      RR
SS            CC      BB      BB  VV      VV  EE          CC           TT       00    00  RR      RR
SS            CC      BB      BB  VV      VV  EE          CC           TT       00    00  RR      RR
SS            CC      BB      BB  VV      VV  EE          CC           TT       00    00  RR      RR
SSSSSS        CC      BBBBBBBB   VV      VV  EEEEEEE      CC           TT       00    00  RRRRRRRR
SSSSSS        CC      BBBBBBBB   VV      VV  EEEEEEE      CC           TT       00    00  RRRRRRRR
    SS        CC      BB      BB  VV      VV  EE          CC           TT       00    00  RR  RR
    SS        CC      BB      BB  VV      VV  EE          CC           TT       00    00  RR  RR
    SS        CC      BB      BB   VV    VV   EE          CC           TT       00    00  RR      RR     ....
    SS        CC      BB      BB   VV    VV   EE          CC           TT       00    00  RR      RR     ....
SSSSSSSS    CCCCCCC   BBBBBBBB       VV       EEEEEEEEEE   CCCCCCC     TT        000000   RR      RR     ....
SSSSSSSS    CCCCCCC   BBBBBBBB       VV       EEEEEEEEEE   CCCCCCC     TT        000000   RR      RR     ....
```

```
LL           IIIIII     SSSSSSSS
LL           IIIIII     SSSSSSSS
LL             II       SS
LL             II       SS
LL             II       SS
LL             II       SS
LL             II       SSSSSS
LL             II       SSSSSS
LL             II            SS
LL             II            SS
LL             II            SS
LL             II            SS
LLLLLLLLLL   IIIIII     SSSSSSSS
LLLLLLLLLL   IIIIII     SSSSSSSS
```

```
0000      1          .TITLE  SCBVECTOR
0000      2          .IDENT  'V04-000'
0000      3  ;
0000      4  ;*******************************************************************
0000      5  ;*                                                                 *
0000      6  ;*    COPYRIGHT (c) 1978, 1980, 1982, 1984 BY                      *
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0000     22  ;*    SOFTWARE ON EQUIPMENT WHICH IS NOT SUPPLIED BY DIGITAL.       *
0000     23  ;*                                                                 *
0000     24  ;*                                                                 *
0000     25  ;*******************************************************************
0000     26  ;
0000     27  ;++
0000     28  ;   VMS SCB
0000     29  ;--
0000     30  ;
0000     31  ;  VERSION X01 (BASE LEVEL 0)
0000     32  ;
0000     33  ;  AUTHOR: R. HEINEN 9-AUG-76 (X01)
0000     34  ;
0000     35  ;  MODIFIED BY:
0000     36  ;
0000     37  ;          V02-010 LMP0009         L. Mark Pilant   5-Feb-1982  18:25
0000     38  ;                  Add support for IPL 12 interrupts.
0000     39  ;
0000     40  ;          V07     NPKCOMET         N. KRONENBERG   19-FEB-1979
0000     41  ;                  REMOVED GLOBAL LABELS FOR SPECIFIC VECTORS;
0000     42  ;                  REMOVED MASTERWAKE TO INIT AND UNEXP TO ERRORLOG.
0000     43  ;                  CHANGED UNUSED VECTOR NAMES FROM LOCAL TO GLOBAL.
0000     44  ;
0000     45  ;          V08     NPKCOMET         N. KRONENBERG   12-APR-1979
0000     46  ;                  GAVE CPU-INDEPENDENT NAMES TO VECTORS
0000     47  ;                  ^X54,58,5C,60.
0000     48  ;
0000     49  ;          V09     STJ0130         STEVEN T. JEFFREYS 27-OCT-1981
0000     50  ;                  PLUGGED THE FORK INTERRUPT SERVICE ROUTINE VECTORS
0000     51  ;                  WITH THE ADDRESS OF THE NEW ENTRY POINTS.  SEE THE
0000     52  ;                  MODULE "FORKCNTRL" FOR DETAILS.
0000     53  ;
```

```
          0000    55              .SBTTL  SCB TEMPLATE
          0000    56  ;++
          0000    57  ; SCB TEMPLATE COPIED INTO PHYSICALLY CONTIGUOUS PAGES
          0000    58  ; ALLOCATED AND MAPPED BY SYSBOOT.
          0000    59  ;--
          0000    60              $IPLDEF                 ; DEFINE IPL LEVELS
          0000    61              $PRDEF                  ; DEFINE PROCESSOR REGISTERS
          0000    62
          0000    63  ;
          0000    64  ; LOCAL MACROS
          0000    65  ;
          0000    66  ; MACRO DEFVEC DEFINES AN UNUSED VECTOR.
          0000    67  ;
          0000    68
          0000    69              .MACRO  DEFVEC,VNUM
          0000    70              .LONG   ERL$VEC'VNUM+INTSTK
          0000    71              .ENDM   DEFVEC
          0000    72
 00000000 0000    73              .PSECT  $$$00SCB,LONG   ;
 00000001 0000    74  INTSTK=1                            ; DEFINITION FOR INTERRUPT STACK
          0000    75  SCB$AL_BASE::                        ; SCB BASE ADDRESS
 00000001' 0000   76              .LONG   ERL$VEC0+INTSTK ; UNUSED, RESERVED TO DIGITAL
 00000001' 0004   77              .LONG   EXE$MCHK+INTSTK ; MACHINE CHECK HANDLER.          ^X004
 00000001' 0008   78              .LONG   EXE$KERSTKNV+INTSTK; KERNEL STACK NOT VALID HALT. ^X008
 00000001' 000C   79              .LONG   EXE$POWERFAIL+INTSTK; POWER FAIL INTERUPT. ^X00C
 00000000' 0010   80              .LONG   EXE$OPCDEC      ; RESERVED/PRIVILEGED INSTRUCTION FAULT. ^X010
 00000000' 0014   81              .LONG   EXE$OPCCUS      ; CUSTOMER RESERVED INSTRUCTION FAULT. ^X014
 00000000' 0018   82              .LONG   EXE$ROPRAND     ; RESERVED OPERAND FAULT/HALT. ^X018
 00000000' 001C   83              .LONG   EXE$RADRMOD     ; RESERVED ADDRESSING MODE FAULT. ^X01C
 00000000' 0020   84              .LONG   EXE$ACVIOLAT    ; ACCESS CONTROL VIOLATION FAULT. ^X020
 00000000' 0024   85              .LONG   MMG$PAGEFAULT   ; TRANSLATION NOT VALID FAULT.   ^X024
 00000000' 0028   86              .LONG   EXE$TBIT        ; TBIT FAULT.                    ^X028
 00000000' 002C   87              .LONG   EXE$BREAK       ; BREAKPOINT FAULT.              ^X02C
 00000000' 0030   88              .LONG   EXE$COMPAT      ; COMPATIBILITY FAULT.           ^X030
 00000000' 0034   89              .LONG   EXE$ARITH       ; ARITHMETIC TRAP.               ^X034
 00000000' 0038   90              .LONG   ERL$VEC56       ; UNUSED                         ^X038
 00000000' 003C   91              .LONG   ERL$VEC60       ; UNUSED                         ^X03C
 00000000' 0040   92              .LONG   EXE$CMODKRNL    ; CHMK TRAP.                     ^X040
 00000000' 0044   93              .LONG   EXE$CMODEXEC    ; CHME TRAP.                     ^X044
 00000000' 0048   94              .LONG   EXE$CMODSUPR    ; CHMS TRAP.                     ^X048
 00000000' 004C   95              .LONG   EXE$CMODUSER    ; CHMU TRAP.                     ^X04C
 00000000' 0050   96              .LONG   ERL$VEC80       ; UNUSED.                        ^X050
 00000001' 0054   97              .LONG   EXE$INT54+INTSTK        ; CPU-DEPENDENT FAULT    ^X054
 00000001' 0058   98              .LONG   EXE$INT58+INTSTK        ; CPU-DEPENDENT FAULT    ^X058
 00000001' 005C   99              .LONG   EXE$INT5C+INTSTK        ; CPU-DEPENDENT FAULT    ^X05C
 00000001' 0060  100              .LONG   EXE$INT60+INTSTK        ; CPU-DEPENDENT FAULT    ^X060
 00000000' 0064  101              .LONG   ERL$VEC100      ; UNUSED.
 00000000' 0068  102              .LONG   ERL$VEC104      ; UNUSED.
 00000000' 006C  103              .LONG   ERL$VEC108      ; UNUSED.
 00000000' 0070  104              .LONG   ERL$VEC112      ; UNUSED.
 00000000' 0074  105              .LONG   ERL$VEC116      ; UNUSED.
 00000000' 0078  106              .LONG   ERL$VEC120      ; UNUSED.
 00000000' 007C  107              .LONG   ERL$VEC124      ; UNUSED.
 00000000' 0080  108              .LONG   ERL$VEC128      ; UNUSED.
 00000001' 0084  109              .LONG   ERL$VEC132+INTSTK; SOFTWARE LEVEL 1 INTERUPT.
 00000000' 0088  110              .LONG   SCH$ASTDEL      ; SOFTWARE LEVEL 2 INTERUPT.     ^X088
 00000000' 008C  111              .LONG   SCH$RESCHED     ; SOFTWARE LEVEL 3 INTERUPT.     ^X08C
```

SCBVECTOR
V04-000

SCB TEMPLATE

I 4

16-SEP-1984 01:07:37   VAX/VMS Macro V04-00   Page   3
5-SEP-1984 03:47:15 [SYS.SRC]SCBVECTOR.MAR;1   (1)

SC'
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00000001' 0090   112          .LONG   IOC$IOPOST+INTSTK; SOFTWARE LEVEL 4 INTERUPT.    ^X090
00000001' 0094   113          .LONG   INI$MASTERWAKE+INTSTK; SOFTWARE LEVEL 5 INTERUPT.      ^X094
00000001' 0098   114          .LONG   EXE$FRKIPL6DSP+INTSTK; SOFTWARE LEVEL 6 INTERUPT.      ^X098
00000001' 009C   115          .LONG   EXE$SWTIMINT+INTSTK; SOFTWARE LEVEL 7 INTERUPT. ^X09C
00000001' 00A0   116          .LONG   EXE$FRKIPL8DSP+INTSTK; SOFTWARE LEVEL 8 INTERUPT.      ^X0A0
00000001' 00A4   117          .LONG   EXE$FRKIPL9DSP+INTSTK; SOFTWARE LEVEL 9 INTERUPT.      ^X0A4
00000001' 00A8   118          .LONG   EXE$FRKIPL10DSP+INTSTK; SOFTWARE LEVEL 10 INTERUPT.    ^X0A8
00000001' 00AC   119          .LONG   EXE$FRKIPL11DSP+INTSTK; SOFTWARE LEVEL 11 INTERUPT.    ^X0AC
00000001' 00B0   120          .LONG   EXE$IPCONTROL+INTSTK    ; SOFTWARE LEVEL 12 INTERUPT.  ^X0B0
00000001' 00B4   121          .LONG   ERL$VEC180+INTSTK       ; SOFTWARE LEVEL 13 INTERUPT.  ^X0B4
00000001' 00B8   122          .LONG   ERL$VEC184+INTSTK       ; SOFTWARE LEVEL 14 INTERUPT.  ^X0B8
00000001' 00BC   123          .LONG   ERL$VEC188+INTSTK       ; SOFTWARE LEVEL 15 INTERUPT. (^XBC)
00000001' 00C0   124          .LONG   EXE$HWCLKINT+INTSTK; INTERVAL TIMER   (^XC0)
00000001' 00C4   125          .LONG   ERL$VEC196+INTSTK       ; UNUSED.            (^XC4)
00000001' 00C8   126          .LONG   ERL$VEC200+INTSTK       ; UNUSED.            (^XC8)
00000001' 00CC   127          .LONG   ERL$VEC204+INTSTK       ; UNUSED.            (^XCC)
00000001' 00D0   128          .LONG   ERL$VEC208+INTSTK       ; UNUSED.            (^XD0)
00000001' 00D4   129          .LONG   ERL$VEC212+INTSTK       ; UNUSED.            (^XD4)
00000001' 00D8   130          .LONG   ERL$VEC216+INTSTK       ; UNUSED.            (^XD8)
00000001' 00DC   131          .LONG   ERL$VEC220+INTSTK       ; UNUSED.            (^XDC)
00000001' 00E0   132          .LONG   ERL$VEC224+INTSTK       ; UNUSED.            (^XE0)
00000001' 00E4   133          .LONG   ERL$VEC228+INTSTK       ; UNUSED.            (^XE4)
00000001' 00E8   134          .LONG   ERL$VEC232+INTSTK       ; UNUSED.            (^XE8)
00000001' 00EC   135          .LONG   ERL$VEC236+INTSTK       ; UNUSED.            (^XEC)
00000001' 00F0   136          .LONG   ERL$VEC240+INTSTK       ; UNUSED.            (^XF0)
00000001' 00F4   137          .LONG   ERL$VEC244+INTSTK       ; UNUSED.            (^XF4)
00000001' 00F8   138          .LONG   CON$INTDISI+INTSTK; CONSOLE INTERRUPT FOR INPUT (^XF8)
00000001' 00FC   139          .LONG   CON$INTDISO+INTSTK; CONSOLE INTERRUPT FOR OUTPUT (^XFC)
          0100   140
00000100  0100   141  VNUM=256                    ; VECTORS ^X100-^X1FC FOR NEXUS DEVICES
          0100   142
          0100   143          .REPT   16           ; NEXUS VECTORS, IPL ^X14
          0100   144          DEFVEC  \VNUM
          0100   145          VNUM=VNUM+4
          0100   146          .ENDR
          0140   147
          0140   148          .REPT   16           ; NEXUS VECTORS, IPL ^X15
          0140   149          DEFVEC  \VNUM
          0140   150          VNUM=VNUM+4
          0140   151          .ENDR
          0180   152
          0180   153          .REPT   16           ; NEXUS VECTORS, IPL ^X16
          0180   154          DEFVEC  \VNUM
          0180   155          VNUM=VNUM+4
          0180   156          .ENDR
          01C0   157
          01C0   158          .REPT   16           ; NEXUS VECTORS, IPL ^X17
          01C0   159          DEFVEC  \VNUM
          01C0   160          VNUM=VNUM+4
          01C0   161          .ENDR
          0200   162
          0200   163          .END
```

Symbol	Value			Symbol	Value			Symbol	Value		
CON$INTDIS1	********	X	02	ERL$VEC372	********	X	02	EXE$KERSTKNV	********	X	02
CON$INTDIS0	********	X	02	ERL$VEC376	********	X	02	EXE$MCHK	********	X	02
ERL$VEC0	********	X	02	ERL$VEC380	********	X	02	EXE$OPCCUS	********	X	02
ERL$VEC100	********	X	02	ERL$VEC384	********	X	02	EXE$OPCDEC	********	X	02
ERL$VEC104	********	X	02	ERL$VEC388	********	X	02	EXE$POWERFAIL	********	X	02
ERL$VEC108	********	X	02	ERL$VEC392	********	X	02	EXE$RADRMOD	********	X	02
ERL$VEC112	********	X	02	ERL$VEC396	********	X	02	EXE$ROPRAND	********	X	02
ERL$VEC116	********	X	02	ERL$VEC400	********	X	02	EXE$SWTIMINT	********	X	02
ERL$VEC120	********	X	02	ERL$VEC404	********	X	02	EXE$TBIT	********	X	02
ERL$VEC124	********	X	02	ERL$VEC408	********	X	02	INI$MASTERWAKE	********	X	02
ERL$VEC128	********	X	02	ERL$VEC412	********	X	02	INTSTK	= 00000001		
ERL$VEC132	********	X	02	ERL$VEC416	********	X	02	IOC$IOPOST	********	X	02
ERL$VEC180	********	X	02	ERL$VEC420	********	X	02	MMG$PAGEFAULT	********	X	02
ERL$VEC184	********	X	02	ERL$VEC424	********	X	02	OP$_ACBD	= 0000006F		
ERL$VEC188	********	X	02	ERL$VEC428	********	X	02	OP$_ACBF	= 0000004F		
ERL$VEC196	********	X	02	ERL$VEC432	********	X	02	OP$_ACBG	= 00004FFD		
ERL$VEC200	********	X	02	ERL$VEC436	********	X	02	OP$_ACBH	= 00006FFD		
ERL$VEC204	********	X	02	ERL$VEC440	********	X	02	OP$_ADDD2	= 00000060		
ERL$VEC208	********	X	02	ERL$VEC444	********	X	02	OP$_ADDD3	= 00000061		
ERL$VEC212	********	X	02	ERL$VEC448	********	X	02	OP$_ADDF2	= 00000040		
ERL$VEC216	********	X	02	ERL$VEC452	********	X	02	OP$_ADDF3	= 00000041		
ERL$VEC220	********	X	02	ERL$VEC456	********	X	02	OP$_ADDG2	= 000040FD		
ERL$VEC224	********	X	02	ERL$VEC460	********	X	02	OP$_ADDG3	= 000041FD		
ERL$VEC228	********	X	02	ERL$VEC464	********	X	02	OP$_ADDH2	= 000060FD		
ERL$VEC232	********	X	02	ERL$VEC468	********	X	02	OP$_ADDH3	= 000061FD		
ERL$VEC236	********	X	02	ERL$VEC472	********	X	02	OP$_ADDP4	= 00000020		
ERL$VEC240	********	X	02	ERL$VEC476	********	X	02	OP$_ADDP6	= 00000021		
ERL$VEC244	********	X	02	ERL$VEC480	********	X	02	OP$_ASHP	= 000000F8		
ERL$VEC256	********	X	02	ERL$VEC484	********	X	02	OP$_CLRD	= 0000007C		
ERL$VEC260	********	X	02	ERL$VEC488	********	X	02	OP$_CLRF	= 000000D4		
ERL$VEC264	********	X	02	ERL$VEC492	********	X	02	OP$_CLRG	= 0000007C		
ERL$VEC268	********	X	02	ERL$VEC496	********	X	02	OP$_CLRH	= 00007CFD		
ERL$VEC272	********	X	02	ERL$VEC500	********	X	02	OP$_CMPD	= 00000071		
ERL$VEC276	********	X	02	ERL$VEC504	********	X	02	OP$_CMPF	= 00000051		
ERL$VEC280	********	X	02	ERL$VEC508	********	X	02	OP$_CMPG	= 000051FD		
ERL$VEC284	********	X	02	ERL$VEC56	********	X	02	OP$_CMPH	= 000071FD		
ERL$VEC288	********	X	02	ERL$VEC60	********	X	02	OP$_CMPP3	= 00000035		
ERL$VEC292	********	X	02	ERL$VEC80	********	X	02	OP$_CMPP4	= 00000037		
ERL$VEC296	********	X	02	EXE$ACVIOLAT	********	X	02	OP$_CRC	= 0000000B		
ERL$VEC300	********	X	02	EXE$ARITH	********	X	02	OP$_CVTBD	= 0000006C		
ERL$VEC304	********	X	02	EXE$BREAK	********	X	02	OP$_CVTBF	= 0000004C		
ERL$VEC308	********	X	02	EXE$CMODEXEC	********	X	02	OP$_CVTBG	= 00004CFD		
ERL$VEC312	********	X	02	EXE$CMODKRNL	********	X	02	OP$_CVTBH	= 00006CFD		
ERL$VEC316	********	X	02	EXE$CMODSUPR	********	X	02	OP$_CVTDB	= 00000068		
ERL$VEC320	********	X	02	EXE$CMODUSER	********	X	02	OP$_CVTDF	= 00000076		
ERL$VEC324	********	X	02	EXE$COMPAT	********	X	02	OP$_CVTDH	= 000032FD		
ERL$VEC328	********	X	02	EXE$FRKIPL10DSP	********	X	02	OP$_CVTDL	= 0000006A		
ERL$VEC332	********	X	02	EXE$FRKIPL11DSP	********	X	02	OP$_CVTDW	= 00000069		
ERL$VEC336	********	X	02	EXE$FRKIPL6DSP	********	X	02	OP$_CVTFB	= 00000048		
ERL$VEC340	********	X	02	EXE$FRKIPL8DSP	********	X	02	OP$_CVTFD	= 00000056		
ERL$VEC344	********	X	02	EXE$FRKIPL9DSP	********	X	02	OP$_CVTFG	= 000099FD		
ERL$VEC348	********	X	02	EXE$HWCLKINT	********	X	02	OP$_CVTFH	= 000098FD		
ERL$VEC352	********	X	02	EXE$INT54	********	X	02	OP$_CVTFL	= 0000004A		
ERL$VEC356	********	X	02	EXE$INT58	********	X	02	OP$_CVTFW	= 00000049		
ERL$VEC360	********	X	02	EXE$INT5C	********	X	02	OP$_CVTGB	= 000048FD		
ERL$VEC364	********	X	02	EXE$INT60	********	X	02	OP$_CVTGF	= 000033FD		
ERL$VEC368	********	X	02	EXE$IPCONTROL	********	X	02	OP$_CVTGH	= 000056FD		

```
OPS_CVTGL      = 00004AFD        OPS_MULG3      = 000045FD
OPS_CVTGW      = 000049FD        OPS_MULH2      = 000064FD
OPS_CVTHB      = 000068FD        OPS_MULH3      = 000065FD
OPS_CVTHD      = 0000F7FD        OPS_MULP       = 00000025
OPS_CVTHF      = 0000F6FD        OPS_POLYD      = 00000075
OPS_CVTHG      = 000076FD        OPS_POLYF      = 00000055
OPS_CVTHL      = 00006AFD        OPS_POLYG      = 000055FD
OPS_CVTHW      = 000069FD        OPS_POLYH      = 000075FD
OPS_CVTLD      = 0000006E        OPS_SCANC      = 0000002A
OPS_CVTLF      = 0000004E        OPS_SKPC       = 0000003B
OPS_CVTLG      = 00004EFD        OPS_SPANC      = 0000002B
OPS_CVTLH      = 00006EFD        OPS_SUBD2      = 00000062
OPS_CVTLP      = 000000F9        OPS_SUBD3      = 00000063
OPS_CVTPL      = 00000036        OPS_SUBF2      = 00000042
OPS_CVTPS      = 00000008        OPS_SUBF3      = 00000043
OPS_CVTPT      = 00000024        OPS_SUBG2      = 000042FD
OPS_CVTRDL     = 0000006B        OPS_SUBG3      = 000043FD
OPS_CVTRFL     = 0000004B        OPS_SUBH2      = 000062FD
OPS_CVTRGL     = 00004BFD        OPS_SUBH3      = 000063FD
OPS_CVTRHL     = 00006BFD        OPS_SUBP4      = 00000022
OPS_CVTSP      = 00000009        OPS_SUBP6      = 00000023
OPS_CVTTP      = 00000026        OPS_TSTD       = 00000073
OPS_CVTWD      = 0000006D        OPS_TSTF       = 00000053
OPS_CVTWF      = 0000004D        OPS_TSTG       = 000053FD
OPS_CVTWG      = 00004DFD        OPS_TSTH       = 000073FD
OPS_CVTWH      = 00006DFD        SCB$AL_BASE    00000000 RG    02
OPS_DIVD2      = 00000066        SCH$ASTDEL     ******** X    02
OPS_DIVD3      = 00000067        SCH$RESCHED    ******** X    02
OPS_DIVF2      = 00000046        VNUM           = 00C00200
OPS_DIVF3      = 00000047
OPS_DIVG2      = 000046FD
OPS_DIVG3      = 000047FD
OPS_DIVH2      = 000066FD
OPS_DIVH3      = 000067FD
OPS_DIVP       = 00000027
OPS_EDITPC     = 00000038
OPS_EMODD      = 00000074
OPS_EMODF      = 00000054
OPS_EMODG      = 000054FD
OPS_EMODH      = 000074FD
OPS_MATCHC     = 00000039
OPS_MNEGD      = 00000072
OPS_MNEGF      = 00000052
OPS_MNEGG      = 000052FD
OPS_MNEGH      = 000072FD
OPS_MOVD       = 00000070
OPS_MOVF       = 00000050
OPS_MOVG       = 000050FD
OPS_MOVH       = 000070FD
OPS_MOVP       = 00000034
OPS_MOVTC      = 0000002E
OPS_MOVTUC     = 0000002F
OPS_MULD2      = 00000064
OPS_MULD3      = 00000065
OPS_MULF2      = 00000044
OPS_MULF3      = 00000045
OPS_MULG2      = 000044FD
```

```
                                    +-----------------+
                                    ! Psect synopsis !
                                    +-----------------+

PSECT name                    Allocation          PSECT No.   Attributes
----------                    ----------          ---------   ----------
.  ABS  .                     00000000 (     0.)  00 (   0.)  NOPIC   USR   CON   ABS   LCL NOSHR NOEXE  NORD  NOWRT NOVEC BYTE
$ABS$                         00000000 (     0.)  01 (   1.)  NOPIC   USR   CON   ABS   LCL NOSHR  EXE   RD    WRT  NOVEC BYTE
$$$00SCB                      00000200 (   512.)  02 (   2.)  NOPIC   USR   CON   REL   LCL NOSHR  EXE   RD    WRT  NOVEC LONG

                               +----------------------------+
                               ! Performance indicators !
                               +----------------------------+

Phase                     Page faults    CPU Time      Elapsed Time
-----                     -----------    --------      ------------
Initialization                     29    00:00:00.03   00:00:01.90
Command processing                121    00:00:00.60   00:00:05.67
Pass 1                            375    00:00:10.23   00:00:36.96
Symbol table sort                   0    00:00:00.72   00:00:01.92
Pass 2                             51    00:00:03.24   00:00:13.04
Symbol table output                22    00:00:00.15   00:00:00.15
Psect synopsis output               2    00:00:00.03   00:00:00.02
Cross-reference output              0    00:00:00.00   00:00:00.00
Assembler run totals              602    00:00:15.00   00:00:59.68
```

The working set limit was 1650 pages.
45545 bytes (89 pages) of virtual memory were used to buffer the intermediate code.
There were 40 pages of symbol table space allocated to hold 584 non-local and 0 local symbols.
2915 source lines were read in Pass 1, producing 21 object records in Pass 2.
137 pages of virtual memory were used to define 136 macros.

```
                              +------------------------------+
                              ! Macro library statistics !
                              +------------------------------+

Macro library name                           Macros defined
------------------                           --------------
_$255$DUA28:[SYS.OBJ]LIB.MLB;1                       1
_$255$DUA28:[SYSLIB]STARLET.MLB;2                    5
TOTALS (all libraries)                              6
```

520 GETS were required to define 6 macros.

There were no errors, warnings or information messages.

MACRO/LIS=LIS$:SCBVECTOR/OBJ=OBJ$:SCBVECTOR MASD$:[EMULAT.SRC]MISSING/UPDATE=(MASD$:[EMULAT.ENH]MISSING)+MASD$:[SYS.SRC]SCBVECTOR/UP

SHELL
LIS

RSE
LIS

SCBVECTOR
LIS

SDAT
LIS

SHMGSDRTN
LIS

SCSVEC
LIS

SCHED
LIS

SECAUDIT
LIS

SPTSKEL
LIS

RUFSYSVEC
LIS