


```
PPPPPPPP  RRRRRRRR  DDDDDDDD  EEEEEEEEE  FFFFFFFFFF
PPPPPPPP  RRRRRRRR  DDDDDDDD  EEEEEEEEE  FFFFFFFFFF
PP      PP  RR      RR  DD      DD  EE      FF
PP      PP  RR      RR  DD      DD  EE      FF
PP      PP  RR      RR  DD      DD  EE      FF
PP      PP  RR      RR  DD      DD  EE      FF
PPPPPPPP  RRRRRRRR  DD      DD  EEEEEEEEE  FFFFFFFFFF
PPPPPPPP  RRRRRRRR  DD      DD  EEEEEEEEE  FFFFFFFFFF
PP      RR  RR  DD      DD  EE      FF
PP      RR  RR  DD      DD  EE      FF
PP      RR  RR  DD      DD  EE      FF
PP      RR  RR  DD      DD  EE      FF
PP      RR  RR  DDDDDDDD  EEEEEEEEE  FF
PP      RR  RR  DDDDDDDD  EEEEEEEEE  FF
```

```
....
....
....
....
```

```
LL      IIIIII  SSSSSSSS
LL      IIIIII  SSSSSSSS
LL      II     SS
LL      II     SS
LL      II     SS
LL      II     SS
LL      II     SSSSSS
LL      II     SSSSSS
LL      II     SS
LL      II     SS
LL      II     SS
LL      II     SS
LLLLLLLLLL IIIIII  SSSSSSSS
LLLLLLLLLL IIIIII  SSSSSSSS
```



```

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0000 2      .IDENT 'V04-000'
0000 3
0000 4
0000 5 *****
0000 6
0000 7
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0000 25     *
0000 26     *****
0000 27
0000 28     FACILITY: SYS
0000 29
0000 30     ABSTRACT:
0000 31
0000 32         This module defines global symbols for the processor registers and
0000 33         protection codes for inclusion in STARLET.OLB
0000 34
0000 35     ENVIRONMENT: No executable code
0000 36
0000 37     AUTHOR: Benn Schreiber, CREATION DATE: 9-Aug-1981
0000 38
0000 39     MODIFIED BY:
0000 40

```

```
0000 42      .DISABLE TRACEBACK
0000 43
0000 44      $PRDEF GLOBAL      ;Define processor registers
0000 45      $PRTDEF GLOBAL     ;Define protection codes
0000 46
0000 47      .END
```


SYSSPRDEF
Symbol table

C 9

16-SEP-1984 00:59:30 VAX/VMS Macro V04-00
5-SEP-1984 03:46:32 [SYS.SRC]PRDEF.MAR;1

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PR\$S_PRDEF = 00000004 G
 PR\$S_SID_ECO = 00000009 G
 PR\$S_SID_PL = 00000003 G
 PR\$S_SID_SN = 0000000C G
 PR\$S_SID_TYPE = 00000008 G
 PR\$V_SID_ECO = 0000000F G
 PR\$V_SID_PL = 0000000C G
 PR\$V_SID_SN = 00000000 G
 PR\$V_SID_TYPE = 00000018 G
 PR\$_ACCS790 = 0000004B G
 PR\$_ASTLVL = 00000013 G
 PR\$_CADR = 00000025 G
 PR\$_CAER = 00000027 G
 PR\$_CMIERR = 00000017 G
 PR\$_CRBT = 00000048 G
 PR\$_CSRD = 0000001D G
 PR\$_CSRS = 0000001C G
 PR\$_CSTD = 0000001F G
 PR\$_CSTS = 0000001E G
 PR\$_CSWP = 00000042 G
 PR\$_DFI = 00000049 G
 PR\$_EHSR = 0000004A G
 PR\$_ESP = 00000001 G
 PR\$_ICCS = 00000018 G
 PR\$_IPL = 00000012 G
 PR\$_ISP = 00000004 G
 PR\$_KSP = 00000000 G
 PR\$_LSPA = 0000004E G
 PR\$_MAPEN = 00000038 G
 PR\$_MCCTL = 00000046 G
 PR\$_MCESR = 00000026 G
 PR\$_MDCTL = 00000045 G
 PR\$_MDECC = 00000043 G
 PR\$_MENA = 00000044 G
 PR\$_MERG = 00000047 G
 PR\$_POBR = 00000008 G
 PR\$_POLR = 00000009 G
 PR\$_P1BR = 0000000A G
 PR\$_P1LR = 0000000B G
 PR\$_PAMACC = 00000040 G
 PR\$_PAMLOC = 00000041 G
 PR\$_PCBB = 00000010 G
 PR\$_RSPD = 0000004F G
 PR\$_RXCS = 00000020 G
 PR\$_RXDB = 00000021 G
 PR\$_SBIER = 00000034 G
 PR\$_SBIFS = 00000030 G
 PR\$_SBIMT = 00000033 G
 PR\$_SBIQC = 00000036 G
 PR\$_SBIS = 00000031 G
 PR\$_SBISC = 00000032 G
 PR\$_SBITA = 00000035 G
 PR\$_SBR = 0000000C G
 PR\$_SCBB = 00000011 G
 PR\$_SID = 0000003E G
 PR\$_SID_TYP730 = 00000003 G
 PR\$_SID_TYP750 = 00000002 G

PR\$_SID_TYP780 = 00000001 G
 PR\$_SID_TYP790 = 00000004 G
 PR\$_SID_TYP8NN = 00000006 G
 PR\$_SID_TYP8SS = 00000005 G
 PR\$_SID_TYPMAX = 00000008 G
 PR\$_SID_TYPUV1 = 00000007 G
 PR\$_SID_TYPUV2 = 00000008 G
 PR\$_SIRR = 00000014 G
 PR\$_SISR = 00000015 G
 PR\$_SLR = 0000000D G
 PR\$_SSP = 00000002 G
 PR\$_STXCS = 0000004C G
 PR\$_STXDB = 0000004D G
 PR\$_TBCHK = 0000003F G
 PR\$_TBDR = 00000024 G
 PR\$_TBIA = 00000039 G
 PR\$_TBIS = 0000003A G
 PR\$_TXCS = 00000022 G
 PR\$_TXDB = 00000023 G
 PR\$_UBRESET = 00000037 G
 PR\$_USP = 00000003 G
 PR\$_WCSA = 0000002C G
 PR\$_WCSD = 0000002D G
 PRT\$_C_ER = 00000007 G
 PRT\$_C_ERKW = 00000006 G
 PRT\$_C_EW = 00000005 G
 PRT\$_C_KR = 00000003 G
 PRT\$_C_KW = 00000002 G
 PRT\$_C_NA = 00000000 G
 PRT\$_C_RESERVED = 00000001 G
 PRT\$_C_SR = 0000000B G
 PRT\$_C_SREW = 00000009 G
 PRT\$_C_SRKW = 0000000A G
 PRT\$_C_SW = 00000008 G
 PRT\$_C_UR = 0000000F G
 PRT\$_C_UREW = 0000000D G
 PRT\$_C_URKW = 0000000E G
 PRT\$_C_URSW = 0000000C G
 PRT\$_C_UW = 00000004 G

PRC
V04

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78

This image displays a grid of 100 small technical diagrams or charts, arranged in 10 rows and 10 columns. Each diagram is a miniature version of a larger technical document page, featuring various data visualizations, tables, and text. The diagrams are titled with various system-related terms, including:

- RELOCDRU LIS
- RMSRESET LIS
- PROCSTR LIS
- PHOUTL LIS
- PMSDAT LIS
- POSTEF LIS
- POWERFAL LIS
- PRDEF LIS
- PTEDUMP LIS
- RMSVECTOR LIS

The diagrams themselves contain a mix of text, tables, and graphical elements such as bar charts and line graphs, representing different aspects of system performance or configuration.