


```
IIIIII    000000    PPPPPPP    EEEEEEEEE    RRRRRRR    FFFFFFFF    000000    RRRRRRR    MM    MM
IIIIII    000000    PPPPPPP    EEEEEEEEE    RRRRRRR    FFFFFFFF    000000    RRRRRRR    MM    MM
  II      00      00    PP      PP    EE      RR      RR    FF      FF      00      00    RR      RR    MMM    MMM
  II      00      00    PP      PP    EE      RR      RR    FF      FF      00      00    RR      RR    MMM    MMM
  II      00      00    PP      PP    EE      RR      RR    FF      FF      00      00    RR      RR    MM    MM
  II      00      00    PPPPPPP    EEEEEEEEE    RRRRRRR    FFFFFFFF    00      00    RRRRRRR    MM    MM
  II      00      00    PPPPPPP    EEEEEEEEE    RRRRRRR    FFFFFFFF    00      00    RRRRRRR    MM    MM
  II      00      00    PP      PP      EE      RR      RR    FF      FF      00      00    RR      RR    MM    MM
  II      00      00    PP      PP      EE      RR      RR    FF      FF      00      00    RR      RR    MM    MM
  II      00      00    PP      PP      EE      RR      RR    FF      FF      00      00    RR      RR    MM    MM
  II      00      00    PP      PP      EE      RR      RR    FF      FF      00      00    RR      RR    MM    MM
  II      00      00    PP      PP      EE      RR      RR    FF      FF      00      00    RR      RR    MM    MM
  II      00      00    PP      PP      EE      RR      RR    FF      FF      00      00    RR      RR    MM    MM
  II      00      00    PP      PP      EE      RR      RR    FF      FF      00      00    RR      RR    MM    MM
  II      00      00    PP      PP      EE      RR      RR    FF      FF      00      00    RR      RR    MM    MM
IIIIII    000000    PP      PP      EE      RR      RR    FF      FF      00      00    RR      RR    MM    MM
IIIIII    000000    PP      PP      EE      RR      RR    FF      FF      000000  000000  RR      RR    MM    MM
```

...
...
...
...

```
LL        IIIIII    SSSSSSS
LL        IIIIII    SSSSSSS
LL        II      SS
LL        II      SS
LL        II      SS
LL        II      SS
LL        II      SSSSSS
LL        II      SSSSSS
LL        II      SS
LL        II      SS
LL        II      SS
LL        II      SS
LLLLLLLLL IIIIII    SSSSSSS
LLLLLLLLL IIIIII    SSSSSSS
```

(1)	71	ABORTED I/O REQUEST
(1)	107	END OF I/O TRANSACTION
(1)	140	END OF I/O REQUEST
(1)	185	START OF I/O TRANSACTION
(1)	219	START OF I/O REQUEST
(1)	270	ALLOCATE MESSAGE BUFFER

```
0000 1 .TITLE IOPERFORM - I/O PERFORMANCE DATA COLLECTION
0000 2 .IDENT 'V04-000'
0000 3
0000 4
0000 5 *****
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0000 27
0000 28 D. N. CUTLER '8-NOV-77
0000 29
0000 30 I/O PERFORMANCE MEASUREMENT DATA COLLECTION ROUTINES
0000 31
0000 32 MODIFIED BY:
0000 33
0000 34 V03-005 SSA00024 Stan Amway 9-Apr-1984
0000 35 If available, store file access information from WCB
0000 36 (SRQ transaction). When monitoring disks, this will allow
0000 37 trace-driven reduction & simulation programs (e.g., disk
0000 38 cache analysis) to make use of the information.
0000 39
0000 40 V03-004 SSA00008 Stan Amway 10-Feb-1984
0000 41 Changed time stamp to quadword.
0000 42
0000 43 V03-003 SSA00002 Stan Amway 30-Sep-1983
0000 44 Added routine PMSSABORT_RQ to be called by FDI routines
0000 45 (usually BACKOUT_QIO in SYSQIOFDT) that have aborted or
0000 46 restarted an I/O request AND the IRP will not be posted
0000 47 via IOCIPOST.
0000 48
0000 49 Changed IPL 15 references to IPL$_PERFMON, a new symbol.
0000 50
0000 51 V03-002 SSA00001 Stan Amway 13-Sep-1983
0000 52 Changed PMSSSTART_IO to capture longword transfer
0000 53 byte count.
0000 54
0000 55 V03-001 LJK47917 Lawrence J. Kenah 10-Aug-1982
0000 56 Insure that stack looks the same at all entry points so that
0000 57 common exit code can be used.
```

```
0000 58 :  
0000 59 : MACRO LIBRARY CALLS  
0000 60 :  
0000 61 :  
0000 62 $CADEF  
0000 63 $IPLDEF  
0000 64 $IRPDEF  
0000 65 $PBHDEF  
0000 66 $PDBDEF  
0000 67 $PIBDEF  
0000 68 $UCBDEF  
0000 69 $WCBDEF
```

```
:DEFINE CONDITIONAL ASSEMBLY PARAMETERS  
:DEFINE PROCESSOR IPL LEVELS  
:DEFINE IRP OFFSETS  
:DEFINE PBH OFFSETS  
:DEFINE PDB OFFSETS  
:DEFINE PIB OFFSETS  
:DEFINE UCB OFFSETS  
:Define WCB offsets
```

```

0000 71      .SBTTL  ABORTED I/O REQUEST
0000 72      :+
0000 73      : PMSS$ABORT_RQ - ABORTED I/O REQUEST
0000 74      :
0000 75      : THIS ROUTINE IS CALLED WHEN AN I/O REQUEST HAS BEEN ABORTED
0000 76      : TO ENTER A MESSAGE IN THE I/O PERFORMANCE DATA COLLECTION BUFFE .
0000 77      :
0000 78      : INPUTS:
0000 79      :
0000 80      :     R3 = ADDRESS OF I/O REQUEST PACKET.
0000 81      :
0000 82      :     IPL CAN BE ANY SOFTWARE IPL >= IPL$ASTDEL
0000 83      :
0000 84      : OUTPUTS:
0000 85      :
0000 86      :     IF I/O PERFORMANCE DATA COLLECTION IS ENABLED AND THE CURRENT I/O PACKET
0000 87      :     CONFORMS TO THE SELECTION CRITERIA, THEN AN ABORTED I/O REQUEST IS
0000 88      :     PLACED IN THE CURRENT DATA BUFFER.
0000 89      :
0000 90      :     ALL REGISTERS ARE PRESERVED ACROSS CALL.
0000 91      : -
0000 92      :
0000 93      : .IF DF  CAS_MEASURE_IOT
0000 94      :
0000 95      : .PSECT  $AEXENUNPAGED
0000 96      PMSS$ABORT_RQ::  :ABORTED I/O REQUEST
0000 97      TSTL  W^PMSS$GL_IOPFMPDB  :DATA COLLECTION ENABLED?
0000 98      BNEQ  5$                    :BR IF YES
0000 99      RSB                    :ELSE RETURN
0000 100     5$:  DSBINT #IPL$ PERFMON  :DISABLE SOFTWARE INTERRUPTS
0000 101     PUSHR #^M<R0,R1,R2,R3>  :SAVE REGISTERS
0000 102     MOVZBL #PIBSK_ARQ_SIZE,R1 :SET SIZE OF MESSAGE BUFFER
0000 103     BSBW  ALLOCATE                :ALLOCATE MESSAGE BUFFER
0000 104     MOVB  #PIBSK_ARQ,PIBSB_TYPE(R0) :INSERT MESSAGE TYPE
0000 105     BRW  COMMON                    :

```

```

001B 107 .SBTTL END OF I/O TRANSACTION
001B 108 :+
001B 109 : PMS$END_IO - END OF I/O TRANSACTION
001B 110 :
001B 111 : THIS ROUTINE IS CALLED AT THE END OF AN I/O TRANSACTION TO ENTER A MESSAGE
001B 112 : IN THE I/O PERFORMANCE DATA COLLECTION BUFFER.
001B 113 :
001B 114 : INPUTS:
001B 115 :
001B 116 : R3 = ADDRESS OF I/O REQUEST PACKET.
001B 117 :
001B 118 : OUTPUTS:
001B 119 :
001B 120 : IF I/O PERFORMANCE DATA COLLECTION IS ENABLED AND THE CURRENT I/O PACKET
001B 121 : CONFORMS TO THE SELECTION CRITERIA, THEN AN END OF I/O TRANSACTION IS
001B 122 : PLACED IN THE CURRENT DATA BUFFER.
001B 123 :
001B 124 : ALL REGISTERS ARE PRESERVED ACROSS CALL.
001B 125 :-
001B 126
001B 127 PMS$END_IO::
0000'CF 05 001B 128 TSTL W*PMS$GL_IOPFMPDB ;END OF I/O TRANSACTION
01 12 001F 129 BNEQ 5$ ;DATA COLLECTION ENABLED?
05 0021 130 RSB ;BR IF YES
0022 131 5$: SAVIPL ;ELSE RETURN
0025 132 PUSHR #*M<R0,R1,R2,R3> ;STORE OLD IPL ON STACK
51 18 9A 0027 133 MOVZBL #PIB$K_EIO_SIZE,R1 ;SAVE REGISTERS
002A 134 SETIPL #IPL$ PERFMON ;SET SIZE OF MESSAGE BUFFER
00B8 30 002D 135 BSBW ALLOCATE ;DISABLE SOFTWARE INTERRUPTS
60 02 90 0030 136 MOVB #PIB$K_EIO,PIB$B_TYPE(R0) ;ALLOCATE MESSAGE BUFFER
10 A0 38 A3 7D 0033 137 MOVQ IRPSL_MEDIA(R3),PIB$Q_EIO_IOSB(R0) ;INSERT MESSAGE TYPE
009F 31 0038 138 BRW COMMON ;INSERT FINAL I/O STATUS
;

```

```

003B 140 .SBTTL END OF I/O REQUEST
003B 141 :+
003B 142 : PMS$END_RQ - END OF I/O REQUEST
003B 143 :
003B 144 : THIS ROUTINE IS CALLED AT THE END OF AN I/O REQUEST TO ENTER A MESSAGE IN THE
003B 145 : I/O PERFORMANCE DATA COLLECTION BUFFER.
003B 146 :
003B 147 : INPUTS:
003B 148 :
003B 149 : R5 = ADDRESS OF I/O REQUEST PACKET.
003B 150 :
003B 151 : ROUTINE IS ASSUMED TO HAVE BEEN CALLED FROM I/O POST AT I/O POST
003B 152 : LEVEL.
003B 153 :
003B 154 : OUTPUTS:
003B 155 :
003B 156 : IF I/O PERFORMANCE DATA COLLECTION IS ENABLED AND THE CURRENT I/O PACKET
003B 157 : CONFORMS TO THE SELECTION CRITERIA, THEN AN END OF I/O REQUEST TRANSACTION
003B 158 : IS PLACED IN THE CURRENT DATA BUFFER.
003B 159 :
003B 160 : IF ANY BUFFERS ARE READY TO PROCESS, THEN THE I/O PERFORMANCE DATA
003B 161 : COLLECTION PROCESS IS AWAKENED.
003B 162 :
003B 163 : ALL REGISTERS ARE PRESERVED ACROSS CALL.
003B 164 :-
003B 165
003B 166 PMS$END_RQ::
003B 167 TSTL W*PMS$GL_IOPFMPDB ;END OF I/O REQUEST
003F 168 BNEQ 5$ ;DATA COLLECTION ENABLED?
0041 169 RSB ;BR IF YES
0042 170 5$: SAVIPL ;ELSE RETURN
0045 171 PUSHR #*M<R0,R1,R2,R3> ;STORE OLD IPL ON STACK
0047 172 MOVL R5,R3 ;SAVE REGISTERS
004A 173 MOVZBL #PIB$K_ERQ_SIZE,R1 ;COPY ADDRESS OF I/O PACKET
004D 174 SETIPL #IPL$_PERFMON ;SET SIZE OF MESSAGE BUFFER
0050 175 BSBW ALLOCATE ;DISABLE SOFTWARE INTERRUPTS
0053 176 MOVB #PIB$K_ERQ,PIB$B_TYPE(R0) ;ALLOCATE MESSAGE BUFFER
0056 177 TSTW PDB$W_BUFcnt(R2) ;INSERT MESSAGE TYPE
0059 178 BEQL 10$ ;ANY FULL BUFFERS?
005B 179 MOVQ R4,-(SP) ;IF EQL NO
005E 180 MOVL PDB$L_PID(R2),R1 ;SAVE REGISTERS
0062 181 BSBW SCH$WAKE ;GET DATA COLLECTION PROCESS IDENTIFICATION
0065 182 MOVQ (SP)+,R4 ;WAKE PROCESS
0068 183 10$: BRB COMMON ;RESTORE REGISTERS

```



```

006A 185 .SBTTL START OF I/O TRANSACTION
006A 186 :+
006A 187 : PMS$START_IO ~ START OF I/O TRANSACTION
006A 188 :
006A 189 : THIS ROUTINE IS CALLED AT THE START OF AN I/O TRANSACTION TO ENTER A MESSAGE
006A 190 : IN THE CURRENT I/O PERFORMANCE DATA COLLECTION BUFFER.
006A 191 :
006A 192 : INPUTS:
006A 193 :
006A 194 : R3 = ADDRESS OF I/O REQUEST PACKET.
006A 195 :
006A 196 : OUTPUTS:
006A 197 :
006A 198 : IF I/O PERFORMANCE DATA COLLECTION IS ENABLED AND THE CURRENT I/O PACKET
006A 199 : CONFORMS TO THE SELECTION CRITERIA, THEN AN START OF I/O TRANSACTION IS
006A 200 : PLACED IN THE CURRENT DATA BUFFER.
006A 201 :
006A 202 : ALL REGISTERS ARE PRESERVED ACROSS CALL.
006A 203 :-
006A 204
006A 205 PMS$START IO::
006A 206 TSTL W^PMS$GL_IOPFMPDB ;START OF I/O TRANSACTION
006E 207 BNEQ S$ ;DATA COLLECTION ENABLED?
0070 208 RSB ;BR IF YES
0071 209 S$: SAVIPL ;ELSE RETURN
0074 210 PUSHR #^M<R0,R1,R2,R3> ;STORE OLD IPL ON STACK
0076 211 MOVZBL #PIB$K_SIO_SIZE,R1 ;SAVE REGISTERS
0079 212 SETIPL #IPL$_PERFMON ;SET SIZE OF MESSAGE BUFFER
007C 213 BSBB ;DISABLE SOFTWARE INTERRUPTS
007E 214 MOVB #PIB$K_SIO,PIB$B_TYPE(R0) ;ALLOCATE MESSAGE BUFFER
0081 215 MOVL IRPSL_BCNT(R3),PIB$S_SIO_BCNT(R0) ;INSERT MESSAGE TYPE
0086 216 MOVL IRPSL_MEDIA(R3),PIB$S_SIO_MEDIA(R0) ;INSERT TRANSFER BYTE COUNT
008B 217 BRB COMMON ;INSERT MEDIA ADDRESS
;

```

```

0000'CF D5
01 12
05 0070
OF BB 0074
51 18 9A 0076
6A 10 007C
60 01 90 007E
14 A0 32 A3 D0 0081
10 A0 38 A3 D0 0086
4D 11 008B

```

```

008D 219      .SBTTL  START OF I/O REQUEST
008D 220      :+
008D 221      : PMS$START_RQ - START OF I/O REQUEST
008D 222      :
008D 223      : THIS ROUTINE IS CALLED AT THE START OF AN I/O REQUEST TO ENTER A MESSAGE IN THE
008D 224      : I/O PERFORMANCE DATA COLLECTION BUFFER.
008D 225      :
008D 226      : INPUTS:
008D 227      :
008D 228      :     R3 = ADDRESS OF I/O REQUEST PACKET.
008D 229      :
008D 230      : OUTPUTS:
008D 231      :
008D 232      : IF I/O PERFORMANCE DATA COLLECTION IS ENABLED AND THE CURRENT I/O PACKET
008D 233      : CONFORMS TO THE SELECTION CRITERIA, THEN AN START OF I/O REQUEST TRANSACTION
008D 234      : IS PLACED IN THE CURRENT DATA BUFFER.
008D 235      :
008D 236      : ALL REGISTERS ARE PRESERVED ACROSS CALL.
008D 237      :-
008D 238
008D 239 PMS$START_RQ::
008D 240      DSBINT #IPL$ PERFMON ;START OF I/O REQUEST
008D 241      MOVL  W*PMS$GL_IOPFMSEQ,IRP$LSB_SEQNUM(R3) ;DISABLE SOFTWARE INTERRUPTS
50 A3 0000'CF D0 0093 241      MOVL  W*PMS$GL_IOPFMSEQ,IRP$LSB_SEQNUM(R3) ;INSERT PACKET SEQUENCE NUMBER
008D 242      INCL  W*PMS$GL_IOPFMSEQ ;INCREMENT I/O TRANSACTION SEQUENCE NUMBER
008D 243      TSTL  W*PMS$GL_IOPFMPDB ;DATA COLLECTION ENABLED?
008D 244      BNEQ  $$ ;BR IF YES
008D 245      ENBINT ;ENABLE INTERRUPTS
008D 246      RSB ;AND RETURN
008D 247      S$: PUSHR #M<R0,R1,R2,R3> ;SAVE REGISTERS
51 20 9A 00A9 248      MOVZBL #PIB$K_SRQ_SIZE,R1 ;SET SIZE OF MESSAGE BUFFER
008D 249      BSBB ALLOCATE ;ALLOCATE MESSAGE BUFFER
60 00 90 00AE 250      MOVVB #PIB$K_SRQ,PIB$B_TYPE(R0) ;INSERT MESSAGE TYPE
008D 251      .ENABLE LSB
52 18 A3 D0 00B1 252      MOVL  IRP$LSB_WIND(R3),R2 ; R2 <= WCB address
008D 253      BGEQ  20$ ; BR if not SVA
008D 254      MOVW  WCB$W_ACON(R2),PIB$W_SRQ_ACON(R0) ; Insert file access
1C A0 0B A2 90 00BC 255      MOVVB WCB$B_ACCESS(R2),PIB$B_SRQ_ACCESS(R0) ; information from WCB
01 A0 23 A3 90 00C1 256      MOVVB IRP$B_PRI(R3),PIB$B_SRQ_PRI(R0) ;INSERT REQUEST PRIORITY
10 A0 0C A3 D0 00C6 257      MOVL  IRP$LSB_PID(R3),PIB$LSB_SRQ_PID(R0) ;INSERT REQUESTER PID
14 A0 1C A3 D0 00CB 258      MOVL  IRP$LSB_UCB(R3),PIB$LSB_SRQ_UCB(R0) ;INSERT DEVICE UCB ADDRESS
18 A0 20 A3 B0 00D0 259      MOVW  IRP$W_FUNC(R3),PIB$W_SRQ_FUNC(R0) ;INSERT I/O FUNCTION CODE
1A A0 2A A3 B0 00D5 260      MOVW  IRP$W_STS(R3),PIB$W_SRQ_STS(R0) ;INSERT PACKET STATUS WORD
008D 261      COMMON: POPR #M<R0,R1,R2,R3> ;RESTORE REGISTERS
008D 262      ENBINT ;ENABLE INTERRUPTS
008D 263      RSB ;
008D 264
008D 265      20$: CLRW  PIB$W_SRQ_ACON(R0) ; Zero ACON and ACCESS fields
1C A0 94 00E3 266      CLRB  PIB$B_SRQ_ACCESS(R0) ; to indicate no WCB available
008D 267      BRB  10$ ; Rejoin mainline code
008D 268      .DISABLE LSB

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```

00E8 270      .SBTTL  ALLOCATE MESSAGE BUFFER
00E8 271      :
00E8 272      : ALLOCATE - ALLOCATE MESSAGE BUFFER
00E8 273      :
00E8 274      : THIS ROUTINE IS CALLED TO:
00E8 275      :
00E8 276      :     1. TEST IF I/O PERFORMANCE MEASUREMENT IS ENABLED,
00E8 277      :     2. TEST IF THE DEVICE CLASS MATCHES,
00E8 278      :     3. TEST IF THE DEVICE TYPE MATCHES,
00E8 279      :     4. TEST IF THE FUNCTION CODE IS TO BE RECORDED, AND
00E8 280      :     5. TEST IF THE STATUS FIELD MATCHES.
00E8 281      :
00E8 282      : IF ALL OF THESE TESTS ARE SUCCESSFUL, THEN AN ATTEMPT IS MADE TO ALLOCATE
00E8 283      : AN I/O PERFORMANCE MEASUREMENT MESSAGE BUFFER.
00E8 284      :
00E8 285      : INPUTS:
00E8 286      :
00E8 287      :     R1 = SIZE OF MESSAGE TO ALLOCATE.
00E8 288      :     R3 = ADDRESS OF I/O REQUEST PACKET.
00E8 289      :
00E8 290      :     REGISTERS R0, R1, R2, AND R3 ARE ASSUMED TO HAVE BEEN SAVED AND ALL
00E8 291      :     SOFTWARE INTERRUPTS ARE DISABLED.
00E8 292      :
00E8 293      : OUTPUTS:
00E8 294      :
00E8 295      : IF I/O PERFORMANCE MEASUREMENT IS NOT ENABLED OR ANY OF THE SELECTION
00E8 296      : CRITERIA FAILS, THEN THE RETURN ADDRESS IS REMOVED FROM THE STACK, THE
00E8 297      : INTERRUPT LEVEL AND REGISTERS ARE RESTORED, AND A RETURN TO THE ORIGINAL
00E8 298      : CALLER IS EXECUTED. ELSE AN ATTEMPT IS MADE TO ALLOCATE AN I/O PERFORM-
00E8 299      : ANCE MEASUREMENT MESSAGE BUFFER.
00E8 300      :
00E8 301      : IF THE ALLOCATION ATTEMPT FAILS, THEN THE RETURN ADDRESS IS REMOVED FROM
00E8 302      : THE STACK, THE INTERRUPT LEVEL AND REGISTERS ARE RESTORED, AND A RETURN
00E8 303      : TO THE ORIGINAL CALLER IS EXECUTED. ELSE THE MESSAGE BUFFER IS ALLOCATED,
00E8 304      : THE TIME AND SEQUENCE NUMBER ARE FILLED IN, AND A RETURN TO THE CALLER
00E8 305      : IS EXECUTED.
00E8 306      :
00E8 307      :
00E8 308      : ALLOCATE:
52 0000'CF  D0 00E8 309      MOVL  W*PMS$GL_IOPFMPDB,R2      ;ALLOCATE MESSAGE BUFFER
                                BEQL  60$              ;GET ADDRESS OF I/O PERFORMANCE DATA BLOCK
50 1C A3    D0 00ED 310      BEQL  60$              ;IF EQL I/O PERFORMANCE MEASUREMENT DISABLED
                                24 A2    95 00EF 311      MOVL  IRP$L_UCB(R3),R0      ;GET ADDRESS OF DEVICE UCB
                                07 19    95 00F3 312      TSTB  PDB$B_DEVCLASS(R2)      ;ALL CLASSES MATCH?
24 A2 40 A0 91 00F6 313      BLSS  10$              ;IF LSS YES
                                5C 12    95 00F8 314      CMPB  UCBSB_DEVCLASS(R0),PDB$B_DEVCLASS(R2) ;DEVICE CLASS MATCH?
                                25 A2 95 00FD 315      BNEQ  60$              ;IF NEQ NO
25 A2 41 A0 91 00FF 316 10$:  TSTB  PDB$B_DEVTYPE(R2)      ;ALL TYPES MATCH?
                                07 19    0102 317      BLSS  20$              ;IF LSS YES
                                50 12    0104 318      CMPB  UCBSB_DEVTYPE(R0),PDB$B_DEVTYPE(R2) ;DEVICE TYPE MATCH?
                                06 00    0109 319      BNEQ  60$              ;IF NEQ NO
50 50 20 A3  E1 010B 320 20$:  EXTZV  #IRP$V_FCODE,#IRP$S_FCODE,- ;GET I/O FUNCTION CODE
                                45 2C A2 50 010E 321      IRP$W_FUNC(R3),R0      ;
50 2A A3 26 A2 AB 0111 322      BBC   RO,PDB$Q_FUNC(R2),60$ ;IF CLR, FUNCTION NOT ENABLED
                                50 28 A2 AC 0116 323      BICW3 PDB$W_ANDM(R2),IRP$W_STS(R3),RO ;CLEAR MISCELLANEOUS BITS
                                39 12    011C 324      XORW  PDB$W_XORM(R2),RO      ;TEST FOR EQUALITY
                                14 A2  D5 0120 325      BNEQ  60$              ;IF NEQ NO MATCH
                                326 30$:  TSTL  PDB$L_CURBUF(R2)      ;ANY BUFFER CURRENTLY AVAILABLE?

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```

50 18 A2 16 13 0125 327 BEQL 40$ ;IF EQL NO
1C A2 51 C1 0127 328 ADDL3 R1,PDB$NXTBUF(R2),R0 ;CALCULATE ADDRESS OF FREE SPACE
1C A2 50 D1 012C 329 CMPL R0,PDB$ENDBUF(R2) ;BEYOND END OF BUFFER?
31 1B 0130 330 BLEQU 70$ ;IF LEQU NO
10 B2 14 B2 0E 0132 331 INSQUE @PDB$CURBUF(R2),@PDB$FILLBL(R2) ;INSERT BUFFER IN FILLED LIST
2A A2 B6 0137 332 INCW PDB$W_BUFcnt(R2) ;INCREMENT FULL BUFFER COUNT
14 A2 14 A2 D4 013A 333 CLRL PDB$CURBUF(R2) ;CLEAR ADDRESS OF CURRENT BUFFER
50 00 B2 0F 013D 334 40$: REMQUE @PDB$FREEFL(R2),R0 ;GET NEXT BUFFER FROM FREE LIST
14 14 1D 0141 335 BVS 50$ ;IF VS NO BUFFER AVAILABLE
18 A2 50 DO 0143 336 MOVL R0,PDB$CURBUF(R2) ;SET ADDRESS OF AVAILABLE BUFFER
0D A0 9E 0147 337 MOVAB PBH$K_START(R0),PDB$NXTBUF(R2) ;SET ADDRESS OF FREE SPACE
7E 08 A0 3C 014C 338 MOVZWL PBH$W_SIZE(R0),-(SP) ;GET LENGTH OF DATA BUFFER
1C A2 9E40 9E 0150 339 MOVAB @(SP)+[R0],PDB$ENDBUF(R2) ;SET ENDING ADDRESS OF BUFFER
0B A2 01 88 0157 341 50$: BRB 30$ ;
8E 01 88 0157 341 50$: BISB #1,PDB$B_OVERRUN(R2) ;SET OVERRUN INDICATOR
OF 8E D5 015B 342 60$: TSTL (SP)+ ;REMOVED RETURN FROM STACK
0F BA 015D 343 60$: POPR #^M<R0,R1,R2,R3> ;RESTORE REGISTERS
05 015F 344 ENBINT ;ENABLE INTERRUPTS
05 0162 345 RSB ;
05 0163 346 ;
05 0163 347 ;
05 0163 348 ; MESSAGE BUFFER SUCCESSFULLY ALLOCATED
05 0163 349 ;
05 0163 350 ;
50 18 A2 DO 0163 351 70$: MOVL PDB$NXTBUF(R2),R0 ;GET ADDRESS OF NEXT SPACE IN BUFFER
18 A2 51 50 C1 0167 352 ADDL3 R0,R1,PDB$NXTBUF(R2) ;CALCULATE ADDRESS OF NEXT AVAILABLE SPACE
51 14 A2 DO 016C 353 MOVL PDB$CURBUF(R2),R1 ;GET ADDRESS OF CURRENT BUFFER
08 A1 B6 0170 354 INCW PBH$W_MSGCNT(R1) ;INCREMENT MESSAGE COUNT
04 A0 0000'CF 7D 0173 355 MOVQ W^EXE$GQ_SYSTIME,PIB$Q_SRQ_TIME(R0) ;INSERT CURRENT TIME
0C A0 50 A3 DO 0179 356 MOVL IRP$SEQNUM(R3),PIB$SEQNUM(R0) ;INSERT SEQUENCE NUMBER
05 017E 357 RSB
05 017F 358
05 017F 359 .ENDC
05 017F 360
05 017F 361 .END

```

IOPERFORM
Symbol table

- I/O PERFORMANCE DATA COLLECTION ¹ ²

16-SEP-1984 00:20:36 VAX/VMS Macro V04-00
5-SEP-1984 03:43:22 [SYS.SRC]IOPERFORM.MAR;1

ALLOCATE	000000E8	R	02	PMSSGL_IOPFMPDB	*****	X	02
COMMON	000000DA	R	02	PMSSGL_IOPFMSEQ	*****	X	02
EXESGQ_SYSTIME	*****	X	02	PMSSSTART_IO	0000006A	RG	02
IPL\$ PERFMON	= 0000000F			PMSSSTART_RQ	0000008D	RG	02
IRPSB_PRI	= 00000023			PRS IPL	*****	X	02
IRPSL_BCNT	= 00000032			SCH\$WAKE	*****	X	02
IRPSL_MEDIA	= 00000038			UCBSB_DEVCLASS	= 00000040		
IRPSL_PID	= 0000000C			UCBSB_DEVTYPE	= 00000041		
IRPSL_SEQNUM	= 00000050			WCBSB_ACCESS	= 0000000B		
IRPSL_UCB	= 0000001C			WCBSW_ACON	= 00000014		
IRPSL_WIND	= 00000018						
IRPSS_FCODE	= 00000006						
IRPSV_FCODE	= 00000000						
IRPSW_FUNC	= 00000020						
IRPSW_STS	= 0000002A						
PBHSK_START	= 0000000D						
PBHSW_MSGCNT	= 0000000B						
PBHSW_SIZE	= 00000008						
PDBSB_DEVCLASS	= 00000024						
PDBSB_DEVTYPE	= 00000025						
PDBSB_OVERRUN	= 0000000B						
PDBSL_CURBUF	= 00000014						
PDBSL_ENDBUF	= 0000001C						
PDBSL_FILLBL	= 00000010						
PDBSL_FREEFL	= 00000000						
PDBSL_NXTBUF	= 00000018						
PDBSL_PID	= 00000020						
PDBSQ_FUNC	= 0000002C						
PDBSW_ANDM	= 00000026						
PDBSW_BUF CNT	= 0000002A						
PDBSW_XORM	= 00000028						
PIBSB_SRQ_ACCESS	= 0000001C						
PIBSB_SRQ_PRI	= 00000001						
PIBSB_TYPE	= 00000000						
PIBSK_ARQ	= 00000004						
PIBSK_ARQ_SIZE	= 00000010						
PIBSK_EIO	= 00000002						
PIBSK_EIO_SIZE	= 00000018						
PIBSK_ERQ	= 00000003						
PIBSK_ERQ_SIZE	= 00000010						
PIBSK_SIO	= 00000001						
PIBSK_SIO_SIZE	= 00000018						
PIBSK_SRQ	= 00000000						
PIBSK_SRQ_SIZE	= 00000020						
PIBSL_SIO_BCNT	= 00000014						
PIBSL_SIO_MEDIA	= 00000010						
PIBSL_SRQ_PID	= 00000010						
PIBSL_SRQ_SEQN	= 0000000C						
PIBSL_SRQ_UCB	= 00000014						
PIBSQ_EIO_IOSB	= 00000010						
PIBSQ_SRQ_TIME	= 00000004						
PIBSW_SRQ_ACON	= 00000002						
PIBSW_SRQ_FUNC	= 00000018						
PIBSW_SRQ_STS	= 0000001A						
PMSSABORT_RQ	00000000	RG	02				
PMSSEND_IO	0000001B	RG	02				
PMSSEND_RQ	0000003B	RG	02				

! Psect synopsis !

PSECT name	Allocation	PSECT No.	Attributes
. ABS .	00000000 (0.)	00 (0.)	NOPIC USR CON ABS LCL NOSHR NOEXE NORD NOWRT NOVEC BYTE
\$ABSS	00000000 (0.)	01 (1.)	NOPIC USR CON ABS LCL NOSHR EXE RD WRT NOVEC BYTE
\$AEXENONPAGED	0000017F (383.)	02 (2.)	NOPIC USR CON REL LCL NOSHR EXE RD WRT NOVEC BYTE

! Performance indicators !

Phase	Page faults	CPU Time	Elapsed Time
Initialization	35	00:00:00.10	00:00:01.16
Command processing	131	00:00:00.51	00:00:02.77
Pass 1	253	00:00:06.89	00:00:22.63
Symbol table sort	0	00:00:01.01	00:00:02.57
Pass 2	84	00:00:01.43	00:00:04.61
Symbol table output	9	00:00:00.08	00:00:00.30
Psect synopsis output	2	00:00:00.02	00:00:00.02
Cross-reference output	0	00:00:00.00	00:00:00.00
Assembler run totals	516	00:00:10.06	00:00:34.10

The working set limit was 1350 pages.
38553 bytes (76 pages) of virtual memory were used to buffer the intermediate code.
There were 40 pages of symbol table space allocated to hold 672 non-local and 15 local symbols.
361 source lines were read in Pass 1, producing 14 object records in Pass 2.
19 pages of virtual memory were used to define 18 macros.

! Macro library statistics !

Macro library name	Macros defined
_\$255\$DUA28:[SYS.OBJ]LIB.MLB;1	12
-\$255\$DUA28:[SYSLIB]STARLET.MLB;2	3
TOTALS (all libraries)	15

776 GETS were required to define 15 macros.

There were no errors, warnings or information messages.

MACRO/LIS=LIS\$:IOPERFORM/OBJ=OBJ\$:IOPERFORM MSRC\$:IOPERFORM/UPDATE=(ENHS:IOPERFORM)+EXECMLS/LIB

This image displays a grid of 100 small document thumbnails, arranged in 10 rows and 10 columns. Each thumbnail represents a page from a technical manual or software documentation. The pages contain various types of content, including:

- Text-based instructions and code snippets.
- Tables with multiple columns and rows of data.
- Diagrams and flowcharts.
- Section headers and titles.

Several thumbnails are clearly legible and contain the following text:

- IOSUBRAMS LIS** (Row 2, Column 5)
- IPIPCONTROL LIS** (Row 3, Column 6)
- IOSUBPAGD LIS** (Row 5, Column 4)
- LNMSUB LIS** (Row 7, Column 7)
- LINKVEC LIS** (Row 8, Column 6)
- IOPERFORM LIS** (Row 9, Column 1)
- IOSUBNPAG LIS** (Row 9, Column 2)

The thumbnails are arranged in a regular grid pattern, with each page showing a different view of the documentation. The overall appearance is that of a comprehensive technical manual or software reference guide.