```
RRRRRRRRRRRR     UUU         UUU  NNN         NNN     000000000     FFFFFFFFFFFFFFF  FFFFFFFFFFFFFFF
RRRRRRRRRRRR     UUU         UUU  NNN         NNN     000000000     FFFFFFFFFFFFFFF  FFFFFFFFFFFFFFF
RRRRRRRRRRRR     UUU         UUU  NNN         NNN     000000000     FFFFFFFFFFFFFFF  FFFFFFFFFFFFFFF
RRR         RRR  UUU         UUU  NNN         NNN  000         000  FFF              FFF
RRR         RRR  UUU         UUU  NNN         NNN  000         000  FFF              FFF
RRR         RRR  UUU         UUU  NNN         NNN  000         000  FFF              FFF
RRR         RRR  UUU         UUU  NNNNNN      NNN  000         000  FFF              FFF
RRR         RRR  UUU         UUU  NNNNNN      NNN  000         000  FFF              FFF
RRR         RRR  UUU         UUU  NNNNNN      NNN  000         000  FFF              FFF
RRRRRRRRRRRR     UUU         UUU  NNN   NNN   NNN  000         000  FFFFFFFFFFFF     FFFFFFFFFFFF
RRRRRRRRRRRR     UUU         UUU  NNN   NNN   NNN  000         000  FFFFFFFFFFFF     FFFFFFFFFFFF
RRRRRRRRRRRR     UUU         UUU  NNN   NNN   NNN  000         000  FFFFFFFFFFFF     FFFFFFFFFFFF
RRR   RRR        UUU         UUU  NNN      NNNNNN  000         000  FFF              FFF
RRR   RRR        UUU         UUU  NNN      NNNNNN  000         000  FFF              FFF
RRR   RRR        UUU         UUU  NNN      NNNNNN  000         000  FFF              FFF
RRR      RRR     UUU         UUU  NNN         NNN  000         000  FFF              FFF
RRR      RRR     UUU         UUU  NNN         NNN  000         000  FFF              FFF
RRR      RRR     UUU         UUU  NNN         NNN  000         000  FFF              FFF
RRR         RRR  UUUUUUUUUUUUUUU  NNN         NNN     000000000     FFF              FFF
RRR         RRR  UUUUUUUUUUUUUUU  NNN         NNN     000000000     FFF              FFF
RRR         RRR  UUUUUUUUUUUUUUU  NNN         NNN     000000000     FFF              FFF
```

```
VV      VV  RRRRRRRR
VV      VV  RRRRRRRR
VV      VV  RR      RR
VV      VV  RR      RR
VV      VV  RR      RR
VV      VV  RR      RR
VV      VV  RRRRRRRR
VV      VV  RRRRRRRR
VV      VV  RR  RR
VV      VV  RR  RR
  VV  VV    RR    RR        ....
  VV  VV    RR    RR        ....
    VV      RR      RR      ....
    VV      RR      RR      ....

RRRRRRRR    EEEEEEEEEE    QQQQQQ
RRRRRRRR    EEEEEEEEEE    QQQQQQ
RR      RR  EE          QQ      QQ
RR      RR  EE          QQ      QQ
RR      RR  EE          QQ      QQ
RR      RR  EE          QQ      QQ
RRRRRRRR    EEEEEEEE    QQ      QQ
RRRRRRRR    EEEEEEEE    QQ      QQ
RR  RR      EE          QQ  QQ  QQ
RR  RR      EE          QQ  QQ  QQ
RR    RR    EE          QQ    QQ
RR    RR    EE          QQ    QQ
RR      RR  EEEEEEEEEE    QQQQ  QQ
RR      RR  EEEEEEEEEE    QQQQ  QQ
```

```
 Version:      'V04-000'
```


```
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*****************************************************************************
```
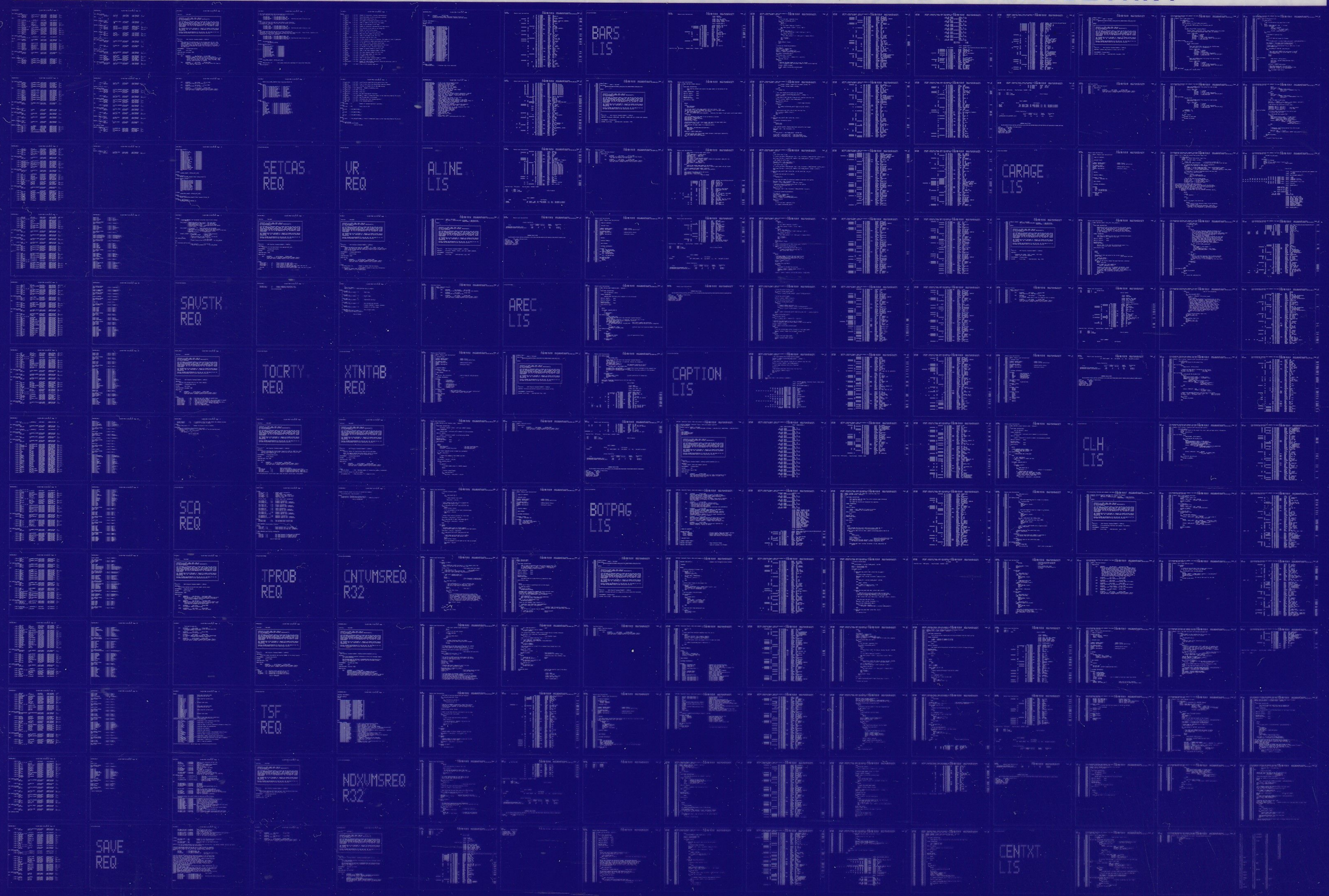


```
!++
! FACILITY:      DSR (Digital Standard RUNOFF) / DSRPLUS

! ABSTRACT:
!       Tables and definitions used for .VARIABLE, .IF, .IFNOT, .ELSE, and
!       .ENDIF. The tables all contain space for an extra entry, so that various
!       other pieces of code need not continually check to see if there is an
!       entry in the tables.

! ENVIRONMENT:   Transportable BLISS

! AUTHOR:        Rich Friday

! CREATION DATE: 1978

! MODIFIED BY:

!       002     KAD00002        Keith Dawson    07-Mar-1983
!               Global edit of all modules. Updated module names, idents,
!               copyright dates. Changed require files to BLISS library.

!--

LITERAL
    vr_table_count = 5,         !Number of tables that are vectors.

    vr_tab_size =               !Total BPVALs needed to save this information.
        !Space for tables that are vectors.
        (max_vr_names + 1) * (vr_table_count)
```

```
        +
        !Space for names,
        (max_vr_names + 1) * CH$ALLOCATION (max_vr_length);

MACRO
    vrtflg_def  =
        VECTOR [max_vr_names + 1]       ! TRUE flag.
    %,

    vrfflg_def  =
        VECTOR [max_vr_names + 1]       ! FALSE flags.
    %,

    vrbool_def  =
        VECTOR [max_vr_names + 1]       ! TRUE/FALSE setting.
    %,

    vrsrc_def   =
        VECTOR [max_vr_names + 1]       ! How variable got defined.
    %,
                                        ! 0 means /VARIANT, 1 means .VARIABLE.
    vrlng_def   =
        VECTOR [max_vr_names + 1]       ! Lengths of variable names.
    %,

    vrname_def  =                       ! The variable names.
        BLOCKVECTOR [max_vr_names + 1
         ,ch$allocation (max_vr_length)]
    %;

!                       End of VR.REQ
```

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