

MP.MDL - Multi-processing block definitions

Version 'V04-000'

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FACILITY: Executive , Multi-processing definition macros

ABSTRACT:

This file contains the MDL source for all multi-processing block structure definitions.

ENVIRONMENT:

n/a

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AUTHOR: Kathleen D. Morse, CREATION DATE: 26-Feb-1981

MODIFIED BY:

V03-003 KDM0018 Kathleen D. Morse 13-Oct-1982
 Added secondary request flag to check an event flag wait condition.

V03-002 KDM0012 Kathleen D. Morse 20-Sep-1982
 Add second error log buffer flag.

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MPSSGL_BUGCHECK bit definitions

This longword contains indicators for handling bugcheck.

```
$STRUCT BUG
V<          BUGCHK,,,M      : Bits in MPSSGL_BUGCHECK
          ACK1,,,M         : Set by primary to request a bugcheck
          >                : Set by secondary when acknowleging a bugcheck
E
```

MPSSGL_INTERLOCK bit definitions

This longword is accessed by interlocked instructions to flush the cache so that non-interlocked accesses to other pieces of data get the correct values.

```
$STRUCT LCK
V<      INTERLOCK,,,M      ; Interlock bit
>
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```

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: *
: MPSSGL_STATE value definitions
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: This longword contains the state of the secondary processor.
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$STRUCT MPS

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```

K<,$K_      ;Secondary states
             ; Idle
             ; Dropping current process; CURPCB valid
             ; Busy; CURPCB valid but LDPCTX not
             ; yet done
             ; Executing process; CURPCB valid and
             ; LDPCTX done
             ; Initialization uncomplete
             ; Processor stopped

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V<M         ;Secondary request flags
             ; Bugcheck requested by secondary
             ; Error log requested by secondary
             ; Event flag wait check requested by sec

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V<M         ;Bits in MPSSGL_STOPFLAG (for STOP/CPU)
             ; Primary request flag
             ; Secondary acknowledgement flag

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V<M         ;Error log buffer flags
             ; Buffer 1 busy
             ; Buffer 2 busy

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K<,$K_      ;Error log buffer information
             ; Size of error log buffer in bytes

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MPSSGL_STATE bit definitions

This longword records the state of the secondary processor.

```
$STRUCT STA
V<
    INIT...M      ; Initialization
    IDLE...M      ; Idle
    DROP...M      ; Dropping current process
    BUSY...M      ; Busy executing a process
    STOP...M      ; Stopped
>
E
```

