


```

LL          AAAAAA  DDDDDDDD  MM      MM  DDDDDDDD  TTTTTTTTTT
LL          AAAAAA  DDDDDDDD  MM      MM  DDDDDDDD  TTTTTTTTTT
LL          AA      AA  DD      DD  :MMM  MMMM  DD      DD  TT
LL          AA      AA  DD      DD  MMMM  MMMM  DD      DD  TT
LL          AA      AA  DD      DD  MM   MM   MM   DD      DD  TT
LL          AA      AA  DD      DD  MM   MM   MM   DD      DD  TT
LL          AA      AA  DD      DD  MM   MM   MM   DD      DD  TT
LL          AA      AA  DD      DD  MM   MM   MM   DD      DD  TT
LL          AAAAAAAAAA  DD      DD  MM   MM   MM   DD      DD  TT
LL          AAAAAAAAAA  DD      DD  MM   MM   MM   DD      DD  TT
LL          AA      AA  DD      DD  MM   MM   MM   DD      DD  TT
LL          AA      AA  DD      DD  MM   MM   MM   DD      DD  TT
LLLLLLLLLLL AA      AA  DDDDDDDD  MM      MM  DDDDDDDD  TT
LLLLLLLLLLL AA      AA  DDDDDDDD  MM      MM  DDDDDDDD  TT

```

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....
....
....
....

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```

LL          IIIIII  SSS:SSSS
LL          IIIIII  SSSSSSSS
LL          II      SS
LL          II      SS
LL          II      SS
LL          II      SS
LL          II      SSSSSS
LL          II      SSSSSS
LL          II      SS
LL          II      SS
LL          II      SS
LL          II      SS
LLLLLLLLLLL IIIIII  SSSSSSSS
LLLLLLLLLLL IIIIII  SSSSSSSS

```

```

0000 1 .TITLE LADMDT - LPA-11 DEDICATED MODE DISPATCH TABLE
0000 2 .IDENT 'V04-000'
0000 3
0000 4
0000 5 *****
0000 6
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0000 24
0000 25 *****
0000 26
0000 27
0000 28
0000 29 CHARLES A. SAMUELSON
0000 30 FEBRUARY 4, 1977
0000 31
0000 32 MODIFIED BY:
0000 33 V02-004 SRB0003 Steve Beckhardt 3-Sep-1980
0000 34 Changed value of SEX parameter from ^0120 to ^020.
0000 35
0000 36 +
0000 37 DMDT -- DEDICATED MODE DISPATCH TABLE
0000 38 TABLE FOR LPA11 MICRO PROCESSOR DEDICATED MODE SAMPLING
0000 39 -
0000 40
00000000 41 .PSECT _LPA$CODE,NOWRT,WORD
0000 42
0000 43 ; DEFINED VALUES
0000 44
00000012 0000 45 DMDSIZ=18. ;LENGTH OF DMDT BUFFER IN BYTES
00000040 0000 46 SDT=^0100 ;SLAVE DISPATCH TABLE START ADDRESS
00000000 0000 47 CAINC=0 ;CHANNEL ADDRESS INCREMENT VALUE
00000000 0000 48 AD1SRL=0 ;ADC #1 STATUS REGISTER ADDRESS LOW BYTE
00000010 0000 49 SEX=^020 ;SELECT EXTERNAL CLOCK START
00000010 0000 50 RONPR=^035 ;REQUEST OUTPUT NPR IN MICRO-PROCESSOR
00000000 0000 51 CLR=0 ;CLEAR AD STATUS REGISTER
00000090 0000 52 RONPRL=^0235 ;REQUEST OUTPUT NPR LOW BYTE IN MICRO-PROCESSOR
00000002 0000 53 AD1DRL=2 ;ADC #1 DATA REGISTER ADDRESS LOW BYTE
00000000 0000 54 RINPR=^015 ;REQUEST INPUT NPR
00000020 0000 55 SCS=^040 ;SELECT CLOCK OVERFLOW START FOR ADC'S
00000020 0000 56 AD2SRL=^040 ;ADC #2 STATUS REGISTER ADDRESS LOW BYTE
00000022 0000 57 AD2DRL=^042 ;ADC #2 DATA REGISTER ADDRESS LOW BYTE

```


LADMDT
V04-000

- LPA-11 DEDICATED MODE DISPATCH TABLE

D 10

16-SEP-1984 01:56:26
5-SEP-1984 01:53:27

VAX/VMS Macro V04-00
[MCLDR.SRC]LADMDT.MAR;1

Page 3
(1)

0100 115 .END

LAL
Pse

PSE

LPA
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SRM
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Mac

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The

MAC

```

AD1DRL      = 00000002
AD1SRH      = 00000001
AD1SRL      = 00000000
AD2DRL      = 00000022
AD2SRH      = 00000021
AD2SRL      = 00000020
CAINC       = 00000000
CLR         = 00000000
D.OCQ       00000039 RG    01
D.OCS       00000026 RG    01
D.OEQ       00000013 RG    01
D.OES       00000000 RG    01
D.TCQ       00000085 RG    01
D.TCQP      000000D1 RG    01
D.TCS       00000072 RG    01
D.TCSP      000000BE RG    01
D.TEQ       0000005F RG    01
D.TEQP      000000AB RG    01
D.TES       0000004C RG    01
D.TESP      00000098 RG    01
DMDSIZ      = 00000012
LPASSDMDT   00000000 RG    01
RINPR       = 00000000
RONPR       = 00000010
RONPRL      = 00000090
SCS         = 00000020
SDT         = 00000040
SEN         = 00000010
SEX         = 00000010
    
```

! Psect synopsis !

PSECT name	Allocation	PSECT No.	Attributes
ABS	00000000 (0.)	00 (0.)	NOPIC USR CON ABS LCL NOSHR NOEXE NORD NOWRT NOVEC BYTE
_LPASCODE	00000100 (256.)	01 (1.)	NOFIC USR CON REL LCL NOSHR EXE RD NOWRT NOVEC WORD

! Performance indicators !

Phase	Page faults	CPU Time	Elapsed Time
Initialization	29	00:00:00.07	00:00:00.26
Command processing	100	00:00:00.43	00:00:01.44
Pass 1	67	00:00:00.60	00:00:01.25
Symbol table sort	0	00:00:00.01	00:00:00.01
Pass 2	38	00:00:00.28	00:00:01.45
Symbol table output	4	00:00:00.04	00:00:00.04
Psect synopsis output	1	00:00:00.02	00:00:00.02
Cross-reference output	0	00:00:00.00	00:00:00.00
Assembler run totals	242	00:00:01.46	00:00:04.48

The working set limit was 900 pages.
2394 bytes (5 pages) of virtual memory were used to buffer the intermediate code.

There were 10 pages of symool table space allocated to hold 29 non-local and 0 local symbols.
115 source lines were read in Pass 1, producing 11 object records in Pass 2.
0 pages of virtual memory were used to define 0 macros.

↑-----↑
! Macro library statistics !
↑-----↑

Macro library name	Macros defined
-----	-----
_\$255\$DUA28:[SYSLIB]STARLET.MLB;2	0

0 GETS were required to define 0 macros.

There were no errors, warnings or information messages.

MACRO/LIS=LIS\$:LADMDT/OBJ=OBJ\$:LADMDT MSRCS:LADMDT/UPDATE=(ENHS:LADMDT)

This image displays a large grid of technical diagrams and code snippets, likely from a manual or reference document. The diagrams are arranged in a regular grid pattern across the page. Each diagram typically consists of a header or label, followed by a series of lines of code or data, and often includes a small graphical element such as a bar chart or a flow diagram. The labels for the diagrams include:

- MELDR
- LALOAD MAP
- XFLORDER MAP
- LADAMCODE LIS
- LALOAD LIS
- LAMRMCODE LIS
- MARBLI
- MARBLI MAP
- LALOADER MAP
- LADAMCODE LIS
- MARBLI LIS
- LADMT LIS
- MDL32
- MDL32 MAP
- XFLORDER LIS
- LALOADER LIS

The diagrams are presented in a monospaced font, characteristic of early computer systems. The overall layout is highly structured and repetitive, suggesting a comprehensive reference or a detailed technical specification.