



```

LL      IIIIII  BBBB8888  EEEEEEEEE  MM      MM      000000  DDDDDDDDD  GGGGGGGG
LL      IIIIII  BBBB8888  EEEEEEEEE  MM      MM      000000  DDDDDDDDD  GGGGGGGG
LL      II      BB      BB  EE      EE      MMMM  MMMM  00      00  DD      DD  GG
LL      II      BB      BB  EE      EE      MMMM  MMMM  00      00  DD      DD  GG
LL      II      BB      BB  EE      EE      MM  MM  MM  00      00  DD      DD  GG
LL      II      BB      BB  EE      EE      MM  MM  MM  00      00  DD      DD  GG
LL      II      BBBB8888  EEEEEEEEE  MM      MM      00      00  DD      DD  GG
LL      II      BBBB8888  EEEEEEEEE  MM      MM      00      00  DD      DD  GG
LL      II      BB      BB  EE      EE      MM      MM  00      00  DD      DD  GG
LL      II      BB      BB  EE      EE      MM      MM  00      00  DD      DD  GG
LL      II      BB      BB  EE      EE      MM      MM  00      00  DD      DD  GG
LL      II      BB      BB  EE      EE      MM      MM  00      00  DD      DD  GG
LL      II      BB      BB  EE      EE      MM      MM  00      00  DD      DD  GG
LL      II      BB      BB  EE      EE      MM      MM  00      00  DD      DD  GG
LLLLLLLLLLLL  IIIIII  BBBB8888  EEEEEEEEE  MM      MM      000000  DDDDDDDDD  GGGGGG
LLLLLLLLLLLL  IIIIII  BBBB8888  EEEEEEEEE  MM      MM      000000  DDDDDDDDD  GGGGGG

```

```

LL      IIIIII  SSSSSSSS
LL      IIIIII  SSSSSSSS
LL      II      SS
LL      II      SS
LL      II      SS
LL      II      SS
LL      II      SSSSSS
LL      II      SSSSSS
LL      II      SS
LL      II      SS
LL      II      SS
LL      II      SS
LLLLLLLLLLLL  IIIIII  SSSSSSSS
LLLLLLLLLLLL  IIIIII  SSSSSSSS

```

(2)	45	Edit History
(3)	52	DECLARATIONS
(4)	93	LIBSEMODG - Extended multiply and integerize

```
0000 1 .TITLE LIBSEMODG - Extended multiply and integerize G
0000 2 .IDENT /1-003/ ; File: LIBEMODG.MAR Edit: SBL1003
0000 3
0000 4
0000 5 *****
0000 6 *
0000 7 * COPYRIGHT (c) 1978, 1980, 1982, 1984 BY
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0000 24 *
0000 25 *
0000 26 *****
0000 27
0000 28
0000 29 :++
0000 30 : FACILITY: General Utility Library
0000 31
0000 32 : ABSTRACT:
0000 33
0000 34 : Extend precision of multiplier, multiply by multiplicand
0000 35 : and extract integer and fractional portion of result.
0000 36
0000 37 : ENVIRONMENT: User Mode, AST Reentrant
0000 38
0000 39 :--
0000 40 : AUTHOR: Steven B. Lionel, CREATION DATE: 05-Feb-79
0000 41
0000 42 : MODIFIED BY:
0000 43 :
```

```
0000 45 .SBTTL Edit History
0000 46 : 1-001 - Original. SBL 05-Feb-79
0000 47 : 1-002 - Fix comments. SBL 31-July-1979
0000 48 : 1-003 - use local handler to resignal exceptions other than those documented
0000 49 : as being returned as statuses. SBL 25-Sept-1980
0000 50 :
```

LIE  
Sys  
CHI  
CHI  
CHI  
CHI  
FRA  
HAM  
INI  
LIE  
LIE  
MUL  
MUL  
MUL  
MUL  
SSA  
SSA  
SSA  
SSA  
SSA

PSE  
--  
SAE  
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Phi  
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In  
Con  
Pas  
Sys  
Pas  
Sys  
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As

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21  
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19  
9

```

0000 52      .SBTTL  DECLARATIONS
0000 53      :
0000 54      : INCLUDE FILES:
0000 55      :
0000 56      $SCHDEF      ; Condition handling macros
0000 57      $SSDEF       ; System symbol definitions
0000 58      :
0000 59      : EXTERNAL SYMBOLS:
0000 60      :
0000 61      :
0000 62      .EXTRN  LIB$SIG_TO_RET      ; Library routine to convert a signal
0000 63      :                          ; to error return to caller
0000 64      :                          ; of LIB$MODG.
0000 65      :                          ; R0 = signaled condition
0000 66      :
0000 67      :
0000 68      : MACROS:
0000 69      :
0000 70      :
0000 71      :
0000 72      :
0000 73      : EQUATED SYMBOLS:
0000 74      :
0000 75      :
00000004 0000 76      mulr = 4           ; multiplier
00000008 0000 77      mulrx = 8          ; multiplier extension
0000000C 0000 78      muld = 12         ; multiplicand
00000010 0000 79      int = 16          ; integer portion returned
00000014 0000 80      fract = 20        ; fractional portion returned
0000 81      :
0000 82      :
0000 83      : OWN STORAGE:
0000 84      :
0000 85      :
0000 86      :
0000 87      : PSECT DECLARATIONS:
0000 88      :
00000000 89      .PSECT _LIB$CODE      PIC, USR, CON, REL, LCL, SHR, -
0000 90      EXE, RD, NOWRT, LONG
0000 91

```

```
0000 93 .SBTTL LIB$EMODG - Extended multiply and integerize
0000 94 :++
0000 95 : FUNCTIONAL DESCRIPTION:
0000 96 :
0000 97 : LIB$EMODG provides the functionality of the VAX hardware
0000 98 : instruction EMODG to high-level language users.
0000 99 :
0000 100 : The floating point multiplier extension operand (second operand)
0000 101 : is concatenated with the floating point multiplier (first
0000 102 : operand) to gain 11 additional low order fraction bits.
0000 103 : The multiplicand operand is multiplied by the extended
0000 104 : multiplier operand. After multiplication, the integer
0000 105 : portion is extracted and a 64 bit floating point number is
0000 106 : formed from the fractional part of the product by truncating
0000 107 : extra bits. The multiplication is such that the result is
0000 108 : equivalent to the exact product truncated to a fraction
0000 109 : field of 64 bits. Regarding the result as the sum of an
0000 110 : integer and fraction of the same sign, the integer operand
0000 111 : is replaced by the integer part of the result and the
0000 112 : fraction operand is replaced by the rounded fractional
0000 113 : part of the result.
0000 114 :
0000 115 : CALLING SEQUENCE:
0000 116 :
0000 117 : status.wlc.v = LIB$EMODG (mulr.rg.r, mulrx.rw.r, muld.rg.r,
0000 118 : int.wl.r, fract.wg.r)
0000 119 :
0000 120 : INPUT PARAMETERS:
0000 121 :
0000 122 : mulr.rg.r - floating point multiplier
0000 123 : mulrx.rw.r - word to be appended to multiplier fraction
0000 124 : muld.rg.r - floating point multiplicand
0000 125 :
0000 126 : IMPLICIT INPUTS:
0000 127 :
0000 128 : NONE
0000 129 :
0000 130 : OUTPUT PARAMETERS:
0000 131 :
0000 132 : int.wl.r - integer portion of result
0000 133 : fract.wg.r - fractional portion of result
0000 134 :
0000 135 : IMPLICIT OUTPUTS:
0000 136 :
0000 137 : NONE
0000 138 :
0000 139 : FUNCTION VALUE:
0000 140 :
0000 141 : $$$_NORMAL - successful execution
0000 142 : $$$_INTOVF - integer overflow or floating overflow
0000 143 : $$$_FLTUND - floating underflow
0000 144 : $$$_ROPRAND - reserved operand
0000 145 :
0000 146 : SIDE EFFECTS:
0000 147 :
0000 148 : All other exceptions are signalled.
0000 149 :
```

```

0000 150 ;--
0000 151
4000 0000 152 .ENTRY LIB$EMODG, ^M<IV> ; Entry point
0002 153
6D 16'AF 9E 0002 154 MOVAB B^HANDLER, (FP) ; Enable local handler to
0006 155 ; process exceptions
0006 156
10 BC 0C BC 08 BC 04 BC 54FD 0006 157 EMODG @mulr(AP), - ; perform multiplication
14 BC 0010 ;
0012 158 @mulrx(AP), - ; trap on exception to
0012 159 @muld(AP), - ; handler which will
0012 160 @int(AP), - ; unwind a return error
0012 161 @fract(AP) ; condition in R0 to
0012 162 ; caller of LIB$EMODG.
50 01 9A 0012 163
0012 164 MOVZBL #1, R0 ; success status code
0015 165
04 0015 166 RET ; return
0016 167
0000 0016 168 HANDLER:
0018 169 .WORD 0
0018 170
0018 171 ;+
0018 172 ; If the exception is one of the documented exceptions for this routine,
0018 173 ; call LIB$$SIG_TO_RET to return it as a status. Otherwise, resignal.
0018 174 ; Also, resignal if the depth is not zero.
0018 175 ;-
0018 176
50 08 AC D0 0018 177 MOVL CHF$MCHARGLIST(AP), R0 ; Get mechanism vector address
08 A0 D5 001C 178 TSTL CHF$MCH_DEPTH(R0) ; Is depth zero?
32 12 001F 179 BNEQ 90$ ; If not, resignal
51 04 AC D0 U021 180 MOVL CHF$SIGARGLIST(AP), R1 ; Get signal vector address
50 04 A1 D0 0025 181 MOVL CHF$SIG_NAME(R1), R0 ; Get signalled condition
047C 8F 50 B1 0029 182 CMPW R0, #SS$_INTOVF ; Compare conditions
18 13 002E 183 BEQL 10$ ; If it matches, don't resignal
049C 8F 50 B1 0030 184 CMPW R0, #SS$_FLTUND
14 13 0035 185 BEQL 10$
0454 8F 50 B1 0037 186 CMPW R0, #SS$_ROPRAND
0D 13 003C 187 BEQL 10$
04C4 8F 50 B1 003E 188 CMPW R0, #SS$_FLTUND_F
0E 12 0043 189 BNEQ 90$
04 A1 049C 8F 3C 0045 190 MOVZWL #SS$_FLTUND, CHF$SIG_NAME(R1) ; Change fault code to trap code
00000000'GF 6C FA 004B 191 10$: CALLG (AP), G^LIB$$SIG_TO_RET ; Return signal as a status
04 0052 192 RET
50 0918 8F 3C 0053 193 90$: MOVZWL #SS$_RESIGNAL, R0 ; Resignal condition
04 0058 194 RET
0059 195
0059 196 .END

```

```

CHFSL_MCHARGLST = 00000008
CHFSL_MCH_DEPTH = 00000008
CHFSL_SIGARGLST = 00000004
CHFSL_SIG_NAME  = 00000004
FRACT           = 00000014
HANDLER         = 00000016 R    02
INT             = 00000010
LIB$EMODG       = 00000000 RG   02
LIB$SIG_TO_RET  = ***** X   00
MULD            = 0000000C
MULR            = 00000004
MULRX           = 00000008
SS$FLTUND      = 0000049C
SS$FLTUND_F    = 000004C4
SS$INTOVF      = 0000047C
SS$RESIGNAL    = 00000918
SS$ROPRAND     = 00000454
    
```

-----  
 ! Psect synopsis !  
 -----

PSECT name	Allocation	PSECT No.	Attributes
. ABS .	00000000 ( 0.)	00 ( 0.)	NOPIC USR CON ABS LCL NOSHR NOEXE NORD NOWRT NOVEC BYTE
\$AB\$\$	000C0000 ( 0.)	01 ( 1.)	NOPIC USR CON ABS LCL NOSHR EXE RD WRT NOVEC BYTE
_LIB\$CODE	00000059 ( 89.)	02 ( 2.)	PIC USR CON REL LCL SHR EXE RD NOWRT NOVEC LONG

-----  
 ! Performance indicators !  
 -----

Phase	Page faults	CPU Time	Elapsed Time
Initialization	29	00:00:00.02	00:00:00.75
Command processing	107	00:00:00.32	00:00:02.27
Pass 1	192	00:00:02.70	00:00:10.69
Symbol table sort	0	00:00:00.41	00:00:01.51
Pass 2	52	00:00:00.58	00:00:02.49
Symbol table output	4	00:00:00.02	00:00:00.02
Psect synopsis output	2	00:00:00.01	00:00:00.02
Cross-reference output	0	00:00:00.00	00:00:00.00
Assembler run totals	388	00:00:04.07	00:00:17.75

The working set limit was 1050 pages.  
 21526 bytes (43 pages) of virtual memory were used to buffer the intermediate code.  
 There were 30 pages of symbol table space allocated to hold 428 non-local and 2 local symbols.  
 196 source lines were read in Pass 1, producing 13 object records in Pass 2.  
 9 pages of virtual memory were used to define 8 macros.

-----  
! Macro library statistics !  
-----

Macro library name

Macros defined

-----  
\_ \$255\$DUA28:[SYSLIB]STARLET.MLB;2

-----  
5

486 GETS were required to define 5 macros.

There were no errors, warnings or information messages.

MACRO/ENABLE=SUPPRESSION/DISABLE=(GLOBAL,TRACEBACK)/LIS=LIS\$:LIBEMODG/OBJ=OBJ\$:LIBEMODG MSRC\$:LIBEMODG/UPDATE=(ENH\$:LIBEMODG)

