


```

0095 character*18 v1mcesr(0:0,0:1)
0096
0097 character*27 v2mcesr(1:3)
0098
0099 character*17 v1caer(0:3)
0100
0101 CHARACTER*21 V1CMIER(0:3)
0102
0103 CHARACTER*7 V2CMIER(4:4)
0104
0105 character*17 v3cmier(8:11)
0106
0107 CHARACTER*18 V4CMIER(12:12)
0108
0109 CHARACTER*9 V5CMIER(18:19,0:1)
0110
0111 CHARACTER*12 V6CMIER(20:20)
0112
0113
0114
0115
0116
0117 data v1mcesr(0,0) /'OPERAND REFERENCE*'/
0118
0119 data v1mcesr(0,1) /'XB REFERENCE*'/
0120
0121
0122
0123
0124
0125
0126 data v2mcesr(1) /'UNALIGNED UNIBUS REFERENCE*'/
0127
0128 data v2mcesr(2) /'TB PARITY ERROR*'/
0129
0130 data v2mcesr(3) /'BUS ERROR*'/
0131
0132
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0134
0135 data v1caer(0) /'CACHE HIT*'/
0136
0137 data v1caer(1) /'2ND ERROR*'/
0138
0139 data v1caer(2) /'DATA PARITY ERROR*'/
0140
0141 data v1caer(3) /'TAG PARITY ERROR*'/
0142
0143
0144
0145
0146 DATA V1CMIER(0) /'CORRECTED READ DATA*'/
0147
0148 DATA V1CMIER(1) /'LOST ERROR*'/
0149
0150 DATA V1CMIER(2) /'READ DATA SUBSTITUTE*'/
0151

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0266      write(lun,25) 'DIAGNOSTIC MODE'
0267      format(' ',t40,a)
0268      endif
0269
0270      RETURN

```

```

0271
0272
0273
0274
0275      ENTRY COMET_MCESR (LUN,REGISTER)
0276
0277

```

```

0278
0279      CALL LINCHK (LUN,1)
0280
0281      WRITE(LUN,30) REGISTER
0282      FORMAT(' ',T8,'MCESR',T24,Z8.8)
0283
0284      call output (lun,register,v1mcesr,0,0,0,'2')
0285      call output (lun,register,v2mcesr,1,1,3,'0')
0286
0287      RETURN
0288
0289
0290
0291

```

```

0292
0293      ENTRY COMET_CAER (LUN,REGISTER)
0294
0295
0296

```

```

0297      CALL LINCHK (LUN,1)
0298
0299      WRITE(LUN,35) REGISTER
0300      FORMAT(' ',T8,'CAER',T24,Z8.8)
0301
0302      CALL OUTPUT (LUN,REGISTER,V1CAER,0,0,3,'0')
0303
0304      RETURN
0305

```

```

0306      C
0307      C
0308      C
0309      C
0310      C
0311      C
0312      C
0313      C
0314      C
0315      C

```

```

THE CMIER REGISTER IS USED BY SEVERAL MODULES.THE
MICRO-CODE WITHIN COMET SPLITS THE REGISTER INTO
INDIVIDUAL 4 BIT SLICES AND PUSHES THAT RIGHT
JUSTIFIED ONTO THE STACK WHEN A MACHINE CHECK
OCCURS. ANY MODULE CALLING CMIER_XXX MUST
PRESENT THE BITS IN THEIR CORRECT POSITION.
ALSO THE MODULE MUST HANDLE THE UNUSED BITS
WITHIN THE BIT SLICE ITSELF.

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0316
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0318
0319      ENTRY CMIER_30 (LUN,REGISTER)
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0321
0322

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PROGRAM SECTIONS

Name	Bytes	Attributes
0 \$CODE	1079	PIC CON REL LCL SHR EXE RD NOWRT LONG
1 \$pdata	258	PIC CON REL LCL SHR NOEXE RD NOWRT LONG
2 \$LOCAL	1672	PIC CON REL LCL NOSHR NOEXE RD WRT LONG
3 MODE	55	PIC OVR REL GBL SHR NOEXE RD WRT LONG
Total Space Allocated		3064

ENTRY POINTS

Address	Type	Name	Address	Type	Name	Address	Type	Name	Address	Type	Name
0-00000306		C1750_CNFGR	0-00000200		CMIER_118	0-00000223		CMIER_1512	0-00000246		CMIER_1916
0-000002BC		CMIER_2320	0-000001BA		CMIER_30	0-000001DD		CMIER_74	0-00000086		COMET_CADR
0-00000166		COMET_CAER	0-000000FD		COMET_MCESR	0-0000000F		COMET_TBDR	0-00000000		VAX750REG

VARIABLES

Address	Type	Name	Address	Type	Name
AP-00000004a	L*1	LUN	2-00000300a	I*4	REGISTER

ARRAYS

Address	Type	Name	Bytes	Dimensions
3-00000000	CHAR	MODES	55	(0:4)
2-00000075	CHAR	V1CAER	68	(0:3)
2-000000B9	CHAR	V1CMIER	84	(0:3)
2-0000019A	CHAR	V1CNFGR	57	(8:10)
2-00000000	CHAR	V1MCESR	36	(0:0, 0:1)
2-0000010D	CHAR	V2CMIER	7	(4:4)
2-000001D3	CHAR	V2CNFGR	204	(12:17)
2-00000024	CHAR	V2MCESR	81	(3)
2-00000114	CHAR	V3CMIER	68	(8:11)
2-0000029F	CHAR	V3CNFGR	58	(19:20)
2-00000158	CHAR	V4CMIER	18	(12:12)
2-000002D9	CHAR	V4CNFGR	38	(22:23)
2-0000016A	CHAR	V5CMIER	36	(18:19, 0:1)
2-0000018E	CHAR	V6CMIER	12	(20:20)

LABELS

Address	Label	Address	Label	Address	Label	Address	Label	Address	Label	Address	Label
1-00000074	10'	1-00000086	15'	1-0000008D	20'	1-0000009E	25'	1-000000A5	30'	1-000000B7	35'
1-000000C8	75'	1-000000E1	80'	1-000000E8	85'	1-000000FB	90'				

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FUNCTIONS AND SUBROUTINES REFERENCED

Type	Name	Type	Name	Type	Name	Type	Name
I*4	COMPRESSC	I*4	LIB\$EXTZV		LINCHK		OUTPUT

COMMAND QUALIFIERS

FORTRAN /LIS=LISS:VAX750REG/OBJ=OBJ\$:VAX750REG MSRC\$:VAX750REG
 /CHECK=(NOBOUNDS,OVERFLOW,NOUNDERFLOW)
 /DEBUG=(NOSYMBOLS,TRACEBACK)
 /STANDARD=(NOSYNTAX,NOSOURCE FORM)
 /SHOW=(NOPREPROCESSOR,NOINCLUDE,MAP)
 /F77 /NOG_FLOATING /I4 /OPTIMIZE /WARNINGS /NOD_LINES /NOCROSS_REFERENCE /NOMACHINE_CODE /CONTINUATIONS=19

COMPILATION STATISTICS

Run Time: 3.78 seconds
 Elapsed Time: 11.56 seconds
 Page Faults: 164
 Dynamic Memory: 187 pages

