

PROGRAM SECTIONS

Name	Bytes	Attributes
0 \$CODE	35	PIC CON REL LCL SHR EXE RD NOWRT LONG
1 \$PDATA	18	PIC CON REL LCL SHR NOEXE RD NOWRT LONG
2 \$LOCAL	28	PIC CON REL LCL NOSHR NOEXE RD WRT LONG
Total Space Allocated	81	

ENTRY POINTS

Address	Type	Name
0-00000000		SYS_POWERFAIL

VARIABLES

Address	Type	Name
AP-00000004@	L*1	LUN

FUNCTIONS AND SUBROUTINES REFERENCED

Type	Name	Type	Name
	HEADER		LOGGER

COMMAND QUALIFIERS

```

FORTRAN /LIS=LIS$:SYSPWRFL/OBJ=OBJ$:SYSPWRFL MSRC$:SYSPWRFL
/CHECK=(NOBOUNDS,OVERFLOW,NOUNDERFLOW)
/DEBUG=(NOSYMBOLS,TRACEBACK)
/STANDARD=(NOSYNTAX,NOSOURCE FORM)
/SHOW=(NOPREPROCESSOR,NOINCLUDE,MAP)
/F77 /NOG_FLOATING /I4 /OPTIMIZE /WARNINGS /NOD_LINES /NOCROSS_REFERENCE /NOMACHINE_CODE /CONTINUATIONS=19

```

COMPILATION STATISTICS

Run Time:	0.74 seconds
Elapsed Time:	3.36 seconds
Page Faults:	101
Dynamic Memory:	163 pages

This image displays a grid of 150 small technical diagrams or schematics, arranged in 10 rows and 15 columns. Each diagram is a separate page from a manual, showing various types of logic, timing, or control diagrams. The diagrams are labeled with names and 'LIS' (likely representing a Logical Interchange Symbol or similar notation). The labels are as follows:

- Row 1: TIMCMP LIS, TUTAPE LIS
- Row 2: SBI LIS, TRNS BITS LIS, LBAERR LIS
- Row 3: STSEVENT LIS, TIMRB LIS, LBA LIS
- Row 4: TUBSENSE LIS, UNDEFINED LIS
- Row 5: SYSPWFL LIS
- Row 6: SUMMARY LIS, UNKN DISP LIS
- Row 7: SHRVECTOR LIS, SYSTARTUP LIS, LIBAINT LIS
- Row 8: TOF LIS, UNKNOWN LIS
- Row 9: TSTAPE LIS

The diagrams themselves contain various symbols, lines, and text, representing different aspects of digital logic or system control. Some diagrams include waveforms, truth tables, or circuit-like structures. The overall layout is a dense grid of these small technical illustrations.