


```

SSSSSSSS HH      HH RRRRRRRR VV      VV EEEEEEEEE EEEEEEEEE CCCCCCCC TTTTTTTTT 000000 RRRRRRRR
SSSSSSSS HH      HH RRRRRRRR VV      VV EEEEEEEEE EEEEEEEEE CCCCCCCC TTTTTTTTT 000000 RRRRRRRR
SS      HH      HH RR      RR VV      VV EE      EE CC      CC TT      TT 00      00 RR      RR
SS      HH      HH RR      RR VV      VV EE      EE CC      CC TT      TT 00      00 RR      RR
SS      HH      HH RR      RR VV      VV EE      EE CC      CC TT      TT 00      00 RR      RR
SSSSSS HH      HH RRRRRRRR VV      VV EEEEEEEEE EEEEEEEEE CCCCCCCC TTTTTTTTT 000000 RRRRRRRR
SSSSSS HH      HH RRRRRRRR VV      VV EEEEEEEEE EEEEEEEEE CCCCCCCC TTTTTTTTT 000000 RRRRRRRR
      SS      HH      HH RR      RR VV      VV EE      EE CC      CC TT      TT 00      00 RR      RR
      SS      HH      HH RR      RR VV      VV EE      EE CC      CC TT      TT 00      00 RR      RR
      SS      HH      HH RR      RR VV      VV EE      EE CC      CC TT      TT 00      00 RR      RR
SSSSSSSS HH      HH RR      RR VV      VV EE      EE CCCCCCCC TTTTTTTTT 000000 RR      RR
SSSSSSSS HH      HH RR      RR VV      VV EE      EE CCCCCCCC TTTTTTTTT 000000 RR      RR

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LL      IIIIII SSSSSSSS
LL      IIIIII SSSSSSSS
LL      II     SS
LL      II     SS
LL      II     SS
LL      II     SS
LL      II     SSSSSS
LL      II     SSSSSS
LL      II     SS
LL      II     SS
LL      II     SS
LL      II     SS
LLLLLLLLLL IIIIII SSSSSSSS
LLLLLLLLLL IIIIII SSSSSSSS

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```
0000 1 .TITLE erfshavec - Transfer vectors for shareable ERF modules.
0000 2 .IDENT /V04-000/
0000 3
0000 4 *****
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0000 23 *
0000 24 *
0000 25 *****
0000 26
0000 27
0000 28 Author: Elliott A. Drayton
0000 29
0000 30 Modified By:
0000 31
0000 32 V03-009 SAR0285 Sharon A. Reynolds 2-Jul-1984
0000 33 Added entry points for translate_bits and output_lines.
0000 34
0000 35 V03-008 EAD0172 Elliott A. Drayton 8-May-1984
0000 36 Add entry for ERLLOGSTS.
0000 37
0000 38 V03-007 EAD0149 Elliott A. Drayton 9-Apr-1984
0000 39 Add entry point for routines which have UCB like names.
0000 40
0000 41 V03-006 EAD0136 Elliott A. Drayton 9-Apr-1984
0000 42 Moved EMB and SYECOM to seperate shareable image and added
0000 43 new entry points.
0000 44
0000 45 V03-005 EAD0108 Elliott A. Drayton 5-Mar-1984
0000 46 Added global references to EMB and SYECOM.
0000 47
0000 48 V03-004 EAD0102 Elliott A. Drayton 29-Feb-1984
0000 49 Created a macro for transfer vectors.
0000 50
0000 51 V03-003 SAR0149 Sharon A. Reynolds, 5-Oct-1983
0000 52 Added an entry point for HEADER3.
0000 53
0000 54 V03-002 SAR0137 Sharon A. Reynolds, 12-Sep-1983
0000 55 Added entry points for 'intervene' module.
0000 56
0000 57 V03-001 SAR0059 Sharon A. Reynolds, 13-Jun-1983
```

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0000 58 :-- Added a few entry points.
0000 59 :--
0000 60
0000 61
0000 62 .Macro Vector Routine, Target
0000 63 .TRANSFER ROUTINE
0000 64 .MASK ROUTINE
0000 65 .IIF NOT BLANK, TARGET, JMP TARGET+2
0000 66 .IIF BLANK, TARGET, JMP ROUTINE+2
0000 67 .Endm Vector
0000 68
0000 69
00000000 70 .PSECT $ERFXFERVECS,EXE,RD,SHR,NOPIC,NOWRT
0000 71
0000 72 VECTOR LABEL
0008 73 VECTOR MEMORY_Q
0010 74 VECTOR GET_QUEUE_INFO
0018 75 VECTOR CALC_MAP
0020 76 VECTOR DHEAD1
0028 77 VECTOR DHEAD2
0030 78 VECTOR GET_CURRENT_LABEL ;Needed by dhead2
0038 79 VECTOR IRPSL_PID
0040 80 VECTOR IRPSQ_IOSB
0048 81 VECTOR IRPSW_BCNT
0050 82 VECTOR IRPSW_BOFF
0058 83 VECTOR IRPSW_FUNC
0060 84 VECTOR MOVCS
0068 85 VECTOR MOVCS
0070 86 VECTOR MOVQ
0078 87 VECTOR MOVL
0080 88 VECTOR OUTPUT
0088 89 VECTOR UBA_DATAPATH
0090 90 VECTOR UBA_MAPPING
0098 91 VECTOR UCBSB_ERTCNT
00A0 92 VECTOR UCBSB_ERTMAX
00A8 93 VECTOR UCBSL_CHAR
00B0 94 VECTOR UCBSL_MEDIA
00B8 95 VECTOR UCBSL_OPCNT
00C0 96 VECTOR UCBSW_ERRCNT
00C8 97 VECTOR UCBSW_STS
00D0 98 VECTOR VAXPSL
00D8 99 VECTOR VAX780REG
00E0 100 VECTOR ACCS_780
00E8 101 VECTOR SBI_FAULTREG
00F0 102 VECTOR SBI_COMPARATOR
00F8 103 VECTOR SBI_MAINTENANCE
0100 104 VECTOR SBI_ERROR
0108 105 VECTOR SBI_TIMEOUT
0110 106 VECTOR SBI_SILO
0118 107 VECTOR SBI_COMMAND
0120 108 VECTOR DR780_REGA
0128 109 VECTOR MS780C_REGA
0130 110 VECTOR MS780E_REGA
0138 111 VECTOR MA780_REGA
0140 112 VECTOR RH780_CONFIGURATION_REGISTER
0148 113 VECTOR UBA_REGA
0150 114 VECTOR CI780_REGA
```


0158	115	VECTOR	MOUNT
0160	116	VECTOR	DISMOUNT
0168	117	VECTOR	INSQUE
0170	118	VECTOR	INSQUE_TAIL
0178	119	VECTOR	REMQUE
0180	120	VECTOR	DISMOUNT VOLUME
0188	121	VECTOR	MOUNT VOLUME
0190	122	VECTOR	COMPRESSF
0198	123	VECTOR	GET_MEMORY_Q_INFO
01A0	124	VECTOR	CLASSIFY
01A8	125	VECTOR	ORB\$L_OWNER
01B0	126	VECTOR	UNDEFINED
01B8	127	VECTOR	B_UNDEFINED
01C0	128	VECTOR	ERLLOGSTS
01C8	129	VECTOR	PADRIVER_ATTENTION_ERROR_CODE
01D0	130	VECTOR	PADRIVER_INITIALIZATION
01D8	131	VECTOR	COMPRESS4
01E0	132	VECTOR	COMPRESSC
01E8	133	VECTOR	DUMPREG
01F0	134	VECTOR	HEADER
01F8	135	VECTOR	HEADER2
0200	136	VECTOR	HEADER3
0208	137	VECTOR	IMAGE_LOADER
0210	138	VECTOR	INTERVENE_INCREMENT
0218	139	VECTOR	INTERVENE_OUTPUT
0220	140	VECTOR	INTERVENE_DECREMENT
0228	141	VECTOR	LINCHK
0230	142	VECTOR	LOGGER
0238	143	VECTOR	LOG_FILENAME
0240	144	VECTOR	MAP_IMAGE
0248	145	VECTOR	PARSE_OUTPUT_FILES
0250	146	VECTOR	FRCTOF
0258	147	VECTOR	WRITE_MSG
0260	148	VECTOR	UNKNOWN1
0268	149	VECTOR	UNKNOWN2
0270	150	VECTOR	UNKNOWN3
0278	151	VECTOR	ORB\$L_OWNER RTN, ORB\$L_OWNER
0280	152	VECTOR	UCB\$W_STS RTN
0288	153	VECTOR	UCB\$W_OPCNT RTN
0290	154	VECTOR	UCB\$W_ERRCNT RTN
0298	155	VECTOR	ERLLOGSTS
02A0	156	VECTOR	TRANSLATE BITS
02A8	157	VECTOR	OUTPUT_LINES
02B0	158	.END	

ERFSHRVEC
Symbol table

ACCS 780	*****	X	01	SBI_COMMAND	*****	X	01
B_UNDEFINED	*****	X	01	SBI_COMPARATOR	*****	X	01
CALC_MAP	*****	X	01	SBI_ERROR	*****	X	01
C1780_REGA	*****	X	01	SBI_FAULTREG	*****	X	01
CLASSIFY	*****	X	01	SBI_MAINTENANCE	*****	X	01
COMPRESS4	*****	X	01	SBI_SILO	*****	X	01
COMPRESSC	*****	X	01	SBI_TIMEOUT	*****	X	01
COMPRESSF	*****	X	01	TRANSLATE_BITS	*****	X	01
DHEAD1	*****	X	01	UBA_DATAPATH	*****	X	01
DHEAD2	*****	X	01	UBA_MAPPING	*****	X	01
DISMOUNT	*****	X	01	UBA_REGA	*****	X	01
DISMOUNT_VOLUME	*****	X	01	UCBSB_ERTCNT	*****	X	01
DR780_REGA	*****	X	01	UCBSB_ERTMAX	*****	X	01
DUMPREG	*****	X	01	UCBSL_CHAR	*****	X	01
ERLOGSTS	*****	X	01	UCBSL_MEDIA	*****	X	01
ERLOGSTS2	*****	X	01	UCBSL_OPCNT	*****	X	01
FRCTOF	*****	X	01	UCBSL_OPCNT_RTN	*****	X	01
GET_CURRENT_LABEL	*****	X	01	UCBSW_ERRCNT	*****	X	01
GET_MEMORY_Q_INFO	*****	X	01	UCBSW_ERRCNT_RTN	*****	X	01
GET_QUEUE_INFO	*****	X	01	UCBSW_STS	*****	X	01
HEADER	*****	X	01	UCBSW_STS_RTN	*****	X	01
HEADER2	*****	X	01	UNDEFINED	*****	X	01
HEADER3	*****	X	01	UNKNOWN1	*****	X	01
IMAGE_LOADER	*****	X	01	UNKNOWN2	*****	X	01
INSQUE	*****	X	01	UNKNOWN3	*****	X	01
INSQUE_TAIL	*****	X	01	VAX780REG	*****	X	01
INTERVENE_DECREMENT	*****	X	01	VAXPSL	*****	X	01
INTERVENE_INCREMENT	*****	X	01	WRITE_MSG	*****	X	01
INTERVENE_OUTPUT	*****	X	01				
IRPSL_PID	*****	X	01				
IRPSQ_IOSB	*****	X	01				
IRPSW_BCNT	*****	X	01				
IRPSW_BOFF	*****	X	01				
IRPSW_FUNC	*****	X	01				
LABEL	*****	X	01				
LINCHK	*****	X	01				
LOGGER	*****	X	01				
LOG_FILENAME	*****	X	01				
MA780_REGA	*****	X	01				
MAP_IMAGE	*****	X	01				
MEMORY_Q	*****	X	01				
MOUNT	*****	X	01				
MOUNT_VOLUME	*****	X	01				
MOV3	*****	X	01				
MOV5	*****	X	01				
MOVL	*****	X	01				
MOVQ	*****	X	01				
MS780C_REGA	*****	X	01				
MS780E_REGA	*****	X	01				
ORBSL_OWNER	*****	X	01				
OUTPUT	*****	X	01				
OUTPUT_LINES	*****	X	01				
PADRIVER_ATTENTION_ERROR_CODE	*****	X	01				
PADRIVER_INITIALIZATION	*****	X	01				
PARSE_OUTPUT_FILES	*****	X	01				
REMQUE	*****	X	01				
RH780_CONFIGURATION_REGISTER	*****	X	01				

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! Psect synopsis !

PSECT name	Allocation	PSECT No.	Attributes
ABS	00000000 (0.)	00 (0.)	NOPIC USR CON ABS LCL NOSHR NOEXE NORD NOWRT NOVEC BYTE
\$ERFXFERVEC\$	000002B0 (688.)	01 (1.)	NOPIC USR CON REL LCL SHR EXE RD NOWRT NOVEC BYTE

! Performance indicators !

Phase	Page faults	CPU Time	Elapsed Time
Initialization	30	00:00:00.08	00:00:00.37
Command processing	133	00:00:00.49	00:00:01.49
Pass 1	96	00:00:01.67	00:00:03.43
Symbol table sort	0	00:00:00.05	00:00:00.06
Pass 2	53	00:00:00.56	00:00:01.25
Symbol table output	11	00:00:00.06	00:00:00.27
Psect synopsis output	2	00:00:00.02	00:00:00.02
Cross-reference output	0	00:00:00.00	00:00:00.00
Assembler run totals	328	00:00:02.95	00:00:06.90

The working set limit was 900 pages.
7972 bytes (16 pages) of virtual memory were used to buffer the intermediate code.
There were 10 pages of symbol table space allocated to hold 85 non-local and 0 local symbols.
158 source lines were read in Pass 1, producing 21 object records in Pass 2.
1 page of virtual memory was used to define 1 macro.

! Macro library statistics !

Macro library name	Macros defined
_\$255\$DUA28:[SYSLIB]STARLET.MLB;2	0

0 GETS were required to define 0 macros.

There were no errors, warnings or information messages.

MACRO/LIS=LIS\$:SHRVECTOR/OBJ=OBJ\$:SHRVECTOR MSRC\$:SHRVECTOR/UPDATE=(ENH\$:SHRVECTOR)

