


```

EEEEEEEEEE RRRRRRRR FFFFFFFFFF 222222 VV VV EEEEEEEEEE CCCCCCCC TTTTTTTTTT
EEEEEEEEEE RRRRRRRR FFFFFFFFFF 222222 VV VV EEEEEEEEEE CCCCCCCC TTTTTTTTTT
EE          RR      RR FF          22      22 VV      VV EE          CC          TT
EE          RR      RR FF          22      22 VV      VV EE          CC          TT
EE          RR      RR FF          22      22 VV      VV EE          CC          TT
EE          RR      RR FF          22      22 VV      VV EE          CC          TT
EEEEEEEEEE RRRRRRRR FFFFFFFFFF          22      22 VV      VV EEEEEEEEE CCCCCCCC TT
EEEEEEEEEE RRRRRRRR FFFFFFFFFF          22      22 VV      VV EEEEEEEEE CCCCCCCC TT
EE          RR      RR FF          22      22 VV      VV EE          CC          TT
EE          RR      RR FF          22      22 VV      VV EE          CC          TT
EE          RR      RR FF          22      22 VV      VV EE          CC          TT
EEEEEEEEEE RRR      RR FF          22      22 VV      VV EEEEEEEEE CCCCCCCC TT
EEEEEEEEEE RRR      RR FF          22      22 VV      VV EEEEEEEEE CCCCCCCC TT

```

```

LL          IIIIIII SSSSSSSS
LL          IIIIIII SSSSSSSS
LL          II      SS
LL          II      SS
LL          II      SS
LL          II      SS
LL          II      SSSSSS
LL          II      SSSSSS
LL          II      SS
LL          II      SS
LL          II      SS
LL          II      SS
LLLLLLLLLL IIIIIII SSSSSSSS
LLLLLLLLLL IIIIIII SSSSSSSS

```

```
0000 1
0000 2 .TITLE ERFPROC2VECT - Transfer vectors for ERFPROC2 module.
0000 3 .IDENT /V04-000/
0000 4
0000 5 *****
0000 6 *
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0000 24 *
0000 25 *
0000 26 *****
0000 27
0000 28
0000 29 Modified by:
0000 30
0000 31 V01-002 SAR0250 Sharon A. Reynolds 11-Apr-1984
0000 32 Changed mchecks entry point to mchk_dispatcher.
0000 33
0000 34 V01-001 SAR0203 Sharon A. Reynolds 27-Feb-1984
0000 35 Removed the SBI entry point.
0000 36 --
```

```
00000000 38 .PSECT $$ERFPROC2VECT,EXE,NOWRT
          39
          40 PROC2INIT::
          41
          42 .TRANSFER ERFPRC2INI
00000002'EF 0000' 0000 43 .MASK ERFPRC2INI
          17 0002 44 JMP L^ERFPRC2INI+2
          0008 45
          0008 46 .TRANSFER MCHK_DISPATCHER
00000002'EF 0000' 0008 47 .MASK MCHK_DISPATCHER
          17 000A 48 JMP L^MCHK_DISPATCHER+2
          0010 49
          0010 50 .TRANSFER BUGCHK
00000002'EF 0000' 0010 51 .MASK BUGCHK
          17 0012 52 JMP L^BUGCHK+2
          0018 53
          0018 54 .END PROC2INIT
```

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```

BUGCHK          ***** X 01
ERFPROC2INI     ***** X 01
MCHK_DISPATCHER ***** X 01
PROC2INIT       00000000 RG 01
    
```

 ! Psect synopsis !

PSECT name	Allocation	PSECT No.	Attributes
ABS	00000000 (0.)	00 (0.)	NOPIC USR CON ABS LCL NOSHR NOEXE NORD NOWRT NOVEC BYTE
\$\$\$ERFPROC2VECT	00000018 (24.)	01 (1.)	NOPIC USR CON REL LCL NOSHR EXE RD NOWRT NOVEC BYTE

 ! Performance indicators !

Phase	Page faults	CPU Time	Elapsed Time
Initialization	30	00:00:00.08	00:00:00.48
Command processing	129	00:00:00.47	00:00:01.34
Pass 1	69	00:00:00.27	00:00:00.81
Symbol table sort	0	00:00:00.00	00:00:00.00
Pass 2	28	00:00:00.20	00:00:00.67
Symbol table output	2	00:00:00.01	00:00:00.01
Psect synopsis output	1	00:00:00.02	00:00:00.02
Cross-reference output	0	00:00:00.00	00:00:00.00
Assembler run totals	262	00:00:01.05	00:00:03.33

The working set limit was 750 pages.
 560 bytes (2 pages) of virtual memory were used to buffer the intermediate code.
 There were 10 pages of symbol table space allocated to hold 4 non-local and 0 local symbols.
 54 source lines were read in Pass 1, producing 11 object records in Pass 2.
 0 pages of virtual memory were used to define 0 macros.

 ! Macro library statistics !

Macro library name	Macros defined
_\$255\$DUA28:[SYSLIB]STARLET.MLB;2	0

0 GETS were required to define 0 macros.

There were no errors, warnings or information messages.

MACRO/LIS=LIS\$:ERF2VECT/OBJ=OBJ\$:ERF2VECT MSRC\$:ERF2VECT/UPDATE=(ENH\$:ERF2VECT)

ERFCOM LIS

ERFMSG LIS

ERF LIS

ERF5VECT LIS

ERF2VECT LIS

ERFCOMVEC LIS

ERFBRIVEC LIS

ERF3VECT LIS

ERFDISKVE LIS

ERFPARSER LIS

ERFBUSVEC LIS

ERFINIVEC LIS

ERF4VECT LIS

ERFINICOM LIS

ERF1VECT LIS