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0273 integer*4 byte_control_register
0274 integer*4 disk_address_register
0275 integer*4 multi_purpose_register
0276 integer*4 ecc_position_register
0277 integer*4 ecc_pattern_register
0278 integer*4 data_path_number
0279 integer*4 data_path_register
0280 integer*4 final_map_register
0281 integer*4 previous_map_register
0282 integer*4 vec$l_mapreg
0283
0284 equivalence (emb$l_dv_regsav(0),control_status_register)
0285 equivalence (emb$l_dv_regsav(1),bus_address_register)
0286 equivalence (emb$l_dv_regsav(2),byte_control_register)
0287 equivalence (emb$l_dv_regsav(3),disk_address_register)
0288 equivalence (emb$l_dv_regsav(4),multi_purpose_register)
0289 equivalence (emb$l_dv_regsav(5),ecc_position_register)
0290 equivalence (emb$l_dv_regsav(6),ecc_pattern_register)
0291 equivalence (emb$l_dv_regsav(7),data_path_number)
0292 equivalence (emb$l_dv_regsav(8),data_path_register)
0293 equivalence (emb$l_dv_regsav(9),final_map_register)
0294 equivalence (emb$l_dv_regsav(10),previous_map_register)
0295 equivalence (emb$l_dv_regsav(11),vec$l_mapreg)
0296
0297 character*12 v1csr(0:0)
0298 data v1csr(0) /*DRIVE READY*/
0299
0300 character*17 v2csr(6:7)
0301 data v2csr(6) /*INTERRUPT ENABLE*/
0302 data v2csr(7) /*CONTROLLER READY*/
0303
0304 character*21 v3csr(10:10)
0305 data v3csr(10) /*OPERATION INCOMPLETE*/
0306
0307 character*20 v4csr(13:15)
0308 data v4csr(13) /*NON-EXISTENT MEMORY*/
0309 data v4csr(14) /*DRIVE ERROR*/
0310 data v4csr(15) /*COMPOSITE ERROR*/
0311
0312 character*22 v5csr(22:24)
0313 data v5csr(22) /*R80 SKIP SECTOR ERROR*/
0314 data v5csr(23) /*R80 SKIP SECTOR ERROR*/
0315 data v5csr(24) /*INTERRUPT REQUEST*/
0316
0317 character*30 v6csr(26:28)
0318 data v6csr(26) /*R80*/
0319 data v6csr(27) /*AUTOMATIC SKIP SECTOR INHIBIT*/
0320 data v6csr(28) /*TIMEOUT INHIBIT*/
0321
0322 character*11 v1rl02_mpr(3:5)
0323 data v1rl02_mpr(3) /*BRUSH HOME*/
0324 data v1rl02_mpr(4) /*HEADS OUT*/
0325 data v1rl02_mpr(5) /*COVER OPEN*/
0326
0327 character*19 v2rl02_mpr(8:15)
0328 data v2rl02_mpr(8) /*DRIVE SELECT ERROR*/
0329 data v2rl02_mpr(9) /*VOLUME CHECK*/

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0330 data v2rl02_mpr(10) /*WRITE GATE ERROR*/
0331 data v2rl02_mpr(11) /*SPINDLE ERROR*/
0332 data v2rl02_mpr(12) /*SEEK TIMEOUT*/
0333 data v2rl02_mpr(13) /*WRITE LOCK*/
0334 data v2rl02_mpr(14) /*HEAD CURRENT ERROR*/
0335 data v2rl02_mpr(15) /*WRITE DATE ERROR*/
0336
0337 character*14 v1r80_mpr(8:13)
0338 data v1r80_mpr(8) /*FAULT*/
0339 data v1r80_mpr(9) /*PLUG VALID*/
0340 data v1r80_mpr(10) /*SEEK ERROR*/
0341 data v1r80_mpr(11) /*ON CYLINDER*/
0342 data v1r80_mpr(12) /*DRIVE READY*/
0343 data v1r80_mpr(13) /*WRITE PROTECT*/
0344
0345 integer*4 compress4
0346 integer*4 compressc
0347 integer*4 field
0348
0349 character*27 idc_command(0:7)
0350 data idc_command(0) /*NO DRIVE OPERATION*/
0351 data idc_command(1) /*WRITE CHECK DATA*/
0352 data idc_command(2) /*GET STATUS*/
0353 data idc_command(3) /*SEEK*/
0354 data idc_command(4) /*READ HEADER*/
0355 data idc_command(5) /*WRITE DATA*/
0356 data idc_command(6) /*READ DATA*/
0357 data idc_command(7) /*READ DATA W/O HEADER CHECK*/
0358
0359 logical*1 diagnostic_mode
0360
0361 integer*4 lib$extzv
0362 integer*4 data_check_and_opi_bits
0363 integer*4 data_late_and_opi_bits
0364 integer*4 sector_count
0365 integer*4 ecc_status_bits
0366 integer*4 rl02_status_bits
0367
0368 character*20 v1rl02_status_bits(0:7)
0369 data v1rl02_status_bits(0) /*LOAD STATE*/
0370 data v1rl02_status_bits(1) /*SPIN UP*/
0371 data v1rl02_status_bits(2) /*BRUSH CYCLE*/
0372 data v1rl02_status_bits(3) /*LOAD HEADS*/
0373 data v1rl02_status_bits(4) /*SEEK TRACK COUNTING*/
0374 data v1rl02_status_bits(5) /*SEEK LINEAR MODE*/
0375 data v1rl02_status_bits(6) /*UNLOAD HEADS*/
0376 data v1rl02_status_bits(7) /*SPIN DOWN*/
0377
0378 integer*4 device_function
0379 integer*4 device_type
0380 integer*4 sector
0381 integer*4 cylinder
0382 integer*4 tag
0383 integer*4 head
0384
0385 character*11 vldar(0:1)
0386 data vldar(0) /*MARKER*/

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0387      data          v1dar(1)          /'GET STATUS*'/
0388
0389      character*6    v2dar(3:3)
0390      data          v2dar(3)          /'RESET*'/
0391
0392      character*8    v4dar(2:2,0:1)
0393      data          v4dar(2,0)        /'REVERSE*'/
0394      data          v4dar(2,1)        /'FORWARD*'/
0395
0396      character*18   v6dar(4:4,0:1)
0397      data          v6dar(4,0)        /'SELECT LOWER HEAD*'/
0398      data          v6dar(4,1)        /'SELECT UPPER HEAD*'/
0399
0400      character*15   v7dar(6:6)
0401      data          v7dar(6)          /'RETURN-TO-ZERO*'/
0402
0403
0404      call frctof (lun)
0405
0406      call dhead1 (lun,'RB730')
0407
0408      diagnostic_mode = .false.
0409
0410      if (lib$extzv(25,1,control_status_register) .eq. 1)
0411      1 diagnostic_mode = .true.
0412
0413      device_function = lib$extzv (1,3,control_status_register)
0414
0415      device_type = lib$extzv (26,1,control_status_register)
0416
0417      call linchk (lun,2)
0418
0419      write(lun,5) 'RB CSR',control_status_register
0420      5 format(/' ',t8,a,t24,z8.8)
0421
0422      if (.not. diagnostic_mode) then
0423
0424      call output (lun,control_status_register,v1csr,0,0,0,'0')
0425
0426      call linchk (lun,1)
0427
0428      if (lib$extzv(29,1,control_status_register) .eq. 1) then
0429
0430      10 write(lun,10) 'R80 WRITE FORMAT FUNCTION'
0431      10 format(' ',t40,a)
0432      else
0433
0434      idc_function = lib$extzv(1,3,control_status_register)
0435
0436      15 write(lun,15) idc_command(idc_function)
0437      15 format(' ',t40,a<compressc (idc_command(idc_function))>)
0438      endif
0439
0440      call output (lun,control_status_register,v2csr,6,6,7,'0')
0441
0442      call linchk (lun,1)
0443

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0444 write(lun,20) 'DRIVE #',lib$extzv(8,2,control_status_register),
0445 1 ' . SELECTED'
0446 20 format(' ',t40,a,i1.1,a)
0447
0448 call output (lun,control_status_register,v3csr,10,10,10,'0')
0449
0450 data_check_and_opi_bits = lib$extzv(10,2,control_status_register)
0451
0452 if (
0453 1 data_check_and_opi_bits .eq. 2
0454 1 .or.
0455 1 data_check_and_opi_bits .eq. 3
0456 1 ) then
0457
0458 call linchk (lun,1)
0459 endif
0460
0461 if (data_check_and_opi_bits .eq. 2) then
0462
0463 25 write(lun,25) 'DATA CHECK ERROR'
0464 format(' ',t40,a)
0465
0466 else if (data_check_and_opi_bits .eq. 3) then
0467
0468 write(lun,25) 'HEADER CRC ERROR'
0469 endif
0470
0471 data_late_and_opi_bits = lib$extzv(10,3,control_status_register)
0472
0473 if (
0474 1 data_late_and_opi_bits .eq. 4
0475 1 .or.
0476 1 data_late_and_opi_bits .eq. 5
0477 1 ) then
0478
0479 call linchk (lun,1)
0480 endif
0481
0482 if (data_late_and_opi_bits .eq. 4) then
0483
0484 write(lun,25) 'DATA LATE'
0485
0486 else if (data_late_and_opi_bits .eq. 5) then
0487
0488 write(lun,25) 'HEADER NOT FOUND'
0489 endif
0490
0491 call output (lun,control_status_register,v4csr,13,13,15,'0')
0492
0493 do 35,i = 16,19
0494
0495 if (lib$extzv(i,1,control_status_register) .eq. 1) then
0496
0497 call linchk (lun,1)
0498
0499 30 write(lun,30) 'ATTENTION DRIVE #',i-16.'.'
0500 format(' ',t40,a,i1.1,a)
```



```
0729       1 ) then
0730
0731       call uba_mapping (lun,(field-1),previous_map_register)
0732       endif
0733
0734       call vecmapreg (lun,vec$l_mapreg)
0735       endif
0736
0737       call linchk (lun,1)
0738
0739       75 write(lun,75)
0740       format(' ',:))
0741
0742       if (emb$w_hd_entry .ne. 98) then
0743
0744       call uc$b_ertcnt (lun,emb$b_dv_ertcnt)
0745
0746       call uc$b_ertmax (lun,emb$b_dv_ertmax)
0747       endif
0748
0749       call orb$l_owner (lun,emb$l_dv_ownuic)
0750
0751       call uc$b_l_char (lun,emb$l_dv_char)
0752
0753       call uc$b_w_sts (lun,emb$w_dv_sts)
0754
0755       call uc$b_l_opcnt (lun,emb$l_dv_opcnt)
0756
0757       call uc$b_w_errcnt (lun,emb$w_dv_errcnt)
0758
0759       if (emb$w_hd_entry .ne. 98) then
0760
0761       call uc$b_l_media (lun,emb$l_dv_media)
0762
0763       call linchk (lun,1)
0764
0765       write(lun,75)
0766
0767       call dqdisks_qio (lun,emb$w_dv_func)
0768
0769       call irp$w_bcnt (lun,emb$w_dv_bcnt)
0770
0771       call irp$w_boff (lun,emb$w_dv_boff)
0772
0773       call irp$l_pid (lun,emb$l_dv_rapid)
0774
0775       call irp$q_iosb (lun,emb$l_dv_iosb1)
0776       endif
0777
0778       return
0779       end
```

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ARRAYS

Address	Type	Name	Bytes	Dimensions
3-00000000	L*1	EMB	512	(0:511)
3-00000052	I*4	EMBSL_DV_REGSAV	420	(0:104)
3-00000006	I*4	EMBSQ_HD_TIME	8	(2)
2-00000228	CHAR	IDC_COMMAND	216	(0:7)
2-00000000	CHAR	V1CSR	12	(0:0)
2-000003A0	CHAR	V1DAR	22	(0:1)
2-000001D4	CHAR	V1R80_MPR	84	(8:13)
2-0000011B	CHAR	V1RL02_MPR	33	(3:5)
2-00000300	CHAR	V1RL02_STATUS_BITS	160	(0:7)
2-0000000C	CHAR	V2CSR	34	(6:7)
2-000003B6	CHAR	V2DAR	6	(3:7)
2-0000013C	CHAR	V2RL02_MPR	152	(8:15)
2-0000002E	CHAR	V3CSR	21	(10:10)
2-00000043	CHAR	V4CSR	60	(13:15)
2-000003BC	CHAR	V4DAR	16	(2:2, 0:1)
2-0000007F	CHAR	V5CSR	66	(22:24)
2-000000C1	CHAR	V6CSR	90	(26:28)
2-000003CC	CHAR	V6DAR	36	(4:4, 0:1)
2-000003F0	CHAR	V7DAR	15	(6:6)

LABELS

Address	Label	Address	Label	Address	Label	Address	Label	Address	Label	Address	Label
1-00000176	5'	1-00000183	10'	1-0000018A	15'	1-00000196	20'	1-000001A1	25'	1-000001A8	30'
**	35	1-000001B3	40'	1-000001BA	45'	1-000001C6	46'	1-000001F7	47'	1-0000021A	48'
1-00000233	50'	1-0000023F	55'	1-0000024D	60'	1-00000259	65'	1-00000260	70'	1-00000275	75'

FUNCTIONS AND SUBROUTINES REFERENCED

Type	Name	Type	Name	Type	Name	Type	Name	Type	Name	
	CALC_MAP		CALC_MAP2	I*4	COMPRESS4	I*4	COMPRESSC		DHEAD1	
	FRCTOF		IRP\$C_PID		IRP\$Q_IOSB		IRP\$W_BCNT		IRP\$W_BOFF	
	LINCHK		ORB\$S_OWNER		OUTPUT		UBA_D\$TAPATH		UBA_MAPPING	
	UCB\$B_ERTMAX		UCB\$S_CHAR		UCB\$S_MEDIA		UCB\$S_OP\$CNT		UCB\$W_ERRCNT	
	VECMAPREG								UCB\$W_STS	
									I*4	DQDISKS_QIO
										LIB\$EXTZV


```
0321      qiocode(1,49) = %loc(io$_readvblk)
0322
0323      qiocode(1,50) = %loc(io$_access)
0324
0325      qiocode(1,51) = %loc(io$_create)
0326
0327      qiocode(1,52) = %loc(io$_deaccess)
0328
0329      qiocode(1,53) = %loc(io$_delete)
0330
0331      qiocode(1,54) = %loc(io$_modify)
0332
0333      qiocode(1,56) = %loc(io$_acpcontrol)
0334
0335      qiocode(1,57) = %loc(io$_mount)
0336
0337      do 10,i = 0,63
0338
0339      qiocode(0,i) = 33
0340
0341      if (qiocode(1,i) .eq. 0) then
0342
0343      qiocode(1,i) = %loc(qio_string)
0344      endif
0345
0346      10 continue
0347      endif
0348
0349      call irp$w_func (lun,emb$w_dv_func,
0350      1 qiocode(0,lib$extzv(0,6,emb$w_dv_func)))
0351
0352      return
0353
0354      end
```

PROGRAM SECTIONS

Name	Bytes	Attributes
0 \$CODE	255	PIC CON REL LCL SHR EXE RD NOWRT LONG
1 \$PDATA	8	PIC CON REL LCL SHR NOEXE RD NOWRT LONG
2 \$LOCAL	548	PIC CON REL LCL NOSHR NOEXE RD WRT LONG
3 QIOCOMMON	1247	PIC OVR REL GBL SHR NOEXE RD WRT LONG
Total Space Allocated		2058

ENTRY POINTS

Address	Type	Name
0-00000000		DQDISKS_Q:0

VARIABLES

Address	Type	Name	Address	Type	Name
AP-00000008	I*2	EMBSW DV FUNC	2-00000200	I*4	I
3-00000442	CHAR	IOS_ABORT	3-0000034D	CHAR	IOS_ACCESS
3-000003C2	CHAR	IOS_ACPCONTROL	3-000004B3	CHAR	IOS_AVAILABLE
3-00000297	CHAR	IOS_CLEAN	3-00000369	CHAR	IOS_CREATE
3-00000385	CHAR	IOS_DEACCESS	3-00000393	CHAR	IOS_DELETE
3-0000026D	CHAR	IOS_DIAGNOSE	3-00000065	CHAR	IOS_DRVCLR
3-000004CB	CHAR	IOS_DSE	3-000000A9	CHAR	IOS_ERASETAPE
3-00000276	CHAR	IOS_FORMAT	3-00000071	CHAR	IOS_INITIALIZE
3-00000014	CHAR	IOS_LOADMCODE	3-000003A1	CHAR	IOS_MODIFY
3-000003E2	CHAR	IOS_MOUNT	3-00000000	CHAR	IOS_NOP
3-0000009D	CHAR	IOS_OFFSET	3-000000EB	CHAR	IOS_PACKACK
3-000000E0	CHAR	IOS_QSTOP	3-000003EF	CHAR	IOS_RDSTATS
3-00000421	CHAR	IOS_READCSR	3-00000169	CHAR	IOS_READHEAD
3-000002B6	CHAR	IOS_READLBLK	3-0000013F	CHAR	IOS_READPBLK
3-00000200	CHAR	IOS_READPRESET	3-00000195	CHAR	IOS_READTRACKD
3-0000033A	CHAR	IOS_READVBLK	3-0000045A	CHAR	IOS_READWTHBUF
3-00000484	CHAR	IOS_READWTHXBUF	3-0000004D	CHAR	IOS_RECAL
3-0000007C	CHAR	IOS_RELEASE	3-000001AB	CHAR	IOS_REREADN
3-000001B8	CHAR	IOS_REREADP	3-000000CA	CHAR	IOS_RETCENTER
3-000002E6	CHAR	IOS_REWIND	3-000002C9	CHAR	IOS_REWINDOFF
3-000000FC	CHAR	IOS_SEARCH	3-00000024	CHAR	IOS_SEEK
3-00000231	CHAR	IOS_SENSECHAR	3-00000309	CHAR	IOS_SENSEMODE
3-0000021D	CHAR	IOS_SETCHAR	3-00000388	CHAR	IOS_SETCLOCK
3-00000088	CHAR	IOS_SETCLOCKP	3-000002DD	CHAR	IOS_SETMODE
3-000002ED	CHAR	IOS_SKIPFILE	3-000002FA	CHAR	IOS_SKIPRECORD
3-00000029	CHAR	IOS_SPACEFILE	3-0000010E	CHAR	IOS_SPACERECORD
3-000003D7	CHAR	IOS_STARTDATA	3-000000B4	CHAR	IOS_STARTDATAP
3-00000037	CHAR	IOS_STARTMPROC	3-0000020F	CHAR	IOS_STARTSPNDL
3-00000059	CHAR	IOS_STOP	3-0000000D	CHAR	IOS_UNLOAD
3-00000468	CHAR	IOS_WRITEBUFNCRC	3-0000011E	CHAR	IOS_WRITECHECK
3-000001E4	CHAR	IOS_WRITECHECKM	3-000003FF	CHAR	IOS_WRITECSR
3-00000153	CHAR	IOS_WRITEHEAD	3-000002A2	CHAR	IOS_WRITELBLK
3-00000247	CHAR	IOS_WRITEMARK	3-00000314	CHAR	IOS_WRITEOF
3-0000012A	CHAR	IOS_WRITEPBLK	3-000001C9	CHAR	IOS_WRITERET

3-0000017E CHAR IOS_WRITETRACKD
3-00000448 CHAR IOS_WRITEWITHBUF
AP-00000004a L*1 LUN

3-00000326 CHAR IOS_WRITEVBLK
3-00000257 CHAR IOS_WRTTMKR
3-000004A1 CHAR QIO_STRING

ARRAYS

Address	Type	Name	Bytes	Dimensions
2-00000000	I*4	QIICODE	512	(0:1, 0:63)

LABELS

Address	Label
**	10

FUNCTIONS AND SUBROUTINES REFERENCED

Type	Name	Type	Name
	IRPSW_FUNC	I*4	LIB\$EXTZV

E 8
16-Sep-1984 00:02:07
5-Sep-1984 13:52:37

VAX-11 FORTRAN V3.4-56
DISK\$VMMASTER:[ERF.SRC]DQDISKS.FOR;1 Page 18

0001
0002

COMMAND QUALIFIERS

FORTRAN /LIS=LIS\$:DQDISKS/OBJ=OBJ\$:DQDISKS MSRC\$:DQDISKS

/CHECK=(NOBOUNDS,OVERFLOW,NOUNDERFLOW)

/DEBUG=(NOSYMBOLS,TRACEBACK)

/STANDARD=(NOSYNTAX,NOSOURCE FORM)

/SHOW=(NOPREPROCESSOR,NOINCLUDE,MAP)

/F77 /NOG_FLOATING /I4 /OPTIMIZE /WARNINGS /NOD_LINES /NOCROSS_REFERENCE /NOMACHINE_CODE /CONTINUATIONS=19

COMPILATION STATISTICS

Run Time: 10.94 seconds

Elapsed Time: 25.02 seconds

Page Faults: 269

Dynamic Memory: 248 pages

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This image displays a grid of 150 small thumbnail images, each representing a different software utility or program. The thumbnails are arranged in 10 rows and 15 columns. Many thumbnails contain text labels such as "CLASSIFY LIS", "CSTRING LIS", "DHEADS LIS", "DR250 LIS", "DR780 LIS", "DR11W LIS", "COMPRESS LIS", "DECODECC LIS", "DUMPREG LIS", "DUTDIRTUR LIS", "CALCMAP LIS", "DUP3271 LIS", "CRYPTK LIS", and "DODISKS LIS". Other thumbnails show various graphical elements like bar charts, histograms, and data tables.