


```

DDDDDDDD  RRRRRRRR  333333  222222  CCCCCCCC  000000  MM  MM
DDDDDDDD  RRRRRRRR  333333  222222  CCCCCCCC  000000  MM  MM
DD  DD  RR  RR  33  33  22  22  CC  00  00  MMMM  MMMM
DD  DD  RR  RR  33  33  22  22  CC  00  00  MMMM  MMMM
DD  DD  RR  RR  33  33  22  22  CC  00  00  MM  MM  MM
DD  DD  RR  RR  33  33  22  22  CC  00  00  MM  MM  MM
DD  DD  RRRRRRRR  33  22  22  CC  00  00  MM  MM  MM
DD  DD  RRRRRRRR  33  22  22  CC  00  00  MM  MM  MM
DD  DD  RR  RR  33  22  22  CC  00  00  MM  MM  MM
DD  DD  RR  RR  33  22  22  CC  00  00  MM  MM  MM
DD  DD  RR  RR  33  22  22  CC  00  00  MM  MM  MM
DD  DD  RR  RR  33  22  22  CC  00  00  MM  MM  MM
DD  DD  RR  RR  33  22  22  CC  00  00  MM  MM  MM
DDDDDDDD  RR  RR  333333  2222222222  CCCCCCCC  000000  MM  MM
DDDDDDDD  RR  RR  333333  2222222222  CCCCCCCC  000000  MM  MM

```

```

FFFFFFFFFF  000000  RRRRRRRR
FFFFFFFFFF  000000  RRRRRRRR
FF  00  00  RR  RR
FF  00  00  RR  RR
FF  00  00  RR  RR
FF  00  00  RR  RR
FFFFFFFFFF  00  00  RRRRRRRR
FFFFFFFFFF  00  00  RRRRRRRR
FF  00  00  RR  RR
FF  00  00  RR  RR
FF  00  00  RR  RR
FF  00  00  RR  RR
FF  000000  RR  RR
FF  000000  RR  RR

```

DR32COM

This is the DR32 common for text used by both the DR780 and DR750 modules.

Version: 'V04-000'

```

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```

Author: Sharon Reynolds Creation Date: 10-Aug-1981

```

Modified by:
             EAD0008                      Elliott A. Drayton                      26-Jan-1983
             Moved data statements which initialized CHARACTER arrays
             to module ERRPRT.

```

```

Common /DR32/
1 v1dr_sl, v2dr_sl,
2 v1dr_bb, v1dr_cb

```

```

CHARACTER*23    V1DR_SL(0:14)
CHARACTER*22    V2DR_SL(21:23)
CHARACTER*31    V1DR_BB(1:3)
CHARACTER*32    V1DR_CB(1:3)

```

