


```

WW      W  FFFFFFFF 88888888 000000  TTTTTTTTTT  TTTTTTTTTT  000000  MM      MM
WW      WW FFFFFFFF 88888888 000000  TTTTTTTTTT  TTTTTTTTTT  000000  MM      MM
WW      WW FF          88      88 00      00  TT      TT  00      00  MMMM  MMMM
WW      WW FF          88      88 00      00  TT      TT  00      00  MMMM  MMMM
WW      WW FF          88      88 00      00  TT      TT  00      00  MM   MM  MM
WW      WW FF          88      88 00      00  TT      TT  00      00  MM   MM  MM
WW      WW FFFFFFFF 88888888 00      00  TT      TT  00      00  MM      MM
WW      WW FFFFFFFF 88888888 00      00  TT      TT  00      00  MM      MM
WW      WW FF          88      88 00      00  TT      TT  00      00  MM      MM
WW      WW FF          88      88 00      00  TT      TT  00      00  MM      MM
WWW     WWW FF          88      88 00      00  TT      TT  00      00  MM      MM
WWW     WWW FF          88      88 00      00  TT      TT  00      00  MM      MM
WW      WW FF          88888888 000000  TT      TT  000000  MM      MM
WW      WW FF          88888888 000000  TT      TT  000000  MM      MM

```

....
....
....
....

```

LL      I I I I I  S S S S S S S
LL      I I I I I  S S S S S S S
LL      I I          S S
LL      I I          S S
LL      I I          S S
LL      I I          S S
LL      I I          S S S S S
LL      I I          S S S S S
LL      I I          S S
LL      I I          S S
LL      I I          S S
LL      I I          S S
LLLLLLLLLLLL I I I I I  S S S S S S S
LLLLLLLLLLLL I I I I I  S S S S S S S

```

```

1 0001 0 %TITLE 'EDT$WFBOTTOM - bottom of buffer'
2 0002 0 MODULE EDT$WFBOTTOM ( ! Move to bottom of current buffer
3 0003 0 IDENT = 'V04-000' ! File: WFBOTTOM.BLI Edit: JBS1008
4 0004 0 ) =
5 0005 1 BEGIN
6 0006 1
7 0007 1 *****
8 0008 1 *
9 0009 1 * COPYRIGHT (c) 1978, 1980, 1982, 1984 BY *
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25 0025 1 * SOFTWARE ON EQUIPMENT WHICH IS NOT SUPPLIED BY DIGITAL. *
26 0026 1 *
27 0027 1 *****
28 0028 1
29 0029 1
30 0030 1
31 0031 1 **
32 0032 1 FACILITY: FDT -- The DEC Standard Editor
33 0033 1
34 0034 1 ABSTRACT:
35 0035 1
36 0036 1 Move to the bottom of the current buffer.
37 0037 1
38 0038 1 ENVIRONMENT: Runs at any access mode - AST reentrant
39 0039 1
40 0040 1 AUTHOR: Bob Kushlis, CREATION DATE: October 16, 1978
41 0041 1
42 0042 1 MODIFIED BY:
43 0043 1
44 0044 1 1-001 - Original. DJS 23-Feb-1981. This module was created by
45 0045 1 extracting routine EDT$WFBOT from module EDTWF.
46 0046 1 1-002 - Regularize headers. JBS 16-Mar-1981
47 0047 1 1-003 - Abort on control C. JBS 04-Jan-1982
48 0048 1 1-004 - Set a flag if control C actually aborts something. JBS 24-May-1982
49 0049 1 1-005 - Remove EDT$SET_WKLN. JBS 14-Sep-1982
50 0050 1 1-006 - Check control C about once per second. SMB 17-Sep-1982
51 0051 1 1-007 - Go back to checking control C every record. STS 20-Sep-1982
52 0052 1 1-008 - Keep TBCB_CUR_LIN accurate, for updating the select range. JBS 28-Dec-1982
53 0053 1
54 0054 1

```

EDT\$WFBOTTOM
V04-000

EDT\$WFBOTTOM - bottom of buffer
Declarations

C 4
16-Sep-1984 02:02:35
14-Sep-1984 12:25:26

VAX-11 Bliss-32 V4.0-742 Page 2
DISK\$VMSMASTER:[EDT.SRC]WFBOTTOM.BLI;1 (2)

```

: 56 0055 1 %SBTTL 'Declarations'
: 57 0056 1
: 58 0057 1 : TABLE OF CONTENTS:
: 59 0058 1 :
: 60 0059 1
: 61 0060 1 REQUIRE 'EDT$SRC:TRAROUNAM';
: 62 0499 1
: 63 0500 1 FORWARD ROUTINE
: 64 0501 1 EDT$WF_BOT : NOVALUE;
: 65 0502 1
: 66 0503 1 :
: 67 0504 1 : INCLUDE FILES:
: 68 0505 1 :
: 69 0506 1
: 70 0507 1 REQUIRE 'EDT$SRC:EDTREQ';
: 71 0642 1
: 72 0643 1 :
: 73 0644 1 : MACROS:
: 74 0645 1
: 75 0646 1 : NONE
: 76 0647 1
: 77 0648 1 : EQUATED SYMBOLS:
: 78 0649 1
: 79 0650 1 : NONE
: 80 0651 1
: 81 0652 1 : OWN STORAGE:
: 82 0653 1
: 83 0654 1 : NONE
: 84 0655 1
: 85 0656 1 : EXTERNAL REFERENCES:
: 86 0657 1
: 87 0658 1 : In the routine
```

```

89 0659 1 %SBTTL 'EDT$$WF_BOT - move to bottom of buffer'
90 0660 1
91 0661 1 GLOBAL ROUTINE EDT$$WF_BOT ! Move to bottom of current buffer
92 0662 1 : NOVALUE =
93 0663 1
94 0664 1 +-+
95 0665 1 FUNCTIONAL DESCRIPTION:
96 0666 1
97 0667 1 Move to the bottom of the current buffer.
98 0668 1
99 0669 1 FORMAL PARAMETERS:
100 0670 1
101 0671 1 NONE
102 0672 1
103 0673 1 IMPLICIT INPUTS:
104 0674 1
105 0675 1 EDT$$A_CUR_BUF
106 0676 1 EDT$$A_WK_BUK
107 0677 1
108 0678 1 IMPLICIT OUTPUTS:
109 0679 1
110 0680 1 EDT$$A_CUR_BUF
111 0681 1 EDT$$G_CC_DONE
112 0682 1 EDT$$A_WK_LN
113 0683 1
114 0684 1 ROUTINE VALUE:
115 0685 1
116 0686 1 NONE
117 0687 1
118 0688 1 SIDE EFFECTS:
119 0689 1
120 0690 1 NONE
121 0691 1
122 0692 1 --
123 0693 1
124 0694 2 BEGIN
125 0695 2
126 0696 2 EXTERNAL ROUTINE
127 0697 2 EDT$$WF_MAKECUR : NOVALUE,
128 0698 2 EDT$$RD_NXTLN,
129 0699 2 EDT$$CHR_CC; ! Check for a control C
130 0700 2
131 0701 2 EXTERNAL
132 0702 2 EDT$$A_CUR_BUF : REF TBCB_BLOCK, ! Current text buffer control block
133 0703 2 EDT$$A_WK_BUK : ! Pointer to current bucket
134 0704 2 REF_BLOCK [WF_BUKT_SIZE, BYTE] FIELD (WFB_FIELDS),
135 0705 2 EDT$$G_CC_DONE, ! Set to 1 if control C actually aborts something
136 0706 2 EDT$$A_WK_LN : REF LIN_BLOCK; ! Pointer to work line
137 0707 2
138 0708 2 LOCAL
139 0709 2 CONTROL_C,
140 0710 2 READ_STATUS;
141 0711 2
142 0712 2 +-+
143 0713 2 ! Just read the last bucket in the buffer, then
144 0714 2 ! read lines until we can't read any more.
145 0715 2 -

```

```

: 146      0716  2      EDTSSA_CUR_BUF [TBCB_CUR_BUKT] = .EDTSSA_CUR_BUF [TBCB_LAST_BUKT];
: 147      0717  2      MOVELINE (EDTSSA_CUR_BUF [TBCB_LINE_COUNT], EDTSSA_CUR_BUF [TBCB_CUR_LIN]);
: 148      0718  2      EDT$$WF_MAKECUR (.EDTSSA_CUR_BUF [TBCB_CUR_BUKT]);
: 149      0719  2      EDTSSA_CUR_BUF [TBCB_LINE_ADDR] = .EDTSSA_WK_BUK [WFB_END];
: 150      0720  2      EDTSSA_CUR_BUF [TBCB_CHAR_POS] = 0;
: 151      0721  2      EDTSSA_WK_LN = CHSPTR (.EDTSSA_WK_BUK, .EDTSSA_CUR_BUF [TBCB_LINE_ADDR]);
: 152      0722  2
: 153      0723  2      DO
: 154      0724  3          BEGIN
: 155      0725  3          READ_STATUS = EDT$$RD_NXTLN ();
: 156      0726  3
: 157      0727  3          IF .READ_STATUS
: 158      0728  3          THEN
: 159      0729  4              BEGIN
: 160      0730  4              CONTROL_C = EDT$$CHK_CC ();
: 161      0731  4
: 162      0732  4              IF .CONTROL_C THEN EDT$$G_CC_DONE = 1;
: 163      0733  4
: 164      0734  3              END;
: 165      0735  3
: 166      0736  3          END
: 167      0737  2      UNTIL (.CONTROL_C OR ( NOT .READ_STATUS));
: 168      0738  2
: 169      0739  1      END;

```

! of routine EDT\$\$WF_BOT

```

.TITLE EDT$WFBOTTOM EDT$WFBOTTOM - bottom of buffer
.IDENT \V04-000

.EXTRN EDT$$WF_MAKECUR
.EXTRN EDT$$RD_NXTLN, EDT$$CHK_CC
.EXTRN EDTSSA_CUR_BUF, EDTSSA_WK_BUK
.EXTRN EDT$$G_CC_DONE, EDTSSA_WK_LN

.PSECT _EDT$CODE, NOWRT, SHR, PIC, 2

```

				00FC 00000	.ENTRY	EDT\$\$WF_BOT, Save R2,R3,R4,R5,R6,R7	: 0661
			57 00000000G	00 9E 00002	MOVAB	EDTSSA_CUR_BUF, R7	: 0716
			56	67 D0 00009	MOVL	EDTSSA_CUR_BUF, R6	: 0717
	06	A6	04	A6 B0 0000C	MOVW	16(R6), 4(R6)	: 0718
			18	A6 28 00011	MOVW	#6, 24(R6), 6(R6)	: 0719
			00000000G	7E 04 A6 3C 00017	MOVZWL	4(R6), -(SP)	: 0720
			00	01 FB 0001B	CALLS	#1, EDT\$\$WF_MAKECUR	: 0721
			50	67 D0 00022	MOVL	EDTSSA_CUR_BUF, R0	: 0722
			51 00000000G	00 D0 00025	MOVL	EDTSSA_WK_BUK, R1	: 0723
			60	04 A1 D0 0002C	MOVL	4(R1), -(R0)	: 0724
			00000000G	0C A0 B4 00030	CLRW	12(R0)	: 0725
			00	60 C1 00033	ADDL3	(R0), R1, EDTSSA_WK_LN	: 0726
			00000000G	00 00 FB 0003B 1\$:	CALLS	#0, EDT\$\$RD_NXTLN	: 0727
			53	50 D0 00042	MOVL	R0, READ_STATUS	: 0728
			14	53 E9 00045	BLBC	READ_STATUS, 2\$: 0729
			00000000G	00 00 FB 00048	CALLS	#0, EDT\$\$CHK_CC	: 0730
			52	50 D0 0004F	MOVL	R0, CONTROL_C	: 0731
			0A	52 E9 00052	BLBC	CONTROL_C, 3\$: 0732
			00000000G	00 01 D0 00055	MOVL	#1, EDT\$\$G_CC_DONE	: 0733
			03	52 E8 0005C 2\$:	BLBS	CONTROL_C, 4\$: 0734
			09	53 E8 0005F 3\$:	BLBS	READ_STATUS, 1\$: 0735

EDT\$WFBOTTOM
V04-000

EDT\$WFBOTTOM - bottom of buffer
EDT\$\$WF_BOT - move to bottom of buffer

G 4
16-Sep-1984 02:02:35
14-Sep-1984 12:25:26

VAX-11 Bliss-32 V4.0-742
DISK\$VMSMASTER:[EDT.SRC]WFBOTTOM.BLI;1 (4)

EDT\$
V04-

: 173 0742 1 END
: 174 0743 1
: 175 0744 0 ELUDOM

! of module EDT\$WFBOTTOM

PSECT SUMMARY

Name	Bytes	Attributes
_EDT\$CODE	99	NOVEC,NOWRT, RD, EXE, SHR, LCL, REL, CON, PIC,ALIGN(2)

Library Statistics

File	Total	Symbols Loaded	Percent	Pages Mapped	Processing Time
_\$255\$DUA28:[EDT.SRC]EDT.L32;1	377	39	10	40	00:00.2
_\$255\$DUA28:[EDT.SRC]PSECTS.L32;1	2	1	50	7	00:00.1

COMMAND QUALIFIERS

BLISS/CHECK=(FIELD,INITIAL,OPTIMIZE)/NOTRACEBACK/LIS=LIS\$:WFBOTTOM/OBJ=OBJ\$:WFBOTTOM MSRC\$:WFBOTTOM.BLI/UPDATE=(ENH\$:WFBOTTOM)

: Size: 99 code + 0 data bytes
: Run Time: 00:11.6
: Elapsed Time: 00:32.8
: Lines/CPU Min: 3848
: Lexemes/CPU-Min: 11968
: Memory Used: 77 pages
: Compilation Complete

This section contains a grid of 144 small diagrams, each representing a different Logical Interconnect System (LIS) component. The diagrams are arranged in 12 rows and 12 columns. Each diagram typically shows a block of logic with various lines, gates, and buffers connecting them. The following table lists the names of the LIS components shown in the grid:

UMMSG LIS	WFCOPLIN LIS										
USSTRINS LIS										WFCOPY LIS	
			WFDLIN LIS	WGETBKT LIS	WFOPNBUF LIS	WFREABCK LIS	WFREAFWD LIS				
											WFSTRINS LIS
	WFAPPBKT LIS										
				WFCOIN LIS						WFCSSO LIS	
UGBUFFER LIS	WFCLEAR LIS										
	USSUBS LIS		WFDEL BKT LIS								WFSPLBKT LIS
						WFLOCLIN LIS	WFRBKT LIS				
					WFINSLIN LIS			WFREACUR LIS	WFRAINP LIS		WFTOP LIS
									WFREPLIN LIS		
	WFBOTTOM LIS		WFCOPY LIS								