DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD		RRRR	RRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRR		VVV VVV	VVV VVV		RRRRR	RRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRR	
DDD	DDD	RRR	RR		VVV	VVV	EEE	RRR		RRR
DDD	DDD	RRR	RR RR		VVV	VVV	EEE	RRR		RRR
DDD	DDD	RRR	RR		VVV	VVV	ÉÉÉ	RRR		RRR
DDD	DDD	RRR	RR	R III	VVV	VVV	EEE	RRR	R	RRR
DDD	DDD	RRR	RR	R III	VVV	VVV	EEE	RRR		RRR
DDD	DDD		RRRRRRR	iii	VVV	AAA	EEEEEEEEEEE		RRRRRRR	
DDD	DDD		RRRRRRRR	111	VVV	VVV	EEEEEEEEEEE		RRRRRRRR	
DDD	DDD	RRR	RRR	iii	VVV	VVV	EEE	RRR	RRR	
DDD	DDD	RRR	RRR	iii	VVV	VVV	ĒĒĒ	RRR	RRR	
DDD	DDD	RRR	RRR	III	VVV	VVV	EEE	RRR	RRR	
DDD	DDD	RRR	RRR	III	VVV	VVV	EEE	RRR	RRR	
DDD	DDD	RRR	RRR	III	VVV	VVV	EEE	RRR	RRR	
DDD	DDD	RRR	RRR	R IIIIIIIII	VVV	vv	EEE	RRR	RRR	RRR
DDDDDDDDDDDD		RRR	RR			VV	EEEEEEEEEEEE	RRR		RRR
DDDDDDDDDDDD		RRR	RR			VV	EEEEEEEEEEEE	RRR		RRR

_1

XX	GGGGGGGG GGGGGGGG GG GG GG GG GG GG GG	DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD
MM	DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD	

: XGDEF.MDL - System definitions for the XGDRIVER not included in LIB.MLB

Version: 'V04-000'

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Facility VAX/VMS System Macro Library

ABSTRACT:

This file contains the MDL file for the XGDRIVER for those definitions not included in LIB.MLB.

AUTHOR: M. M. Dumont CREATION DATE: 1-Apr-1981

Modified by:

MMD0309 Meg Dumont, 16-Jul-1984 15:43 Remove XGDEF to make it part of LIB.MLB.

MMD0208 Meg Dumont, 8-Dec-1983 13:02
Add some comments

MMD0001 M. M. Dumont 13-0ct-1982

Add definitions for LAPB and DDCMP protocols.

LAPB - Data structure for the buffer allocated when the xgdriver is running in LAPB mode.

SSTRUCT LAPB F HDR,L,3 L HDRLEN F ,L,1

: 3 longwords for the buffer header : The header length : reserved

Please note that the following field is necessary because the XMTER in the XGDRIVER expects this field to be there. It is only useful however when the driver is running DDCMP mode.

XQCNT.B
.B.1
ERRSTRT
DEITYP.W
DEIBC.W
<M
IHCRC...M
IDCRC...M
DEI.B
ERREND
.B.1
.L.2

Count of messages queued
Reserved
Start of the error counters
Data errors inbound
Data error inbound bit counter
Inbound header CRC error

Data error inbound counter End of the error counters reserved

; Inbound data CRC error

The following fields must appear in this place in the LAPB buffer. The reason is that the xgdriver expects to transmit all IO's from a given queue. The queue it expects to use is at this offset in the DDCMP definitions. There is an ASSUME statement which will break in the driver if this is not the case. Also remember that DDCMP has two transmit queus, one for control messages and one for data messages.

: reserved

F KMTQ,Q F CLEANQ,Q F BLANK,Q L LENGTH : List head for the XMTQ : List head for the CLEAN queue : End of LAPB XMT QUEUES

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XGDEF . MDL:1
BISYNC- Data structure for the buffer allocated when the xgdriver is
             running in BISYNC mode.
           SSTRUCT BISYNC
F HDR.L.3
L HDRLEN
                                                           : 3 longwords for the buffer header ; the header length
                        RCV INDEX, W
                                                           ; Current index into RCV buffer
                                                           : Status word
                        <M
                       RCV_COMPLETE ... M
                                                          : Receive completed posted to user : TQE is running
                        TIMER_RUNNING ... M
; See explanation in LAPB
                       XQCNT,B
DROP_RCV,W
B,1
INIT_STATE_INFO,Q
RCV_BUFFER,A
                                                           : Count of messages xmted
: Count of RCVs dropped to large for buff
                                                           : resvered
                                                           ; Initial state information
                                                           : Device RCV buffer address
The following fields must appear in this place in the BISYNC buffer. For partly the same reason as LAPB except that we can not queue messages to the same queues. The reason we can not use the same gueues is that BISYNC
; can run in HALF duplex mode. In order to be consistent with half
; duplex mode for DDCMP both modes must queue the messages to the same ; queue, the ddcmp XMTQ and the bisync XMTQ are made the same queue.
                                                          : spare queue (ddcmp ctlq and lapb xmtq)
: Bisync (LEANQ
                       BLANK,Q
                        CLEANQ,Q
                                                           : Bisync XMTQ
: Buffer for TQE
                        MTQ.Q
                       TOE.L.15
LENGTH
```

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