


```

XX      XX  DDDDDDD  EEEEEEEEE  LL      TTTTTTTTT  AAAAAA
XX      XX  DDDDDDD  EEEEEEEEE  LL      TTTTTTTTT  AAAAAA
XX      XX  DD      DD  EE      LL      TT      AA      AA
XX      XX  DD      DD  EE      LL      TT      AA      AA
  XX    XX  DD      DD  EE      LL      TT      AA      AA
  XX    XX  DD      DD  EE      LL      TT      AA      AA
    XX  XX  DD      DD  EEEEEEE  LL      TT      AA      AA
    XX  XX  DD      DD  EEEEEEE  LL      TT      AA      AA
  XX    XX  DD      DD  EE      LL      TT      AAAAAAAAAA
  XX    XX  DD      DD  EE      LL      TT      AAAAAAAAAA
XX      XX  DD      DD  EE      LL      TT      AA      AA
XX      XX  DD      DD  EE      LL      TT      AA      AA
XX      XX  DDDDDDD  EEEEEEEEE  LLLLLLLLLL  TT      AA      AA
XX      XX  DDDDDDD  EEEEEEEEE  LLLLLLLLLL  TT      AA      AA

```

```

LL      IIIIII  SSSSSSSS
LL      IIIIII  SSSSSSSS
LL      II     SS
LL      II     SS
LL      II     SS
LL      II     SS
LL      II     SSSSSS
LL      II     SSSSSS
LL      II     SS
LL      II     SS
LL      II     SS
LL      II     SS
LLLLLLLLLLLL IIIIII  SSSSSSSS
LLLLLLLLLLLL IIIIII  SSSSSSSS

```

(1)	51	HISTORY ; DETAILED
(1)	130	DECLARATIONS
(1)	378	PRIMARY COMMAND CHARACTER SWITCH
(1)	419	PRIMARY COMMAND SCANNER
(1)	493	ENDEXPR - END EXPRESSION
(1)	522	SLASH - OPEN CELL
(1)	550	RETURN - CLOSE CURRENT OPEN CELL
(1)	569	ENDFIELD - TERMINATE CURRENT FIELD
(1)	590	FETCH - OBTAIN DATA SPECIFIED
(1)	632	NEXTDOT - INCREMENT CURRENT LOCATION
(1)	649	OUTPUT - DISPLAY CONTENT
(1)	656	LINE FEED - DISPLAY NEXT
(1)	682	OUTINS - OUTPUT INSTRUCTION
(1)	784	DETERMINE CLOSEST RELOCATION REGISTER
(1)	816	OUTPUT - OUTPUT ADDRESS
(1)	959	GETCHAR - GET INPUT CHARACTER ROUTINE
(1)	1041	PLUS/MINUS OPERATORS
(1)	1061	TAB - INDIRECT DISPLAY
(1)	1083	DISPLAY INSTRUCTION RANGE
(1)	1102	EQUALS - DISPLAY VALUE
(1)	1124	SEMI - SECONDARY COMMAND SET
(1)	1155	LEFT BRACKET - MODE SELECTION
(1)	1186	SINGLE STEP
(1)	1194	STEPOVER - STEP OVER ROUTINE CALL
(1)	1228	BRKPOINT - SET/CLEAR BREAKPOINTS
(1)	1292	GO - START EXECUTION AT SPECIFIED LOCATION
(1)	1306	SEMI-I, PC VALUE
(1)	1400	REGISTER SAVE AND RESTORE
(1)	1524	GET SCB ADDRESS
(1)	1545	BPT TRAP HANDLER
(1)	1628	TBIT EXCEPTION HANDLER
(1)	1656	UNBRK - RESTORE OPCODES FOR BREAKPOINTS
(1)	1680	SETBRK - SET BREAK POINT INSTRUCTIONS
(1)	1709	GETBPTX - GET INDEX FOR BREAKPOINT
(1)	1720	QUOTE - INPUT CHARACTER STRING
(1)	1734	DEPOSIT
(1)	1819	EXECUTE - PERFORM COMMAND STRING
(1)	1831	P - PROCESSOR REGISTER PREFIX
(1)	1839	PROCESS DEBUGGER INITIALIZATION

```

0000 1      .IF      DF,SW_PROCESS
0000 2      .TITLE  DELTA - MULTIMODE PROCESS DEBUGGER
0000 3      .IFF
0000 4      .TITLE  XDELTA - EXECUTIVE DEBUGGER
0000 5      .ENDC
0000 6      .IDENT  'V04-000'
0000 7
0000 8
0000 9
0000 10
0000 11  *
0000 12  *  COPYRIGHT (c) 1978, 1980, 1982, 1984 BY
0000 13  *  DIGITAL EQUIPMENT CORPORATION, MAYNARD, MASSACHUSETTS.
0000 14  *  ALL RIGHTS RESERVED.
0000 15  *
0000 16  *  THIS SOFTWARE IS FURNISHED UNDER A LICENSE AND MAY BE USED AND COPIED
0000 17  *  ONLY IN ACCORDANCE WITH THE TERMS OF SUCH LICENSE AND WITH THE
0000 18  *  INCLUSION OF THE ABOVE COPYRIGHT NOTICE. THIS SOFTWARE OR ANY OTHER
0000 19  *  COPIES THEREOF MAY NOT BE PROVIDED OR OTHERWISE MADE AVAILABLE TO ANY
0000 20  *  OTHER PERSON. NO TITLE TO AND OWNERSHIP OF THE SOFTWARE IS HEREBY
0000 21  *  TRANSFERRED.
0000 22  *
0000 23  *  THE INFORMATION IN THIS SOFTWARE IS SUBJECT TO CHANGE WITHOUT NOTICE
0000 24  *  AND SHOULD NOT BE CONSTRUED AS A COMMITMENT BY DIGITAL EQUIPMENT
0000 25  *  CORPORATION.
0000 26  *
0000 27  *  DIGITAL ASSUMES NO RESPONSIBILITY FOR THE USE OR RELIABILITY OF ITS
0000 28  *  SOFTWARE ON EQUIPMENT WHICH IS NOT SUPPLIED BY DIGITAL.
0000 29  *
0000 30  *
0000 31  *
0000 32  *+
0000 33  * FACILITY: EXECUTIVE, DEBUGGING TOOLS
0000 34  *
0000 35  * ABSTRACT:
0000 36  * THIS MODULE PRODUCES TWO DIFFERENT DEBUGGERS DEPENDING ON THE SETTING
0000 37  * OF THE ASSEMBLY SWITCH, SW_PROCESS. DELTA IS A MULTIMODE PROCESS
0000 38  * DEBUGGER USING SYSTEM SERVICES WHILE XDELTA IS A STANDALONE EXEC
0000 39  * DEBUGGING TOOL.
0000 40  *
0000 41  * COMMAND SYNTAX IS IDENTICAL FOR BOTH VERSIONS EXCEPT FOR ENVIRONMENTAL
0000 42  * DIFFERENCES. THE SYNTAX IS QUITE TERSE AND SOMEWHAT CRYPTIC AND
0000 43  * IS DOCUMENTED IN THE "GUIDE TO WRITING AN I/O DRIVER".
0000 44  *
0000 45  * ENVIRONMENT:
0000 46  * DELTA - NORMAL PROCESS ENVIRONMENT, VARIOUS ACCESS MODES.
0000 47  * XDELTA - STANDALONE, RESIDENT, KERNEL MODE, IPL=31
0000 48  * BOTH VERSIONS MUST BE POSITION INDEPENDENT - BEWARE!
0000 49  *--

```

```

0000 51 : .SBTTL HISTORY ; DETAILED
0000 52 :
0000 53 : AUTHOR: R. HUSTVEDT CREATION DATE: 15-NOV-76
0000 54 :
0000 55 : MODIFIED BY:
0000 56 :
0000 57 : V03-016 WHM0001 Bill Matthews 18-Jul-1984
0000 58 : Call CON$GETCHAR and CON$PUTCHAR to do I/O to the console
0000 59 : terminal. Call CON$OWNCTY to allocate and CON$RELEASECTY to
0000 60 : release the console terminal.
0000 61 :
0000 62 : V03-015 MSH0039 Michael S. Harvey 1-May-1984
0000 63 : Adjust image activation SET exception vector index
0000 64 : when setting up a DELTA rundown vector so that it
0000 65 : won't be lost by a subsequent image activation prior
0000 66 : to actual rundown.
0000 67 :
0000 68 : V03-014 MSH0002 Michael S. Harvey 16-Jan-1984
0000 69 : Reenable AST delivery in EXIT command to ensure process
0000 70 : doesn't hang up when EXIT issued from kernel mode. Also,
0000 71 : lengthen input command buffer to match specified maximum
0000 72 : length in input QIO.
0000 73 :
0000 74 : V03-013 TCM0003 Trudy C. Matthews 13-Dec-1983
0000 75 : Use 'write enable bit' when enabling and disabling
0000 76 : console terminal access for venus.
0000 77 :
0000 78 : V03-012 KDM0084 Kathleen D. Morse 27-Sep-1983
0000 79 : Add MicroVAX I support to CPUDISP macros.
0000 80 :
0000 81 : V03-011 RLRCPUISP Robert L. Rappaport 15-Jun-1983
0000 82 : Recode CPUDISP macros to use new format.
0000 83 :
0000 84 : V03-010 MIR1039 Michael I. Rosenblum 27-May-1983
0000 85 : Fix non PIC reference in New format QIO
0000 86 :
0000 87 : V03-009 MIR0039 Michael I. Rosenblum 29-Apr-1983
0000 88 : Make the process based DELTA use itemlist qio's with
0000 89 : The no editing bit set.
0000 90 :
0000 91 : V03-008 JLV0236 Jake VanNoy 25-MAR-1983
0000 92 : Make QIO a QIOW in OUTZSTRING so that a read will
0000 93 : not block write.
0000 94 :
0000 95 : V03-007 TCM0002 Trudy C. Matthews 16-Feb-1983
0000 96 : Correct console enable mask in TCM0001.
0000 97 :
0000 98 : V03-006 ROW0159 Ralph O. Weber 28-JAN-1983
0000 99 : Enhance DELTA initialization to set all pages in DELTA to user
0000 100 : writable. This corrects a problem encountered while trying to
0000 101 : debug DCL with DELTA. It also guarantees that DELTA will work
0000 102 : in all access modes. Change limit on rundown handler vector
0000 103 : table from 505 to <256-7>.
0000 104 :
0000 105 : V03-005 TCM0001 Trudy C. Matthews 11-Jan-1983
0000 106 : Change 11/780 machine check handler to write PR$ SBIFS back
0000 107 : to itself to clear error bit. Add 11/790 machine check

```

0000 108 :
0000 109 :
0000 110 :
0000 111 :
0000 112 :
0000 113 :
0000 114 :
0000 115 :
0000 116 :
0000 117 :
0000 118 :
0000 119 :
0000 120 :
0000 121 :
0000 122 :
0000 123 :
0000 124 :
0000 125 :
0000 126 :
0000 127 :
0000 128 :

handler; initialize 11/790 console interface registers.

V03-004 ROW0143 Ralph O. Weber 24-NOV-1982
Change process-mode OUTZSTRING to do single QIO for whole
string. Make terminal read/write QIOs do a \$WAITEF and retry
if insufficient resources error is returned by the QIO system
service. Make reference to CTLSGL_USRUNDWN in SETRUNDWN a
weak reference so that DELTA can be linked with SYSINIT.
Fix numerous branch destinations broken by the above. Add
call to \$IODEF definition macro.

V03-003 ACG0290 Andrew C. Goldstein, 5-May-1982 20:01
Condition rundown handler on user mode startup

V03-002 ACG0286 Andrew C. Goldstein, 13-Apr-1982 15:12
Use privileged rundown handler to reset exception vectors

V03-001 RIH0097 Richard I. Mustvedt 1-Apr-1982
Turn off processor register mode when proceeding.

```

0000 130          .SBTTL  DECLARATIONS
0000 131
0000 132 :
0000 133 : INCLUDE FILES:
0000 134 :
0000 135          $ACBDEF          : DEFINE AST CONTROL BLOCK
0000 136          $CADEF           : DEFINE ASSEMBLY SWITCHES
0000 137          $CLIDDEF        : DEFINE CLI VALUES
0000 138          $IODEF          : DEFINE I/O FUNCTION CODES
0000 139          $IPLDEF         : DEFINE IPL VALUES
0000 140          $IRPDEF         : DEFINE IRP VALUES
0000 141          $PCBDEF         : DEFINE PROCESS CONTROL BLOCK
0000 142          $PRDEF          : DEFINE PROCESSOR REGISTERS
0000 143          $PRIDDEF        : DEFINE PRIORITY INCREMENT CLASSES
0000 144          $PRTDEF         : DEFINE PROTECTION VALUES
0000 145          $PSLDEF         : DEFINE PSL FIELDS
0000 146          $SSDEF          : DEFINE SYSTEM SERVICE STATUS CODES
0000 147          $TRMDEF         : DEFINE TERMINAL ITEMLIST DEFINITIONS
0000 148
0000 149 :
0000 150 : MACROS:
0000 151 :
0000 152 :
0000 153 :
0000 154 : EQUATED SYMBOLS:
0000 155 :
00000008 0000 156 V_F1=8          : FIELD 1 PRESENT FLAG
00000009 0000 157 V_F2=9          : FIELD 2 PRESENT FLAG
0000000A 0000 158 V_F3=10         : FIELD 3 PRESENT FLAG
0000000B 0000 159 V_F4=11         : FIELD 4 PRESENT FLAG
0000000C 0000 160 V_F5=12         : FIELD 5 PRESENT FLAG
0000000D 0000 161 V_INSTR=13      : INSTRUCTION DISPLAY MODE
0000 162 : (OVERRIDES HEX OR ASCII & CURTYPE)
0000 163
00000000 0000 164 V_OPEN=0         : OPEN CELL FLAG
00000001 0000 165 V_ASCII=1       : ASCII
00000002 0000 166 V_INFIELD=2     : FIELD IN PROGRESS
00000003 0000 167 V_TBIT=3       : ENABLE TBIT
00000004 0000 168 V_ATBRK=4       : AT BREAKPOINT
00000005 0000 169 V_TBITOK=5     : TBIT EXPECTED
00000006 0000 170 V_RUB=6        : RUBOUT IN PROGRESS
00000007 0000 171 V_NEGATE=7     : NEGATE BIT
0000000F 0000 172 V_PRMODE=15    : PROCESSOR REGISTER MODE
0000001F 0000 173 V_PREG=31     : PROCESSOR REGISTER FLAG
0000 174
00000000 0000 175 RDCR=0         : READ CSR
00000002 0000 176 RDBUF=2       : READ BUFFER
00000004 0000 177 OUTCR=4      : OUTPUT CSR
00000006 0000 178 OUTB=6      : OUTPUT BUFFER
0000 179
0000005C 0000 180 BSLSH=92     : BACK SLASH CODE
0000000D 0000 181 CR=13        : CARRIAGE RETURN
0000000A 0000 182 LF=10        : LINE FEED
00000027 0000 183 QUOT=39     : QUOTE
0000007F 0000 184 RUBOUT=127   : RUBOUT CODE
0000002F 0000 185 SLSH=47     : SLASH CODE
0000 186

```

```

0000 187
0000 188
0000 189 :
0000 190 :
0000 191 :
00000000 192 : OWN STORAGE:
0000 193 .PSECT Z$DEBUG_CODE, LONG, PIC, EXE, WRT
0000 194 .IF DF, SW PROCESS
0000 195 DELBASE: .LONG DELBASE-DELBASE ; RELATIVE PAGE NUMBER OF WRITABLE
0000 196 .LONG <511+DELEND-DELBASE>8^C511 ; REL PAGE NUMBER OF END OF WRITABLE
0000 197 .LONG DELTA_START-DELBASE ; START ADDRESS
0000 198 .ENDC
0000 199
0000 200 CONTEXT:
00000000 0000 201 .LONG 0 ; BUFFER PADDING
00000054 0004 202 INBUF: .BLKB 80 ; INPUT BUFFER
00000000 0054 203 STATUS: .LONG 0 ; STATUS FLAGS
00000000 0058 204 F1: .LONG 0 ; FIELDS
00000000 005C 205 F2: .LONG 0 ; 1-5
00000000 0060 206 F3: .LONG 0
00000000 0064 207 F4: .LONG 0
00000000 0068 208 F5: .LONG 0
006C 209
00000000 006C 210 MFYFLG: .LONG 0 ; MODIFY ENABLE FLAG FOR OTHER PROCESS
0070 211 ; ADDRESS SPACES
00000000 0070 212 PID: .LONG 0 ; PID FOR ADDRESS SPACE 0=>SELF
00000000 0074 213 INSLEN: .LONG 0 ; LENGTH OF PREVIOUS INSTRUCTION
00000000 0078 214 INSBUF: .LONG 0 ; ADDRESS OF INSTRUCTION STREAM BUFFER
007C 215 ; (FOR OUTPUT ADDRESS ROUTINE)
00 007C 216 FCTR: .BYTE 0 ; FIELD COUNTER
007D 217
02 007D 218 DTYPE: .BYTE 2 ; DATA TYPE
02 007E 219 CURTYPE: .BYTE 2 ; CURRENT TYPE
007F 220
00 007F 221 OPER: .BYTE 0 ; OPERATOR
0080 222 B: ; BASE OF DATA AREA(CENTER)
00000000 0080 223 CURDOT: .LONG 0 ; CURRENT LOCATION
00000000 0084 224 QUAN: .LONG 0 ; QUANTITY (;Q)
00000098 0088 225 OUTBUF: .BLKL 4 ; OUTPUT BUFFER
0098 226 ;
0098 227 ; REGISTER SAVE AREA
0098 228 ;
0098 229 SAVREG: ; REGISTER SAVE AREA
0000009C 0098 230 .BLKL 1 ; R0
000000A0 009C 231 .BLKL 1 ; R1
000000A4 00A0 232 SAVR2: .BLKL 1 ; R2
000000A8 00A4 233 .BLKL 1 ; R3
000000AC 00A8 234 .BLKL 1 ; R4
000000B0 00AC 235 .BLKL 1 ; R5
000000B4 00B0 236 .BLKL 1 ; R6
000000B8 00B4 237 .BLKL 1 ; R7
000000BC 00B8 238 .BLKL 1 ; R8
000000C0 00BC 239 .BLKL 1 ; R9
000000C4 00C0 240 .BLKL 1 ; R10
000000C8 00C4 241 .BLKL 1 ; R11
000000CC 00C8 242 SAVAP: .BLKL 1 ; AP
000000D0 00CC 243 .BLKL 1 ; (FP)

```



```

000000D4 00D0 244 SAVSP: .BLKL 1 : SP
000000D8 00D4 245 SAVPC: .BLKL 1 : PC
000000DC 00D8 246 SAVPSL: .BLKL 1 : PSL
000000DE 00DC 247 SAVOCR: .BLKW 1 : OUTPUT CSR SAVE
000000E0 00DE 248 SAVRCR: .BLKW 1 : INPUT CSR SAVE
000000E4 00E0 249 ASTEN: : AST ENABLE SAVE LOCATION
000000E4 00E0 250 SAVRXCS: .BLKL 1 : CONSOLE RECEIVER STATUS
000000E4 00E4 251 :
000000E4 00E4 252 CONTEXTSZ=.-CONTEXT : SIZE OF PER MODE CONTEXT AREA
000000E4 00E4 253 :
000000E4 00E4 254 : RESERVE SPACE FOR MULTIPLE MODE CONTEXT AREA
000000E4 00E4 255 :
000000E4 00E4 256 .IF DF,SW_PROCESS :
000000E4 00E4 257 .REPT 3 :
000000E4 00E4 258 .BLKB CONTEXTSZ : FOR EXEC,SUPER AND USER
000000E4 00E4 259 SAV...= :
000000E4 00E4 260 =.-CONTEXTSZ+<DTYPE-CONTEXT> : POINT AT DTYPE,CURTYP
000000E4 00E4 261 .BYTE 2,2 : SET TYPE TO LONGWORD
000000E4 00E4 262 =SAV... : RESTORE LOCATION COUNTER
000000E4 00E4 263 .ENDR :
000000E4 00E4 264 .ENDC :
000000E4 00E4 265 :
000000E4 00E4 266 :
000000E4 00E4 267 :
000000E4 00E4 268 : BREAK POINT DATA
000000E4 00E4 269 :
000000E4 00E4 270 :
00 00E4 271 OYROP: .BYTE 0 : OPCODE IN STEP-OVER BREAKPOINT
000000E4 00E5 272 .ALIGN LONG
000000E4 00E8 273 :
000000E4 00E8 274 BRKADR=-4
000000E4 00E8 275 .IF NDF,SW_PROCESS
00000000 00E8 276 XDELIBRK:: : ADDRESS OF INITIAL BREAKPOINT
00000000 00E8 277 .LONG INISBRK : FOR PROCESS VERSION
00000000 00EC 278 .IFF : INITIAL BREAKPOINT
00000000 00EC 279 INIBRKA: .LONG 0
00000000 00EC 280 .ENDC
00000108 00EC 281 .BLKL 7 : OTHER BREAK POINT ADDRESSES
00000008 0108 282 NBRK=<.-4-BRKADR>/4 : NUMBER OF BREAKPOINTS
0000010C 0108 283 OVRADR: .BLKL 1 : TEMPORARY BREAKPOINT FOR STEP-OVER
00000001 010C 284 NTMPBRK=1 : NUMBER OF TEMPORARY BREAKPOINTS
0000010B 010C 285 BRKOP=-1 : SAVED OPCODE
00000101 010C 286 NOP : INITIAL OPCODE
00000114 010D 287 .BLKB 7 : REMAINING OPCODES
00000115 0114 288 .BLKB 1 : TEMPORARY BREAKPOINT OPCODE
00000115 0115 289 :
00000111 0115 290 BRKDSP=-4 : DISPLAY LOCATION START
00000135 0115 291 .BLKL 8 :
00000131 0135 292 BRKCOM=-4 : COMMAND START
00000155 0135 293 .BLKL 8 :
00000155 0155 294 :
00000161 0155 295 XREGV: .BLKL 3 : X REGISTER VECTOR
00000161 0161 296 .IF NDF,SW_PROCESS :
00000161 0161 297 XDEL_LOADBASE:: : BASE OF LOADABLE CPU DEPENDANT CODE
00000000 0161 298 .LONG 0 : X3 = BASE OF SYSLOA CODE
00000000 0165 299 .LONG SCH$GL_CURPCB : X4 = CURRENT PCB ADDRESS
00000000 0169 300 .LONG SCH$GL_PCBVEC : X5 = BASE OF PCB VECTOR

```

```

00000000' 016D 301 .LONG PFNSAW_SWPVBN ; X6 = SWAP VBN
00000000' 0171 302 .LONG PFNSAL_PTE ; X7 = PTE BACK POINTER
00000000' 0175 303 .LONG PFNSAL_BAK ; X8 = BACKUP ADDRESS
00000000' 0179 304 .LONG PFNSAW_REFcnt ; X9 = REFERENCE COUNT
00000000' 017D 305 .LONG PFNSAx_FLINK ; XA = FORWARD LINK
00000000' 0181 306 .LONG PFNSAx_BLINK ; XB = BACK LINK
00000000' 0185 307 .LONG PFNSAB_STATE ; XC = STATE
00000000' 0189 308 .LONG PFNSAB_TYPE ; XD = TYPE
018D 309 XDS$GL_XESTRING::
00000000' 018D 310 .LONG XDS$GT_WORD_PFN ; XE;E WITH X0 = PFN , DEFAULT TO WORD ARRAY
0191 311 XDS$GL_XFSTRING::
00000000' 0191 312 .LONG XDS$GT_WORD_PFN ; XF;E WITH R0 = PFN , DEFAULT TO WORD ARRAY
00000199' 0195 313 MCHKSAV: .BLKL 1 ; SAVED CONTENT OF MACHINE CHECK VECTOR
0199 314 .IFF ; FOR PROCESS VERSION
0199 315 .BLKL 13
0199 316 TTIOSB: .BLKL 2 ; IO STATUS BLOCK FOR TERMINAL READ
0199 317 TTCHAN: .BLKL 1 ; CHANNEL NUMBER
0199 318 TTNAMD: .LONG 2 ; DESCRIPTOR OF INPUT/OUTPUT DEVICE
0199 319 .BLKL 1 ;TTNAMD+8 ; (ADDRESS SET BY INITIALIZATION)
0199 320 .ASCII /TT/
0199 321 TTITMLST: ; THE ITEMLIST TO ALLOW DELTA TO TURN OFF ED
0199 322 .WORD 0
0199 323 .WORD TRMS_MODIFIERS ; SPECIFY THE MODIFIERS
0199 324 .LONG TRMSM_TM_NOEDIT ; SPECIFY NO EDITING
0199 325 .LONG 0
0199 326 .WORD TERMASKLEN ; LENGTH OF TERMINATOR MASK
0199 327 .WORD TRMS_TERM ; SPECIFY THE TERMINATOR MASK
0199 328 TERMASKADR: ; ALLOW FOR RELOCATION
0199 329 .BLKL 2
0199 330 TTITMLSTLEN=-TTITMLST
0199 331 DBGINPUT:
0199 332 .LONG 9 ; DESCRIPTOR OF DEFAULT INPUT/OUTPUT
0199 333 .BLKL 1 ;DBGINPUT+8
0199 334 .ASCII /DBG$DELTA/ ; FIRST DEFAULT DELTA INPUT
0199 335 TRNINPUT:
0199 336 .LONG 64 ; TRANSLATED DBG$DELTA
0199 337 .BLKL 1 ;TRNINPUT+8 ; (ADDRESS SET BY INITIALIZATION)
0199 338 .BLKB 64
0199 339 DBGACTIVE: ; ACTIVE FLAGS BY ACCESS MODE
0199 340 .LONG 0
0199 341 EXITBLK: ; EXIT HANDLER BLOCK
0199 342 .LONG 0
0199 343 EXIHADR: .BLKL 1 ;EXIHANDLE ; EXIT HANDLER (ADDRESS SET BY INIT)
0199 344 .LONG 1 ; ARGUMENT COUNT
0199 345 EXCODA: .BLKL 1 ;EXITCODE ; ADDRESS TO STORE STATUS (ADDRESS SET BY IN
0199 346 EXITCODE:
0199 347 .LONG 1 ; RECEIVER FOR EXIT CODE
0199 348 KCOND_PRIMARY:
0199 349 .LONG 0 ; PREVIOUS KERNEL PRIMARY HANDLER
0199 350 ECOND_PRIMARY:
0199 351 .LONG 0 ; PREVIOUS EXEC PRIMARY HANDLER
0199 352 SCOND_PRIMARY:
0199 353 .LONG 0 ; PREVIOUS SUPER PRIMARY HANDLER
0199 354 KCOND_LASTCHANC:
0199 355 .LONG 0 ; PREVIOUS KERNEL LAST CHANCE HANDLER
0199 356 ECOND_LASTCHANC:
0199 357 .LONG 0 ; PREVIOUS EXEC LAST CHANCE HANDLER

```

```
0199 358 SCOND_LASTCHANC:
0199 359 .LONG 0 ; PREVIOUS SUPER LAST CHANCE HANDLER
0199 360 TERMASK: ; TERMINATOR MASK DESCRIPTOR
0199 361 .LONG <1@9>!<1@10>!<1@13>!<1@27> ; TAB,LF,CR,ESC
0199 362 .LONG <1@1>!<1@2>!<1@15>!<1@29> ; '!',',','.', '/', '=',
0199 363 .LONG <1@15>!<1@19> ; '0', '$'
0199 364 .LONG 0 ;
0199 365 TERMASKLEN = .-TERMASK ;
0199 366 .ENDC ;
0199 367 :
0199 368 : LIST OF OPCODES WHICH CALL ROUTINES
0199 369 :
0199 370 OVEROPCODES:
10 0199 371 .BYTE ^X10 ; BSBB
16 019A 372 .BYTE ^X16 ; JSB
30 019B 373 .BYTE ^X30 ; BSBW
FA 019C 374 .BYTE ^XFA ; CALLG
FB 019D 375 .BYTE ^XFB ; CALLS
00000005 019E 376 OVEROPCLN = .-OVEROPCODES
```

```

019E 378 .SBTTL PRIMARY COMMAND CHARACTER SWITCH
019E 379
019E 380 :
019E 381 : PRIMARY CHARACTER LIST
019E 382 :
019E 383 PRIMARY:
42 41 39 38 37 36 35 34 33 32 31 30 019E 384 .ASCII /0123456789ABCDEF/ : DECIMAL AND HEX CHARS
46 45 44 43 01AA 385 .ASCII /./ : DOT - CURRENT LOCATION
2E 01AE 386 .ASCII /,/ : COMMA - FIELD SEPARATOR
2C 01AF 387 OPERBAS=-PRIMARY : OPERATORS
00000012 01B0 388 .ASCII /+/: PLUS - ADD
28 01B0 389 .ASCII / / : BLANK - SAME AS PLUS
20 01B1 390 .ASCII /@/: SHIFT OPERATOR
40 01B2 391 .ASCII /*/: MULTIPLY OPERATOR
2A 01B3 392 .ASCII /%/ : DIVIDE OPERATOR
25 01B4 393 .ASCII /-/: MINUS - SUBTRACT OPERATOR
2D 01B5 394 .ASCII /[: _BRACKET - LEFT BRACKET
5B 01B6 395 TERM: : BASE OF TERMINATOR LIST
09 01B7 396 .ASCII <9> : TAB - INDIRECT
0A 01B8 397 .ASCII <10> : LINEFEED -
0D 01B9 398 .ASCII <CR> : RETURN -
2F 01BA 399 .ASCII ' / ' : SLASH - OPEN FOR DISPLAY
22 01BB 400 .ASCII ' : ' : DOUBLE QUOTE - OPEN FOR ASCII DISPLAY
3D 01BC 401 .ASCII /=/ : EQUALS - DISPLAY
1B 01BD 402 .ASCII <27> : ESCAPE - PREVIOUS LOCATION
53 01BE 403 .ASCII /S/ : STEP
4F 01BF 404 .ASCII /O/ : STEP-OVER ROUTINE
21 01C0 405 .ASCII /!/: DISPLAY INSTRUCTION
0000000A 01C1 406 NTERM=-TERM : NUMBER OF TERMINATORS
3B 01C1 407 .ASCII <59> : SEMI - INITIATE SECONDARY
3A 01C2 408 .ASCII /:/ : COLON - SEPARATE PID FORM ADDRESS
50 01C3 409 .ASCII /P/ : P - PROCESSOR REGISTER PREFIX
51 01C4 410 .ASCII /Q/ : Q - LAST QUANTITY
27 01C5 411 .ASCII /' / : QUOTE - BEGIN CHAR STRING
52 01C6 412 .ASCII /R/ : REGISTER PREFIX
47 01C7 413 .ASCII /G/ : C - GLOBAL PREFIX
48 01C8 414 .ASCII /H/ : H - HIGH, P1 SPACE PREFIX
58 01C9 415 .ASCII /X/ : X REGISTER PREFIX
0000002C 01CA 416 NPRIM=-PRIMARY : NUMBER OF PRIMARY COMMANDS
01CA 417

```

```

01CA 419 .SBTTL PRIMARY COMMAND SCANNER
01CA 420
01CA 421 :
01CA 422 :
01CA 423 :
01CA 424 :
01CA 425
00 OD OA 3F 48 45 OD OA 01CA 426 OUTER: .ASCIZ <LF><CR>/EH?/<LF><CR>
01D2 427
01D2 428 DCOM: .WORD : CALL ENTRY POINT
01D4 429 .IF DF,SW PROCESS : FOR PROCESS VERSION ONLY
01D4 430 MOVAB W*DBGEXCEP,(FP) : SET CONDITION HANDLER ADDRESS
01D4 431 .ENDC
01D4 432 BRB : ENTER SCANP
54 F1 AF 9E 01D6 433 ERROR: MOVAB OUTER,R4 : SET ADDR OF CONTROL STRING
0344 30 01DA 434 BSBW OUTZSTRING : OUTPUT ASCIZ STRING
59 SE 5D D0 01DD 435 SUPERST:MOVL FP,SP : RESET STACK
84 AB 9E 01E0 436 MOVAB INBUF-B(R11),R9 : RESET STRING ADDRESS
69 94 01E4 437 CLRB (R9) : AND FORCE READ
045A 30 01E6 438 BSBW RESET : RESET SCANNER
02 10 01E9 439 SCANP: BSBW NEXTP : SCAN INPUT
FC 11 01EB 440 BRB SCANP : SCAN IT ALL
01ED 441 NEXTP: : PROCESS NEXT PRIMARY CHAR
01ED 442 BSBW GETCHAR : GET CHARACTER
A9 AF 2C 036A 30 01ED 442 BSBW GETCHAR : GET CHARACTER
58 3A 01F0 443 LOCC RB,#NPRIM,PRIMARY : CHECK IT
DF 13 01F5 444 BEQL ERROR : NOT FOUND, ERROR
50 2C 50 C3 01F7 445 SUBL3 RO,#NPRIM,RO : RATIONALIZE INDEX
01FB 446 CASE RO,LIMIT=#16,<-
01FB 447 DOT,-
01FB 448 COMMA,-
01FB 449 OPERATOR,-
01FB 450 OPERATOR,-
01FB 451 OPERATOR,-
01FB 452 OPERATOR,-
01FB 453 OPERATOR,-
01FB 454 NEGATE,-
01FB 455 LBRACKET,-
01FB 456 TAB,-
01FB 457 LINEFEED,-
01FB 458 RETURN,-
01FB 459 SLASH,-
01FB 460 DQUOTE,-
01FB 461 EQUALS,-
01FB 462 ESCAP,-
01FB 463 STEP,-
01FB 464 STEPOVER,-
01FB 465 INSTR,-
01FB 466 SEMI,-
01FB 467 COLON,-
01FB 468 PREG,-
01FB 469 QUANT,-
01FB 470 QUOTE,-
01FB 471 REGISTER,-
01FB 472 GLOBL,-
01FB 473 HIGH,-
01FB 474 XREG,-
01FB 475 >

```

10	50	B1	0237	476		CMPW	R0,#16	:	IS NUMBER > RADIX
	9A	18	023A	477		BGEQ	ERROR	:	YES
56	10	C4	023C	478		MULL	#16,R6	:	SCALE BY RADIX
56	50	C0	023F	479		ADDL	R0,R6	:	AND ADD NEW DIGIT
6A	04	C8	0242	480	INFLD:	BISL	#<1@V_INFIELD>,(R10)	:	NOTE FIELD INPUT
		05	0245	481		RSB		:	NEXT PRIMARY CHARACTER
			0246	482					
			0246	483					
54	01	1F	9C	0246	484	GNBL:	ROTL #31,#1,R4	:	GENERATE SYSTEM SPACE PREFIX
		07	11	024A	485		BRB PRE1	:	MERGE WITH COMMON
54	7FFE0000	8F	D0	024C	486	HIGH:	MOVL #^X7FFE0000,R4	:	P1 SPACE BASE ADDRESS
		06	10	0253	487	PRE1:	BSBB ENDEXPR	:	END EXPRESSION
	56	54	D0	0255	488		MOVL R4,R6	:	SET INTO ACCUM
	E7	AF	9F	0258	489		PUSHAB INFLD	:	RETURN THROUGH INFLD
				025B	490	:	BRB ENDEXPR	:	
				025B	491				

```

                                .SBTTL ENDEXPR - END EXPRESSION
                                025B 493
                                025B 494
                                025B 495 :
                                025B 496 :
                                025B 497 :
                                025B 498 ENDEXPR:
03 6A 07 ES 025B 499 BBCC #V_NEGATE,(R10),SS : SKIP IF NOT NEGATE
   56 56 CE 025F 500 MNEGL R6,R6 : NEGATE ACCUMULATOR
   06 10 0262 501 5$: BSBB 10$ : PERFORM OPERATION
   56 D4 0264 502 CLRL R6 : CLEAR ACCUMULATOR
   FF AB 94 0266 503 CLRB OPER-B(R11) : INIT OPERATOR
   05 0269 504 RSB : AND RETURN
   026A 505 10$: CASE OPER-B(R11),TYPE=B,<- : DO OPERATION
   026A 506 ADD,- : ADD, PLUS
   026A 507 ADD,- : BLANK, PLUS
   026A 508 SHFT,- : SHIFT, @
   026A 509 MUL,- : MULTIPLY, *
   026A 510 DIV,- : DIVIDE, %
   026A 511 >
57 57 56 78 0279 512 SHFT: ASHL R6,R7,R7 : SHIFT
   05 027D 513 RSB : AND EXIT
   57 56 C4 027E 514 MUL: MULL R6,R7 ; MULTIPLY
   05 0281 515 RSB : AND EXIT
   57 56 C6 0282 516 DIV: DIVL R6,R7 ; DIVIDE
   05 0285 517 RSB : AND EXIT
   57 56 C0 0286 518 ADD: ADDL R6,R7 ; ADD
   05 0289 519 RSB : AND EXIT
   028A 520

```

```

028A 522 .SBTTL SLASH - OPEN CELL
028A 523
028A 524 :
028A 525 :
028A 526 :
028A 527 DQUOTE:
6A 02 88 028A 528 BICB #<1@V_ASCII>,(R10) : DISPLAY IN ASCII
05 11 028D 529 BRB OPEN : SET ASCII FLAG
028F 530
028F 531 SLASH:
6A 2002 8F AA 028F 532 BICW #<1@V_ASCII>!<1@V_INSTR>,(R10) : CLEAR ASCII DISPLAY MODE
6A 2000 8F AA 0294 533 OPEN: BICW #<1@V_INSTR>,(R10) : CLEAR INSTRUCTION FLAG
06 6A 08 E0 0299 534 BSBB ENDFIELD : TERMINATE FIELD
6B 04 AB D0 029B 535 BBS #V F1,(R10),5$ : ADDR SPECIFIED?
04 11 02A3 536 MOVL QUAN-B(R11),CURDOT-B(R11) : NO, GO INDIRECT
6B D8 AB D0 02A5 537 BRB 10$ : AND DISPLAY CONTENT
50 6A 01 0F EF 02A9 538 5$: MOVL F1-B(R11),CURDOT-B(R11) : SET NEW DOT
6A 01 1F 50 F0 02AE 539 10$: EXTZV #V_PMODE,#1,(R10) R0 : GET PROCESSOR REGISTER MODE FLAG
0098 30 02B3 540 INSV R0,#V_PREG,#1,(R10) : AND MOVE TO SEMI-PERMANENT COPY
22 6A 09 E1 02B6 541 BSBW LOCOUT : OUTPUT AND OPEN
6B DC AB D1 02BA 542 BBC #V F2,(R10),RSET : RANGE SPECIFIED?
1C 15 02BE 543 15$: CMPL F2-B(R11),CURDOT-B(R11) : CHECK FOR END
0086 30 02C0 544 BLEQ RSET : YES
F5 11 02C3 545 BSBW NEXTLOC : INCREMENT TO NEXT DOT
FF0E 31 02C5 546 BRB 15$ : AND CONTINUE
02C8 547 ERR4: BRW ERROR : DECLARE ERROR
02C8 548

```



```

02C8 550 .SBTTL RETURN - CLOSE CURRENT OPEN CELL
02C8 551
02C8 552 :
02C8 553 :
02C8 554 :
02C8 555 :
02C8 556 RETURN:
02C8 557 BSBB ENDFIELD : TERMINATE CURRENT FIELD
02CA 558 .ENABL LSB :
6A 11 6A 00 E5 02CA 559 BBCC #V OPEN,(R10),10$ : SKIP IF NONE OPEN
6A 2002 8F B3 02CE 560 BITW #<T@V_ASCII>!<I@V_INSTR>,(R10) ; IF ASCII OR INSTRUCTION
03 6A 08 F1 02D3 561 BNEQ RSET : DISPLAY MODE SKIP STORE OPERATION
07AB 30 02D5 562 BBC #V F1,(R10),RSET : SKIP IF NOTHING TO STORE
0364 31 02D9 563 BSBW DEPOSIT : DEPOSIT
F9 6A 08 F1 02DC 564 RSET: BRW RESET : RESET SCANNER
0355 31 02DF 565 10$: BBC #V F1,(R10),RSET : DONE IF NO INPUT
02E3 566 BRW EQC1 : OTHERWISE OUTPUT
02E6 567 .DSABL LSB :

```

```

02E6 569 .SBTTL ENDFIELD - TERMINATE CURRENT FIELD
02E6 570
02E6 571 :
02E6 572 : COMMA TERMINATE CURRENT FIELD
02E6 573 :
FF59 30 02E6 574 COMMA: BSBW INFLD ; ZERO IF NULL FIELD
02E9 575
02E9 576 :
02E9 577 : TERMINATE CURRENT FIELD
02E9 578 :
02E9 579 ENDFIELD:
16 6A 02 E5 02E9 580 BBCC #V INFIELD,(R10),10$ ; CLEAR PENDING FIELD
FF6B 30 02ED 581 BSBW ENDEXPR ; END EXPRESSION
50 FC AB 9A 02F0 582 MOVZBL FCTR-B(R11),R0 ; GET FIELD POINTER
CC 01 AA 50 E2 02F4 583 BBSS R0,1(R10),ERR4 ; ERROR IF TOO MANY FIELDS
DB AB40 57 D0 02F9 584 MOVL R7,F1-B(R11)[R0] ; STORE FIELD VALUE
FC AB 96 02FE 585 INCB FCTR-B(R11) ; INCREMENT FIELD COUNTER
56 7C 0301 586 CLRQ R6 ; CLEAR ACCUMULATORS
05 0303 587 10$: RSB ; RETURN
0304 588

```

```

0304 590      .SBTTL  FETCH - OBTAIN DATA SPECIFIED
0304 591
0304 592      :
0304 593      :
0304 594      :
0304 595      :
1D 6A 1F E0 0304 596      FETCH:  BBS      #V_PREG,(R10),40$      : BR IF PROCESSOR REGISTER
0308 597      .IF      DF,SW_PROCESS      :
0308 598      TSTL     PID-B(R11)      : CHECK FOR PROCESS GET
0308 599      BNEQ     50$      : BR IF YES
0308 600      .ENDC
0308 601      CASE     CURTYPE-B(R11),TYPE=B,<-      : OPERATE ON TYPE
0308 602      10$,-      : BYTE
0308 603      20$,-      : WORD
0308 604      30$,-      : LONG
0308 605      >
04 AB 00 BB 9A 0313 606 10$:  MOVZBL @CURDOT-B(R11),QUAN-B(R11)      : GET BYTE
05 0318 607      RSB      : RETURN
04 AB 00 BB 3C 0319 608 20$:  MOVZWL @CURDOT-B(R11),QUAN-B(R11)      : GET WORD
05 031E 609      RSB      : RETURN
04 AB 00 BB D0 031F 610 30$:  MOVL   @CURDOT-B(R11),QUAN-B(R11)      : GET LONGWORD
05 0324 611      RSB      : RETURN
0325 612      .IF      NDF,SW_PROCESS
04 AB 6B DB 05 0325 613 40$:  MFPR   CURDOT=B(R11),QUAN-B(R11)      : GET PROCESSOR REGISTER
0329 614      RSB
032A 615      .IFF      : FALSE IF PROCESS VERSION
032A 616 40$:
032A 617      $CMKRNL_S      B^FTCHPREG,(AP)      : CALL IN KERNEL MODE TO FETCH
032A 618      RSB
032A 619 50$:  BRW     FTCHP      : FETCH FROM FOREIGN PROCESS
032A 620      .ENDC
032A 621
032A 622      .IF      DF,SW_PROCESS
032A 623 FTCHPREG:
032A 624      .WORD     0      : ENTRY MASK
032A 625      MOVAB   W^PREXC,(FP)      : SET EXCEPTION HANDLER
032A 626      MFPR   CURDOT-B(R11),QUAN-B(R11)      : GET PROCESSOR REGISTER
032A 627      MOVL   #1,R0      : RETURN SUCCESS
032A 628      RET
032A 629
032A 630      .ENDC

```

```

032A 632      .SBTTL NEXTDOT - INCREMENT CURRENT LOCATION
032A 633
032A 634      :
032A 635      : INCREMENT TO NEXT LOCATION
032A 636      :
032A 637 NEXTDOT:
10 6A 0D E0 032A 638      BBS      #V_INSTR,(R10),20$      : BRANCH IF INSTRUCTION MODE
   51 01 D0 032E 639      MOVL     #1,R1      : ASSUME UNIT INCREMENT
   6A 05 D5 0331 640      TSTL     (R10)      : CHECK FOR PREG
51 51 FE AB 9C 0333 641      BLSS     10$      : YES, USE UNIT INCREMENT
   6B 51 C0 0335 642      ROTL     CURTYPE-B(R11),R1,R1 : FORM INCREMENT
   68 F4 AB 05 033A 643 10$: ADDL     R1,CURDOT-B(R11) : AND ADD TO DOT
   05 033D 644      RSB      : RETURN
   C0 033E 645 20$: ADDL     INSLN-B(R11),CURDOT-B(R11) ; SKIP OVER PREVIOUS INSTRUCTION
   05 0342 646      RSB
0343 647

```

```

0343 649 .SBTTL OUTPUT - DISPLAY CONTENT
0343 650 :
0343 651 : OUTPUT CONTENT
0343 652 :
0343 653 :
1C 0C 04 0343 654 OUTBB: .BYTE 4,12,28 : STARTING DIGIT LIST
0346 655
0346 656 .SBTTL LINE FEED - DISPLAY NEXT
0346 657
0346 658 LINEFEED: :
FF7F 30 0346 659 BSBW RETURN : CLOSE OPEN CELL
0349 660 NEXTLOC: : PROMPT WITH NEXT LOCATION
DF 10 0349 661 BSBB NEXTDOT : INCREMENT LOCATION
034B 662 LOCPROMPT: : DISPLAY ADDR/CONTENT
034B 663 BSBW OUTPUTA : OUTPUT ADDRESS
31 6A 0D E0 034E 664 LOCOUT: BBS #V_INSTR,(R10),OUTINS : BRANCH IF INSTRUCTION MODE
6A 01 88 0352 665 BSBB FETCH : FETCH CONTENT
0354 666 BISB #<1@V_OPEN>,(R10) : INDICATE OPEN CELL
0357 667
0357 668 OUTPUT: :
51 FE AB 9A 0357 669 MOVZBL CURTYPE-B(R11),R1 : GET TYPE
52 E4 AF41 9A 035B 670 MOVZBL OUTBB[R1],R2 : INIT DIGIT SELECTOR
53 04 AB D0 0360 671 MOVL QUAN-B(R11),R3 : GET QUANTITY TO DISPLAY
05 6A 01 E0 0364 672 BBS #V_ASCII,(R10),10$ : CHECK FOR ASCII OUT
019C 30 0368 673 BSBW OUTCOM : OUTPUT NUMBER IN HEX
OF 11 036B 674 BRB 20$ : AND EXIT THROUGH OUTSPACE
08 AB 53 D0 036D 675 10$: MOVL R3,OUTBUF-B(R11) : PUT STRING IN BUFFER
52 01 51 78 0371 676 ASHL R1,#1,R2 : GET COUNT
08 AB42 94 0375 677 CLRB OUTBUF-B(R11)[R2] : MARK END OF STRING
01A1 30 0379 678 BSBW OUTZBUF : OUTBUF ASCIIZ BUFFER
01CC 31 037C 679 20$: BRW OUTSPACE : FOLLOW WITH SPACE
037F 680

```

```

037F 682 .SBTTL OUTINS - OUTPUT INSTRUCTION
037F 683 :
037F 684 : OUTPUT RANGE OF INSTRUCTIONS
037F 685 :
00 09 20 20 037F 686 SPACES: .ASCIZ ' ' ; 2 SPACES AND A TAB
0383 687
0383 688 .WEAK LIBSINS_DECODE ; INSTRUCTION DECODE IS OPTIONAL
0383 689
50 00000000'GF 9E 0383 690 OUTINS: MOVAB G^LIBSINS_DECODE,R0 ; GET ADDRESS OF INSTRUCTION DECODER
07 12 038A 691 BNEQ 5$ ; BRANCH IF LINKED WITH DECODER
6A 2000 8F AA 038C 692 BICW #1@V INSTR,(R10) ; SUPPRESS INSTRUCTION MODE
BB 11 0391 693 BRB LOCOOT ; AND PRINT 1ST LONGWORD OF INS STREAM
SE 00000052 8F C2 0393 694 5$: SUBL #32+50,SP ; ALLOCATE SPACE FOR INSTRUCTION STREAM
039A 695 ; AND DECODE OUTPUT BUFFER
F8 AB SE DO 039A 696 MOVL SP,INSBUF-B(R11) ; SAVE ADDRESS FOR OUTPUT_ADDRESS
54 08 DO 039E 697 MOVL #32/4,R4 ; SET ITERATION COUNT
55 SE DO 03A1 698 MOVL SP,R5 ; SET POINTER INTO BUFFER
FE AB 02 90 03A4 699 MOVB #2,CURTYPE-B(R11) ; SET FOR LONGWORD FETCHES
6B DD 03AB 700 PUSHL CURDOT-B(R11) ; SAVE CURRENT LOCATION COUNTER
FF57 30 03AA 701 10$: BSBW FETCH ; FETCH LONGWORD
85 04 AB DO 03AD 702 MOVL QUAN-B(R11),(R5)+ ; STORE INTO INSTRUCTION BUFFER
6B 04 CO 03B1 703 ADDL #4,CURDOT-B(R11) ; SKIP TO NEXT LONGWORD
F3 54 FS 03B4 704 SOBGTR R4,10$ ; FILL ENTIRE BUFFER
6B 8ED0 03B7 705 POPL CURDOT-B(R11) ; RESTORE CURRENT LOCATION
55 DD 03BA 706 PUSHL R5 ; ADDRESS OF DECODE OUTPUT BUFFER
32 DD 03BC 707 PUSHL #50 ; LENGTH OF DECODE OUTPUT BUFFER
FE'AF 9F 03BE 708 PUSHAB B^OUTPUT_ADDRESS ; ADDRESS OF SYMBOLIZE ROUTINE
04 AE 3F 03C1 709 PUSHAW 4(SP) ; ADDRESS OF WORD TO RECEIVE LENGTH
08 AE 7F 03C4 710 PUSHAQ 8(SP) ; ADDRESS OF DECODE OUTPUT DESCRIPTOR
F8 AB DF 03C7 711 PUSHAL INSBUF-B(R11) ; ADDRESS OF INSTRUCTION STREAM POINTER
00000000'GF 04 FB 03CA 712 CALLS #4,G^LIBSINS_DECODE ; DECODE INSTRUCTION INTO BUFFER
53 8E 7D 03D1 713 MOVQ (SP)+,R3 ; GET DESCRIPTOR OF STRING
1A 50 E9 03D4 714 BLBC R0,90$ ; BRANCH IF ERROR DETECTED
6443 94 03D7 715 CLRB (R4)[R3] ; MAKE INTO ASCIZ STRING
0144 30 03DA 716 BSBW OUTZSTRING ; OUTPUT ASCIZ STRING
F4 AB F8 AB SE C3 03DD 717 SUBL3 SP,INSBUF-B(R11),INSLN-B(R11) ; SET LENGTH OF INSTRUCTION
SE 00000052 8F CO 03E3 718 50$: ADDL #32+50,SP ; DEALLOCATE STREAM/DECODE BUFFERS
54 92 AF 9E 03EA 719 MOVAB SPACES,R4 ; SET ADDRESS OF SPACES
0130 31 03EE 720 BRW OUTZSTRING ; FOLLOW INSTRUCTION WITH SOME SPACE
03F1 721 :
03F1 722 : UNABLE TO DECODE INSTRUCTION (ACCVIO OR NEW INSTRUCTION). OUTPUT LONGWORD
03F1 723 :
53 F8 6B DO 03F1 724 90$: MOVL @INSBUF-B(R11),R3 ; GET FIRST LONGWORD OF STREAM
F4 AB 01 DO 03F5 725 MOVL #1,INSLN-B(R11) ; SET INSTRUCTION LENGTH TO 1
0108 30 03F9 726 BSBW OUTLONG ; OUTPUT AS LONGWORD
E5 11 03FC 727 BRB 50$
03FE 728
03FE 729 :
03FE 730 : OUTPUT AN OPERAND WHICH IS A RELATIVE OR ABSOLUTE ADDRESS
03FE 731 :
03FE 732 :
03FE 733 OUTPUT_ADDRESS:
081C 03FE 734 .WORD ^M<R2,R3,R4,R11>
0400 735
53 04 BC DO 0400 736 MOVL @4(AP),R3 ; GET VALUE (ARGUMENT BY REFERENCE)
52 08 AC DO 0404 737 MOVL 8(AP),R2 ; GET ADDRESS OF DESCRIPTOR
OC 10 BC EB 0408 738 BLBS @16(AP),5$ ; BRANCH IF ABSOLUTE ADDRESS

```

				040C	739	.IF	DF,SW_PROCESS	: IF PROCESS VERSION,	
				040C	740	MOVPSL	R1	: GET CURRENT PSL	
				040C	741	EXTZV	#PSLSV_CURMOD,#PSLSS_CURMOD,R1,R1	: ISOLATE CURRENT ACCESS MODE	
				040C	742	MULW	#CONTEXTSZ,R1	: COMPUTE OFFSET FROM KERNEL CONTEXT	
				040C	743	MOVAB	W*B[R1],R1	: GET BASE ADDRESS OF CONTEXT AREA	
				040C	744	.IFF		: FOR EXECUTIVE VERSION,	
5B	FC70	CF	9E	040C	745	MOVAB	W*B,R11	: GET BASE ADDRESS OF CONTEXT AREA	
				0411	746	.ENDC			
53	F8	AB	C2	0411	747	SUBL	INSBUF-B(R11),R3	: GET OFFSET FROM INSTRUCTION	
	53	6B	C0	0415	748	ADDL	CURDOT-B(R11),R3	: AND COMPUTE 'REAL' ADDRESS	
04	AB	53	D0	0418	749	MOVL	R3,QUAN-B(R11)	: SET NEW 'Q' SO THAT INDIRECTION	
				041C	750			: CAN BE USED TO SEE THE LAST OPERAND	
				041C	751			: OR BRANCHED-TO INSTRUCTION	
	0C	BC	B4	041C	752	CLRW	@12(AP)	: ASSUME NOTHING PUT INTO BUFFER	
08	62	B1	041F	753	CMPW	(R2),#8		: ENOUGH ROOM FOR 8 CHARACTERS?	
	39	19	0422	754	BLSS	20\$: BRANCH IF NOT ENOUGH	
	04	A2	DD	0424	755	PUSHL	4(R2)	: SAVE ADDRESS OF RESULT BUFFER	
51	53	D0	0427	756	MOVL	R3,R1		: COPY EFFECTIVE ADDRESS	
	0045	30	042A	757	BSBW	RELOC		: SEE IF CLOSE TO RELOCATION REGISTER	
	1F	19	042D	758	BLSS	8\$: BRANCH IF NONE FOUND	
52	54	D0	042F	759	MOVL	R4,R2		: SAVE OFFSET FROM X REGISTER	
	54	8EDC	0432	760	POPL	R4		: GET ADDRESS OF OUTPUT BUFFER	
84	58	8F	90	0435	761	MOVB	#'A'X',(R4)+	: WRITE 'X'	
	50	D4	0439	762	CLRL	R0		: PRINT ONLY 1 DIGIT	
	24	10	043B	763	BSBB	100\$: WRITE REGISTER NUMBER	
84	2B	90	043D	764	MOVB	#'A'+',(R4)+		: WRITE '+'	
50	08	D0	0440	765	MOVL	#8,R0		: PRINT 3 DIGITS	
53	52	D0	0443	766	MOVL	R2,R3		: RETRIEVE OFFSET FROM X REGISTER	
	19	10	0446	767	BSBB	100\$: WRITE OFFSET IN HEX	
0C	BC	06	B0	0448	768	MOVW	#6,@12(AP)	: STORE LENGTH OF STRING	
	0F	11	044C	769	BRB	20\$: EXIT SUCCESSFULLY	
53	51	D0	044E	770	8\$:	MOVL	R1,R3	: GET EFFECTIVE ADDRESS	
	54	8ED0	0451	771	POPL	R4		: GET ADDRESS OF OUTPUT BUFFER	
50	1C	D0	0454	772	MOVL	#28,R0		: SET STARTING BIT FOR 1ST DIGIT	
	08	10	0457	773	BSBB	100\$: OUTPUT HEX LONGWORD	
0C	BC	08	B0	0459	774	MOVW	#8,@12(AP)	: RETURN LENGTH TO CALLER	
50	01	D0	045D	775	20\$:	MOVL	#1,R0	: SUCCESS	
			04	0460	776	RET			
				0461	777				
51	53	04	50	EF	0461	100\$:	EXTZV	R0,#4,R3,R1	: GET DIGIT
84	FD33	CF41	90	0466	779	MOVB	PRIMARY[R1],(R4)+	: MOVE DIGIT INTO BUFFER	
	50	04	C2	046C	780	SUBL	#4,R0	: SKIP TO NEXT DIGIT	
		FO	18	046F	781	BGEQ	100\$: LOOP UNTIL END OF LONGWORD	
			05	0471	782	RSB			

```

0472 784 .SBTTL DETERMINE CLOSEST RELOCATION REGISTER
0472 785 :
0472 786 : RELOC - GIVEN AN ADDRESS, RETURN CLOSEST RELOCATION REGISTER, IF ANY.
0472 787 :
0472 788 : INPUTS:
0472 789 :
0472 790 : R1 = ADDRESS
0472 791 :
0472 792 : OUTPUTS:
0472 793 :
0472 794 : X1 = EFFECTIVE ADDRESS
0472 795 : R3 = REGISTER #
0472 796 : R4 = OFFSET FROM X REGISTER
0472 797 : PSL CONDITION CODES SET ON R3
0472 798 : R2 DESTROYED.
0472 799 :
53 D4 0472 800 RELOC: CLR R3 : START WITH X0
52 01 CE 0474 801 MNEGL #1,R2 : X REGISTER CLOSEST TO ADDRESS
54 52 3C 0477 802 MOVZWL R2,R4 : CLOSEST SO FAR IS FFFF
50 FCD6 CF43 D0 047A 803 10$: MOVL XREGV[R3],R0 : GET X REGISTER
18 13 0480 804 BEQL 15$ : BRANCH IF NOT VALID
50 51 50 C3 0482 805 SUBL3 R0,R1,R0 : GET OFFSET FROM X#
00000800 8F 50 D1 0486 806 CMPL R0,#^X800 : WITHIN REASONABLE RANGE?
08 1E 048D 807 BGEQU 15$ : BRANCH IF OK
54 50 D1 048F 808 CMPL R0,R4 : CLOSER THAN CLOSEST SO FAR?
06 1A 0492 809 BGTRU 15$ : BRANCH IF NOT
52 53 D0 0494 810 MOVL R3,R2 : SAVE X# CLOSEST TO ADDRESS
54 50 D0 0497 811 MOVL R0,R4 : AND SET NEW CLOSEST OFFSET
DC 53 08 F2 049A 812 15$: AOBLSS #8,R3,10$ : LOOP UNTIL LAST REGISTER TESTED
53 52 D0 049E 813 MOVL R2,R3 : RETURN X# CLOSEST TO ADDRESS
05 04A1 814 RSB

```



```

04A2 816 .SBTTL OUTPUTA - OUTPUT ADDRESS
04A2 817 :
04A2 818 : OUTPUT ADDRESS
04A2 819 :
04A2 820 OUTPUTA:
51 00AB 30 04A2 821 BSBW CRLF : OUTPUT ADDRESS
68 D0 04A5 822 MOVL CURDOT-B(R11),R1 : OUTPUT CR/LF
C8 10 04A8 823 BSBB RELOC : GET ADDRESS
1B 19 04AA 824 BLSS 2$ : SEE IF CLOSE TO RELOCATION REGISTER
54 DD 04AC 825 PUSHL R4 : BRANCH IF NOT
50 58 8F 9A 04AE 826 MOVZBL #^A'X',R0 : SAVE OFFSET FROM RELOCATION REGISTER
007F 30 04B2 827 BSBW OUTCHAR : OUTPUT AN 'X'
52 D4 04B5 828 CLRL R2 : PRINT ONLY 1 HEX DIGIT
4E 10 04B7 829 BSBB OUTCOM : OUTPUT HEX VALUE IN R3
50 2B 9A 04B9 830 MOVZBL #^A'+',R0 : OUTPUT AN '+'
0075 30 04BC 831 BSBW OUTCHAR
52 08 D0 04BF 832 MOVL #8,R2 : PRINT 3 DIGITS
53 31 11 04C2 833 POPL R3 : GET OFFSET FROM RELOCATION REGISTER
18 AB 9E 04C5 834 BRB 10$ : OUTPUT OFFSET AND SLASH
04C7 835 2$: MOVAB SAVREG-B(R11),R3 : BASE OF REGISTER AREA
04CB 836 .IF DF,SW_PROCESS : ONLY FOR PROCESS VERSION
04CB 837 TSTL PID-B(R11) : CHECK FOR OTHER PROCESS ADDRESS
04CB 838 BNEQ 3$ : BR IF YES
04CB 839 .ENDC
53 6B 53 C3 04CB 840 SUBL3 R3,CURDOT-B(R11),R3 : COMPUTE OFFSET INTO REGISTER AREA
13 19 04CF 841 BLSS 5$ : NOT GENERAL REGISTER
53 04 C6 04D1 842 DIVL #4,R3 : SCALE TO LONGWORD NUMBER
OF 53 D1 04D4 843 CMLP R3,#15 : CHECK FOR MAX REG NUMBER
0B 14 04D7 844 BGTR 5$ : GTR, NOT A REGISTER
50 52 8F 9A 04D9 845 MOVZBL #^A'R',R0 : OUTPUT PREFIX
0054 30 04DD 846 BSBW OUTCHAR : OF 'R'
52 D4 04E0 847 CLRL R2 : AND SET FOR ONE DIGIT OF OUTPUT
14 11 04E2 848 BRB 10$
04E4 849 .IF DF,SW_PROCESS : FOR PROCESS VERSION ONLY
04E4 850 3$: TSTL (R10) : CHECK FOR PROCESSOR REGISTER
04E4 851 BLSS 5$ : BR IF YES
04E4 852 MOVL #28,R2 : SET FOR LONGWORD OUTPUT
04E4 853 MOVL PID-B(R11),R3 : GET PID OF TARGET
04E4 854 BSBB OUTCOM : OUTPUT PID AS LONGWORD
04E4 855 MOVZBL #^A':',R0 : SEPARATE WITH ':'
04E4 856 BSBW OUTCHAR : OUTPUT COLON
04E4 857 .ENDC
53 6B D0 04E4 858 5$: MOVL CURDOT-B(R11),R3 : GET ADDRESS
52 1C D0 04E7 859 MOVL #28,R2 : ASSUME LONGWORD OUTPUT
6A D5 04EA 860 TSTL (R10) : CHECK FOR PROCESSOR REGISTER
50 50 8F 9A 04EC 861 BGEQ 10$ : NO, JUST A LONGWORD
003F 30 04EE 862 MOVZBL #^A'P',R0 : PRECEDE WITH A 'P'
52 04 D0 04F2 863 BSBW OUTCHAR : OUTPUT P
0D 10 04F5 864 MOVL #4,R2 : SET FIELD TO 2 DIGITS
50 2F 9A 04F8 865 10$: BSBB OUTCOM : COMMON OUTPUT
0034 31 04FA 866 MOVZBL #SLSH,R0 : OUTPUT SLASH
0500 867 BRW OUTCHAR : RETURN THROUGH OUTCHAR
52 04 0500 868 OUTDIGIT: CLRL R2 : OUTPUT ONE DIGIT
03 11 0502 869 BRB OUTCOM : ZAP DIGIT SELECTOR
0504 870 : AND MERGE WITH COMMON
0504 871 :
0504 872 OUTLONG: : OUTPUT LONGWORD

```

```

52 1C D0 0504 873      MOVL      #28,R2      ; SET DIGIT SELECTOR
      0507 874 OUTCOM:      ; FORMAT IT
51 54 08 AB 9E 0507 875      MOVAB     OUTBUF-B(R11),R4      ; GET ADDRESS OF OUTPUT BUFFER
84 53 04 52 EF 050B 876 10$:  EXTZV     R2,#4,R3,R1      ; GET DIGIT
      FC89 CF41 90 0510 877      MOVB     PRIMARY[R1],(R4)+      ; BUFFER IT
      52 04 C2 0516 878      SUBL     #4,R2      ; NEXT DIGIT
      F0 18 0519 879      BGEQ     10$      ; DO ALL REQUESTED
      64 94 051B 880      CLRB     (R4)      ; MARK END OF BUFFER
54 08 AB 9E 051D 861 OUTZBUF:MOVAB  OUTBUF-B(R11),R4      ; GET START OF BUFFER
      0521 882      ;
      0521 883 OUTZSTRING:      ; OUTPUT ASCIZ STRING
      0521 884      .IF     NDF,SW_PROCESS      ;
50 84 9A 0521 885      MOVZBL   (R4)+,R0      ; GET A CHAR
      04 13 0524 886      BEQL     10$      ; BR IF DONE
      0C 10 0526 887      BSB     OUTCHAR      ; OUTPUT CHAR
      F7 11 0528 888      BRB     OUTZSTRING      ; CONTINUE
      05 052A 889 10$:  RSB      ; RETURN IF DONE
      052B 890      .IFF
      052B 891      PUSHL    R5      ; Save a register.
      052B 892      LOCC     #0,#256,(R4)      ; Locate the terminating zero.
      052B 893      SUBL3   R4,R1,R5      ; Compute the number of bytes to write.
      052B 894      BEQL     90$      ; Branch if zero bytes to write.
      052B 895 50$:  $QIOW_S  EFN=#30,-      ; Write whole buffer.
      052B 896      CHAN=TTCHAN,-
      052B 897      FUNC=#IOS_WRITEVBLK,-
      052B 898      P1=(R4),-
      052B 899      P2=R5
      052B 900      CMPW     R0,#SS$_INSFMEM      ; If any resource error occurs,
      052B 901      BEQL     60$      ; wait for an I/O completion
      052B 902      CMPW     R0,#SS$_EXQUOTA      ; and try again.
      052B 903      BEQL     60$
      052B 904      CMPW     R0,#SS$_EXQUOTASTRT
      052B 905      BLSSU   90$
      052B 906      CMPW     R0,#SS$_EXQUOTAEND
      052B 907      BGTRU   90$
      052B 908 60$:  $WAITFR_S  EFN=#30
      052B 909      BRB     50$
      052B 910 90$:  POPL     R5      ; Restore saved register.
      052B 911      RSB
      052B 912      .ENDC
      052B 913
      052B 914
      052B 915 OUTBSLSH:      ; OUTPUT BACK SLASH
50 5C 8F 9A 052B 916      MOVZBL   #BSLSH,R0      ; SET CHARACTER CODE
      03 11 052F 917      BRB     OUTCHAR      ; AND OUTPUT IT
      50 58 9A 0531 918 OUTR8:  MOVZBL   R8,R0      ; GET CHAR TO OUTPUT
      0534 919 OUTCHAR:      ; OUTPUT CHAR IN R0
      0534 920      .IF     NDF,SW_PROCESS      ;
      5C D5 0534 921      TSTL     AP      ; CHECK FOR CONSOLE
      06 12 0536 922      BNEQ     10$      ; NO, USE DEVICE DIRECTLY
00000000'GF 17 0538 923      JMP     G^CON$PUTCHAR      ; OUTPUT TO THE CONSOLE TERMINAL
      053E 924
      51 04 AC B0 053E 925 10$:  MOVW     OUTCR(AP),R1      ; GET STATUS
      F8 51 07 E1 0542 926      BBC     #7,R1,10$      ; WAIT FOR READY
      06 AC 50 90 0546 927      MOVB     R0,OUTB(AP)      ; OUTPUT CHAR
      054A 928      .IFF      ; FALSE FOR PROCESS VERSION
      054A 929      PUSHL    R0      ; BUFFER CHARACTER ON STACK

```

```

054A 930 50$:   MOVL   SP,RO           ; SAVE POINTER TO IT
054A 931      SQIOW_S EFN=#30,-      ;
054A 932      CHAN=TTCHAN,-        ;
054A 933      FUNC=#IOS_WRITEVBLK,- ;
054A 934      P1=(RO),-            ; BUFFER ADDRESS
054A 935      P2=#1                ; ONE CHARACTER
054A 936      CMPW   RO,#SS$_INSFMEM ; If any resource error occurs,
054A 937      BEQL   60$            ; wait for an I/O completion
054A 938      CMPW   RO,#SS$_EXQUOTA ; and try again.
054A 939      BEQL   60$
054A 940      CMPW   RO,#SS$_EXQUOTASTRT
054A 941      BLSSU  90$
054A 942      CMPW   RO,#SS$_EXQUOTAEND
054A 943      BGTRU  90$
054A 944 60$:   $WAITFR_S EFN=#30
054A 945      BRB    50$
054A 946 90$:   POPR   #^M<RO>      ; RESTORE CHARACTER
054A 947      .ENDC
05 054A 948      RSB                ; AND RETURN
054B 949 OUTSPACE:
50 20 9A 054B 950 MOVZBL #32,RO      ; SET CODE FOR SPACE
   E4 11 054E 951 BRB    OUTCHAR    ; AND SEND IT
50 0D 9A 0550 952 CRLF: MOVZBL #CR,RO ; RETURN
   DF 10 0553 953 BSBB   OUTCHAR    ; SEND IT
50 0A 9A 0555 954 MOVZBL #LF,RO     ; LINE FEED
   DA 11 0558 955 BRB    OUTCHAR    ; SEND IT
055A 956
055A 957

```

```

055A 959      .SBTTL  GETCHAR - GET INPUT CHARACTER ROUTINE
055A 960
055A 961      :
055A 962      : GETCHAR - GET INPUT CHARACTER
055A 963      :
055A 964      : OUTPUT:
055A 965      : R8 - INPUT CHARACTER
055A 966      : R9 - BUFFER POINTER UPDATED (BUFFER IN ASCIZ FORMAT)
055A 967      :
055A 968
055A 969      GETCHAR:
58 89 9A 055A 970      MOVZBL  (R9)+,R8      : GET NEXT CHARACTER
      01 13 055A 971      BEQL    10$      : READ IF NONE AVAIL
59 84 AB 9E 055F 972      RSB
      5C D5 0560 973 10$: MOVAB   INBUF-B(R11),R9      : SET ADDRESS OF INPUT BUFFER
      0B 12 0564 974      .IF     NDF,SW_PROCESS
00000000 GF 16 0564 975 20$: TSTL   AP      : CHECK FOR CONSOLE
      58 50 90 0566 976      BNEQ   30$      : YES
      0D 11 0568 977      JSB    G^CON$GETCHAR      : GET A CHARACTER FROM THE CONSOLE TERMINAL
      50 6C B0 056E 978      MOVB   R0,R8
      F9 50 07 E1 0571 979      BRB   60$      : CONTINUE IN COMMON
      58 02 AC 90 0573 980 30$: MOVW   RDCR(AP),R0      : GET STATUS
      00 11 0576 981 40$: BBC     #7,R0,30$      : WAIT FOR READY
      50 6C B0 057A 982      MOVB   RDBUF(AP),R8      : GET CHARACTER
      00 11 057E 983      BRB   60$      : MERGE WITH COMMON
      0580 984      .IFF
      0580 985 15$: MOVAL   TTITMLST,R0      : FALSE IF PROCESS VERSION
      0580 986      $QIOW_S EFN=#31,-      : get the relocateable address
      0580 987      CHAN=TTCHAN,-      : INPUT DEVICE CHANNEL
      0580 988      ICSB=TTIOSB,-      : IO STATUS BLOCK
      0580 989      FUNC=#<IOS_READVBLK!IOSM_EXTEND>,-      :
      0580 990      P1=(R9),-      : BUFFER ADDRESS
      0580 991      P2=#80,-      : READ SIZE
      0580 992      P5=R0,-
      0580 993      P6=#TTITMLSTLEN
      0580 994      CMPW   R0,#SS$_INSFMEM      : If any resource error occurs,
      0580 995      BEQL   760$      : wait for an I/O completion
      0580 996      CMPW   R0,#SS$_EXQUOTA      : and try again.
      0580 997      BEQL   760$
      0580 998      CMPW   R0,#SS$_EXQUOTA$TRT
      0580 999      BLSSU  790$
      0580 1000     CMPW   R0,#SS$_EXQUOTAEND
      0580 1001     BGTRU  790$
      0580 1002 760$: $WAITFR_S EFN=#31
      0580 1003     BRB   -15$
      0580 1004 790$:
      0580 1005     MOVZWL TTIOSB+2,R0      : GET SIZE READ
      0580 1006     MOVB   TTIOSB+4,(R0)+[R9]      : BUFFER TERMINATOR
      0580 1007     CLRB   (R9)[R0]      : MARK END OF BUFFER
      0580 1008     MOVL   R9,R2      : POINT TO START OF STRING
      0580 1009 20$: MOVZBL  (R2)+,R8      : GET A CHARACTER
      0580 1010     BEQL   15$      : EMPTY, READ SOME MORE
      0580 1011     .ENDC
      0580 1012 60$: BICB   #^X80,R8      : STRIP PARITY
      0584 1013     CMPB   R8,#RUBOUT      : CHECK FOR RUBOUT
      0588 1014     BNEQ   90$      : NO
      03 6A 06 E2 058A 1015     BBSS   #V_RUB,(R10),70$      : SET START OF RUBOUT SEQUENCE

```

		FF9A	30	058E	1016		BSBW	OUTBSLSH	:	OUTPUT BACK SLASH
	58	79	9A	0591	1017	70\$:	MOVZBL	-(R9),R8	:	GET RUBBED OUT CHAR
		04	12	0594	1018		BNEQ	80\$:	SKIP INC
		59	D6	0596	1019		INCL	R9	:	POINT AT START OF BUFFER
		CA	11	0598	1020		BRB	20\$:	AND GET ANOTHER
		FF94	30	059A	1021	80\$:	BSBW	OUTR8	:	OUTPUT RUBBED OUT CHAR
		C5	11	059D	1022		BRB	20\$:	AND GET ANOTHER
	03	6A	06	059F	1023	90\$:	BBCC	#V RUB,(R10),100\$:	TERMINATE RUBOUT SEQUENCE
		FF85	30	05A3	1024		BSBW	OUTBSLSH	:	OUTPUT BACK SLASH
	03	58	06	05A6	1025	100\$:	BBC	#6,R8,110\$:	BR IF NOT ALPHA
		58	20	05AA	1026		BICB	#32,R8	:	SET TO UPPER CASE
				05AD	1027	110\$:			:	
				05AD	1028		.IF	NDF,SW_PROCESS	:	
		FF81	30	05AD	1029		BSBW	OUTR8	:	ECHO CHARACTER
				05B0	1030		.ENDC		:	
		89	58	05B0	1031		MOVB	R8,(R9)+	:	BUFFER NEW CHAR
FBFE	CF	0A	58	05B3	1032		LOCC	R8,#NTERM,TERM	:	CHECK FOR TERMINATOR
			A9	05B9	1033		BEQL	20\$:	NOT A TERMINATOR
		58	0D	05BB	1034		CMPB	#CR,R8	:	IS CHAR = RETURN
			03	05BE	1035		BNEQ	120\$:	NO
			FF8D	05C0	1036		BSBW	CRLF	:	YES, SEND CR/LF
			69	05C3	1037	120\$:	CLRB	(R9)	:	MARK END OF BUFFER
	59	84	AB	05C5	1038		MOVAB	INBUF-B(R11),R9	:	RESTORE BUFFER BASE
			FF8E	05C9	1039		BRW	GETCHAR	:	AND TRY AGAIN

```

                                .SBTTL PLUS/MINUS OPERATORS
                                PLUS/MINUS OPERATORS
                                BLANK:                                ; SAME AS PLUS
                                OPERATOR:                            ;
                                BSBW ENDEXPR                        ; END EXPR
                                SUBB3 #OPERBAS,R0,OPER-B(R11)    ; SET OPERATOR
                                RSB                                ; RETURN
                                MONADIC MINUS - NEGATE
                                NEGATE: TSTL R6                    ; TEST ACCUMULATOR
                                BEQL S$                          ; EMPTY
                                BSBW ENDEXPR                      ; OTHERWISE PERFORM OPERATION
                                5$: XORB #<1@V_NEGATE>,(R10)    ; TOGGLE NEGATE FLAG
                                10$: RSB                          ; AND RETURN
05CC 1041
05CC 1042 :
05CC 1043 :
05CC 1044 :
05CC 1045 :
05CC 1046 :
FF AB 50 FC8C 30 05CC 1047 BSBW ENDEXPR
83 05CF 1048 SUBB3 #OPERBAS,R0,OPER-B(R11)
05 05D4 1049 RSB
05D5 1050 :
05D5 1051 :
05D5 1052 :
56 D5 05D5 1053 NEGATE: TSTL R6
03 13 05D7 1054 BEQL S$
FC7F 30 05D9 1055 BSBW ENDEXPR
6A 80 8F 8C 05DC 1056 5$: XORB #<1@V_NEGATE>,(R10)
05 05E0 1057 10$: RSB
05E1 1058
05E1 1059

```

```

05E1 1061 .SBTTL TAB - INDIRECT DISPLAY
05E1 1062 :
05E1 1063 :
05E1 1064 :
50 6A 6B 04 AB D0 05E1 1065 TAB: MOVL QUAN-B(R11),CURDOT-B(R11) ; GO INDIRECT
6A 6A 01 0F EF 05E5 1066 EXTZV #V_PMODE,#1,(R10),R0 ; GET PROCESSOR REGISTER MODE
6A 01 1F 50 F0 05EA 1067 INSV R0,#V_PREG,#1,(R10) ; AND COPY TO SEMI-PERMANENT FLAG
13 11 05EF 1068 BRB LOCP ; AND DISPLAY IT
05F1 1069
05F1 1070 :
05F1 1071 :
05F1 1072 :
05F1 1073 :
05F1 1074 ESCAP:
OF 6A 0D E0 05F1 1075 BBS #V_INSTR,(R10),LOCP ; BRANCH IF INSTRUCTION MODE
51 01 D0 05F5 1076 MOVL #1,R1 ; ASSUME UNIT INCREMENT
6A D5 05F8 1077 TSTL (R10) ; CHECK FOR PROCESSOR REGISTER
05 19 05FA 1078 BLSS 10$ ; YES, USE UNIT INCREMENT
51 51 FE AB 9C 05FC 1079 ROTL CURTYPE-B(R11),R1,R1 ; FORM INCREMENT
6B 51 C2 0601 1080 10$: SUBL R1,CURDOT-B(R11) ; AND SUBTRACT FROM DOT
FD44 31 0604 1081 LOCP: BRW LOCPROMPT ; PROMPT WITH CONTENT

```

```

                                0607 1083      .SBTTL DISPLAY INSTRUCTION RANGE
                                0607 1084      :
                                0607 1085      :
                                0607 1086      :
                                0607 1087      :
                                0607 1087      INSTR: BSBW      ENDFIELD      ; TERMINATE FIELD
06 6A 02 8A 060A 1088      BICB      #1@V ASCII,(R10)      ; CLEAR CHARACTER DISPLAY MODE
6B 04 08 E0 060D 1089      BBS      #V FT,(R10),5$      ; ADDRESS SPECIFIED?
                                0611 1090      MOVL      QUAN-B(R11),CURDOT-B(R11) ; EXAMINE AT 0 IF UNSPECIFIED
                                0615 1091      BRB      10$
6B 08 AB D0 0617 1092 5$: MOVL      F1-B(R11),CURDOT-B(R11) ; IF ADDRESS SPECIFIED, SET NEW DOT
6A 2000 8F AB 061B 1093 10$: BISW      #1@V INSTR,(R10)      ; SET INSTRUCTION DISPLAY MODE
                                0620 1094      BSBW      OUTINS      ; DISPLAY INSTRUCTION
                                0623 1095      BBC      #V F2,(R10),30$      ; IF NO RANGE SPECIFIED, EXIT
0B 6A 09 E1 0627 1096 20$: CMPL      F2-B(R11),CURDOT-B(R11) ; END OF RANGE?
6B DC AB D1 062B 1097      BLEQ      30$      ; BRANCH IF DONE
                                062D 1098      BSBW      NEXTLOC      ; OUTPUT NEXT INSTRUCTION
                                0630 1099      BRB      20$      ; LOOP UNTIL DONE
                                0632 1100 30$: BRB      RESET      ; RESET SCANNER

```



```

0634 1102 .SBTTL EQUALS - DISPLAY VALUE
0634 1103 :
0634 1104 : EQUALS - VALUE DISPLAY
0634 1105 :
0634 1106 EQUALS:
0634 1107 :
05 6A FCB2 30 0634 1108 .ENABL LSB :
04 AB 08 E1 0637 1109 BSBW ENDFIELD : TERMINATE FIELD
DB AB D0 0638 1110 BBC #V F1, (R10), 10$ : IGNORE IF FIELD BLANK
FD14 30 0638 1110 EQL1: MOVL F1=B(R11),QUAN-B(R11) : SET QUANTITY
0640 1111 10$: BSBW OUTPUT : OUTPUT IT
0643 1112 : BRB RESET : RESET SCANNER
0643 1113 :
0643 1114 :
0643 1115 :
0643 1116 : RESET
0643 1117 :
0643 1118 :
6A 00FFDF80 8F CA 0643 1119 RESET: BICL #*X0FFDF80, (R10) : CLEAR FIELD AND NEGATE FLAGS
FC AB 94 064A 1120 CLR B FCTR-B(R11) : CLEAR FIELD COUNTER
56 7C 064D 1121 CLRQ R6 : RESET ACCUMULATORS
05 064F 1122 RSB : RETURN

```

```

0650 1124 .SBTTL SEMI - SECONDARY COMMAND SET
0650 1125 :
0650 1126 : SEMI
0650 1127 :
0650 1128 :
0650 1129 SECOND:
58 0650 1130 .ASCII /X/ : X REGISTER SET/DISPLAY
50 0651 1131 .ASCII /P/ : P - PROCEED
4D 0652 1132 .ASCII /M/ : M - SET MODIFY FLAG
49 0653 1133 .ASCII /I/ : I - PROGRAM COUNTER
47 0654 1134 .ASCII /G/ : G - GO, START
45 0655 1135 .ASCII /E/ : E - EXECUTE STRING
42 0656 1136 .ASCII /B/ : B - SET/CLR BREAKPOINT
00000007 0657 1137 NSEC=-SECOND : NUMBER OF SECONDARY COMMANDS
0657 1138 :
0657 1139 SEMI: :
6A 01 8A 0657 1140 BICB #<12V OPEN>,(R10) : CLEAR OPEN FLAG
FCBC 30 065A 1141 BSBW ENDFIELD : TERMINATE FIELD
FEFA 30 065D 1142 BSBW GETCHAR : GET SECONDARY COMMAND CHAR
EB AF 07 58 3A 0660 1143 LOCC RB,#NSEC,SECOND : LOCATE SECONDARY COMMAND
0665 1144 108: CASE RO,LIMIT=#1,<- : SWITCH ON TYPE
0665 1145 BRKPOINT,- : SET BREAKPOINT
0665 1146 EXECUTE,- : EXECUTE STRING
0665 1147 GO,- : SEMI-G, GO
0665 1148 PROGCTR,- : SEMI-I, INSTRUCTION CENTER
0665 1149 MFYFLGS,- : SEMI-M, MODIFY FLAG
0665 1150 PROCED,- : SEMI-P, PROCEED
0665 1151 XSET,- : SET XREGISTER
0665 1152 > :
FB5C 31 0677 1153 ERR2: BRW ERROR : ERROR

```

```

067A 1155 .SBTTL LEFT BRACKET - MODE SELECTION
067A 1156 :
067A 1157 :
067A 1158 : LEFT BRACKET
067A 1159 :
067A 1160 MODES: : MODE CHARACTER LIST
49 067A 1161 .ASCII /I/ : INSTRUCTION MODE, VARIABLE LENGTH
43 067B 1162 .ASCII /C/ : CHARACTER, CURRENT LENGTH
4C 067C 1163 .ASCII /L/ : LONG, HEX
57 067D 1164 .ASCII /W/ : WORD, HEX
42 067E 1165 .ASCII /B/ : BYTE, HEX
00000005 067F 1166 NMODES=-MODES : NUMBER OF MODE CHARACTERS
067F 1167
067F 1168
067F 1169 LBRACKET: : MODE SELECTION
F3 AF 05 FEDB 30 067F 1170 BSBW GETCHAR : GET MODE CHAR
58 3A 0682 1171 LOCC RB,#NMODES,MODES : CONVERT TO INDEX
EE 13 0687 1172 BEQL ERR2 : NOT FOUND, ERROR
04 50 D1 0689 1173 CMPL RO,#4 : CHECK FOR 'C'
10 13 068C 1174 BEQL 10$ : BRANCH IF 'C'
17 14 068C 1175 BGTR 20$ : BRANCH IF 'I'
FE AB 50 01 83 0690 1176 SUBB3 #1,RO,CURTYPE-B(R11) : SET MODE
6A 2000 8F AA 0695 1177 BICW #1@V_INSTR,(R10) : CLEAR INSTRUCTION DISPLAY MODE
6A 02 8A 069A 1178 5$: BICB #<1@V_ASCII>,(R10) : CLEAR CHARACTER DISPLAY MODE
05 069D 1179 RSB : RETURN
6A 6A 02 88 069E 1180 10$: BISB #<1@V_ASCII>,(R10) : SET CHARACTER DISPLAY MODE
2000 8F AA 06A1 1181 BICW #1@V_INSTR,(R10) : CLEAR CHARACTER DISPLAY MODE
6A 6A 02 88 069E 1180 10$: BISB #<1@V_ASCII>,(R10) : SET CHARACTER DISPLAY MODE
2000 8F AA 06A1 1181 BICW #1@V_INSTR,(R10) : CLEAR CHARACTER DISPLAY MODE
6A 2000 8F AB 06A7 1183 20$: BISW #1@V_INSTR,(R10) : SET INSTRUCTION DISPLAY MODE
EC 11 06AC 1184 BRB 5$

```

				06AE	1186	.SBTTL	SINGLE STEP		
				06AE	1187				
				06AE	1188				
				06AE	1189				
6A	02	03	01	FO	06AE	1190	STEP.	INSV	#1,#V_TBIT,#2,(R10) ; CLR V_ATBRK, SET V_TBIT
6A	80008000	BF		CA	06B3	1191		BICL	#<<1@V_PMODE>>!<1@V_PREG>>,(R10) ; CLEAR PROCESSOR REGISTER M
				04	06BA	1192		RET	; AND RETURN

```

06BB 1194 .SBTTL STEPOVER - STEP OVER ROUTINE CALL
06BB 1195 :
06BB 1196 : STEPOVER
06BB 1197 :
06BB 1198 STEPOVER:
6A 50 54 BB 9A 06BB 1199 MOVZBL @SAVPC-B(R11),R0 ; GET NEXT INSTRUCTION TO EXECUTE
80008000 8F CA 06BF 1200 BICL #<<1@V.PRMODE>!<1@V.PREG>>,(R10) ; CLEAR PROCESSOR REGISTER M
51 FACF CF 9E 06C6 1201 MOVAB OVEROPCODES,R1 ; ADDRESS OF LIST OF OPCODES
52 05 9A 06CB 1202 MOVZBL #OVEROPCLEN,R2 ; SIZE OF TABLE
81 50 91 06CE 1203 10$: CMPB R0,(R1)+ ; MATCH?
05 13 06D1 1204 BEQL 20$ ; BRANCH IF FOUND
F8 52 F5 06D3 1205 SOBGR R2,10$ ; LOOP UNTIL FOUND
D6 11 06D5 1206 BRB STEP ; IF NOT A ROUTINE CALL, NORMAL STEP
52 00000000 GF 9E 06D8 1207 20$: MOVAB G^LIB$INS_DECODE,R2 ; GET ADDRESS OF FOLLOWING INSTRUCTION
1E 13 06DF 1208 BEQL 30$ ; IF NOT AVAILABLE, ERROR
54 AB DD 06E1 1209 PUSHL SAVPC-B(R11) ; COPY ADDRESS OF INSTRUCTION STREAM
00 DD 06E4 1210 PUSHL #0 ; PUSH NULL DESCRIPTOR
5E DD 06E6 1211 PUSHL SP ; ADDRESS OF OUTPUT DESCRIPTOR
08 AE DF 06E8 1212 PUSHAL 8(SP) ; ACCESS INSTRUCTION STREAM DIRECTLY
62 02 FB 06EB 1213 CALLS #2,(R2) ; FIND ADDRESS OF FOLLOWING INSTRUCTION
0B 50 E9 06EE 1214 BLBC R0,25$ ; IF NOT INTERPRETABLE, ERROR
06F1 1215 .IF DF,SW_PROCESS ;
06F1 1216 MOVL 4(SP),R4 ; GET ADDRESS OF NEXT INSTRUCTION
06F1 1217 MOVL R4,R5 ; MAKE END=START
06F1 1218 BSBW SETWRT ; MAKE INSTRUCTION WRITABLE
06F1 1219 .ENDC ;
61 03 BA 06F1 1220 POPR #^M<R0,R1> ; GET UPDATED STREAM POINTER
FAOD CF 51 90 06F3 1221 MOVB (R1),(R1) ; ERROR IF UNABLE TO WRITE BREAKPOINT
D0 06F6 1222 MOVL R1,OVRADR ; SET TEMPORARY BREAKPOINT
04 06FB 1223 RET ; START EXECUTION
06FC 1224 ;
5E 08 C0 06FC 1225 25$: ADDL #8,SP ; CLEAN STACK
FAD4 31 06FF 1226 30$: BRW ERROR ; REPORT ERROR - UNABLE TO STEP OVER

```

```

                                .SBTTL BRKPOINT - SET/CLEAR BREAKPOINTS
                                BRKPOINT
                                BRKPOINT:
59 6A 08 E1 0702 1228 BBC #V F1, (R10), SHOBRK : DISPLAY BREAKPOINTS
13 6A 09 E0 0706 1229 BBS #V F2, (R10), 20$ : YES, IT WAS SPECIFIED
52 01 D0 070A 1230 : BRKPOINT : INIT INDEX
F9D2 CF42 D5 070D 1231 : : FIND FREE SLOT
14 13 0712 1232 : BRKPOINT: : YES, GOT ONE
FFF3 52 01 08 F1 0714 1233 : BBC #V F1, (R10), SHOBRK : CHECK THEM ALL
FAB9 31 071A 1234 : BBS #V F2, (R10), 20$ : ERROR
52 DC AB D0 071D 1235 10$: MOVL #1, R2 : GET BRKPOINT NUMBER
EA 13 0721 1236 15$: TSTL BRKADR[R2] : NULL FIELD, SCAN FOR SLOT
52 08 D1 0723 1237 : BEQL 30$ : CHECK FOR LEGAL
F2 19 0726 1238 : ACBL #NBRK, #1, R2, 10$ : OUT OF RANGE
F9E4 CF42 D4 0728 1239 30$: BRW ERROR : CLEAR DISPLAY
F9FF CF42 D4 072D 1240 20$: MOVL F2-B(R11), R2 : CLEAR COMMAND ADDRESS
50 DB AB D0 0732 1241 : BEQL 10$ : GET BREAKPOINT ADDRESS
03 13 0736 1242 : CMPL #NBRK, R2 : ALLOW CLEAR OF BREAKPOINT
0738 1243 : BLSS 15$ :
0738 1244 30$: CLRL BRKDSP[R2] :
0738 1245 : CLRL BRKCOM[R2] :
0738 1246 : MOVL F1-B(R11), R0 :
0738 1247 : BEQL 35$ :
0738 1248 : .IF DF, SW PROCESS :
0738 1249 : PUSH R #*M<R0, R1, R2, R3, R4, R5> : SAVE REGISTERS FOR PROTECTION CHANGE
0738 1250 : MOV R0, R4 : SET START ADDRESS
0738 1251 : MOV R0, R5 : AND END ADDRESS
0738 1252 : BSBW SETWRT : SET PAGE WRITABLE
0738 1253 : MOV (SP), R0 : RESTORE BPT ADDRESS
60 60 90 0738 1254 : .ENDC :
0738 1255 : MOV B (R0), (R0) : TEST WRITABILITY OF ADDRESS
0738 1256 : .IF DF, SW PROCESS :
0738 1257 : BSBW REPROT : RESTORE PROTECTION
0738 1258 : POP R #*M<R0, R1, R2, R3, R4, R5> : AND REGISTERS
0738 1259 : .ENDC :
OC 6A 0A E1 0738 1260 35$: BBC #V F3, (R10), 40$ : DISPLAY SPECIFIED?
F9CB CF42 E0 AB D0 073F 1261 : MOVL F3-B(R11), BRKDSP[R2] : SET DISPLAY START
03 13 0746 1262 : BEQL 40$ : SKIP TEST IF NULL
E0 BB D5 0748 1263 : TSTL @F3-B(R11) : CHECK READABILITY
07 6A 08 E1 074B 1264 40$: BBC #V F4, (R10), 45$ : SKIP IF NO COMMAND ADDRESS
F9DB CF42 E4 AB D0 074F 1265 : MOVL F4-B(R11), BRKCOM[R2] : SET COMMAND STRING
F988 CF42 50 D0 0756 1266 45$: MOVL R0, BRKADR[R2] : SAVE BREAKPOINT ADDRESS
FEE4 31 075C 1267 : BRW RESET : RESET SCANNER AND RETURN
075F 1268 :
075F 1269 : : SHOBRK
075F 1270 :
075F 1271 : SHOBRK:
58 55 01 D0 075F 1272 : MOVL #1, R5 : INIT INDEX FOR LOOP
F97D CF45 D0 0762 1273 10$: MOVL BRKADR[R5], R8 : GET BREAKPOINT ADDRESS
2E 13 0768 1274 : BEQL 20$ : SKIP IF NULL
53 55 D0 076A 1275 : MOVL R5, R3 : BREAKPOINT NUMBER
FDE0 30 076D 1276 : BSBW CRLF : NEW LINE
FD8D 30 0770 1277 : BSBW OUTDIGIT : BPT NUMBER
FDD5 30 0773 1278 : BSBW OUTSPACE : SPACE
53 58 D0 0776 1279 : MOV R8, R3 : ADDRESS OF BPT
FD88 30 0779 1280 : BSBW OUTLONG : OUTPUT ADDRESS
FDCC 30 077C 1281 : BSBW OUTSPACE : SPACE OVER
53 F98D CF45 D0 077F 1282 : MOVL BRKDSP[R5], R3 : GET DISPLAY START
03 13 0785 1283 : BEQL 15$ : NONE
FD7A 30 0787 1284 : BSBW OUTLONG : OUTPUT DISPLAY START

```

53	F9A2	CF45	D0	078A	1285	15\$:	MOVL	BRKCOM[R5],R3	:	GET COMMAND STRING ADDRESS
		06	13	0790	1286		BEQL	20\$:	NONE
		FDB6	30	0792	1287		BSBW	OUTSPACE	:	SPACE ANOTHER
		FD6C	30	0795	1288		BSBW	OUTLONG	:	AND OUTPUT A LONGWORD
FFC4 55	01	08	F1	0798	1289	20\$:	ACBL	#NBRK,#1,R5,10\$:	DO THEM ALL
		FDAF	31	079E	1290		BRW	CRLF	:	AND EXIT THROUGH CRLF

```

07A1 1292 .SBTTL GO - START EXECUTION AT SPECIFIED LOCATION
07A1 1293 :
07A1 1294 :
07A1 1295 :
05 6A 08 E1 07A1 1296 GO: BBC #V_F1,(R10),PROCEED ; JUST PROCEED IF NO VALUE
54 AB DB AB D0 07A5 1297 MOVL F1=B(R11),SAVPC-B(R11) ; SET NEW PC
07AA 1298 : BRW PROCED ; FALL INTO PROCEED
07AA 1299 :
07AA 1300 :
07AA 1301 :
07AA 1302 PROCED:
6A 80008000 8F CA 07AA 1303 BICL #<<1@V_PMODE>>!<1@V_PREG>>,(R10) ; CLEAR PROCESSOR REGISTER M
04 07B1 1304 RET ; RETURN

```



```

07B2 1306 .SBTTL SEMI-I, PC VALUE
07B2 1307 :
07B2 1308 : SEMI-I
07B2 1309 :
FO AB FAA6 30 07B2 1310 COLON: BSBW ENDEXPR : TERMINATE EXPRESSION
57 DO 07B5 1311 : MOVL R7,PID-B(R11) : SET PID FOR PROCESS
56 7C 07B9 1312 : CLRQ R6 : RESET ACCUMULATORS
05 07BB 1313 :
07BC 1314 :
51 EC AB DE 07BC 1315 MFYFLGS:MOVAL MFYFLG-B(R11),R1 : SET MODIFY FLAG ADDRESS
17 11 07C0 1316 BRB VALUE : SET/GET VALUE
51 6B DE 07C2 1317 DOT: MOVAL CURDOT-B(R11),R1 : SET ADDRESS OF DOT
18 6A 1F E1 07C5 1318 BBC #V_PREG,(R10),VALR : WAS IT PROCESSOR REGISTER?
14 6A 0F E2 07C9 1319 BBSS #V-PRMODE,(R10),VALR : YES, SET PROCESSOR REGISTER MODE
12 11 07CD 1320 BRB VALR : READ VALUE
51 04 AB DE 07CF 1321 QUANT: MOVAL QUAN-B(R11),R1 : SET QUANTITY ADDRESS
OC 11 07D3 1322 BRB VALR : READ VALUE
07D5 1323 PROGCTR:
51 54 AB DE 07D5 1324 MOVAL SAVPC-B(R11),R1 : SET PC ADDRESS
04 6A 08 E1 07D9 1325 VALUE: BBC #V_F1,(R10),VALR : SKIP IF NO VALUE
61 DB AB DO 07DD 1326 MOVL F1-B(R11),(R1) : SET NEW VALUE FOR PC
56 61 DO 07E1 1327 VALR: MOVL (R1),R6 ; AND GET VALUE
FA5B 31 07E4 1328 VALI: BRW INFLD : SET FIELD IN PROGRESS
07E7 1329 REGISTER:
55 18 AB DE 07E7 1330 MOVAL SAVREG-B(R11),R5 : SET BASE OF REGISTER AREA
02 10 07EB 1331 BSBB REGCOM : FETCH ADDRESS
F5 11 07ED 1332 BRB VALI : AND USE IT
FD68 30 07EF 1333 REGCOM: BSBW GETCHAR : GET SECOND CHAR
F9A6 CF 10 58 3A 07F2 1334 LOCC R8,#16,PRIMARY : TRANSLATE TO HEX
07F8 1335 .IF DF,#SW_PROCESS : FOR PROCESS VERSION
07F8 1336 BNEQ 10$ : LEGAL HEX DIGIT
07F8 1337 CMPW #*A/XI/,-2(R9) : CHECK FOR EXIT COMMAND
07F8 1338 BNEQ ERR3 : NO, ERROR
07F8 1339 TSTL ASTEN-B(R11) : WERE ASTS ENABLED ON DELTA ENTRY?
07F8 1340 BEQL 5$ : IF EQL NO, DON'T REENABLE THEM HERE
07F8 1341 $SETAST_S #1 : YES, UNDO AST DISABLE BY DELTA
07F8 1342 5$: $EXIT_S-EXITCODE : EXIT
07F8 1343 .IFF
6E 13 07F8 1344 BEQL ERR3 : ERROR, NOT HEX
07FA 1345 .ENDC
07FA 1346 10$:
50 10 50 C3 07FA 1347 SUBL3 R0,#16,R0 : INVERT
56 6540 DE 07FE 1348 MOVAL (R5)[R0],R6 : ACCUMULATE
05 0802 1349 RSB : RETURN
0803 1350
51 DC AB 61 6A 09 E1 0803 1351 XSET: BBC #V_F2,(R10),ERR3 : ERROR IF NOT TWO FIELDS
04 00 EF 0807 1352 EXTZV #0,#4,F2-B(R11),R1 : GET REGISTER NUMBER
51 F943 CF41 DE 080D 1353 MOVAL XREGV[R1],R1 : AND COMPUTE REGISTER ADDRESS
C4 11 0813 1354 BRB VALUE : PROCESS VALUE
0815 1355 XREG: : X-REGISTER VALUE
55 F93C CF DE 0815 1356 MOVAL XREGV,R5 : SET ADDRESS OF REGISTER VECTOR
D3 10 081A 1357 BSBB REGCOM : ADDRESS TO R6
56 66 DO 081C 1358 MOVL (R6),R6 : GET VALUE
C3 11 081F 1359 BRB VALI : AND NOTE INPUT IN FIELD
0821 1360 .ALIGN LONG : LONGWORD ALIGN EXCEPTION ROUTINES
0824 1361 XDELACV: : ACCESS VIOLATION HANDLER
0824 1362 MCHK: : MACHINE CHECK

```

```

5C   D5 0824 1363      .IF      NDF,SW_PROCESS      :
40   12 0824 1364      TSTL      AP                      : CHECK FOR SIMULATOR
                                BNEQ      ERR3                    : YES, SKIP RESET
                                0828 1365
                                0828 1366
                                0828 1367
                                0828 1368      CPUDISP <<780,CLR_780>,- : *DISPATCH ON CPU TYPE*
                                0828 1369      <750,CLR_750>,-
                                0828 1370      <730,CLR_730>,-
                                0828 1371      <790,CLR_790>,-
                                0828 1372      <UV1,CLR_UV1>,-
                                0828 1373      ENVIRON=XDELTA;
                                083F 1374
                                083F 1375 CLR_780:
00  7E 30  DB 083F 1376      MFPR      #PR$ SBIFS, -(SP)
    6E 19  E5 0842 1377      BBCC      #25, TSP), 10$
    30 8E  DA 0846 1378 10$:  MTPR      (SP)+, #PR$ SBIFS
    1D 11  BRB 0849 1379      BRB      CLR_END
                                084B 1380
                                084B 1381 CLR_UV1:
                                084B 1382 CLR_730:
                                084B 1383 CLR_750:
    26 0F  DA 084B 1384      MTPR      #^XF, #PR$ MCESR
    18 11  BRB 084E 1385      BRB      CLR_END
                                0850 1386 CLR_790:
7E  0000004A 8F  DB 0850 1387      MFPR      #PR$ EHSR, -(SP)
    00 6E 06  E5 0857 1388      BBCC      #6, (SP), 10$
0000004A 8F 8E  DA 085B 1389 10$:  MTPR      (SP)+, #PR$ EHSR
    00000000'EF 16  JSB 0862 1390      JSB      SYSL$CLRSBIA
                                0868 1391
                                0868 1392 CLR_END:
                                0868 1393
                                0868 1394
                                0868 1395      .ENDC
    F96B 31 0868 1396 10$:
                                ERR3:  BRW      ERROR
                                086B 1398

```

```

086B 1400      .SBTTL REGISTER SAVE AND RESTORE
086B 1401
086B 1402      :
086B 1403      :
086B 1404      :
086B 1405      :
086B 1406      :
086B 1407      :
00000000'EF 16 086E 1408      .IF NDF,SW_PROCESS
FB1F CF 50 7D 0874 1409      SETIPL #31
51 F823 CF 9E 0879 1410      JSB INISWRITABLE
087E 1411      MOVQ RO,SAVREG
087E 1412      MOVAB SAVR2,R1
087E 1413      .IFF
087E 1414      $SETAST_S #0
087E 1415      PUSHAB -(R0)
087E 1416      MOVPSL R1
087E 1417      EXTZV #PSLSV_CURMOD,#PSLSS_CURMOD,R1,R1 ; ISOLATE CURRENT MODE
087E 1418      MULW #CONTEXTSZ,R1 ; COMPUTE OFFSET TO PROPER CONTEXT AREA
087E 1419      MOVAB SAVREG[R1],R1 ; FORM ADDRESS OF REGISTER SAVE
087E 1420      MOVL 8(AP),RO ; GET POINTER TO MECHANISM
087E 1421      MOVQ 12(RO),(R1)+ ; SAVE RO,R1
087E 1422      .ENDC
81 52 7D 087E 1421      MOVQ R2,(R1)+ ; SAVE R2,R3
81 54 7D 0881 1422      MOVQ R4,(R1)+ ; SAVE R4,R5
81 56 7D 0884 1423      MOVQ R6,(R1)+ ; SAVE R6,R7
81 58 7D 0887 1424      MOVQ R8,(R1)+ ; SAVE R8,R9
81 5A 7D 088A 1425      MOVQ R10,(R1)+ ; SAVE R10,R11
81 5C 7D 088D 1426      .IF NDF,SW_PROCESS
81 81 5C 7D 088D 1427      MOVQ AP,(R1)+ ; SAVE AP,FP
81 OC AE 9E 0890 1428      MOVAB 12(SP),(R1)+ ; ASSUME KERNEL STACK
81 04 AE 7D 0894 1429      MOVQ 4(SP),(R1)+ ; SAVE PC,PSL
0898 1430      .IFF
0898 1431      MOVQ 8(FP),(R1)+ ; SAVE AP,FP
0898 1432      SUBL3 #1,@4(AP),RO ; GET NUMBER OF ARGS IN SIGNAL
0898 1433      MOVAL @4(AP)[RO],RO ; POINT TO PC,PSL
0898 1434      MOVAL 8(RO),(R1)+ ; COMPUTE SP
0898 1435      MOVQ (RO),(R1)+ ; SAVE PC,PSL
0898 1436      .ENDC
0898 1437      .IF NDF,SW_PROCESS
52 51 DO 0898 1438      MOVL R1,R2 ; SAVE R1
00000000'GF 16 089B 1439      JSB G^CON$OWNCTY ; ALLOCATE THE CONSOLE TERMINAL
82 50 DO 08A1 1440      MOVL RO,(R2)+ ; SAVE CONSOLE TRANSMIT STATUS
82 51 DO 08A4 1441      MOVL R1,(R2)+ ; SAVE CONSOLE RECVR STATUS
51 52 DO 08A7 1442      MOVL R2,R1 ; RESTORE R1
51 5C D4 08AA 1443      CLRL AP ; ZAP DEVICE ADDRESS BASE
5B F7D0 CF 9E 08AC 1444      MOVAB B,R11 ; AND DATA BASE ADDRESS
08B1 1445      .IFF ; FALSE FOR PROCESS VERSION
08B1 1446      MOVAB W^<B-<SAVPSL+4>>(R1),R11 ; SET BASE OF CONTEXT AREA
08B1 1447      MOVL (SP)+,ASTEN-B(R11) ; SAVE AST ENABLE
08B1 1448      .ENDC
SA D4 AB 9E 08B1 1449      MOVAB STATUS-B(R11),R10 ; SET STATUS BASE
59 84 AB 9E 08B5 1450      MOVAB INBUF-B(R11),R9 ; POINT TO INPUT BUFFER
69 94 08B9 1451      CLRB (R9) ; MAKE BUFFER EMPTY
08BB 1452      .IF NDF,SW_PROCESS
0094 30 08BB 1453      BSBW GET$CB ; GET BASE OF SCB
FB D1 CF 04 A0 DO 08BE 1454      MOVL 4(RO),MCHK$AV ; SAVE ORIGINAL MCHK VECTOR
04 A0 FF5C CF 9E 08C4 1455      MOVAB MCHK,4(RO) ; SET TO XDELTA VECTOR
20 A0 FF56 CF 9E 08CA 1456      MOVAB XDEL$CV,^X20(RO) ; SET ACCESS VIOLATION VECTOR

```

```

24 AO FF50 CF 9E 08D0 1457 MOVAB XDELACV,^X24(R0) ; SET PG FAULT VECTOR
18 AO FF4A CF 9E 08D6 1458 MOVAB XDELACV,^X18(R0) ; SET RESERVED OPERAND HANDLER
50 08 AE 02 18 EF 08DC 1459 EXTZV #PSLSV_CURMOD,#PSLSS_CURMOD,8(SP),R0 ; GET MODE
07 13 08E2 1460 BEQL 30$ ; CORRECT ALREADY IF KERNEL
50 00 C0 08E4 1461 ADDL #PRS_KSP,R0 ; COMPUTE PRCESSOR REGISTER
50 AB 50 DB 08E7 1462 MFPR R0,SAVSP-B(R11) ; AND SAVE CORRECT SP
08EB 1463 .ENDC
FD55 31 08EB 1464 30$: BRW RESET ; RESET SCANNER
08EE 1465
08EE 1466 ;
08EE 1467 ; RESTORE - RESTORE TARGET REGISTERS
08EE 1468 ;
08EE 1469 RESTORE: ; RESTORE EVERYTHING
08EE 1470 .IF NDF,SW_PROCESS ;
04 AE 54 AB 7D 08EE 1471 MOVQ SAVPC-B(R11),4(SP) ; SET PC,PSL
08F3 1472 .IFF ; FALSE IF PROCESS
08F3 1473 SUBL3 #1,@4(AP),R0 ; GET SIGNAL ARG COUNT
08F3 1474 MOVAL @4(AP)[R0],R0 ; COMPUTE ADDRESS OF PC,PSL
08F3 1475 MOVQ SAVPC-B(R11),(R0) ; STORE UPDATED PC,PSL
08F3 1476 .ENDC
08F3 1477 RESTORR: ; RESTORE REGISTERS ONLY
08F3 1478 .IF NDF,SW_PROCESS ;
20 AO 00000000'5D 10 08F3 1479 BSBB GETSCB ; GET BASE OF SCB
24 AO 00000000'EF 9E 08F5 1480 MOVAB EXESACVIOLAT,^X20(R0) ; RESTORE ACCESS VECTOR
04 AO F88C CF D0 08FD 1481 MOVAB MMG$PAGEFAULT,^X24(R0) ; AND PAGE FAULT VECTOR
18 AO 00000000'EF 9E 0905 1482 MOVL MCHKSAV,4(R0) ; RESTORE MACHINE CHECK VECTOR
5C B5 0913 1483 MOVAB EXESROPRAND,^X18(R0) ; RESTORE RESERVED OPERAND VECTOR
10 12 0915 1484 TSTW AP ; CHECK FOR CONSOLE
50 5C AB D0 0917 1485 BNEQ 10$ ; NO, OTHER DEVICE
51 60 AB D0 091B 1486 MOVL SAVOCR-B(R11),R0 ; RESTORE INITIAL TX STATUS
00000000'GF 16 091F 1487 MOVL SAVRXCS-B(R11),R1 ; AND INITIAL RECEIVER STATE
09 11 0925 1488 JSB G^CON$RELEASECTY ;
0927 1489 BRB 20$ ; MERGE WITH COMMON CODE
04 AC 5C AB B0 0927 1491 10$: MOVW SAVOCR-B(R11),OUTCR(AP) ; RESTORE OUTPUT CSR
6C 5E AB B0 092C 1492 MOVW SAVRCR-B(R11),RDCR(AP) ; AND INPUT CSR CONTENT
0930 1493 .IFF
0930 1494 PUSHL ASTEN-B(R11) ; SAVE AST ENABLE
0930 1495 .ENDC
51 20 AB 9E 0930 1496 20$: MOVAB SAVR2-B(R11),R1 ; SET BASE FOR RESTORE
52 81 7D 0934 1497 MOVQ (R1)+,R2 ; RESTORE R2,R3
54 81 7D 0937 1498 MOVQ (R1)+,R4 ; RESTORE R4,R5
56 81 7D 093A 1499 MOVQ (R1)+,R6 ; RESTORE R6,R7
58 81 7D 093D 1500 MOVQ (R1)+,R8 ; RESTORE R8,R9
5A 81 7D 0940 1501 MOVQ (R1)+,R10 ; RESTORE R10,R11
0943 1502 .IF NDF,SW_PROCESS ;
50 5C 81 7D 0943 1503 MOVQ (R1)+,AP ; RESTORE AP,FP
F74E CF 7D 0946 1504 MOVQ SAVREG,R0 ; RESTORE R0,R1
094B 1505 .IFF ; FALSE IF PROCESS VERSION
094B 1506 MOVQ (R1)+,8(FP) ; SET NEW VALUES FOR AP,FP
094B 1507 MOVL 8(AP),R0 ; GET MECHANISM POINTER
094B 1508 MOVQ <SAVREG-SAVSP>(R1),12(R0) ; STORE UPDATED R0,R1
094B 1509 MOVPSL R1 ; GET CURRENT PSL
094B 1510 EXTZV #PSLSV_CURMOD,#PSLSS_CURMOD,R1,R1 ; GET CURRENT MODE
094B 1511 BBCC R1,DBG$ACTIVE,30$ ; CLEAR ACTIVE BIT FOR MODE
094B 1512 30$:
094B 1513 TSTL (SP)+ ; CHECK FOR AST ENABLE

```

```
00000000'EF 16 094B 1514 BEQL 35$ : NO
094B 1515 $SETAST_S #1 : RE- ENABLE AST RECOGNITION
094B 1516 35$: :
094B 1517 .ENDC :
094B 1518 .IF NDF_SW_PROCESS :
094B 1519 JSB INI$RDONLY : REPROTECT THE SYSTEM CODE
0951 1520 .ENDC :
05 0951 1521 RSB : AND RETURN
```

```

0952 1524      .SBTTL GET SCB ADDRESS
0952 1525
0952 1526 :
0952 1527 : SUBROUTINE GETSCB IS CALLED TO GET THE PHYSICAL OR VIRTUAL
0952 1528 : ADDRESS OF THE CURRENT SCB.
0952 1529 :
0952 1530 : INPUTS:      NONE
0952 1531 :
0952 1532 : OUTPUTS:     R0 = SCB ADDRESS
0952 1533 :             OTHER REGISTERS PRESERVED
0952 1534 :
0952 1535 :
0952 1536 :
50 38 DB 0952 1537 GETSCB: .IF      NDF_SW_PROCESS      : NOT FOR PROCESS VERSION
05 05 12 0955 1538      BNEQ     #PR$_MAPEN,R0      : GET MAPPING STATUS
50 11 DB 0957 1539      MFPR     #PR$_SCBB,R0      : BRANCH IF MAPPING ENABLED
07 11 11 095A 1540      BRB      20$              : ELSE GET PHY ADDR OF SCB
50 00000000'EF DO 095C 1541 10$:  MOVL     EXE$GL_SCB,R0      : JOIN COMMON RETURN
05 05 05 0963 1542 20$:  RSB              : IF MAPPING ENABLED, GET SCB VA
0964 1543      .ENDC      : RETURN
                                :

```

```

00 20 54 41 20 4B 52 42 20
FEF8 30
00ED 30
53 D5
21 12
F788 CF 54 AB D1
10 13
FF6E 30
7E 06 AE 9A
0000000'EF 17
0096 30
F76F CF D4
40 11
6A 18 88
008A 30
4D 58 AB 04 E0
55 53 D0
FBA4 30
FB51 30
54 B2 AF 9E
FB68 30
53 54 AB D0
FB47 30
51 F74F CF45 D0
OB 13
68 51 D0
6A 2000 BF AA
F978 30
51 75C CF45 D0
03 13
59 51 D0
68 54 AB D0
09DF 1601

0964 1545 .SBTTL BPT TRAP HANDLER
0964 1546 :
0964 1547 : HANDLE BREAKPOINT TRAPS
0964 1548 :
0964 1549 BMSG: .ASCIZ / BRK AT / : BREAK POINT MESSAGE
096D 1550 .ALIGN LONG : LONGWORD ALIGNMENT
0970 1551 .IF NDF,SW_PROCESS : EXEC VERSION
0970 1552 XDELBPT: : XDELTA BPT ENTRY
0970 1553 .IFF :
0970 1554 XDELBPT: : DELTA BPT ENTRY
0970 1555 .ENDC :
0970 1556 BSBW SAVE : SAVE REGS AND DISABLE
0973 1557 BSBW GETBPTX : GET INDEX OF BPT
0976 1558 TSTL R3 : CHECK FOR MATCH
0978 1559 BNEQ 10$ : YES, FOUND IT
097A 1560 CMPL SAVPC-B(R11),OVRADR : IS THIS A TEMPORARY BREAKPOINT?
0980 1561 BEQL 20$ : BRANCH IF SO
0982 1562 BSBW RESTORR : RESTORE REGISTERS ONLY
0985 1563 .IF NDF,SW_PROCESS :
0985 1564 MOVZBL 6(SP),=(SP) : GET IPL
0989 1565 EYBINT : ENABLE
098C 1566 JMP EXESBREAK : AND HANDLE NORMALLY
0992 1567 .IFF : FALSE IF PROCESS VERSION
0992 1568 :
0992 1569 : ***** UNEXPECTED BREAKPOINT *****
0992 1570 CLRL R0 : RETURN FALSE
0992 1571 RET :
0992 1572 .ENDC :
0992 1573 :
0992 1574 : WE JUST HIT A TEMPORARY BREAKPOINT! SET FROM A STEP-OVER
0992 1575 :
0992 1576 20$: BSBW UNBRK : RESTORE OPCODES, INCLUDING TEMP BRKPT
0995 1577 CLRL OVRADR : REMOVE TEMPORARY BREAKPOINT
0999 1578 BRB OUTPC : AND PRETEND WE JUST STEPPED
099B 1579 :
099B 1580 10$: BISB #<<1@V_TBIT>!<1@V_ATBRK>>,(R10) ; SET STATUS
099E 1581 30$: :
099E 1582 BSBW UNBRK : RESTORE OPCODES
09A1 1583 BBS #PSL@V_TBIT,SAVPSL-B(R11) : PROCEED ; PROCEED IF BPT AND TBIT
09A6 1584 MOVL R3,R5 : SAVE BPT NUMBER
09A9 1585 BSBW CRLF : OUTPUT CR/LF PAIR
09AC 1586 BSBW OUTDIGIT : OUTPUT BPT NUMBER
09AF 1587 MOVAB BMSG,R4 : MSG ADDRESS
09B3 1588 BSBW OUTZSTRING : OUTPUT ASCIIZ
09B6 1589 MOVL SAVPC-B(R11),R3 : OUTPUT PC
09BA 1590 BSBW OUTLONG : OUTPUT HEX LONGWORD
09BD 1591 MOVL BRKDSP[R5],R1 : GET ADDRESS TO DISPLAY
09C3 1592 BEQL 40$ : NONE
09C5 1593 MOVL R1,CURDOT-B(R11) : SET AS CURRENT DOT
09C8 1594 BICW #1@V INSTR,(R10) : CLEAR INSTRUCTION DISPLAY MODE
09CD 1595 BSBW LOCPROMPT : AND DISPLAY
09D0 1596 40$: MOVL BRKCOM[R5],R1 : GET COMMAND STRING ADDRESS
09D6 1597 BEQL OUTPC : NONE OUTPUT INSTRUCTION AT PC
09D8 1598 MOVL R1,R9 : SET TO SCAN STORED COMMAND
09DB 1599 OUTPC: : OUTPUT PC INSTRUCTION & GET COMMANDS
09DB 1600 MOVL SAVPC-B(R11),CURDOT-B(R11) : SET ADDRESS
09DF 1601 IFNORD #4,@CURDOT-B(R11),GETCMD : SKIP DISPLAY IF NOT READABLE

```

6A	2000	8F	AB	09E6	1602	BISW	#1@V INSTR,(R10)	:	SET TO INSTRUCTION DISPLAY MODE
		F95D	30	09EB	1603	BSBW	LOCPROMPT	:	PROMPT WITH ADDRESS/INSTRUCTION
				09EE	1604			:	GET COMMANDS
F7DF	CF	6C	FA	09EE	1605	CALLG	(AP),DCOM	:	PERFORM DEBUG COMMANDS
				09F3	1606	PROCEED:		:	PROCEED
		4B	10	09F3	1607	BSBB	SETBRK	:	SET BREAKPOINTS
09	6A	03	E5	09F5	1608	BBCC	#V TBIT,(R10),50\$:	TEST AND CLR TRACE FLAG
00	58	AB	E2	09F9	1609	BBSS	#PSLSV_TBIT,SAVPSL-B(R11),40\$:	SET TBIT
				09FE	1610			:	
				09FE	1611	.IF	DF,SW_PROCESS	:	FOR PROCESS VERSION
				09FE	1612	CMPB	#2,@SAVPC-B(R11)	:	CHECK FOR REI OPCODE
				09FE	1613	BNEQ	45\$:	NO, NOTHING SPECIAL
				09FE	1614	EXTZV	#PSLSV_CURMOD,#PSLSS_CURMOD,SAVPSL-B(R11),R0	:	GET NEW MODE
				09FE	1615	MULW	#CONTEXTSZ,R0	:	SCALE BY PER MODE CONTEXT AREA SIZE
				09FE	1616	MOVAB	STATUS[R0],R10	:	POINT TO NEW FLAGS
				09FE	1617	.ENDC		:	
00	6A	05	E2	09FE	1618	BBSS	#V TBITOK,(R10),50\$:	SET TBIT EXPECTED
		FEE9	30	0A02	1619	BSBW	RESTORE	:	RESTORE EVERYTHING
				0A05	1620	.IF	NDF,SW_PROCESS	:	
			02	0A05	1621	REI		:	AND RETURN
				0A06	1622	.IFF		:	FALSE IF PROCESS VERSION
				0A06	1623	MOVL	#1,R0	:	RETURN TRUE
				0A06	1624	RET		:	
				0A06	1625	.ENDC		:	
				0A06	1626			:	


```

0A06 1628      .SBTTL  TBIT EXCEPTION HANDLER
0A06 1629      :
0A06 1630      :      HANDLER FOR TBIT EXCEPTION
0A06 1631      :
0A06 1632      .ALIGN  LONG      : LONGWORD ALIGNED
0A08 1633      .IF      NDF,SW_PROCESS :
0A08 1634      XDELTBIT: : XDELTA TBIT HANDLER
0A08 1635      .IFF
0A08 1636      XDELTBIT:
0A08 1637      .ENDC
10 6A  FE60  30 0A08 1638      BSBW      SAVE      : SAVE AND DISABLE
FE60  05  E4  0A08 1639      BBSC      #V TBITOK,(R10),XDELDBG : BR IF TBIT EXPECTED
FEE1  30  0A0F 1640      BSBW      RESTORR   : RESTORE REGISTERS
7E  06  AE  9A 0A12 1641      .IF      NDF,SW_PROCESS :
0A12 1642      MOVZBL  6(SP),=(SP) : GET IPL FOR ENABLE
00000000'EF 17 0A16 1643      ENBINT   : ENABLE
0A19 1644      JMP      EXESTBIT : OTHERWISE LET EXEC HANDLE
0A1F 1645      .IFF      FALSE IF PROCESS VERSION
0A1F 1646      CLRL    RO      : RESIGNAL
0A1F 1647      RET
0A1F 1648      .ENDC   UNEXPECTED TBIT EXCEPTION
58 AB  10  CA 0A1F 1649      XDELDBG: : COMMON WITH DEBUG EXCEPTION
06  10  0A1F 1650      BICL    #<1@PSLSV_TBIT>,SAVPSL-B(R11) : CLEAR TBIT IN PSL
CA 6A  04  E4 0A23 1651      BSBB    UNBRK      : REPLACE OPCODES
B0  11  0A25 1652      BBSC    #V ATBRK,(R10),PROCEED : CHECK FOR PROCEED
0A29 1653      BRB     OUTPC     : DISPLAY INSTRUCTION AND GET COMMANDS
0A2B 1654

```

```

      0A2B 1656      .SBTTL UNBRK - RESTORE OPCODES FOR BREAKPOINTS
      0A2B 1657      :
      0A2B 1658      :
      0A2B 1659      :
      0A2B 1660 UNBRK:
50    51 09 D0 0A2B 1661      MOVL #NBRK+NTMPBRK,R1      : TOTAL PERM & TEMPORARY BREAKPOINTS
      F6B1 CF41 D0 0A2E 1662 10$: MOVL BRKADR[R1],R0      : GET BREAKPOINT ADDRESS
      06      13 0A34 1663      BEQL 20$      : SKIP IF NOT ENABLED
      0A36 1664      .IF DF,SW PROCESS      :
      0A36 1665      PUSHR #*M<R0,R1,R2,R3,R4,R5>      : SAVE REGS FOR PROTECTION CHANGE
      0A36 1666      MOVL R0,R4      : FORM INADR RANGE FOR SET PROTECTION
      0A36 1667      MOVL R0,R5      :
      0A36 1668      BSBW SEFWRT      : SET PAGE WRITABLE
      0A36 1669      MOVQ (SP),R0      : RESTORE R0,R1
      0A36 1670      .ENDC      :
60    F6D0 CF41 90 0A36 1671      MOVB BRKOP[R1],(R0)      : RESTORE OPCODE
      0A3C 1672      .IF DF,SW PROCESS      :
      0A3C 1673      BSBW REPROT      : RESTORE PROTECTION
      0A3C 1674      POPR #*M<R0,R1,R2,R3,R4,R5>      : RESTORE REGISTERS
      0A3C 1675      .ENDC      :
      EF 51 F5 0A3C 1676 20$: SOBGTR R1,10$      : DO THEM ALL
      05      0A3F 1677      RSB      : AND RETURN
      0A40 1678

```

```

      0A40 1680      .SBTTL SETBRK - SET BREAK POINT INSTRUCTIONS
      0A40 1681      :
      0A40 1682      :
      0A40 1683      :
      50 51 09 D0 0A40 1684 SETBRK: MOVL #NBRK+NTMPBRK,R1      : TOTAL PERMANENT & TEMPORARY BRKPOINTS
      F69C CF41 D0 0A43 1685 10$: MOVL BRKADR[R1],R0      : GET ADDRESS
      14 13 0A49 1686      : BEQL 20$      : SKIP IF NOT ENABLED
      F6BA CF41 60 90 0A4B 1687      : MOVB (R0),BRKOP[R1]      : SAVE OPCODE
      6A 18 93 0A51 1688      : BITB #<<1@V_TBIT>!<1@V_ATBRK>>,(R10) : CHECK FOR TRACE
      06 13 0A54 1689      : BEQL 15$      : NO TRACE, SET ANYWAY
      54 AB 50 D1 0A56 1690      : CMPL R0,SAVPC-B(R11)      : CHECK FOR AT BPT
      03 13 0A5A 1691      : BEQL 20$      : YES, DONT SET IT
      0A5C 1692 15$:      :
      0A5C 1693      : .IF DF,SW PROCESS      :
      0A5C 1694      : PUSHR #^M<R0,R1,R2,R3,R4,R5>      : SAVE REGISTERS FOR PROTECTION CHANGE
      0A5C 1695      : MOVL R0,R4      : SET START ADDRESS OF RANGE
      0A5C 1696      : MOVL R0,R5      : AND END ADDRESS
      0A5C 1697      : BSBW SETWRT      : SET PAGE WRITABLE
      0A5C 1698      : MOVL (SP),R0      : RESTORE BPT ADDRESS
      0A5C 1699      : .ENDC      :
      60 03 90 0A5C 1700      : MOVB #3,(R0)      : SET BREAKPOINT OPCODE
      0A5F 1701      : .IF DF,SW PROCESS      :
      0A5F 1702      : BSBW REPROT      : RESTORE ORIGINAL PROTECTION VALUE
      0A5F 1703      : POPR #^M<R0,R1,R2,R3,R4,R5>      : AND REGISTERS
      0A5F 1704      : .ENDC      :
      E1 51 F5 0A5F 1705 20$: SOBGTR R1,10$      : DO THEM ALL
      05 0A62 1706      : RSB      : AND RETURN
      0A63 1707

```

```

                                .SBTTL GETBPTX - GET INDEX FOR BREAKPOINT
                                GETBPTX
                                GETBPTX:
F677 (F43 53 08 D0 0A63 1709
                    54 AB D1 0A63 1710 :
                    03 13 0A63 1711 :
                    F4 53 F5 0A63 1712 :
                                GETBPTX:
                                MOVL #NBRK,R3      : INIT LOOP
                                CMPL SAVPC-B(R11),BRKADR[R3] : IS THIS A BPT?
                                BEQL 20$           : YES
                                SOBGTR R3,10$      : NO, CONTINUE
                                RSB                : RETURN
                                05 0A72 1718 20$:

```

```

      0A73 1720      .SBTTL QUOTE - INPUT CHARACTER STRING
      0A73 1721      :
      0A73 1722      :
      0A73 1723      :
      0A73 1724      :
      0A73 1725      QUOTE:
55    6B    D0    0A73 1725      MOVL    CURDOT-B(R11),R5      : POINT TO STRING BUFFER
      FAE1 30    0A76 1726      5$:  BSBW    GETCHAR      : GET CHARACTER
58    27    91    0A79 1727      CMPB    #QUOT,R8      : CHECK FOR QUOTE
      05    13    0A7C 1728      BEQL    10$          : YES, END OF STRING
85    58    90    0A7E 1729      MOVB    R8,(R5)+     : INSERT IN BUFFER
      F3    11    0A81 1730      BRB     5$           : AND CONTINUE
6B    55    D0    0A83 1731      10$:  MOVL    R5,CURDOT-B(R11) : SAVE NEW DOT
      05    0A86 1732      RSB                    : RETURN

```

```

0A87 1734 .SBTTL DEPOSIT
0A87 1735 :
0A87 1736 : DEPOSIT DATA
0A87 1737 :
0A87 1738 DEPOSIT:
1D 6A 1F E0 0A87 1739 BBS #V_PREG,(R10),40$ BR IF PROCESSOR REGISTER
0A8B 1740 .IF DF_SW_PROCESS
0A8B 1741 MOVL CURDOT-B(R11),R4 GET CURRENT DOT
0A8B 1742 TSTL PID-B(R11) CHECK FOR ARBITRARY PROCESS DEPOSIT
0A8B 1743 BNEQ 50$ BR IF YES
0A8B 1744 .ENDC
0A8B 1745 CASE CURTYPE-B(R11),TYPE=B,<- ; SWITCH ON TYPE
0A8B 1746 10$,- BYTE
0A8B 1747 20$,- WORD
0A8B 1748 30$,- LONG
0A8B 1749 >
0A96 1750 .IF
00 BB D8 AB 90 0A96 1751 10$: MOVB F1-B(R11),@CURDOT-B(R11) STORE BYTE
05 0A9B 1752 RSB RETURN
00 BB D8 AB B0 0A9C 1753 20$: MOVW F1-B(R11),@CURDOT-B(R11) STORE WORD
05 0AA1 1754 RSB RETURN
00 BB D8 AB D0 0AA2 1755 30$: MOVL F1-B(R11),@CURDOT-B(R11) STORE LONG
05 0AA7 1756 RSB RETURN
6B D8 AB DA 0AAB 1757 40$: MTPR F1-B(R11),CURDOT-B(R11) SET VALUE IN PROCESSOR REGISTER
05 0AAC 1758 RSB
0AAD 1759 .IFF ; FALSE IF PROCESS VERSION
0AAD 1760 10$: ; BYTE DEPOSIT
0AAD 1761 MOVL R4,R5 ; START AND END ADDRESSES EQUAL
0AAD 1762 BSBW SETWRT ; SET WRITABLE, OLD PROT TO R2
0AAD 1763 MOVB F1-B(R11),(R4) STORE BYTE
0AAD 1764 BSBW REPROT ; RESTORE PROTECTION
0AAD 1765 RSB
0AAD 1766
0AAD 1767 20$: ADDL3 #1,R4,R5 ; WORD DEPOSIT, FORM END ADDRESS
0AAD 1768 BSBW SETWRT ; SET WRITABLE
0AAD 1769 MOVW F1-B(R11),(R4) STORE WORD
0AAD 1770 BSBW REPROT ; RESTORE PROTECTION
0AAD 1771 RSB
0AAD 1772
0AAD 1773 30$: ADDL3 #3,R4,R5 ; LONGWORD DEPOSIT, FORM END ADDRESS
0AAD 1774 BSBW SETWRT ; SET WRITABLE
0AAD 1775 MOVL F1-B(R11),(R4) STORE LONG WORD
0AAD 1776 BSBW REPROT ; RESTORE PROTECTION
0AAD 1777 RSB
0AAD 1778
0AAD 1779 40$: ; PROCESSOR REGISTER
0AAD 1780 $CMKRNLS B*DEPPREG,(AP) DEPOSIT IN PROCESSOR REGISTER
0AAD 1781 RSB
0AAD 1782 50$: ; DEPOSIT IN ARBITRARY PROCESS
0AAD 1783 CASE CURTYPE-B(R11),TYPE=B,<- ; SWITCH ON TYPE
0AAD 1784 60$,- BYTE
0AAD 1785 70$,- WORD
0AAD 1786 80$> LONGWORD
0AAD 1787 RSB
0AAD 1788 60$: PUSHAB W*DPBYTE SET ADDRESS OF BYTE ROUTINE
0AAD 1789 BRB 90$
0AAD 1790 70$: PUSHAB W*DPWORD SET ADDRESS OF WORD ROUTINE

```

```

OAA 1791 BRB 90$ ;
OAA 1792 80$: PUSHAB W^DPLONG ; SET ADDRESS OF LONG ROUTINE
OAA 1793 90$: PUSHL PID-B(R11) ; SET PID OF TARGET
OAA 1794 PUSHL CURDOT-B(R11) ; ADDRESS FOR STORE
OAA 1795 PUSHL F1-B(R11) ; VALUE TO STORE
OAA 1796 PUSHL #4 ; ARGUMENT COUNT
OAA 1797 MOVL SP,RO ; POINTER TO ARGUMENT LIST
OAA 1798 TSTL MFYFLG-B(R11) ; CHECK FOR STORE ENABLED
OAA 1799 BEQL 100$ ; BR IF NOT
OAA 1800 $CMKRNLS W^QGET,(RO) ; CALL TO QUEUE REQUEST
OAA 1801 100$: ADDL #20,SP ; CLEAN STACK
OAA 1802 RSB ; AND RETURN
OAA 1803
OAA 1804 DEPPREG: .WORD 0 ; DEPOSIT INTO PROCESSOR REGISTER
OAA 1805 MOVAB W^PREXC,(FP) ; SET EXCEPTION HANDLER
OAA 1806 MTPR F1-B(R11),CURDOT-B(R11) ; PLACE FIELD VALUE IN REG
OAA 1807 MOVL #1,RO ; RETURN SUCCESS
OAA 1808 RET ;
OAA 1809
OAA 1810 PREXC: .WORD 0 ; PROCESSOR REGISTER EXCEPTION HANDLER
OAA 1811 ADDL3 #4,8(AP),R1 ; POINT TO EXCEPTION FP
OAA 1812 MOVL (R1),12(FP) ; SET AS RETURN FP
OAA 1813 MOVAB B^10$,16(FP) ; SET RETURN ADDRESS
OAA 1814 10$: MOVZWL #1,RO ; SET NORMAL STATUS
OAA 1815 RET ; AND RETURN
OAA 1816
OAA 1817 .ENDC

```

XDI
Syl
SSI
SSI
SS
SS
SS
SS
SS
SS
SS
AD
AS
B
BL
BM
BR
BR
BR
BR
BS
CL
CL
CL
CL
CL
CL
CO
CO
CO
CO
CO
CO
CO
CR
CR
CU
CU
DC
DE
DI
DO
DO
DY
EN
EN
FO
FR
FR
FR
ES
EX
EX
EX
EX


```

OACO 1839      .SBTTL  PROCESS DEBUGGER INITIALIZATION
OACO 1840
OACO 1841      .IF    DF,SW PROCESS
OACO 1842 SALUTE: .ASCIZ <CR><CF>/DELTA Version x2.2/<CR><LF> ;
OACO 1843
OACO 1844 TEST: ; START ADDRESS OF IMAGE ENTRY
OACO 1845 XDT$START:: ; GLOBAL START ADDRESS FOR CLI DEBUG
OACO 1846      .WORD  0
OACO 1847 DELTA_START: ; START ADDRESS FOR DEBUGGER ENTRY
OACO 1848      $WAKE_S ; NULL WAKE AND
OACO 1849      $HIBER_S ; HIBERNATE TO GET SYNCHRONIZED
OACO 1850      MOVAB  TERMASK,TERMASKADR ; RELOCATE TERMINATOR MASK DESCR
OACO 1851      MOVAB  TTNAMD+8,TTNAMD+4 ; RELOCATE DESCRIPTOR
OACO 1852      MOVAB  DBGINPUT+8,DBGINPUT+4
OACO 1853      MOVAB  TRNINPUT+8,TRNINPUT+4
OACO 1854      MOVAB  EXIHANDLE,EXIHADR ;
OACO 1855      MOVAB  EXITCODE,EXICODA ; RELOCATE EXIT HANDLER ARGS
OACO 1856      CALLG  (AP),B^INITCALL ; GENERATE CALL FRAME
OACO 1857      RET ;
OACO 1858
OACO 1859 NOBRK: MOVL  4(AP),AP ; GET EXCEPTION ARGUMENT LIST
OACO 1860      BRW    EXCEPT+2 ; AND GOTO EXCEPTION HANDLER
OACO 1861
OACO 1862 INITCALL:
OACO 1863      .WORD  0 ; ENTRY MASK
OACO 1864      MOVAB  W^CATCHALL,(FP) ; SET CATCHALL EXCEPTION HANDLER
OACO 1865      $CMKRNLS S W^SETKEXC,(AP) ; SET EXCEPTION VECTORS FROM KERNEL MODE
OACO 1866      BLBS  RO,1$ ; SUCCESS WITH KERNEL, IF NOT, TRY EXEC
OACO 1867      $CMEXEC S W^SETEEXC,(AP) ; SET EXEC & SUPV EXCEPTION VECTORS
OACO 1868      BLBS  RO,1$ ; BRANCH IF SUCCESS
OACO 1869      CMPL  RO,#SS$ _NOPRIV ; CHECK FOR LACK OF PRIVILEGE
OACO 1870      BEQL  1$ ; WHICH IS THE ONLY ACCEPTABLE ERROR
OACO 1871      RET ; OTHERWISE GET OUT
OACO 1872 1$: $SETEXV_S  ADDRESS=W^EXCEPT, ;
OACO 1873      ACMODE=#PSL$C_USER,- ;
OACO 1874      VECTOR=#0 ; SET PRIMARY FOR USER
OACO 1875      $SETEXV_S  ADDRESS=W^CATCHALL,- ; SET LAST CHANCE HANDLER
OACO 1876      ACMODE=#PSL$C_USER,- ; FOR USER MODE
OACO 1877      VECTOR=#2 ; SPECIFY LAST CHANCE HANDLER
OACO 1878      $DCLEXH_S  EXITBLK ; DECLARE USER MODE EXIT HANDLER
OACO 1879      PUSHR  #^M<R2,R3,R4,R5>
OACO 1880      MOVAB  W^DELBASE,R4 ; Set page protection on entire
OACO 1881      MOVAB  W^DELEND,R5 ; DELTA image to user writeable.
OACO 1882      BSBW  SETWRT
OACO 1883      POPR   #^M<R2,R3,R4,R5>
OACO 1884      $STRNLOG_S  LOGNAM=DBGINPUT,- ; FIRST DEFAULT INPUT
OACO 1885      RSLLEN=TRNINPUT,-
OACO 1886      RSLBUF=TRNINPUT
OACO 1887      BLBC  RO,9$ ; ON ERROR, USE TT
OACO 1888      CMPL  RO,#SS$ _NOTRAN
OACO 1889      BEQL  9$ ; USE TT IF NO TRANSLATION
OACO 1890 3$: $STRNLOG_S  LOGNAM=TRNINPUT,- ; TRANSLATE ALL THE WAY
OACO 1891      RSLLEN=TRNINPUT,-
OACO 1892      RSLBUF=TRNINPUT
OACO 1893      CMPL  RO,#SS$ _NOTRAN
OACO 1894      BNEQ  3$ ; TRY ANOTHER LEVEL
OACO 1895      CMPB  @TRNINPUT+4,#^X1B ; CHECK FOR PROCESS PERMANENT

```

```

OACO 1896      BNEQ      5$
OACO 1897      SUBL      #4,TRNINPUT      ; REMOVE THE ESCAPE HEADER
OACO 1898      ADDL      #4,TRNINPUT+4
OACO 1899 5$:   $ASSIGN_S TRNINPUT,TTCHAN ; DO THE ASSIGN
OACO 1900      BLBS      -RO,10$          ; TRY IT ON ERROR
OACO 1901 9$:   $ASSIGN_S TTNAMD,TTCHAN  ; ASSIGN DEVICE
OACO 1902      BLBS      -RO,10$          ; CONTINUE IF SUCCESS
OACO 1903      RET
OACO 1904 10$:  MOVAB     SALUTE,R4        ; ELSE EXIT WITH ERROR CODE IN RO
OACO 1905      BSBW     OUTZSTRING      ; SET ADDRESS OF SALUTATION
OACO 1906      BBC      #CLISV_DBGEXCP,24(AP),15$ ; OUTPUT IT
OACO 1907      BRW      NOBRK          ; BR IF LATER INVOCATION
OACO 1908 15$:  CALLG     (AP),B^20$        ; VIA $DEBUG COMMAND
OACO 1909      RET
OACO 1910 20$:  .WORD      0            ; CREATE TOP CALL FRAME
OACO 1911      ADDL      #4,4(AP)        ; NULL ENTRY MASK
OACO 1912      MOVPSL   -(SP)          ; ADVANCE STARTING ADDRESS POINTER
OACO 1913      ADDL3    #2,24(AP),-(SP) ; SAVE PSL
OACO 1914      MOVZWL   #$$$_DEBUG,-(SP) ; FETCH CURRENT STARTING ADDRESS
OACO 1915      PUSHL   #3            ; SET EXCEPTION CODE
OACO 1916      MOVL    SP,RO          ; SIGNAL ARG COUNT
OACO 1917      MOVQ    RO,-(SP)       ; SAVE POINTER
OACO 1918      PUSHL   #0            ; SAVE PHONY RO,R1
OACO 1919      PUSHL   FP            ; DEPTH
OACO 1920      PUSHL   #4            ; FP
OACO 1921      PUSHL   SP            ; ARG COUNT
OACO 1922      PUSHL   RO            ; POINTER TO MECH
OACO 1923      CALLS   #2,W^EXCEPT ; POINTER TO SIGNAL
OACO 1924      ADDL    #12,SP         ; SIGNAL PHONY EXCEPTION
OACO 1925      MOVQ    (SP)+,RO      ; CLEAN BACK TO RO,R1
OACO 1926      ADDL    #8,SP         ; RESTORE RO,R1
OACO 1927      REI
OACO 1928
OACO 1929
OACO 1930      .ENABLE LOCAL_BLOCK
OACO 1931
OACO 1932 SETKEXC: .WORD      0            ; ENTRY MASK FOR CMKRNL PRIVILEGE
OACO 1933      PUSHAB  W^CLREXV KERNEL ; SET TO USE KERNEL RUNDOWN HANDLER
OACO 1934      CALLS   #1,W^SETRUNDWN ; SET UP APPROPRIATE RUNDOWN HANDLER
OACO 1935      BLBC    RO,20$        ; BRANCH IF CAN'T SET UP HANDLER
OACO 1936      $SETEXV_S
OACO 1937      ADDR= B^EXCEPT, -
OACO 1938      PRVHND=KCOND PRIMARY,-
OACO 1939      ACMODE=#PSL$C_KERNEL,- ; SET KERNEL
OACO 1940      VECTOR=#0 ; PRIMARY VECTOR
OACO 1941      $SETEXV_S
OACO 1942      ADDR= W^CATCHALL, -
OACO 1943      PRVHND=KCOND_LASTCHANC,- ; SET KERNEL MODE LAST CHANCE HANDLE
OACO 1944      ACMODE=#PSL$C_KERNEL,- ;
OACO 1945      VECTOR=#2 ; SPECIFY LAST CHANCE VECTOR
OACO 1946      BRB     10$          ; SKIP ALTERNATE ENTRY MASK
OACO 1947
OACO 1948 -----
OACO 1949 SETEEXC: .WORD      0            ; ENTRY MASK FOR CMEXEC PRIVILEGE
OACO 1950      PUSHAB  W^CLREXV EXEC  ; SET TO USE EXEC RUNDOWN HANDLER
OACO 1951      CALLS   #1,W^SETRUNDWN ; SET UP APPROPRIATE RUNDOWN HANDLER
OACO 1952      BLBC    RO,20$        ; BRANCH IF CAN'T SET UP HANDLER
OACO 1953      $SETEXV_S
OACO 1954      ADDR= B^EXCEPT, -
OACO 1955      PRVHND=ECOND PRIMARY,- ;
OACO 1956      ACMODE=#PSL$C_EXEC,- ; SET EXEC MODE EXCEPTION HANDLER

```

```

OACO 1953          VECTOR=#0          ; PRIMARY VECTOR
OACO 1954          $SETEXV_S          ADDR= W^CATCHALL, -
OACO 1955          PRVHND= ECOND_LASTCHANC, -
OACO 1956          ACMODE= #PSLSC_EXEC, - ; SET EXEC MODE LAST CHANCE HANDLER
OACO 1957          VECTOR=#2          ; SPECIFY LAST CHANCE VECTOR
OACO 1958          ;-----
OACO 1959          $SETEXV_S          ADDR= B^EXCEPT, -
OACO 1960          PRVHND= SCOND_PRIMARY, -
OACO 1961          ACMODE= #PSLSC_SUPER, - ; SET SUPERVISOR MODE EXCEPTION HAND
OACO 1962          VECTOR=#0          ; PRIMARY VECTOR
OACO 1963          $SETEXV_S          ADDR= W^CATCHALL, -
OACO 1964          PRVHND= SCOND_LASTCHANC, -
OACO 1965          ACMODE= #PSLSC_SUPER, - ; SET SUPERVISOR LAST CHANCE HANDLER
OACO 1966          VECTOR=#2          ; SPECIFY LAST CHANCE VECTOR
OACO 1967 20$:    RET
OACO 1968
OACO 1969          .DISABLE          LOCAL_BLOCK
OACO 1970
OACO 1971 EXCEPT: .WORD 0          ; EXCEPTION HANDLER ENTRY MASK
OACO 1972          $SETEXV_S          ADDR= B^EXCEPT, -
OACO 1973          ACMODE= #PSLSC_USER, -
OACO 1974          VECTOR=#0          ; RE-ESTABLISH USER PRIMARY VECTOR
OACO 1975          ADDL3 #4,4(AP),R0 ; GET POINTER TO SIGNAL
OACO 1976          MOVPSL R1          ; GET CURRENT PSL
OACO 1977          EXTZV #PSL$V_CURMOD,#PSL$S_CURMOD,R1,R1 ;
OACO 1978          BBSS R1,DBGACTIVE,40$ ; BR IF ALREADY ACTIVE
OACO 1979          CMPL #SS$_TBIT,(R0) ; IS IT TBIT?
OACO 1980          BNEQ 10$          ; NO
OACO 1981 5$:    BRW XDELTBIT        ; YES, A TBIT
OACO 1982 10$:   CMPL #SS$_BREAK,(R0) ; IS IT BREAKPOINT?
OACO 1983          BNEQ 20$          ; NO
OACO 1984 15$:   BRW XDELBPT        ; YES, A BREAKPOINT
OACO 1985 20$:   ; SOME OTHER EXCEPTION
OACO 1986          CMPL #SS$_UNWINDING,(R0) ; IS IT UNWINDING
OACO 1987          BEQL 60$          ; YES
OACO 1988          CMPL #SS$_COMPAT,(R0)+ ; IS IT COMPATIBILITY MODE EXCEPT?
OACO 1989          BNEQ 30$          ; NO
OACO 1990          CMPL #1,(R0)      ; IS IT COMPATIBILITY BPT?
OACO 1991          BEQL 15$          ; YES
OACO 1992          CMPL #7,(R0)      ; IS IT COMPATIBILITY TBIT?
OACO 1993          BEQL 5$           ; YES
OACO 1994 30$:   CMPL #SS$_DEBUG,-(R0) ; IS IT DEBUG EXCEPTION?
OACO 1995          BNEQ 40$          ; NO
OACO 1996          BSBW SAVE          ; SAVE EVERYTHING
OACO 1997          BRW XDELDBG        ; AND TREAT AS FUNNY BPT
OACO 1998 40$:   ; UNEXPECTED EXCEPTION
OACO 1999          BBCC R1,DBGACTIVE,50$ ; CLEAR DEBUG ACTIVE
OACO 2000 50$:   CLRL R0            ; RETURN FALSE FOR RESIGNAL
OACO 2001          RET
OACO 2002 60$:   MOVL #1,R0          ; IGNORE AND RESIGNAL
OACO 2003          RET
OACO 2004          .PAGE
OACO 2005          .SBTTL HANDLER FOR DEBUG EXCEPTIONS
OACO 2006
OACO 2007 DBGEXCEP:
OACO 2008          .WORD 0
OACO 2009          ADDL3 #4,8(AP),R1 ; POINT TO EXCEPTION FP

```

```

OACO 2010          MOVL    FP,R0          : INIT LINK FOR CALL FRAMES
OACO 2011 10$:     CMPL    12(R0),(R1)     : IS THIS THE LAST ONE?
OACO 2012          BEQL    20$            : YES
OACO 2013          MOVAB   B*30$,16(R0)    : SET FOR RETURN
OACO 2014          MOVL    12(R0),R0      :
OACO 2015          BRB     10$            : CONTINUE
OACO 2016 20$:     MOVAB   XDELACV,16(R0)  : SET RETURN FOR ERROR
OACO 2017 30$:     RET
OACO 2018
OACO 2019 CATCHALL: : CATCHALL EXCEPTION HANDLER
OACO 2020          .WORD   0              : ENTRY MASK
OACO 2021          MOVPSL  R1            : GET CURMOD
OACO 2022          EXTZV   #PSLSV_CURMOD,#PSLSS_CURMOD,R1,R1 : ISOLATE CURRENT MODE
OACO 2023          BBCS    R1,DBGACTIVE,10$ : MUST NOT BE DEBUGGER EXCEPTION
OACO 2024          CLRL    R0            : RESIGNAL
OACO 2025          RET
OACO 2026 10$:     BSBW    SAVE          : SAVE EVERYTHING
OACO 2027          ADDL3   #4,4(AP),R0    : POINT TO EXCEPTION CODE
OACO 2028          MOVL    (R0),R3       : GET IT
OACO 2029          BSBW    CRLF          : OUTPUT CR/LF
OACO 2030          BSBW    OUTLONG       : OUTPUT EXCEPTION CODE
OACO 2031          MOVAB   B*EXCMMSG,R4   : OUTPUT MESSAGE
OACO 2032          BSBW    OUTZSTRING    : TEXT FOR EXCEPTION
OACO 2033          BRW     XDELDBG       : AND DISPLAY INSTRUCTION
OACO 2034 EXCMMSG: .ASCIZ  / EXCEPTION /
OACO 2035
OACO 2036 EXIHANDLE: : EXIT HANDLER
OACO 2037          .WORD   0              : ENTRY MASK
OACO 2038          BITB    #15,DBGACTIVE  : TEST FOR DEBUG ACTIVE IN ANY MODE
OACO 2039          BEQL    10$           : NO, REPORT EXIT
OACO 2040          RET
OACO 2041 10$:     PROGRAM EXIT
OACO 2042          MOVPSL  -(SP)          : BUILD EXCEPTION FRAME
OACO 2043          PUSHL   16(FP)
OACO 2044          PUSHL   @4(AP)
OACO 2045          PUSHL   #3
OACO 2046          PUSHR   #*M<R0,R1>
OACO 2047          MOVQ    AP,-(SP)
OACO 2048          PUSHL   #4
OACO 2049          PUSHL   SP
OACO 2050          PUSHAL  24(SP)
OACO 2051          PUSHL   #2
OACO 2052          MOVL    SP,AP
OACO 2053          BSBW    SAVE          : SET AP FOR EXCEPTION
OACO 2054          MOVAB   B*EXIMSG,R4   : SAVE EVERYTHING
OACO 2055          BSBW    OUTZSTRING    : DISPLAY EXIT MESSAGE
OACO 2056          MOVL    SAVAP-B(R11),R3 : OUTPUT TEXT
OACO 2057          MOVL    4(R3),R3      : GET POINTER TO EXCEPTION ARGLIST
OACO 2058          BSBW    OUTLONG       : GET EXIT CODE ADDRESS
OACO 2059          $DCLEXM S          EXITBLK : DISPLAY IT
OACO 2060          MOVPSL  R1            : RE-ESTABLISH EXIT HANDLER
OACO 2061          EXTZV   #PSLSV_CURMOD,#PSLSS_CURMOD,R1,R1 : GET CURRENT PSL
OACO 2062          BBSS    R1,DBGACTIVE,20$ : GET CURRENT MODE
OACO 2063 20$:     BRW     XDELDBG       : SET DELTA ACTIVE FOR MODE
OACO 2064
OACO 2065 EXIMSG: .ASCIZ  <CR><LF>/ EXIT /
OACO 2066

```

```

OACO 2067
OACO 2068      .ENABLE      LOCAL_BLOCK
OACO 2069 :
OACO 2070 : RESET INNER MODE EXCEPTION VECTORS. THIS CODE IS CALLED AS A
OACO 2071 : PRIVILEGED IMAGE RUNDOWN HANDLER TO ENSURE ITS EXECUTION.
OACO 2072 :
OACO 2073 : THIS ENTRY POINT IS USED IF THE PROCESS HAS CMKRNL PRIVILEGE
OACO 2074 :
OACO 2075 CLREXV_KERNEL: ; CLEAR EXCEPTION VECTORS
OACO 2076 :
OACO 2077 : RESET PRIMARY AND LAST CHANCE EXCEPTION VECTORS FOR KERNEL MODE
OACO 2078 :
OACO 2079      $SETEXV_S      ADDRESS=@KCOND_PRIMARY,- ;
OACO 2080      ACMODE=#PSL$C_KERNEL,- ;
OACO 2081      VECTOR=#0 ; PRIMARY VECTOR
OACO 2082      $SETEXV_S      ADDRESS=@KCOND_LASTCHANC,- ;
OACO 2083      ACMODE=#PSL$C_KERNEL,- ;
OACO 2084      VECTOR=#2 ; LAST CHANCE VECTOR
OACO 2085 :
OACO 2086 : THIS ENTRY POINT IS USED IF THE PROCESS HAS CMEXEC PRIVILEGE
OACO 2087 :
OACO 2088 CLREXV_EXEC: ; CLEAR EXCEPTION VECTORS
OACO 2089 :
OACO 2090 : RESET PRIMARY AND LAST CHANCE EXCEPTION VECTORS FOR EXECUTIVE MODE
OACO 2091 :
OACO 2092      $SETEXV_S      ADDRESS=@ECOND_PRIMARY,- ;
OACO 2093      ACMODE=#PSL$C_EXEC,- ;
OACO 2094      VECTOR=#0 ; PRIMARY VECTOR
OACO 2095      $SETEXV_S      ADDRESS=@ECOND_LASTCHANC,- ;
OACO 2096      ACMODE=#PSL$C_EXEC,- ;
OACO 2097      VECTOR=#2 ; LAST CHANCE VECTOR
OACO 2098 :
OACO 2099 : RESET PRIMARY AND LAST CHANCE EXCEPTION VECTORS FOR SUPERVISOR MODE
OACO 2100 :
OACO 2101      $SETEXV_S      ADDRESS=@SCOND_PRIMARY,- ;
OACO 2102      ACMODE=#PSL$C_SUPER,- ;
OACO 2103      VECTOR=#0 ; PRIMARY VECTOR
OACO 2104      $SETEXV_S      ADDRESS=@SCOND_LASTCHANC,- ;
OACO 2105      ACMODE=#PSL$C_SUPER,- ;
OACO 2106      VECTOR=#2 ; LAST CHANCE VECTOR
OACO 2107      RSB ;
OACO 2108 :
OACO 2109      .DISABLE      LOCAL_BLOCK
OACO 2110      .PAGE
OACO 2111      .SBTTL SETRUNDWN - SET UP RUNDOWN HANDLER
OACO 2112 :
OACO 2113 :++
OACO 2114 :
OACO 2115 : FUNCTIONAL DESCRIPTION:
OACO 2116 :
OACO 2117 : This routine inserts the specified routine entry point into
OACO 2118 : the process' rundown vector. CMKRNL privilege of running in
OACO 2119 : kernel or exec mode is required.
OACO 2120 :
OACO 2121 : CALLING SEQUENCE:
OACO 2122 : CALLx SETRUNDWN
OACO 2123 :

```

```

OACO 2124 : INPUT PARAMETERS:
OACO 2125 :     4(AP): address of rundown handler routine
OACO 2126 :
OACO 2127 : IMPLICIT INPUTS:
OACO 2128 :     NONE
OACO 2129 :
OACO 2130 : OUTPUT PARAMETERS:
OACO 2131 :     NONE
OACO 2132 :
OACO 2133 : IMPLICIT OUTPUTS:
OACO 2134 :     NONE
OACO 2135 :
OACO 2136 : COMPLETION CODES:
OACO 2137 :     $$$_NORMAL if successfully completed
OACO 2138 :     $$$_NOPRIV if not privileged
OACO 2139 :
OACO 2140 : SIDE EFFECTS:
OACO 2141 :     NONE
OACO 2142 :
OACO 2143 : --
OACO 2144 :
OACO 2145 :     .WEAK    CTL$GL_USRUNDWN
OACO 2146 :
OACO 2147 : SETRUNDWN:
OACO 2148 :     .WORD    ^M<>
OACO 2149 :     MOVPSL  RO                ; get PSL of caller
OACO 2150 :     CMPZV   #PSL$V_PVMOD,#PSL$_PRVMOD,RO,#PSL$_SC_USER
OACO 2151 :     BLSSU   5$                ; branch if DELTA started up in inner mode
OACO 2152 :     PUSHL   AP                ; build $CMKRNL arg list on stack
OACO 2153 :     PUSHAB  10$               ; address of kernel mode routine
OACO 2154 :     CALLS   #2,G^SYSS$CMKRNL ; call actual routine in kernel mode
OACO 2155 :     RET
OACO 2156 :
OACO 2157 : 5$:     MOVL   #$$$_NORMAL,RO ; DELTA started up in an inner mode
OACO 2158 :     RET     ; do not set up a rundown handler
OACO 2159 :
OACO 2160 :
OACO 2161 : ; The following code executes in kernel mode and actually inserts the
OACO 2162 : ; entry point into the rundown vector.
OACO 2163 :
OACO 2164 :
OACO 2165 : 10$:    .WORD    ^M<>                ; save no registers
OACO 2166 :     SETIPL  #IPL$_ASTDEL          ; lock out AST's while we modify the vector
OACO 2167 :     MOVAB   G^CTL$GL_USRUNDWN, R1 ; Get rundown vector pointer address.
OACO 2168 :     BEQL   19$                   ; Branch to NOP routine if no present.
OACO 2169 :     SUBL3   #4,(R1),R1           ; Get address of rundown vector.
OACO 2170 :     MOVL   #$$$_VEC(FULL),RO
OACO 2171 :     CMPL   (R1),#<256-7>        ; check if another vector will fit
OACO 2172 :     BGEQU  20$                   ; branch if not
OACO 2173 :     ADDL3   (R1),R1,RO           ; point to free vector
OACO 2174 :     MOVW   #^X9F16,(RO)+        ; insert JSB @#...
OACO 2175 :     MOVL   4(AP),(RO)+          ; insert routine address
OACO 2176 :     MOVB   #^X05,(RO)+         ; insert final RSB
OACO 2177 :     ADDL   #6,(R1)              ; update end pointer
OACO 2178 :     ADDW   #6,G^IAC$AW_VECSET+4 ; adjust image activation end pointer
OACO 2179 : 19$:    MOVL   #$$$_NORMAL,RO    ; indicate success
OACO 2180 : 20$:    SETIPL  #0                ; enable AST's again

```

```

OACO 2181      RET
OACO 2182      .PAGE
OACO 2183      .SBTTL  SETWRT - SET PAGES WRITABLE
OACO 2184      :
OACO 2185      : MAKE SPECIFIED RANGE OF PAGES WRITABLE
OACO 2186      :
OACO 2187      : R4 = STARTING ADDRESS
OACO 2188      : R5 = ENDING ADDRESS
OACO 2189      :
OACO 2190      : R0-R2 DESTROYED
OACO 2191      :
OACO 2192      :
OACO 2193      SETWRT:
OACO 2194      MOVQ  R4, -(SP)
OACO 2195      MOVAL -(SP), R2
OACO 2196      $CMKRNLS B^SETPRTK, (R2)
OACO 2197      BLBS  R0, 10$
OACO 2198      CALLG (R2), B^SETPRTK
OACO 2199      10$: POPR  #^M<R2>
OACO 2200      ADDL  #8, SP
OACO 2201      RSB
OACO 2202      :
OACO 2203      SETPRTK: .WORD 0
OACO 2204      $SETPRTS INADR=4(AP), -
OACO 2205      PROT=#PRTS_UW, -
OACO 2206      ACMODE=#0, -
OACO 2207      PRVPRT=(AP)
OACO 2208      MOVL  #1, R0
OACO 2209      RET
OACO 2210      :
OACO 2211      REPROT:
OACO 2212      RSB
OACO 2213      .PAGE
OACO 2214      .SBTTL  FETCHP - FETCH DATA FROM ANOTHER PROCESS
OACO 2215      :
OACO 2216      FETCHP: CASE CURTYPE-B(R11), TYPE=B, <-
OACO 2217      10$, -
OACO 2218      20$, -
OACO 2219      30$>
OACO 2220      RSB
OACO 2221      10$: PUSHAB W^FPBYTE
OACO 2222      BRB  40$
OACO 2223      20$: PUSHAB W^FPWORD
OACO 2224      BRB  40$
OACO 2225      30$: PUSHAB W^FPLONG
OACO 2226      40$: PUSHL PID-B(R11)
OACO 2227      PUSHAB QUAN-B(R11)
OACO 2228      PUSHL CURDOT-B(R11)
OACO 2229      PUSHL #4
OACO 2230      MOVL  SP, R0
OACO 2231      $CMKRNLS W^QGET, (R0)
OACO 2232      BLBC  R0, 50$
OACO 2233      $HIBERS
OACO 2234      50$: ADDL  #20, SP
OACO 2235      RSB
OACO 2236      .PAGE
OACO 2237      .SBTTL  QGET - QUEUE AST TO GET DATA FROM ANOTHER PROCESS

```

```

: PUSH RANGE ONTO STACK
: ADDRESS FOR RETURN OF PROT
: TRY IN KERNEL MODE FIRST
: CONTINUE IF NO ERROR
: IF NOT ENOUGH PRIV, TRY USER MODE
: RESTORE PROTECTION VALUE
: CLEAN STACK
: RETURN

```

```

: WRITABLE BY ALL
: ADDRESS AT WHICH TO RETURN PROT
: ALWAYS SUCCESS

```

```

: RESTORE PROTECTION

```

```

: 0 => BYTE
: 1 => WORD
: 2 => LONG
: UNKNOWN
: SET FOR BYTE FETCH
: SET FOR WORD FETCH
: SET FOR LONGWORD FETCH
: PID OF TARGET PROCESS
: SET ADDRESS TO RETURN VALUE
: AND ADDRESS OF VALUE
: ARGUMENT COUNT
: SAVE POINTER TO ARG LIST
: Q AST FOR DATA FETCH
: BR IF FAILED
: WAIT FOR DATA TO RETURN
: CLEAN STACK
: AND RETURN DATA

```



```
OACO 2238 :  
OACO 2239 :  
OACO 2240 : INPUTS: 04(AP) - LOCATION OF DATA  
OACO 2241 : 08(AP) - RETURN LOCATION  
OACO 2242 : 12(AP) - PID OF TARGET PROCESS  
OACO 2243 : 16(AP) - CODE SEGMENT POINTER  
OACO 2244 :  
OACO 2245 : .WEAK EXE$ALLOCBUF ; MAKE FOLLOWING CODE OPTIONAL  
OACO 2246 : .WEAK EXE$DEANONPAGED  
OACO 2247 : .WEAK SCH$WAKE  
OACO 2248 : .WEAK SCH$QAST  
OACO 2249 : .WEAK SCH$GL_MAXPIX  
OACO 2250 : FP_ORIGPID=ACB$L_AST  
OACO 2251 : FP_ADDR=ACB$L_ASTPRM  
OACO 2252 : FP_VALUE=ACB$C_ASTPRM  
OACO 2253 : FP_RETLOC=ACB$C_KAST+4  
OACO 2254 :  
OACO 2255 QGET: .WORD ^M<R2,R3,R4,R5> ; ENTRY MASK  
OACO 2256 MOVZWL #SS$ NONEXPR,R0 ; ASSUME BAD PIX  
OACO 2257 MOVAB G^EXE$ALLOCBUF,R1 ; WERE WE LINKED WITH SYS.STB SYMBOLS?  
OACO 2258 BEQL 10$ ; IF NOT, RETURN WITH ERROR  
OACO 2259 CMPW 12(AP),@#SCH$GL_MAXPIX ; CHECK PIX FOR LEGAL PROCESS  
OACO 2260 BGTRU 10$ ; BR IF NOT
```

```

OACO 2262      MOVZWL @16(AP),R1          ; GET SIZE OF CODE SEGMENT
OACO 2263      MOVAB  IRPSC_LENGTH(R1),R1      ; ADD SIZE OF PACKET DATA
OACO 2264      JSB    @#EXES$ALLOCBUF         ; ALLOCATE BUFFER TO CONTAIN CODE
OACO 2265      BLBC  R0,10$                 ; BRANCH IF NONE
OACO 2266      MOVL  R2,R5                  ; SAVE ADDRESS OF PACKET
OACO 2267      MOVL  PCBSL_PID(R4),FP_ORIGPID(R5) ; SET PID FOR RETURN
OACO 2268      MOVB  #^X80,ACBSB_RMOD(R5)    ; SET FOR SPECIAL KERNEL AST
OACO 2269      MOVAB ACBSL_KAST+8(R5),ACBSL_KAST(R5) ; SET ADDRESS FOR AST
OACO 2270      MOVL  4(AP),FP_ADDR(R5)      ; SET ADDRESS FOR FETCH
OACO 2271      MOVL  8(AP),FP_RETLOC(R5)    ; AND ADDRESS OF RETURN LOCATION
OACO 2272      MOVL  16(AP),R0              ; GET ADDRESS OF CODE SEGMENT
OACO 2273      MOVL  12(AP),ACBSL_PID(R5)   ; SET TARGET PID
OACO 2274      PUSHR #^M<R0,R1,R2,R3,R4,R5> ; SAVE REGS FOR MOVC
OACO 2275      MOVC3 (R0)+,(R0),ACBSL_KAST+8(R5) ; COPY CODE SEGMENT TO BUFFER
OACO 2276      POPR  #^M<R0,R1,R2,R3,R4,R5> ; RESTORE REGISTERS
OACO 2277      MOVZBL #PRIS_TICOM,R2        ; SET PRIORITY INCREMENT CLASS
OACO 2278      JSB  @#SCH$QAST              ; QUEUE AST FOR TARGET
OACO 2279      RET  10$                    ; RETURN TO ORIGINAL MODE
OACO 2280
OACO 2281      .SBTTL FPBYTE - FETCH BYTE FROM PROCESS
OACO 2282      FPBYTE: .WORD 90$-,-2        ; SIZE OF CODE SEGMENT
OACO 2283      IFNORD #1,@FP_ADDR(R5),10$   ; BRANCH IF NOT READABLE
OACO 2284      MOVB  @FP_ADDR(R5),FP_VALUE(R5) ; GET VALUE
OACO 2285      10$: MOVL  FP_ORIGPID(R5),ACBSL_PID(R5) ; SET PID FOR RETURN AST
OACO 2286      MOVB  #^X80,ACBSB_RMOD(R5)    ; SET FOR KAST AGAIN
OACO 2287      MOVAB B^20$,ACBSL_KAST(R5)   ; SET NEW AST ADDRESS
OACO 2288      MOVZBL #PRIS_TICOM,R2        ; SET PRIORITY INCREMENT CLASS
OACO 2289      JMP  @#SCH$QAST              ; QUEUE RETURN AST
OACO 2290      20$: IFNOWRT #1,@FP_RETLOC(R5),30$ ; IF NOT WRITABLE THEN SKIP IT
OACO 2291      MOVB  FP_VALUE(R5),@FP_RETLOC(R5) ; RETURN VALUE
OACO 2292      30$: MOVL  ACBSL_PID(R5),R1   ; GET PID FOR WAKE
OACO 2293      SETIPL #IPL$-SYNCH          ; RAISE TO SYNCH
OACO 2294      JSB  @#SCH$WAKE              ; WAKE PROCESS
OACO 2295      SETIPL #IPL$-ASTDEL         ; LOWER IPL
OACO 2296      MOVL  R5,R0                  ; SET ADDRESS FOR RELEASE
OACO 2297      JMP  @#EXES$DEANONPAGED     ; FREE BLOCK AND EXIT
OACO 2298      90$:                          ; END OF CODE SEGMENT
OACO 2299
OACO 2300      .PAGE
OACO 2301      .SBTTL DPBYTE - DEPOSIT BYTE TO PROCESS
OACO 2302      DPBYTE: .WORD 90$-,-2        ; SIZE OF CODE SEGMENT
OACO 2303      20$: IFNOWRT #1,@FP_RETLOC(R5),30$ ; IF NOT WRITABLE THEN SKIP IT
OACO 2304      MOVB  FP_VALUE(R5),@FP_RETLOC(R5) ; RETURN VALUE
OACO 2305      30$: MOVL  R5,R0                  ; SET ADDRESS FOR RELEASE
OACO 2306      JMP  @#EXES$DEANONPAGED     ; FREE BLOCK AND EXIT
OACO 2307      90$:                          ; END OF CODE SEGMENT
OACO 2308
OACO 2309      .PAGE
OACO 2310      .SBTTL FWORD - FETCH WORD FROM PROCESS
OACO 2311      FWORD: .WORD 90$-,-2        ; SIZE OF CODE SEGMENT
OACO 2312      IFNORD #2,@FP_ADDR(R5),10$   ; BRANCH IF NOT READABLE
OACO 2313      MOVW  @FP_ADDR(R5),FP_VALUE(R5) ; GET VALUE
OACO 2314      10$: MOVL  FP_ORIGPID(R5),ACBSL_PID(R5) ; SET PID FOR RETURN AST
OACO 2315      MOVB  #^X80,ACBSB_RMOD(R5)    ; SET FOR KAST AGAIN
OACO 2316      MOVAB B^20$,ACBSL_KAST(R5)   ; SET FOR NEW AST ADDRESS
OACO 2317      MOVZBL #PRIS_TICOM,R2        ; SET PRIORITY INCREMENT CLASS
OACO 2318      JMP  @#SCH$QAST              ; QUEUE RETURN AST

```

```

OACO 2319 20$: IFNOWRT #2,@FP_RETLOC(R5),30$ : IF NOT WRITABLE THEN SKIP IT
OACO 2320 MOVW FP_VALUE(R5),@FP_RETLOC(R5) : RETURN VALUE
OACO 2321 30$: MOVL ACBSL_PID(R5),R1- : GET PID FOR WAKE
OACO 2322 SETIPL #IPL$-SYNCH : RAISE TO SYNCH
OACO 2323 JSB @#SCH$WAKE : WAKE PROCESS
OACO 2324 SETIPL #IPL$-ASTDEL : LOWER IPL
OACO 2325 MOVL R5,R0 : SET ADDRESS FOR RELEASE
OACO 2326 JMP @#EXE$DEANONPAGED : FREE BLOCK AND EXIT
OACO 2327 90$: : END OF CODE SEGMENT
OACO 2328
OACO 2329 .PAGE
OACO 2330 .SBTTL DPWORD - DEPOSIT WORD TO PROCESS
OACO 2331 DPWORD: .WORD 90$--2 : SIZE OF CODE SEGMENT
OACO 2332 20$: IFNOWRT #2,@FP_RETLOC(R5),30$ : IF NOT WRITABLE THEN SKIP IT
OACO 2333 MOVW FP_VALUE(R5),@FP_RETLOC(R5) : RETURN VALUE
OACO 2334 30$: MOVL R5,R0 : SET ADDRESS FOR RELEASE
OACO 2335 JMP @#EXE$DEANONPAGED : FREE BLOCK AND EXIT
OACO 2336 90$: : END OF CODE SEGMENT
OACO 2337
OACO 2338 .PAGE
OACO 2339 .SBTTL FPLONG - FETCH LONG FROM PROCESS
OACO 2340 FPLONG: .WORD 90$--2 : SIZE OF CODE SEGMENT
OACO 2341 IFNORD #4,@FP_ADDR(R5),10$ : BRANCH IF NOT READABLE
OACO 2342 MOVL @FP_ADDR(R5),FP_VALUE(R5) : GET VALUE
OACO 2343 10$: MOVL FP_ORIGPID(R5),ACBSL_PID(R5) : SET PID FOR RETURN AST
OACO 2344 MOVAB #^X80,ACBSB_RMOD(R5) : SET FOR KAST AGAIN
OACO 2345 MOVAB B^20$,ACBSL_KAST(R5) : SET NEW KAST ADDRESS
OACO 2346 CLRL R2 : NULL PRIO INCR
OACO 2347 JMP @#SCH$QAST : QUEUE RETURN AST
OACO 2348 20$: IFNOWRT #4,@FP_RETLOC(R5),30$ : IF NOT WRITABLE THEN SKIP IT
OACO 2349 MOVL FP_VALUE(R5),@FP_RETLOC(R5) : RETURN VALUE
OACO 2350 30$: MOVL ACBSL_PID(R5),R1- : GET PID FOR WAKE
OACO 2351 SETIPL #IPL$-SYNCH : RAISE TO SYNCH
OACO 2352 JSB @#SCH$WAKE : WAKE PROCESS
OACO 2353 SETIPL #IPL$-ASTDEL : LOWER IPL
OACO 2354 MOVL R5,R0 : SET ADDRESS FOR RELEASE
OACO 2355 JMP @#EXE$DEANONPAGED : FREE BLOCK AND EXIT
OACO 2356 90$: : END OF CODE SEGMENT
OACO 2357
OACO 2358 .PAGE
OACO 2359 .SBTTL DPLONG - DEPOSIT LONGWORD TO PROCESS
OACO 2360 DPLONG: .WORD 90$--2 : SIZE OF CODE SEGMENT
OACO 2361 20$: IFNOWRT #4,@FP_RETLOC(R5),30$ : IF NOT WRITABLE THEN SKIP IT
OACO 2362 MOVL FP_VALUE(R5),@FP_RETLOC(R5) : RETURN VALUE
OACO 2363 30$: MOVL R5,R0 : SET ADDRESS FOR RELEASE
OACO 2364 JMP @#EXE$DEANONPAGED : FREE BLOCK AND EXIT
OACO 2365 90$: : END OF CODE SEGMENT
OACO 2366 DELEND: :
OACO 2367 .ENDC
OACO 1 :
OACO 2 :
OACO 3 :
OACO 4 :
OACO 5 .END

```

NORMAL END STATEMENT WITHOUT START ADDRESS
USED TO ASSEMBLE XDELTA FOR EXEC DEBUGGING.

XDELTA
Symbol table

- EXECUTIVE DEBUGGER

B 9

15-SEP-1984 23:40:27 VAX/VMS Macro V04-00
5-MAR-1980 00:49:06 [DELTA.SRC]END.MAR;1

\$\$BASE	=	00000001		
\$\$DISPL	=	00000008		
\$\$GENSW	=	00000001		
\$\$HIGH	=	00000007		
\$\$LIMIT	=	00000006		
\$\$LOW	=	00000001		
\$\$MNSW	=	00000001		
\$\$MXSW	=	00000001		
ADD		00000286	R	02
ASTEN		000000E0	R	02
B		00000080	R	02
BLANK		000005CC	R	02
BMSG		00000964	R	02
BRKADR	=	000000E4	R	02
BRKCOM	=	00000131	R	02
BRKDSP	=	00000111	R	02
BRKOP	=	0000010B	R	02
BRKPOINT		00000702	R	02
BSLSH	=	0000005C		
CLR_730		0000084B	R	02
CLR_750		0000084B	R	02
CLR_780		0000083F	R	02
CLR_790		00000850	R	02
CLR_END		00000868	R	02
CLR_UV1		0000084B	R	02
COLON		000007B2	R	02
COMMA		000002E6	R	02
CON\$GETCHAR		*****	X	02
CON\$OWNCTY		*****	X	02
CON\$PUTCHAR		*****	X	02
CON\$RELEASECTY		*****	X	02
CONTEXT		00000000	R	02
CONTEXTSZ	=	000000E4		
CR	=	0000000D		
CRLF		00000550	R	02
CURDOT		00000080	R	02
CURTYPE		0000007E	R	02
DCOM		000001D2	R	02
DEPOSIT		00000A87	R	02
DIV		00000282	R	02
DOT		000007C2	R	02
DQUOTE		0000028A	R	02
DTYPE		0000007D	R	02
ENDEXPR		0000025B	R	02
ENDFIELD		000002E9	R	02
EQL1		0000063B	R	02
EQUALS		00000634	R	02
ERR2		00000677	R	02
ERR3		00000868	R	02
ERR4		000002C5	R	02
ERROR		000001D6	R	02
ESCAP		000005F1	R	02
EXE\$ACVIOLAT		*****	X	02
EXE\$BREAK		*****	X	02
EXE\$GB_CPUTYPE		*****	X	02
EXE\$GL_SCB		*****	X	02
EXE\$ROPRAND		*****	X	02

EXE\$TBIT	*****	X	02
EXECUTE	00000AAD	R	02
F1	00000058	R	02
F2	0000005C	R	02
F3	00000060	R	02
F4	00000064	R	02
F5	00000068	R	02
FCTR	0000007C	R	02
FETCH	00000304	R	02
GETBPTX	00000A63	R	02
GETCHAR	0000055A	R	02
GETCMD	000009EE	R	02
GETSCB	00000952	R	02
GLOBL	00000246	R	02
GO	000007A1	R	02
HIGH	0000024C	R	02
INBUF	00000004	R	02
INFLD	00000242	R	02
INISBRK	*****	X	02
INISRONLY	*****	X	02
INISWRITABLE	*****	X	02
INSBUF	00000078	R	02
INSLEN	00000074	R	02
INSTR	00000607	R	02
LBRACKET	0000067F	R	02
LF	= 0000000A		
LIB\$INS_DECODE	*****W	GX	02
LINEFEED	00000346	R	02
LOCOUT	0000034E	R	02
LOCP	00000604	R	02
LOCPROMPT	0000034B	R	02
MCHK	00000824	R	02
MCHKSAV	00000195	R	02
MFYFLG	0000006C	R	02
MFYFLGS	000007BC	R	02
MMG\$PAGEFAULT	*****	X	02
MODES	0000067A	R	02
MUL	0000027E	R	02
NBRK	= 00000008		
NEGATE	000005D5	R	02
NEXTDOT	0000032A	R	02
NEXTLOC	00000349	R	02
NEXTP	000001ED	R	02
NMODES	= 00000005		
NPRIM	= 0000002C		
NSEC	= 00000007		
NTERM	= 0000000A		
NTMPBRK	= 00000001		
OP\$_ACBD	= 0000006F		
OP\$_ACBF	= 0000004F		
OP\$_ACBG	= 00004FFD		
OP\$_ACBH	= 00006FFD		
OP\$_ADDD2	= 00000060		
OP\$_ADDD3	= 00000061		
OP\$_ADDF2	= 00000040		
OP\$_ADDF3	= 00000041		
OP\$_ADDG2	= 000040FD		

OPS_ADDG3 = 000041FD
OPS_ADDH2 = 000060FD
OPS_ADDH3 = 000061FD
OPS_ADDP4 = 00000020
OPS_ADDP6 = 00000021
OPS_ASMP = 000000F8
OPS_CLRD = 0000007C
OPS_CLRF = 000000D4
OPS_CLRG = 0000007C
OPS_CLRH = 00007CFD
OPS_CMPD = 00000071
OPS_CMPF = 00000051
OPS_CMPG = 000051FD
OPS_CMPH = 000071FD
OPS_CMPP3 = 00000035
OPS_CMPP4 = 00000037
OPS_CRC = 0000000B
OPS_CVTBD = 0000006C
OPS_CVTBF = 0000004C
OPS_CVTBG = 00004CFD
OPS_CVTBH = 00006CFD
OPS_CVTDB = 00000068
OPS_CVTDF = 00000076
OPS_CVTDH = 000032FD
OPS_CVTDL = 0000006A
OPS_CVTDW = 00000069
OPS_CVTFB = 00000048
OPS_CVTFD = 00000056
OPS_CVTFG = 000099FD
OPS_CVTFH = 000098FD
OPS_CVTFL = 0000004A
OPS_CVTFW = 00000049
OPS_CVTGB = 000048FD
OPS_CVTGF = 000033FD
OPS_CVTGH = 000056FD
OPS_CVTGL = 00004AFD
OPS_CVTGW = 000049FD
OPS_CVTHB = 000068FD
OPS_CVTHD = 0000F7FD
OPS_CVTHF = 0000F6FD
OPS_CVTHG = 000076FD
OPS_CVTHL = 00006AFD
OPS_CVTHW = 000069FD
OPS_CVTLD = 0000006E
OPS_CVTLF = 0000004E
OPS_CVTLG = 00004EFD
OPS_CVTLH = 00006EFD
OPS_CVTLP = 000000F9
OPS_CVTPL = 00000036
OPS_CVTPS = 00000008
OPS_CVTPT = 00000024
OPS_CVTRDL = 0000006B
OPS_CVTRFL = 0000004B
OPS_CVTRGL = 00004BFD
OPS_CVTRHL = 00006BFD
OPS_CVTSP = 00000009
OPS_CVTTP = 00000026

OPS_CVTWD = 0000006D
OPS_CVTWF = 0000004D
OPS_CVTWG = 00004DFD
OPS_CVTWH = 00006DFD
OPS_DIVD2 = 00000066
OPS_DIVD3 = 00000067
OPS_DIVF2 = 00000046
OPS_DIVF3 = 00000047
OPS_DIVG2 = 000046FD
OPS_DIVG3 = 000047FD
OPS_DIVH2 = 000066FD
OPS_DIVH3 = 000067FD
OPS_DIVP = 00000027
OPS_EDITPC = 00000038
OPS_EMODD = 00000074
OPS_EMODF = 00000054
OPS_EMODG = 000054FD
OPS_EMODH = 000074FD
OPS_MATCHC = 00000039
OPS_MNEG0 = 00000072
OPS_MNEGF = 00000052
OPS_MNEGG = 000052FD
OPS_MNEGH = 000072FD
OPS_MOVD = 00000070
OPS_MOVF = 00000050
OPS_MOVG = 000050FD
OPS_MOVH = 000070FD
OPS_MOVP = 00000034
OPS_MOVTC = 0000002E
OPS_MOVTUC = 0000002F
OPS_MULD2 = 00000064
OPS_MULD3 = 00000065
OPS_MULF2 = 00000044
OPS_MULF3 = 00000045
OPS_MULG2 = 000044FD
OPS_MULG3 = 000045FD
OPS_MULH2 = 000064FD
OPS_MULH3 = 000065FD
OPS_MULP = 00000025
OPS_POLYD = 00000075
OPS_POLYF = 00000055
OPS_POLYG = 000055FD
OPS_POLYH = 000075FD
OPS_SCANC = 0000002A
OPS_SKPC = 0000003B
OPS_SPANC = 0000002B
OPS_SUBD2 = 00000062
OPS_SUBD3 = 00000063
OPS_SUBF2 = 00000042
OPS_SUBF3 = 00000043
OPS_SUBG2 = 000042FD
OPS_SUBG3 = 000043FD
OPS_SUBH2 = 000062FD
OPS_SUBH3 = 000063FD
OPS_SBP4 = 00000022
OPS_SBP6 = 00000023
OPS_TSTD = 00000073

```

OPS_TSTF      = 00000053
OPS_TSTG      = 000053FD
OPS_TSTM      = 000073FD
OPER          = 00000294 R    02
OPER          = 0000007F RR   02
OPERATOR      = 000005CC R    02
OPERBAS       = 00000012
OUTB          = 00000006
OUTBB         = 00000343 R    02
OUTBSLSM     = 0000052B RR   02
OUTBUF        = 00000088 RR   02
OUTCHAR       = 00000534 RR   02
OUTCOM        = 00000507 R    02
OUTCR         = 00000004
OUTDIGIT      = 00000500 R    02
OUTER         = 000001CA RR   02
OUTINS        = 00000383 RR   02
OUTLONG       = 00000504 RR   02
OUTPC         = 000009DB RR   02
OUTPUT        = 00000357 RR   02
OUTPUTA       = 000004A2 RR   02
OUTPUT_ADDRESS = 000003FE RR   02
OUTR8         = 00000531 RR   02
OUTSPACE      = 0000054B RR   02
OUTZBUF       = 0000051D RR   02
OUTZSTRING    = 00000521 R    02
OVEROPCLEN   = 00000005
OVEROPCODES   = 00000199 R    02
OVRADR        = 00000108 RR   02
OVROPC        = 000000E4 R    02
PFNSAB_STATE  = ***** X  02
PFNSAB_TYPE   = ***** X  02
PFNSAL_BAK    = ***** X  02
PFNSAL_PTE    = ***** X  02
PFNSAW_REFCNT = ***** X  02
PFNSAW_SWPVBN = ***** X  02
PFNSAX_BLINK  = ***** X  02
PFNSAX_FLINK  = ***** X  02
PID           = 00000070 R    02
PR$_EMSR      = 0000004A
PR$_IPL       = 00000012
PR$_KSP       = 00000000
PR$_MAPEN     = 00000038
PR$_MCSR      = 00000026
PR$_SBIFS     = 00000030
PR$_SCBB      = 00000011
PR$_SID_TYP730 = 00000003
PR$_SID_TYP750 = 00000002
PR$_SID_TYP780 = 00000001
PR$_SID_TYP790 = 00000004
PR$_SID_TYPUV1 = 00000007
PRET          = 00000253 R    02
PREG          = 00000ABB RR   02
PRIMARY       = 0000019E RR   02
PROCEED       = 000007AA RR   02
PROCEED       = 000009F3 RR   02
PROGCTR       = 000007D5 R    02

```

```

PSL$S_CURMOD = 00000002
PSL$V_CURMOD = 00000018
PSL$V_TBIT   = 00000004
QUAN         = 00000084 R    02
QUANT        = 000007CF R    02
QUOT         = 00000027
QUOTE        = 00000A73 R    02
RDBUF        = 00000002
RDCR         = 00000000
REGCOM       = 000007EF RR   02
REGISTER     = 000007E7 RR   02
RELOC        = 00000472 RR   02
RESET        = 00000643 RR   02
RESTORE      = 000008EE RR   02
RESTORR      = 000008F3 RR   02
RETURN       = 000002C8 RR   02
RSET         = 000002DC R    02
RUBOUT       = 0000007F
SAVAP        = 000000C8 R    02
SAVE         = 0000086B RR   02
SAVOCR       = 000000DC RR   02
SAVPC        = 000000D4 RR   02
SAVPSL       = 000000D8 RR   02
SAVR2        = 000000A0 RR   02
SAVRCR       = 000000DE RR   02
SAVREG       = 00000098 RR   02
SAVRXCS      = 000000E0 RR   02
SAVSP        = 000000D0 RR   02
SCANP        = 000001E9 R    02
SCH$GL_CURPCB = ***** X  02
SCH$GL_PCBVEC = ***** X  02
SECOND       = 00000650 R    02
SEMI         = 00000657 RR   02
SETBRK       = 00000A40 RR   02
SHFT         = 00000279 RR   02
SHOBRK       = 0000075F RR   02
SLASH        = 0000028F R    02
SLSH         = 0000002F
SPACES       = 0000037F R    02
STATUS       = 00000054 RR   02
STEP         = 000006AE RR   02
STEPOVER     = 000006BB RR   02
SUPERST      = 000001DD R    02
SYS$LCLRSBIA = ***** X  02
TAB          = 000005E1 R    02
TERM         = 000001B7 RR   02
UNBRK        = 00000A2B RR   02
VALI         = 000007E4 RR   02
VALR         = 000007E1 RR   02
VALUE        = 000007D9 R    02
V_ASCII      = 00000001
V_ATBRK      = 00000004
V_F1         = 00000008
V_F2         = 00000009
V_F3         = 0000000A
V_F4         = 0000000B
V_F5         = 0000000C

```

XDELTA
Symbol table

- EXECUTIVE DEBUGGER

E 9

15-SEP-1984 23:40:27 VAX/VMS Macro V04-00
5-MAR-1980 00:49:06 [DELTA.SRC]END.MAR;1

Page 68
(1)

```

V_INFIELD      = 00000002
V_INSTR        = 0000000D
V_NEGATE       = 00000007
V_OPEN        = 00000000
V_PREG         = 0000001F
V_PMODE       = 0000000F
V_RUB         = 00000006
V_TBIT        = 00000003
V_TBITOK      = 00000005
XDELACV       = 00000824 R 02
XDELBPT       = 00000970 RG 02
XDELDBG       = 00000A1F R 02
XDELIBRK      = 000000E8 RG 02
XDELTBIT      = 00000A08 RG 02
XDEL_LOADBASE = 00000161 RG 02
XDSSGL_XESTRING = 0000018D RG 02
XDSSGL_XFSTRING = 00000191 RG 02
XDSSGT_WORD_PFN = ***** X 02
XREG          = 00000815 R 02
XREGV         = 00000155 R 02
XSET          = 00000803 R 02
  
```

! Psect synopsis !

PSECT name	Allocation	PSECT No.	Attributes
. ABS	00000000 (0.)	00 (0.)	NOPIC USR CON ABS LCL NOSHR NOEXE NORD NOWRT NOVEC BYTE
\$ABSS	00000000 (0.)	01 (1.)	NOPIC USR CON ABS LCL NOSHR EXE RD WRT NOVEC BYTE
Z\$DEBUG_CODE	00000AC0 (2752.)	02 (2.)	PIC USR CON REL LCL NOSHR EXE RD WRT NOVEC LONG

! Performance indicators !

Phase	Page faults	CPU Time	Elapsed Time
Initialization	30	00:00:00.09	00:00:01.55
Command processing	122	00:00:00.60	00:00:03.43
Pass 1	694	00:00:20.73	00:01:22.10
Symbol table sort	6	00:00:02.27	00:00:05.82
Pass 2	414	00:00:06.03	00:00:28.24
Symbol table output	2	00:00:00.18	00:00:00.32
Psect synopsis output	2	00:00:00.01	00:00:00.01
Cross-reference output	0	00:00:00.00	00:00:00.00
Assembler run totals	1272	00:00:29.92	00:02:01.49

The working set limit was 2250 pages.

144306 bytes (282 pages) of virtual memory were used to buffer the intermediate code.

There were 110 pages of symbol table space allocated to hold 2050 non-local and 118 local symbols.

5124 source lines were read in Pass 1, producing 21 object records in Pass 2.

160 pages of virtual memory were used to define 157 macros.

! Macro library statistics !

Macro library name	Macros defined
-----	-----
_S255SDUA28:[SYS.OBJ]LIB.MLB;1	12
_S255SDUA28:[SYSLIB]STARLET.MLB;2	11
TOTALS (all libraries)	23

2055 GETS were required to define 23 macros.

There were no errors, warnings or information messages.

MACRO/LIS=LIS:XDELTA/OBJ=OBJ\$XDELTA MASDS:[EMULAT.SRC]MISSING/UPDATE=(MASDS:[EMULAT.ENW]MISSING)+MASDS:[DELTA.SRC]XDELTA/UPDATE=(M

0102 AH-BT13A-SE
VAX/VMS V4.0

DIGITAL EQUIPMENT CORPORATION
CONFIDENTIAL AND PROPRIETARY

The image displays a grid of 100 small terminal windows, arranged in 10 rows and 10 columns. Each window shows a different system command and its corresponding output. The commands and outputs are as follows:

- Row 1: `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`
- Row 2: `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`
- Row 3: `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`
- Row 4: `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`
- Row 5: `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`
- Row 6: `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`
- Row 7: `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`
- Row 8: `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`
- Row 9: `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`
- Row 10: `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`, `DIR`

Some windows show specific command outputs, such as:

- `DIFDEF MDL`
- `DIF`
- `DIF MAP`
- `XDSTRING LIS`
- `DIFPRE REQ`
- `DIFGETCMD LIS`
- `DIFHEXOCT LIS`
- `DATA LIS`
- `XDELTA LIS`