

DDDDDDDDDDDD	EEEEEEEEEEEEEE	BBBBBBBBBBBBBB	UUU	UUU	GGGGGGGGGGGG
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DDD	DDD	BBB	UUU	UUU	GGG
DDD	DDD	BBB	UUU	UUU	GGG
DDD	DDD	BBB	UUU	UUU	GGG
DDD	DDD	BBB	UUU	UUU	GGG
DDDDDDDDDDDD	EEEEEEEEEEEEEE	BBBBBBBBBBBBBB	UUUUUUUUUUUUUU	UUUUUUUUUUUUUU	GGGGGGGGGG
DDDDDDDDDDDD	EEEEEEEEEEEEEE	BBBBBBBBBBBBBB	UUUUUUUUUUUUUU	UUUUUUUUUUUUUU	GGGGGGGGGG
DDDDDDDDDDDD	EEEEEEEEEEEEEE	BBBBBBBBBBBBBB	UUUUUUUUUUUUUU	UUUUUUUUUUUUUU	GGGGGGGGGG

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DDDDDDDD  BBBB8888  GGGGGGGG  AAAAAA  DDDDDDDD  DDDDDDDD  EEEEEEEEEE  XX  XX  PPPPPPPP
DDDDDDDD  BBBB8888  GGGGGGGG  AAAAAA  DDDDDDDD  DDDDDDDD  EEEEEEEEEE  XX  XX  PPPPPPPP
DD  DD  BB  BB  GG  AA  AA  DD  DD  DD  DD  EE  XX  XX  PP  PP
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DD  DD  BB  BB  GG  GGGGGG  AAAAAAAAAA  DD  DD  DD  DD  EE  XX  XX  PP
DD  DD  BB  BB  GG  GGGGGG  AAAAAAAAAA  DD  DD  DD  DD  EE  XX  XX  PP
DD  DD  BB  BB  GG  GG  AA  AA  DD  DD  DD  DD  EE  XX  XX  PP
DD  DD  BB  BB  GG  GG  AA  AA  DD  DD  DD  DD  EE  XX  XX  PP
DD  DD  BB  BB  GG  GG  AA  AA  DD  DD  DD  DD  EE  XX  XX  PP
DDDDDDDD  BBBB8888  GGGGGG  AA  AA  DDDDDDDD  DDDDDDDD  EEEEEEEEEE  XX  XX  PP
DDDDDDDD  BBBB8888  GGGGGG  AA  AA  DDDDDDDD  DDDDDDDD  EEEEEEEEEE  XX  XX  PP

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LL  II  SS
LL  II  SS
LLLLLLLLLLLL  IIIIII  SSSSSSSS
LLLLLLLLLLLL  IIIIII  SSSSSSSS

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1 0001 0 MODULE DBGADDEXP (IDENT = 'V04-000') =
2 0002 0
3 0003 1 BEGIN
4 0004 1
5 0005 1 |*****
6 0006 1 |*
7 0007 1 |* COPYRIGHT (c) 1978, 1980, 1982, 1984 BY
8 0008 1 |* DIGITAL EQUIPMENT CORPORATION, MAYNARD, MASSACHUSETTS.
9 0009 1 |* ALL RIGHTS RESERVED.
10 0010 1 |*
11 0011 1 |* THIS SOFTWARE IS FURNISHED UNDER A LICENSE AND MAY BE USED AND COPIED
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13 0013 1 |* INCLUSION OF THE ABOVE COPYRIGHT NOTICE. THIS SOFTWARE OR ANY OTHER
14 0014 1 |* COPIES THEREOF MAY NOT BE PROVIDED OR OTHERWISE MADE AVAILABLE TO ANY
15 0015 1 |* OTHER PERSON. NO TITLE TO AND OWNERSHIP OF THE SOFTWARE IS HEREBY
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17 0017 1 |*
18 0018 1 |* THE INFORMATION IN THIS SOFTWARE IS SUBJECT TO CHANGE WITHOUT NOTICE
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22 0022 1 |* DIGITAL ASSUMES NO RESPONSIBILITY FOR THE USE OR RELIABILITY OF ITS
23 0023 1 |* SOFTWARE ON EQUIPMENT WHICH IS NOT SUPPLIED BY DIGITAL.
24 0024 1 |*
25 0025 1 |*****
26 0026 1 |*****
27 0027 1 |*****
28 0028 1 WRITTEN BY
29 0029 1 |   Rich Title      August, 1982
30 0030 1
31 0031 1 MODULE FUNCTION
32 0032 1 |   This module contains the Address Expression Interpreter.
33 0033 1 |
34 0034 1
35 0035 1 REQUIRE 'SRC$:DBGPROLOG.REQ';
36 0169 1
37 0170 1 FORWARD ROUTINE
38 0171 1   DBG$EVAL_ADDR_OPERATOR,      | Evaluate an Address Expr. operator
39 0172 1   DBG$PRIM_TO_ADDR,           | Convert Primary Descriptor to
40 0173 1                           | Value Descriptor containing
41 0174 1                           | address of descriptor.
42 0175 1   DETERMINE_TYPE: NOVALUE,    | Determine type of inputs
43 0176 1   GET_DEREFERENCE;          | Perform dereference operation
44 0177 1
45 0178 1 EXTERNAL
46 0179 1   DBG$GL_DFLTYP,              | Holds type from SET TYPE command
47 0180 1   DBG$GW_DFLTLENG: WORD,     | Holds length from SET TYPE command
48 0181 1   DBG$REG_VALUES: VECTOR[,LONG]; | Register save area
49 0182 1
50 0183 1 EXTERNAL ROUTINE
51 0184 1   DBG$BUILD_PRIMARY_SUBNODE: NOVALUE, | Build a Primary Subnode
52 0185 1   DBG$CONV_TEXT_VALOE,        | Convert text string to value
53 0186 1   DBG$INS_DECODE,            | Decode instruction
54 0187 1   DBG$MAKE_VAL_DESC,         | Materialize value into Val Descr
55 0188 1   DBG$MAKE_VMS_DESC,         | Convert Primary Descriptor to
56 0189 1                           | VAX standard descriptor
57 0190 1   DBG$MAKE_SKELETON_DESC,   | Build skeleton descriptor

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: 58      0191 1      DBG$NCPY_DESC.      : Copy descriptors
: 59      0192 1      DBG$PRIM_TO_VAL.    : Convert Primary Descriptor to
: 60      0193 1      : Value Descriptor.
: 61      0194 1      DBG$STA_SYMNAME : NOVALUE.    : Obtain name of symbol from SYMID
: 62      0195 1      DBG$STA_SYM_IS_LITERAL. : Determine whether a symid represents
: 63      0196 1      : a literal value.
: 64      0197 1      DBG$STA_TYPEFCODE,      : Find fcode
: 65      0198 1      DBG$STA_TYP_TYPEDPTR: NOVALUE; : Look up typed pointer
: 66      0199 1
: 67      0200 1      ! Define some codes for the kinds of addresses that a descriptor
: 68      0201 1      ! can represent.
: 69      0202 1
: 70      0203 1      LITERAL
: 71      0204 1      ADDR$K_UNKNOWN = -1,      : Unknown type
: 72      0205 1      ADDR$K_MINTYPE = 1,      : Minimum of known type codes below
: 73      0206 1      ADDR$K_LITERAL = 1,      : Literal value
: 74      0207 1      ADDR$K_PRIMARY = 2,      : Primary Descriptor
: 75      0208 1      ADDR$K_INST = 3,      : Address of instruction
: 76      0209 1      ADDR$K_DATA = 4,      : Address of data
: 77      0210 1      ADDR$K_BITFIELD = 5,     : Bit field within address
: 78      0211 1      ADDR$K_MAXTYPE = 5;     : Maximum of codes above
: 79      0212 1

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81      0213 1 GLOBAL ROUTINE DBG$EVAL_ADDR_OPERATOR(OPERATOR, LEFT_ARG, RIGHT_ARG) =
82      0214 1
83      0215 1 FUNCTION
84      0216 1     This routine does the actual evaluation of a DEBUG Address Expression
85      0217 1     operator. It does a CASE on the operator code and does the appropriate
86      0218 1     address computation or other operation for those operators allowed in
87      0219 1     DEBUG Address Expressions. For any operator not allowed in an Address
88      0220 1     Expression, it signals an error message.
89      0221 1
90      0222 1 INPUTS
91      0223 1     OPERATOR - The Operator Token Entry for the operator to be evaluated.
92      0224 1
93      0225 1     LEFT_ARG - A pointer to the left argument Primary Descriptor or Value
94      0226 1     Descriptor. If the operator is a unary operator, LEFT_ARG
95      0227 1     points to the operator's one argument.
96      0228 1
97      0229 1     RIGHT_ARG - A pointer to the right argument Primary Descriptor or Value
98      0230 1     Descriptor. If the operator is a unary operator, RIGHT_ARG
99      0231 1     is not used.
100     0232 1
101     0233 1 OUTPUTS
102     0234 1     A pointer to the Value Descriptor or Primary Descriptor
103     0235 1     which results from the evaluation of
104     0236 1     the operator is returned as this routine's result.
105     0237 1
106     0238 1
107     0239 2 BEGIN
108     0240 2
109     0241 2 MAP
110     0242 2     OPERATOR: REF TOKEN$ENTRY,      ! Token Entry for operator to perform
111     0243 2     LEFT_ARG: REF DBG$VALDESC,    ! Left operand Token Entry
112     0244 2     RIGHT_ARG: REF DBG$VALDESC; ! Right operand Token Entry
113     0245 2
114     0246 2 LOCAL
115     0247 2     BINARY_FLAG,                ! TRUE if the Address Expression Operator
116     0248 2     ! is a binary operator
117     0249 2     LEFT_TYPE,                ! Address type of left operand (one of
118     0250 2     ! the above codes
119     0251 2     OPCODE,                    ! The opcode of the Address Expression Operator
120     0252 2     RIGHT_TYPE;              ! Address type of right operand (one
121     0253 2     ! of the above codes.
122     0254 2
123     0255 2
124     0256 2 ! The following macro containing the processing that is done on
125     0257 2 ! the ADD and SUBTRACT operators. This processing is the same
126     0258 2 ! except for occurrences of either a '+' or a '-'. For that reason,
127     0259 2 ! it was separated out as a macro.
128     0260 2
129     0261 2 MACRO PROCESS_ADD_OR_SUBTRACT (OP) =
130     0262 2
131     0263 2     CASE LEFT_TYPE FROM ADDR$K_MINTYPE TO ADDR$K_MAXTYPE OF
132     0264 2     SET
133     0265 2
134     0266 2     ! Left argument is a Primary Descriptor.
135     0267 2     !
136     0268 2     [ADDR$K_PRIMARY] :
137     0269 2     BEGIN

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: For all right args, we need to convert
: the Primary Descriptor to Value Descriptor before we
: do any operation.
DBG$PRIM TO VAL (.LEFT_ARG, DBG$K_V_VALUE_DESC, LEFT_ARG);
RETURN DBG$EVAL_ADDR_OPERATOR (
    .OPERATOR,
    .LEFT_ARG,
    .RIGHT_ARG );
END;

[ADDR$K_LITERAL] :
BEGIN
    IF .RIGHT_TYPE EQL ADDR$K_PRIMARY
    THEN
        BEGIN
            DBG$PRIM TO VAL (.RIGHT_ARG, DBG$K_V_VALUE_DESC, RIGHT_ARG);
            RETURN DBG$EVAL_ADDR_OPERATOR (
                .OPERATOR,
                .LEFT_ARG,
                .RIGHT_ARG);
        END;
    : For all other right operands, just add the literal
    : value to the right operand, and return the right
    : operand. Thus the result retains the type of the
    : right operand.
    RIGHT_ARG [DBG$L_VALUE_POINTER] =
        .LEFT_ARG [DBG$L_VALUE_POINTER] OP
        .RIGHT_ARG [DBG$C_VALUE_POINTER];
    : Handle result type of instruction.
    IF .RIGHT_TYPE EQL ADDR$K_INST
    THEN
        RIGHT_ARG[DBG$W_VALUE_LENGTH] =
            DBG$INS_DECODE(.RIGHT_ARG[DBG$L_VALUE_POINTER],
                FALSE, FALSE) -
            .RIGHT_ARG[DBG$L_VALUE_POINTER];
    : Handle bitfields on the right.
    IF .RIGHT_TYPE EQL ADDR$K_BITFIELD
    THEN
        BEGIN
            RIGHT_ARG [DBG$L_VALUE_POS] =
                .LEFT_ARG [DBG$L_VALUE_POS] OP
                .RIGHT_ARG [DBG$C_VALUE_POS];
            : Normalize the bit offset.
            RIGHT_ARG [DBG$L_VALUE_POINTER] =
                .RIGHT_ARG [DBG$L_VALUE_POINTER] +

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      .RIGHT_ARG [DBG$L_VALUE_POS] / 8;
RIGHT_ARG [DBG$L_VALUE_POS] =
      .RIGHT_ARG [DBG$L_VALUE_POS] MOD 8;
END;

RETURN .RIGHT_ARG;
END;

[ADDR$K_INST, ADDR$K_DATA] :
BEGIN
  ! Instruction OP Primary
  ! Convert the Primary and try again.
  IF .RIGHT_TYPE EQL ADDR$K_PRIMARY
  THEN
    BEGIN
      DBG$PRIM TO VAL (.RIGHT_ARG, DBG$K_V_VALUE_DESC, RIGHT_ARG);
      RETURN DBG$EVAL_ADDR_OPERATOR (
        .OPERATOR,
        .LEFT_ARG,
        .RIGHT_ARG);
    END;
  ! If the right arg is a bitfield, retain the type
  ! of the right arg.
  IF .RIGHT_TYPE EQL ADDR$K_BITFIELD
  THEN
    BEGIN
      RIGHT_ARG [DBG$L_VALUE_POINTER] =
        .LEFT_ARG [DBG$L_VALUE_POINTER] OP
        .RIGHT_ARG [DBG$L_VALUE_POINTER];
      RIGHT_ARG [DBG$L_VALUE_POS] =
        .LEFT_ARG [DBG$L_VALUE_POS] OP
        .RIGHT_ARG [DBG$L_VALUE_POS];
      ! Normalize the bit offset.
      RIGHT_ARG [DBG$L_VALUE_POINTER] =
        .RIGHT_ARG [DBG$L_VALUE_POINTER] +
        .RIGHT_ARG [DBG$L_VALUE_POS] / 8;
      RIGHT_ARG [DBG$L_VALUE_POS] =
        .RIGHT_ARG [DBG$L_VALUE_POS] MOD 8;
      RETURN .RIGHT_ARG;
    END;
  ! Add the addresses.
  LEFT_ARG [DBG$L_VALUE_POINTER] =
    .LEFT_ARG [DBG$L_VALUE_POINTER] OP
    .RIGHT_ARG [DBG$L_VALUE_POINTER];
  ! If we are retaining instruction type then
  ! fill in the length correctly.
  IF .RIGHT_TYPE EQL ADDR$K_LITERAL

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THEN
BEGIN
IF .LEFT_TYPE EQL ADDR$K_INST
THEN
LEFT_ARG[DBG$W_VALUE_LENGTH] =
DBG$INS_DECODE(.LEFT_ARG[DBG$L_VALUE_POINTER],
FALSE, FALSE) =
.LEFT_ARG[DBG$L_VALUE_POINTER];
END

! If we are not adding a literal value, zero out
! the type information. This reflects the fact
! that in expressions such as EX INST OP DATA,
! the address being examined is of unknown type.
ELSE
BEGIN
LEFT_ARG [DBG$B_DHDR_KIND] = RST$K_DATA;
LEFT_ARG [DBG$B_DHDR_FCODE] = RST$K_TYPE_DESCR;
LEFT_ARG [DBG$V_DHDR_OVERRIDE] = TRUE;
LEFT_ARG [DBG$B_VALUE_CLASS] = 0;
LEFT_ARG [DBG$B_VALUE_DTYPE] = .DBG$GL_DFLT_TYP;
IF .DBG$GL_DFLT_TYP EQL DSC$K_DTYPE_ZI
THEN
LEFT_ARG[DBG$W_VALUE_LENGTH] =
DBG$INS_DECODE(.LEFT_ARG[DBG$L_VALUE_POINTER],
FALSE, FALSE) =
.LEFT_ARG[DBG$L_VALUE_POINTER]
ELSE
LEFT_ARG [DBG$W_VALUE_LENGTH] = .DBG$GW_DFLT_LEN;
END;

! Return the left argument.
RETURN .LEFT_ARG;
END;

[ADDR$K_BITFIELD] :
BEGIN
! Bitfield OP Primary.
! Convert the Primary and try again.
IF .RIGHT_TYPE EQL ADDR$K_PRIMARY
THEN
BEGIN
DBG$PRIM TO VAL (.RIGHT_ARG, DBG$K_V_VALUE_DESC, RIGHT_ARG);
RETURN DBG$EVAL_ADDR_OPERATOR (
.OPERATOR,
.LEFT_ARG,
.RIGHT_ARG);
END;

! In other cases, add the addresses.
LEFT_ARG [DBG$L_VALUE_POINTER] =

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      .LEFT_ARG [DBG$L_VALUE_POINTER] OP
      .RIGHT_ARG [DBG$L_VALUE_POINTER];
LEFT_ARG [DBG$L_VALUE_POS] =
      .LEFT_ARG [DBG$L_VALUE_POS] OP
      .RIGHT_ARG [DBG$C_VALUE_POS];

      ! Normalize the bit offset.
      LEFT_ARG [DBG$L_VALUE_POINTER] =
      .LEFT_ARG [DBG$L_VALUE_POINTER] +
      .LEFT_ARG [DBG$L_VALUE_POS] / 8;
LEFT_ARG [DBG$L_VALUE_POS] =
      .LEFT_ARG [DBG$L_VALUE_POS] MOD 8;

      RETURN .LEFT_ARG;
      END;

      TES %;

      ! Multiply and divide are similar to add and subtract except that
      ! we never try to retain a Primary for symbolization purposes.
MACRO PROCESS_MULTIPLY_OR_DIVIDE (OP) =
      BEGIN
      ! Handle the case where the left arg is a Primary.
      !
      ! IF .LEFT_TYPE EQL ADDR$K_PRIMARY
      ! THEN
      ! BEGIN
      !
      ! For all right args, we need to convert
      ! the Primary Descriptor to Value Descriptor before we
      ! do any operation.
      !
      ! DBG$PRIM TO VAL (.LEFT_ARG, DBG$K_V_VALUE_DESC, LEFT_ARG);
      ! RETURN DBG$EVAL_ADDR_OPERATOR (
      !     .OPERATOR,
      !     .LEFT_ARG,
      !     .RIGHT_ARG );
      !
      ! END;

      ! Handle the case where the right arg is a Primary.
      !
      ! IF .RIGHT_TYPE EQL ADDR$K_PRIMARY
      ! THEN
      ! BEGIN
      !
      ! Convert the Primary and try again.
      !
      ! DBG$PRIM TO VAL (.RIGHT_ARG, DBG$K_V_VALUE_DESC, RIGHT_ARG);
      ! RETURN DBG$EVAL_ADDR_OPERATOR (
      !     .OPERATOR,
      !     .LEFT_ARG,
      !     .RIGHT_ARG);

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END;
! Neither arg is a Primary.
! Multiply the addresses. Zero out the bit offset.
LEFT_ARG [DBG$L_VALUE_POINTER] =
    .LEFT_ARG [DBG$L_VALUE_POINTER] OP
    .RIGHT_ARG [DBG$L_VALUE_POINTER];
! Zero out the bit offset. Change the type to
! unknown and return the left arg.
LEFT_ARG [DBG$V_DHDR_LITERAL] = FALSE;
LEFT_ARG [DBG$L_VALUE_POS] = 0;
LEFT_ARG [DBG$B_DHDR_KIND] = RST$K_DATA;
LEFT_ARG [DBG$B_DHDR_FCODE] = RST$K_TYPE_DESCR;
LEFT_ARG [DBG$V_DHDR_OVERRIDE] = TRUE;
LEFT_ARG [DBG$B_VALUE_CLASS] = 0;
LEFT_ARG [DBG$B_VALUE_DTYPE] = .DBG$GL_DFLTTP;
IF .DBG$GL_DFLTTP EQ[ DSC$K_DTYPE_ZI
THEN
    LEFT_ARG[DBG$W_VALUE_LENGTH] =
        DBG$INS_DECODE(.LEFT_ARG[DBG$L_VALUE_POINTER],
            FALSE, FALSE) -
        .LEFT_ARG[DBG$L_VALUE_POINTER]
ELSE
    LEFT_ARG [DBG$W_VALUE_LENGTH] = .DBG$GW_DFLTLENG;
RETURN .LEFT_ARG;
END %;

! Get the opcode and set the flag saying whether we are processing
! a binary operator. Also initialize the left and right type codes
! to unknown.
OPCODE = .OPERATOR [TOKEN$W_CODE];
BINARY_FLAG = .OPCODE EQL TOKEN$K_ADD
    OR .OPCODE EQL TOKEN$K_SUBTRACT
    OR .OPCODE EQL TOKEN$K_MULTIPLY
    OR .OPCODE EQL TOKEN$K_DIVIDE;

! Call the routine which fills in the codes for LEFT_TYPE and
! RIGHT_TYPE. If an unconverted value needs to be converted,
! this routine constructs a new descriptor and the LEFT_ARG and
! RIGHT_ARG pointers may be modified to point to these new
! descriptors.
DETERMINE_TYPE (.LEFT_ARG, LEFT_ARG, LEFT_TYPE);
IF .BINARY_FLAG
THEN
    DETERMINE_TYPE (.RIGHT_ARG, RIGHT_ARG, RIGHT_TYPE);

! Select the operation to perform based on the operation code.
CASE .OPERATOR [TOKEN$W_CODE] FROM TOKEN$K_MIN_OPERATOR TO TOKEN$K_MAX_OPERATOR OF
    SET

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: 423      0555      2
: 424      0556      2
: 425      0557      2
: 426      0558      2
: 427      0559      2
: 428      0560      2
: 429      0561      2
: 430      0562      2
: 431      0563      2
: 432      0564      2
: 433      0565      2
: 434      0566      2
: 435      0567      2
: 436      0568      2
: 437      0569      2
: 438      0570      2
: 439      0571      2
: 440      0572      2
: 441      0573      2
: 442      0574      2
: 443      0575      2
: 444      0576      2
: 445      0577      2
: 446      0578      2
: 447      0579      4
: 448      0580      4
: 449      0581      4
: 450      0582      4
: 451      0583      4
: 452      0584      4
: 453      0585      4
: 454      0586      4
: 455      0587      4
: 456      0588      4
: 457      0589      4
: 458      0590      4
: 459      0591      4
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: 461      0593      4
: 462      0594      4
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: 465      0597      4
: 466      0598      4
: 467      0599      4
: 468      0600      4
: 469      0601      4
: 470      0602      4
: 471      0603      4
: 472      0604      4
: 473      0605      4
: 474      0606      4
: 475      0607      4
: 476      0608      4
: 477      0609      4
: 478      0610      4
: 479      0611      4

: Do the identity operation.
[TOKEN$K_IDENTITY]:
  BEGIN
  RETURN .LEFT_ARG;
  END;

: Do the indirection (dereferencing) operation.
[TOKEN$K_INDIRECT]:
  BEGIN
  BUILTIN
  PROBER;

  LOCAL
  ADDRESS,          ! Address which is value of left arg
  LENGTH;          ! Bit length given in left arg.

  ! Primary Descriptors.
  IF .LEFT_TYPE EQL ADDR$K_PRIMARY
  THEN
  BEGIN
  LOCAL
  LANG,
  VMS_DESC: DBG$STG_DESC;

  ! If we can dereference a typed pointer, and return
  ! a Primary representing the pointed-to object,
  ! then do so.
  IF GET_DEREFERENCE(.LEFT_ARG)
  THEN
  RETURN .LEFT_ARG;

  ! Use MAKE_VMS_DESC and MAKE_VAL_DESC to do the fetch.
  ! Preserve the language code.
  LANG = .LEFT_ARG[DBG$B_DHDR_LANG];
  DBG$MAKE_VMS_DESC (.LEFT_ARG, VMS_DESC);
  LEFT_ARG = DBG$MAKE_VAL_DESC (VMS_DESC, DBG$K_VALUE_DESC);
  LEFT_ARG[DBG$B_DHDR_LANG] = .LANG;

  ! Change the descriptor back to a Volatile Value Descriptor.
  LEFT_ARG[DBG$B_DHDR_TYPE] = DBG$K_V_VALUE_DESC;
  LEFT_ARG[DBG$L_VALUE_POINTER] = .LEFT_ARG[DBG$L_VALUE_VALUE0];
  IF .LEFT_ARG[DBG$B_VALUE_CLASS] EQL DSC$K_CLASS_UBS
  THEN
  LEFT_ARG[DBG$L_VALUE_POS] = .LEFT_ARG[DBG$L_VALUE_VALUE1];

  ! Do a special case check for ".PC"
  IF .VMS_DESC[DSC$A_POINTER] EQLA DBG$REG_VALUES[15]
  THEN

```

```

480 0612 S
481 0613 S
482 0614 S
483 0615 S
484 0616 S
485 0617 S
486 0618 S
487 0619 S
488 0620 S
489 0621 S
490 0622 S
491 0623 S
492 0624 S
493 0625 S
494 0626 S
495 0627 S
496 0628 S
497 0629 S
498 0630 S
499 0631 S
500 0632 S
501 0633 S
502 0634 S
503 0635 S
504 0636 S
505 0637 S
506 0638 S
507 0639 S
508 0640 S
509 0641 S
510 0642 S
511 0643 S
512 0644 S
513 0645 S
514 0646 S
515 0647 S
516 0648 S
517 0649 S
518 0650 S
519 0651 S
520 0652 S
521 0653 S
522 0654 S
523 0655 S
524 0656 S
525 0657 S
526 0658 S
527 0659 S
528 0660 S
529 0661 S
530 0662 S
531 0663 S
532 0664 S
533 0665 S
534 0666 S
535 0667 S
536 0668 S

```

```

BEGIN
    ! Change dtype to instruction.
    LEFT_ARG[DBG$B_VALUE_CLASS] = DSC$K_CLASS_S;
    LEFT_ARG[DBG$B_VALUE_DTYPE] = DSC$K_DTYPE_Z;
    LEFT_ARG[DBG$W_VALUE_LENGTH] =
        DBG$INS_DECODE(.LEFT_ARG[DBG$L_VALUE_POINTER],
            FALSE, FALSE) =
        .LEFT_ARG[DBG$L_VALUE_POINTER];
    END;
    RETURN .LEFT_ARG;
    END;

! Check that we have read access to the address
! given in the value descriptor.
IF NOT PROBER (%REF(0), %REF(1), .LEFT_ARG[DBG$L_VALUE_POINTER])
THEN
    SIGNAL (DBG$_NOACCESSR, 1, .LEFT_ARG[DBG$L_VALUE_POINTER]);

! For bitfields, do the bit selection.
IF .LEFT_TYPE EQL ADDR$K_BITFIELD
THEN
    BEGIN
        ! Do additional checking for length <= 32 bits.
        LENGTH = .LEFT_ARG [DBG$W_VALUE_LENGTH];
        IF .LENGTH LSS 0
        OR .LENGTH GTR 32
        THEN
            SIGNAL (DBG$_ILLSIZFLD, 1, .LENGTH);

        ! Also check for read access to top of range.
        ADDRESS = .LEFT_ARG[DBG$L_VALUE_POINTER] +
            (.LEFT_ARG[DBG$L_VALUE_POS] + .LENGTH - 1) / 8;
        IF NOT PROBER (%REF(0), %REF(1), .ADDRESS)
        THEN
            SIGNAL (DBG$_NOACCESSR, 1, .ADDRESS);

        ! Do the bit selection.
        LEFT_ARG [DBG$L_VALUE_POINTER] =
            (IF .LEFT_ARG [DBG$B_VALUE_DTYPE] EQL DSC$K_DTYPE_SV
            OR .LEFT_ARG [DBG$B_VALUE_DTYPE] EQL DSC$K_DTYPE_SVU
            THEN
                (.LEFT_ARG [DBG$L_VALUE_POINTER]) <
                .LEFT_ARG [DBG$L_VALUE_POS],
                .LEFT_ARG [DBG$W_VALUE_LENGTH],
                1)
            ELSE
                (.LEFT_ARG [DBG$L_VALUE_POINTER]) <
                .LEFT_ARG [DBG$L_VALUE_POS],
                .LEFT_ARG [DBG$W_VALUE_LENGTH],

```

```

537 0669 4
538 0670
539 0671
540 0672
541 0673
542 0674
543 0675
544 0676
545 0677
546 0678
547 0679
548 0680
549 0681
550 0682
551 0683
552 0684
553 0685
554 0686
555 0687
556 0688
557 0689
558 0690
559 0691
560 0692
561 0693
562 0694
563 0695
564 0696
565 0697
566 0698
567 0699
568 0700
569 0701
570 0702
571 0703
572 0704
573 0705
574 0706
575 0707
576 0708
577 0709
578 0710
579 0711
580 0712
581 0713
582 0714
583 0715
584 0716
585 0717
586 0718
587 0719
588 0720
589 0721
590 0722
591 0723
592 0724
593 0725

```

```

                                0>);
                                END
ELSE
    ! For other left args, just do the indirection.
    LEFT_ARG [DBG$L_VALUE_POINTER] = ..LEFT_ARG [DBG$L_VALUE_POINTER];
    ! Change the type to unknown and return the argument.
    LEFT_ARG [DBG$V_DHDR_LITERAL] = FALSE;
    LEFT_ARG [DBG$B_DHDR_KIND] = RST$K_DATA;
    LEFT_ARG [DBG$B_DHDR_FCODE] = RST$K_TYPE_DESCR;
    LEFT_ARG [DBG$V_DHDR_OVERRIDE] = TRUE;
    LEFT_ARG [DBG$B_VALUE_CLASS] = 0;
    LEFT_ARG [DBG$B_VALUE_DTYPE] = .DBG$GL_DFLTTP;
    IF .DBG$GL_DFLTTP EQL DSC$K_DTYPE_ZI
    THEN
        LEFT_ARG[DBG$W_VALUE_LENGTH] =
            DBG$INS_DECODE(.LEFT_ARG[DBG$L_VALUE_POINTER],
                FALSE, FALSE) -
            .LEFT_ARG[DBG$L_VALUE_POINTER]
    ELSE
        LEFT_ARG [DBG$W_VALUE_LENGTH] = .DBG$GW_DFLTLENG;
        LEFT_ARG [DBG$L_VALUE_POS] = 0;
        RETURN .LEFT_ARG;
    END;

    ! Do the add operation.
    [TOKEN$K_ADD]:
        PROCESS_ADD_OR_SUBTRACT (+);

    ! Do the subtract operation.
    [TOKEN$K_SUBTRACT]:
        PROCESS_ADD_OR_SUBTRACT (-);

    ! Do the unary plus operation.
    [TOKEN$K_UNARY_PLUS]:
        RETURN .LEFT_ARG;

    ! Do the unary minus operation (i.e., negation).
    [TOKEN$K_UNARY_MINUS]:
        BEGIN
            ! First convert Primary Descriptors to Value Descriptors.
            IF .LEFT_TYPE EQL ADDR$K_PRIMARY
            THEN

```

```

594 0726          DBG$PRIM_TO_VAL (.LEFT_ARG, DBG$K_V_VALUE_DESC, LEFT_ARG);
595 0727
596 0728      ! Negate the address.
597 0729
598 0730      LEFT_ARG [DBG$L_VALUE_POINTER] = - .LEFT_ARG [DBG$L_VALUE_POINTER];
599 0731      LEFT_ARG [DBG$L_VALUE_POS] = - .LEFT_ARG [DBG$L_VALUE_POS];
600 0732
601 0733      ! Normalize the address.
602 0734
603 0735      LEFT_ARG [DBG$L_VALUE_POINTER] =
604 0736          .LEFT_ARG [DBG$L_VALUE_POINTER] +
605 0737          .LEFT_ARG [DBG$L_VALUE_POS] / 8;
606 0738      LEFT_ARG [DBG$L_VALUE_POS] =
607 0739          .LEFT_ARG [DBG$L_VALUE_POS] MOD 8;
608 0740
609 0741      ! Handle result type of instruction.
610 0742
611 0743      IF .LEFT_TYPE EQL ADDR$K_INST
612 0744      THEN
613 0745          LEFT_ARG[DBG$W_VALUE_LENGTH] =
614 0746              DBG$INS_DECODE(.LEFT_ARG[DBG$L_VALUE_POINTER],
615 0747                  FALSE, FALSE) =
616 0748                  .LEFT_ARG[DBG$L_VALUE_POINTER];
617 0749
618 0750      ! Return the result.
619 0751      RETURN .LEFT_ARG;
620 0752      END;
621 0753
622 0754      ! Do the multiply operation.
623 0755      [TOKEN$K MULTIPLY]:
624 0756          PROCESS_MULTIPLY_OR_DIVIDE (*);
625 0757
626 0758      ! Do the divide operation.
627 0759      [TOKEN$K DIVIDE]:
628 0760          PROCESS_MULTIPLY_OR_DIVIDE (/);
629 0761
630 0762      ! Do the bit-selection operation, i.e. X<pos,size,ext>.
631 0763      [TOKEN$K BITSELECT]:
632 0764          BEGIN
633 0765              LOCAL
634 0766                  ADDR_INCREMENT,      ! Increment to be added to byte address
635 0767                  BIT_OFFSET,          ! New bit offset from byte address
636 0768                  DTYPE,                ! New dtype
637 0769                  POS;
638 0770
639 0771      ! Convert Primaries to Value Descriptors
640 0772
641 0773      IF .LEFT_ARG [DBG$B_DHDR_TYPE] EQL DBG$K_PRIMARY_DESC
642 0774      THEN
643 0775          DBG$PRIM_TO_VAL (.LEFT_ARG, DBG$K_V_VALUE_DESC, LEFT_ARG);
644 0776
645 0777
646 0778
647 0779
648 0780
649 0781
650 0782

```

```

651 0783
652 0784
653 0785
654 0786
655 0787
656 0788
657 0789
658 0790
659 0791
660 0792
661 0793
662 0794
663 0795
664 0796
665 0797
666 0798
667 0799
668 0800
669 0801
670 0802
671 0803
672 0804
673 0805
674 0806
675 0807
676 0808
677 0809
678 0810
679 0811
680 0812
681 0813
682 0814
683 0815
684 0816
685 0817
686 0818
687 0819
688 0820
689 0821
690 0822
691 0823
692 0824
693 0825
694 0826
695 0827
696 0828
697 0829
698 0830
699 0831
700 0832
701 0833
702 0834
703 0835
704 0836
705 0837
706 0838
707 0839

```

```

!++
! Handle value descriptors.
!
! Clear the literal flag - the result is of type bitfield.
LEFT_ARG [DBG$V_DHDR_LITERAL] = FALSE;
! Add the bit offsets.
IF .LEFT_ARG[DBG$B_VALUE_CLASS] EQL DSC$K_CLASS_UBS
THEN
  POS = .LEFT_ARG [DBG$L_VALUE_POS]
ELSE
  POS = 0;
BIT_OFFSET = .POS + .OPERATOR [TOKEN$W_BIT_OFFSET];
! Compute the new byte address.
LEFT_ARG [DBG$L_VALUE_POINTER] = .LEFT_ARG [DBG$L_VALUE_POINTER] +
  .BIT_OFFSET / 8;
! Compute the bit offset. From it and the sign extension bit,
! determine the new class and dtype.
BIT_OFFSET = .BIT_OFFSET MOD 8;
IF .BIT_OFFSET EQ 0
THEN
  BEGIN
    IF .OPERATOR [TOKEN$V_SGNEXT]
    THEN
      DTYPE = DSC$K_DTYPE_SV
    ELSE
      DTYPE = DSC$K_DTYPE_V;
    LEFT_ARG [DBG$B_VALUE_CLASS] = DSC$K_CLASS_S;
  END
ELSE
  BEGIN
    IF .OPERATOR [TOKEN$V_SGNEXT]
    THEN
      DTYPE = DSC$K_DTYPE_SVU
    ELSE
      DTYPE = DSC$K_DTYPE_VU;
    LEFT_ARG [DBG$B_VALUE_CLASS] = DSC$K_CLASS_UBS;
  END;
! Fill in the new dtype, bit offset, and length.
LEFT_ARG [DBG$B_VALUE_DTYPE] = .DTYPE;
LEFT_ARG [DBG$W_VALUE_LENGTH] = .OPERATOR [TOKEN$W_BIT_LENGTH];
LEFT_ARG [DBG$L_VALUE_POS] = .BIT_OFFSET;
RETURN .LEFT_ARG;
END;
! Any other code constitutes an error and is signalled as such.
! (The operator is not allowed in Address Expressions.)

```

```

: 708      0840      2
: 709      0841      2
: 710      0842      2
: 711      0843      2
: 712      0844      2
: 713      0845      2
: 714      0846      2
: 715      0847      2
: 716      0848      2
: 717      0849      2
: 718      0850      2
: 719      0851      2
: 720      0852      1

```

```

: [INRANGE, OUTRANGE]:
: BEGIN
: SIGNAL(DBG$ _INVOPADDR, 1, OPERATOR[TOKEN$B_OPLEN]);
: ! We never get here but throw in a return to keep the BLISS
: ! compiler happy.
: RETURN 0;
: END;
TES;
END;

```

```

.TITLE  DBGADDEXP
.IDENT  \V04-000\

.EXTRN  DBG$GL_DFLTTP,  DBG$GW_DFLTLENG
.EXTRN  DBG$REG_VALUES,  DBG$BUILD_PRIMARY_SUBNODE
.EXTRN  DBG$CONV_TEXT_VALUE
.EXTRN  DBG$INS_DECODE,  DBG$MAKE_VAL_DESC
.EXTRN  DBG$MAKE_VMS_DESC
.EXTRN  DBG$MAKE_SKELETON_DESC
.EXTRN  DBG$NCPY_DESC,  DBG$PRIM_TO_VAL
.EXTRN  DBG$STA_SYMNAME
.EXTRN  DBG$STA_SYM_IS_LITERAL
.EXTRN  DBG$STA_TYPEFCODE
.EXTRN  DBG$STA_TYP_TYPEDPTR

.PSECT  DBG$CODE, NOWRT, SHR, PIC, 0

.ENTRY  DBG$EVAL_ADDR_OPERATOR, Save R2,R3,R4,R5,- : 0213
        R6,R7,R8,R9
        MOVAB  DBG$PRIM_TO_VAL, R9
        MOVAB  DBG$GW_DFLTLENG, R8
        MOVAB  DBG$GL_DFLTTP, R7
        MOVAB  LIB$SIGNAL, R6
        MOVAB  DBG$INS_DECODE, R5
        SUBL2  #20, SP
        MOVL  OPERATOR, R4 : 0533
        MOVZWL 2(R4), OPCODE
        CLRL  R1 : 0534
        CML  OPCODE, #6
        BNEQ  1$
        INCL  R1
        CLRL  R0 : 0535
        CML  OPCODE, #7
        BNEQ  2$
        INCL  R0
        BISL2  R1, R0 : 0536
        CLRL  R1
        CML  OPCODE, #8
        BNEQ  3$
        INCL  R1
        BISL2  R0, R1 : 0537
        CLRL  R0

```

```

03FC 00000
59 00000000G 00 9E 00002
58 00000000G 00 9E 00009
57 00000000G 00 9E 00010
56 00000000G 00 9E 00017
55 00000000G 00 9E 0001E
5E          14 C2 00025
54          04 AC D0 00028
52          02 A4 3C 0002C
          51 D4 00030
06          52 D1 00032
          02 12 00035
          51 D6 00037
          50 D4 00039 1$:
07          52 D1 0003B
          02 12 0003E
          50 D6 00040
50          51 C8 00042 2$:
          51 D4 00045
08          52 D1 00047
          02 12 0004A
          51 D6 0004C
51          50 C8 0004E 3$:
          50 D4 00051

```







			52	08	AC	DO	00188	12\$:	MOVL	LEFT_ARG, R2	0629
			54	18	A2	DO	0018C		MOVL	24(R2), R4	
64			01		00	OC	00190		PROBER	#0, #1, (R4)	
					0D	12	00194		BNEQ	13\$	
					54	DD	00196		PUSHL	R4	0631
					01	DD	00198		PUSHL	#1	
					8F	DD	0019A		PUSHL	#164392	
66		00028228	03		FB	001A0			CALLS	#3, LIB\$SIGNAL	
05			6E		D1	001A3	13\$:		CMPL	LEFT_TYPE, #5	0635
			5A		12	001A6			BNEQ	20\$	
53			A2	14	3C	001A8			MOVZWL	20(R2), LENGTH	0641
			05		19	001AC			BLSS	14\$	0642
			20		53	D1	001AE		CMPL	LENGTH, #32	0643
					0D	15	001B1		BLEQ	15\$	
					53	DD	001B3	14\$:	PUSHL	LENGTH	0645
					01	DD	001B5		PUSHL	#1	
		00028ED0	8F		DD	001B7			PUSHL	#167632	
66			03		FB	001BD			CALLS	#3, LIB\$SIGNAL	
50			53	1C	A2	C1	001C0	15\$:	ADDL3	28(R2), LENGTH, R0	0650
			50		D7	001C5			DECL	R0	
			50		08	C6	001C7		DIVL2	#8, R0	
			50		54	C0	001CA		ADDL2	R4, ADDRESS	
60			01		00	OC	001CD		PROBER	#0, #1, (ADDRESS)	0651
					0D	12	001D1		BNEQ	16\$	
					50	DD	001D3		PUSHL	ADDRESS	0653
					01	DD	001D5		PUSHL	#1	
		00028228	8F		DD	001D7			PUSHL	#164392	
66			03		FB	001DD			CALLS	#3, LIB\$SIGNAL	
29			A2	16	91	001E0	16\$:		CMPB	22(R2), #41	0658
			06		13	001E4			BEQL	17\$	
			2A	16	A2	91	001E6		CMPB	22(R2), #42	0659
			09		12	001EA			BNEQ	18\$	
50	64	14	A2	1C	A2	EE	001EC	17\$:	EXTV	28(R2), 20(R2), (R4), R0	0663
			07		11	001F3			BRB	19\$	0661
50	64	14	A2	1C	A2	EF	001F5	18\$:	EXTZV	28(R2), 20(R2), (R4), R0	0668
		18	A2		50	DO	001FC	19\$:	MOVL	R0, 24(R2)	0658
					04	11	00200		BRB	21\$	0635
		18	A2		64	DO	00202	20\$:	MOVL	(R4), 24(R2)	0676
		04	A2	40	8F	8A	00206	21\$:	BICB2	#64, 4(R2)	0680
		06	A2	0603	8F	80	0020B		MOVW	#1539, 6(R2)	0682
		04	A2	80	8F	88	00211		BISB2	#128, 4(R2)	0683
		50			67	DO	00216		MOVL	DBG\$GL_DFLTYP, R0	0685
		16	A2		50	9B	00219		MOVZBW	R0, 22(R2)	
			16		50	D1	0021D		CMPL	R0, #22	0686
					10	12	00220		BNEQ	22\$	
					7E	7C	00222		CLRQ	-(SP)	0689
				18	A2	DD	00224		PUSHL	24(R2)	
			65		03	FB	00227		CALLS	#3, DBG\$INS_DECODE	
14	A2		50	18	A2	A3	0022A		SUBW3	24(R2), R0, -20(R2)	0691
					04	11	00230		BRB	23\$	0688
		14	A2		68	B0	00232	22\$:	MOVW	DBG\$GW_DFLTLENG, 20(R2)	0693
				1C	A2	D4	00236	23\$:	CLRL	28(R2)	0694
					02AB	31	00239	24\$:	BRW	69\$	0770
			53	08	AC	DO	0023C	25\$:	MOVL	LEFT_ARG, R3	0702
	04		01		6E	CF	00240		CASEL	LEFT_TYPE, #1, #4	
0047	0047		000A		0016		00244	26\$:	.WORD	28\$-26\$,-	
					00BF		0024C			27\$-26\$,-	



		18	A3	18	A0	C0	0030D		ADDL2	24(R0), 24(R3)
		1C	A3	1C	A0	C0	00312		ADDL2	28(R0), 28(R3)
	50	1C	A3		08	C7	00317		DIVL3	#8, 28(R3), R0
			A3		50	C0	0031C		ADDL2	R0, 24(R3)
7E	00	1C	A3		01	7A	00320		EMUL	#1, 28(R3), #0, -(SP)
50	50		8E		08	7B	00326		EDIV	#8, (SP)+, R0, R0
		1C	A3		50	D0	0032B		MOVL	R0, 28(R3)
					67	11	0032F	38\$:	BRB	45\$
	04		52	08	AC	D0	00331	39\$:	MOVL	LEFT_ARG, R2
0063	0063		01		6E	CF	0C335		CASEL	LEFT_TYPE, #1, #4
			000A		0016		00339	40\$:	.WORD	42\$-40\$,-
					00B7		00341			41\$-40\$,-
										46\$-40\$,-
										46\$-40\$,-
										50\$-40\$
				08	AC	9F	00343	41\$:	PUSHAB	LEFT_ARG
			7E	83	8F	9A	00346		MOVZBL	#131, -(SP)
					52	DD	0034A		PUSHL	R2
					0149	31	0034C		BRW	63\$
					02	AE	D1 0034F	42\$:	CMPL	RIGHT_TYPE, #2
						4B	13 00353	43\$:	BEQL	47\$
									MOVL	RIGHT_ARG, R3
	18	A3	18	53	OC	AC	D0 00355		SUBL3	24(R3), 24(R2), 24(R3)
				03	18	A3	C3 00359		CMPL	RIGHT_TYPE, #3
					04	AE	D1 00360		BNEQ	44\$
						0E	12 00364		CLRQ	-(SP)
						7E	7C 00366		PUSHL	24(R3)
					18	A3	DD 00368		CALLS	#3, DBG\$INS_DECODE
						03	FB 0036B		SUBW3	24(R3), R0, 20(R3)
	14	A3		65	18	A3	A3 0036E		CMPL	RIGHT_TYPE, #5
				50	04	AE	D1 00374	44\$:	BNEQ	45\$
				05		1E	12 00378		MOVAB	28(R3), R0
					1C	A3	9E 0037A		SUBL3	(R0), 28(R2), (R0)
	60			50		60	C3 0037E		DIVL3	#8, (R0), R1
	51			60		08	C7 00383		ADDL2	R1, 24(R3)
				18		51	C0 00387		EMUL	#1, (R0), #0, -(SP)
7E	00			60		01	7A 0038B		EDIV	#8, (SP)+, R1, R1
51	51			8E		08	7B 00390		MOVL	R1, (R0)
				60		51	D0 00395		MOVL	R3, R0
				50		53	D0 00398	45\$:	RET	
						04	0039B		CMPL	RIGHT_TYPE, #2
				02	04	AE	D1 0039C	46\$:	BEQL	51\$
						52	13 003A0	47\$:	CMPL	RIGHT_TYPE, #5
				05	04	AE	D1 003A2		BNEQ	48\$
						2E	12 003A6		MOVL	RIGHT_ARG, R0
				50	OC	AC	D0 003A8		MOVL	LEFT_ARG, R1
				51	08	AC	D0 003AC		SUBL3	24(R0), 24(R1), 24(R0)
	18	A0	18	A1	18	A0	C3 003B0		MOVAB	28(R0), R3
				53	1C	A0	9E 003B7		SUBL3	(R3), 28(R1), (R3)
	63			1C		63	C3 003BB		DIVL3	#8, (R3), R1
	51					08	C7 003C0		ADDL2	R1, 24(R0)
				18		51	C0 003C4		EMUL	#1, (R3), #0, -(SP)
						01	7A 003C8		EDIV	#8, (SP)+, R1, R1
7E	00			63		08	7B 003CD		MOVL	R1, (R3)
51	51			8E		51	D0 003D2		RET	
				63		04	003D5		MOVL	RIGHT_ARG, R0
						OC	AC D0 003D6	48\$:	SUBL2	24(R0), 24(R2)
				50	18	A0	C2 003DA			
				18		A2				

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.....

		01	04	AE	D1	003DF		CMPL	RIGHT_TYPE, #1	
				03	13	003E3		BEQL	49\$	
			00D4	31	003E5		BRW	66\$		
		03		6E	D1	003E8	49\$:	CMPL	LEFT_TYPE, #3	
				2F	12	003EB		BNEQ	53\$	
			00E3	31	003ED		BRW	67\$		
		02	04	AE	D1	003F0	50\$:	CMPL	RIGHT_TYPE, #2	
				7C	13	003F4	51\$:	BEQL	60\$	
		50	0C	AC	D0	003F6		MOVL	RIGHT_ARG, R0	
18		A2	18	A0	C2	003FA		SUBL2	24(R0), 24(R2)	
1C		A2	1C	A0	C2	003FF		SUBL2	28(R0), 28(R2)	
50		A2		08	C7	00404	52\$:	DIVL3	#8, 28(R2), R0	
		18		50	C0	00409		ADDL2	R0, 24(R2)	
7E		A2	1C	01	7A	0040D		EMUL	#1, 28(R2), #0, -(SP)	
50		8E		08	7B	00413		EDIV	#8, (SP)+, R0, R0	
		1C	A2	50	D0	00418		MOVL	R0, 28(R2)	
				00C8	31	0041C	53\$:	BRW	69\$	
		50	08	AC	D0	0041F	54\$:	MOVL	LEFT_ARG, R0	0770
					04	00423		RET		
		02		6E	D1	00424	55\$:	CMPL	LEFT_TYPE, #2	0724
				0D	12	00427		BNEQ	56\$	
			08	AC	9F	00429		PUSHAB	LEFT_ARG	0726
		7E	83	8F	9A	0042C		MOVZBL	#131, -(SP)	
			08	AC	DD	00430		PUSHL	LEFT_ARG	
		69		03	FB	00433		CALLS	#3, DBG\$PRIM_TO_VAL	
		52	08	AC	D0	00436	56\$:	MOVL	LEFT_ARG, R2	0730
		18	A2	CE	0043A		MNEGL	24(R2), 24(R2)		
		50	1C	A2	9E	0043F		MOVAB	28(R2), R0	0731
		60		60	CE	00443		MNEGL	(R0), (R0)	
		51		08	C7	00446		DIVL3	#8, (R0), R1	0737
		18	A2	51	C0	0044A		ADDL2	R1, 24(R2)	
7E		60		01	7A	0044E		EMUL	#1, (R0), #0, -(SP)	0739
51		8E		08	7B	00453		EDIV	#8, (SP)+, R1, R1	
		60		51	D0	00458		MOVL	R1, (R0)	
				8B	11	0045B		BRB	49\$	0743
		02		6E	D1	0045D	57\$:	CMPL	LEFT_TYPE, #2	0758
				0C	12	00460		BNEQ	59\$	
			08	AC	9F	00462	58\$:	PUSHAB	LEFT_ARG	
		7E	83	8F	9A	00465		MOVZBL	#131, -(SP)	
			08	AC	DD	00469		PUSHL	LEFT_ARG	
				2A	11	0046C		BRB	63\$	
		02	04	AE	D1	0046E	59\$:	CMPL	RIGHT_TYPE, #2	
				1A	13	00472	60\$:	BEQL	62\$	
		52	08	AC	D0	00474		MOVL	LEFT_ARG, R2	
		50	0C	AC	D0	00478		MOVL	RIGHT_ARG, R0	
		18	A2	A0	C4	0047C		MULL2	24(R0), 24(R2)	
				31	11	00481		BRB	65\$	
		02		6E	D1	00483	61\$:	CMPL	LEFT_TYPE, #2	
				DA	13	00486		BEQL	58\$	
		02	04	AE	D1	00488		CMPL	RIGHT_TYPE, #2	
				19	12	0048C		BNEQ	64\$	
			0C	AC	9F	0048E	62\$:	PUSHAB	RIGHT_ARG	
		7E	83	8F	9A	00491		MOVZBL	#131, -(SP)	
			0C	AC	DD	00495		PUSHL	RIGHT_ARG	
		69		03	FB	00498	63\$:	CALLS	#3, DBG\$PRIM_TO_VAL	
		7E	08	AC	7D	0049B		MOVQ	LEFT_ARG, -(SP)	
				54	DD	0049F		PUSHL	R4	

FB5A	CF											
			03	FB	004A1	CALLS	#3, DBG\$EVAL_ADDR_OPERATOR					
				04	004A6	RET						
	52	08	AC	DO	004A7	64\$:	MOVL	LEFT_ARG, R2				
	50	0C	AC	DO	004AB		MOVL	RIGHT_ARG, R0				
18	A2	18	A0	C6	004AF		DIVL2	24(R0), 24(R2)				
04	A2	40	8F	8A	004B4	65\$:	BICB2	#64, 4(R2)				
		1C	A2	D4	004B9		CLRL	28(R2)				
06	A2	0603	8F	80	004BC	66\$:	MOVW	#1539, 6(R2)				
04	A2	80	8F	88	004C2		BISB2	#128, 4(R2)				
	50		67	DO	004C7		MOVL	DBG\$GL_DFLTTP, R0				
16	A2		50	9B	004CA		MOVZBW	R0, 22(R2)				
	16		50	D1	004CE		CMPL	R0, #22				
			10	12	004D1		BNEQ	68\$				
			7E	7C	004D3	67\$:	CLRQ	-(SP)				
		18	A2	DD	004D5		PUSHL	24(R2)				
	65		03	FB	004D8		CALLS	#3, DBG\$INS_DECODE				
	50	14	A2	A3	004DB		SUBW3	24(R2), R0, -20(R2)				
			04	11	004E1		BRB	69\$				
	14		68	80	004E3	68\$:	MOVW	DBG\$GW_DFLTLENG, 20(R2)				
	50		52	DO	004E7	69\$:	MOVL	R2, R0				0770
				04	004EA		RET					
00000079	8F	08	BC	08								0779
				10	ED	004EB	70\$:	CMPZV	#16, #8, @LEFT_ARG, #121			
				0D	12	004F5		BNEQ	71\$			
		08	AC	9F	004F7		PUSHAB	LEFT_ARG				0781
	7E	83	8F	9A	004FA		MOVZBL	#131, -(SP)				
		08	AC	DD	004FE		PUSHL	LEFT_ARG				
	69		03	FB	00501		CALLS	#3, DBG\$PRIM_TO_VAL				
	50	08	AC	DO	00504	71\$:	MOVL	LEFT_ARG, R0				0789
	04	40	8F	8A	00508		BICB2	#64, -4(R0)				
	53	14	A0	9E	0050D		MOVAB	20(R0), R3				0793
	0D	03	A3	91	00511		CMPB	3(R3), #13				
			06	12	00515		BNEQ	72\$				
	51	1C	A0	DO	00517		MOVL	28(R0), POS				0795
			02	11	0051B		BRB	73\$				
			51	D4	0051D	72\$:	CLRL	POS				0797
	52	08	A4	3C	0051F	73\$:	MOVZWL	8(R4), R2				0798
	51		52	C0	00523		ADDL2	R2, BIT_OFFSET				
		52	08	C7	00526		DIVL3	#8, BIT_OFFSET, R2				0803
	18		52	C0	0052A		ADDL2	R2, 24(R0)				
	7E	00	01	7A	0052E		EMUL	#1, BIT_OFFSET, #0, -(SP)				0808
	51	51	08	7B	00533		EDIV	#8, (SPT+, BIT_OFFSET, BIT_OFFSET)				0809
			51	D5	00538		TSTL	BIT_OFFSET				
			12	12	0053A		BNEQ	76\$				
	05		0A	E1	0053C		BBC	#10, (R4), 74\$				0812
			29	DO	00540		MOVL	#41, DTYPE				0814
			03	11	00543		BRB	75\$				
			01	DO	00545	74\$:	MOVL	#1, DTYPE				0816
	03		01	90	00548	75\$:	MOVW	#1, 3(R3)				0817
			10	11	0054C		BRB	79\$				0809
	05		0A	E1	0054E	76\$:	BBC	#10, (R4), 77\$				0821
			2A	DO	00552		MOVL	#42, DTYPE				0823
			03	11	00555		BRB	78\$				
			22	DO	00557	77\$:	MOVL	#34, DTYPE				0825
	03		0D	90	0055A	78\$:	MOVW	#13, 3(R3)				0826
	02		52	90	0055E	79\$:	MOVW	DTYPE, 2(R3)				0831
	63	0A	A4	80	00562		MOVW	10(R4), (R3)				0832
	1C		51	DO	00566	80\$:	MOVL	BIT_OFFSET, 28(R0)				0833

DBGADDEXP  
V04-000

F 10  
15-Sep-1984 23:49:16  
14-Sep-1984 12:16:31

VAX-11 Bliss-32 V4.0-742  
[DEBUG.SRC]DBGADDEXP.B32;1

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(2)

04 0056A      RET

; 0852

; Routine Size: 1387 bytes,      Routine Base: DBG\$CODE + 0000



```

: 722 0853 1 GLOBAL ROUTINE DBG$PRIM_TO_ADDR (DESC1, DTYPE, DESC2) =
: 723 0854 1
: 724 0855 1 FUNCTION
: 725 0856 1     Converts a Primary Descriptor into a value descriptor containing
: 726 0857 1     the address of the primary.
: 727 0858 1
: 728 0859 1     For Primary Descriptors representing data or code, the address is stored
: 729 0860 1     as a (byte address, bit offset) pair in the value field of the
: 730 0861 1     descriptor. If the given dtype is zero, the dtype is left alone. If the
: 731 0862 1     user supplied a dtype that he wants the result to be, this
: 732 0863 1     new dtype is stuffed in and the class and length fields are fixed
: 733 0864 1     up accordingly.
: 734 0865 1
: 735 0866 1     For Primary Descriptors representing literals, the literal value
: 736 0867 1     is stored in the value field of the resulting descriptor. The
: 737 0868 1     dtype information is left alone in this case - it presumably
: 738 0869 1     describes the type of the literal constant.
: 739 0870 1
: 740 0871 1     Note that BLISS fields are a special kind of literal. The four
: 741 0872 1     field values are stored in four longwords in the value field
: 742 0873 1     of the resulting value descriptor.
: 743 0874 1
: 744 0875 1 INPUTS
: 745 0876 1     DESC1   - The address of the Primary Descriptor to be converted.
: 746 0877 1     DTYPE   - The desired Dtype code of the resulting value descriptor
: 747 0878 1     DESC2   - An address in which to leave a pointer to the new value
: 748 0879 1             descriptor which is constructed.
: 749 0880 1
: 750 0881 1 OUTPUTS
: 751 0882 1     A Value Descriptor is allocated and filled in, and a pointer
: 752 0883 1     to it is left in DESC2. A status code is returned, which is
: 753 0884 1     one of:
: 754 0885 1     ST$K_SUCCESS - Success.
: 755 0886 1     ST$K_SEVERE  - Failure.
: 756 0887 1
: 757 0888 1
: 758 0889 2 BEGIN
: 759 0890 2
: 760 0891 2 MAP
: 761 0892 2     DESC1: REF DBG$PRIMARY;           ! Pointer to input primary descriptor
: 762 0893 2
: 763 0894 2 LOCAL
: 764 0895 2     COUNT,                          ! For BLISS fields, count of fields
: 765 0896 2     DSTPTR: REF DST$RECORD,          ! Pointer to DST record for the primary
: 766 0897 2     PTR,                             ! Scratch pointer
: 767 0898 2     RSTPTR: REF RST$ENTRY,          ! Pointer to RST entry for the primary
: 768 0899 2     SUBVECTOR: REF VECTOR[],        ! Pointer to vector of field values
: 769 0900 2     TEMP_DESC: REF DBG$VALDESC;    ! Pointer to a value descriptor
: 770 0901 2
: 771 0902 2
: 772 0903 2     ! Check for a volatile value descriptor coming in. In this case,
: 773 0904 2     ! turn it into an ordinary value descriptor with the "address" in
: 774 0905 2     ! the V. Value desc becoming the "value" in the ordinary value desc.
: 775 0906 2
: 776 0907 2 IF .DESC1[DBG$B_DHDR_TYPE] EQL DBG$K_V_VALUE_DESC
: 777 0908 2 THEN
: 778 0909 3     BEGIN

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MAP
  DESC2: REF DBG$VALDESC;
LOCAL
  DUMMY;

! Slightly kludgy solution to an obscure problem:
! If you examine a string of length > 256 and language is
! set to BLISS or MACRO then you get into this routine
! with a volatile value descriptor representing the string.
! In this case we just want to return the descriptor unchanged:
! do not add the level of indirection.
IF .DESC1[DBG$W_VALUE_LENGTH] GTR 256
THEN
  BEGIN
    .DESC2 = .DESC1;
    RETURN ST$K_SUCCESS;
  END;

! Otherwise, copy the descriptor and make it into an ordinary
! value descriptor.
DBG$NCOPY DESC(.DESC1, TEMP_DESC, DUMMY, FALSE);
TEMP_DESC[DBG$B_DHDR_TYPE] = DBG$K_VALUE_DESC;
TEMP_DESC[DBG$L_VALUE_VALUE0] = .TEMP_DESC[DBG$L_VALUE_POINTER];
IF .TEMP_DESC[DBG$B_VALUE_CLASS] EQL DSC$K_CLASS_UBS
THEN
  TEMP_DESC[DBG$L_VALUE_VALUE1] = .TEMP_DESC[DBG$L_VALUE_POS];
  TEMP_DESC[DBG$L_VALUE_POINTER] = TEMP_DESC[DBG$L_VALUE_VALUE0];
  .DESC2 = .TEMP_DESC;
  RETURN ST$K_SUCCESS;
END;

! Check for BLISS field. This is a hack needed to support BLISS structure
! references X[fieldname]. Basically, when we parse the "fieldname"
! primary, we are expecting either an offset as in X[0,0,32,0] or a
! fieldname as in X[fieldname].
! If we get a fieldname, we indicate it by building a special
! kind of value descriptor whose fcode is
! RST$K_TYPE_BLIFLD, and in the value field we have the four integers
! that the fieldname translates into.
IF .DESC1 [DBG$B_DHDR_FCODE] EQL RST$K_TYPE_BLIFLD
THEN
  BEGIN
    RSTPTR = .DESC1 [DBG$L_DHDR_SYMID0];
    DSTPTR = .RSTPTR [RST$C_DSTPTR];

    ! Set up a pointer to the BLISS field components,
    ! and copy those four components into the value descriptor.
    COUNT = .DSTPTR[DST$L_BLIFLD_COMPS];
    PTR = 1 + DSTPTR[DST$B_NAME] + .DSTPTR[DST$B_NAME];

    ! Allocate the result descriptor now that we know how big
    ! it needs to be.

```

```

836 0967      !
837 0968      ! TEMP_DESC = DBG$MAKE SKELETON_DESC (DBG$K_VALUE_DESC, 4*(.COUNT+1));
838 0969      ! TEMP_DESC [DBG$B_DHDR_LANG] = .DESC1 [DBG$B_DHDR_LANG];
839 0970      ! TEMP_DESC [DBG$B_DHDR_KIND] = .DESC1 [DBG$B_DHDR_KIND];
840 0971      ! TEMP_DESC [DBG$B_DHDR_FCODE] = .DESC1 [DBG$B_DHDR_FCODE];
841 0972      ! TEMP_DESC [DBG$L_DHDR_TYPEID] = .DESC1 [DBG$L_DHDR_TYPEID];
842 0973
843 0974      SUBVECTOR = TEMP_DESC [DBG$A_VALUE_ADDRESS];
844 0975      SUBVECTOR[0] = .COUNT;
845 0976      INCR I FROM 1 TO .COUNT DO
846 0977          BEGIN
847 0978              SUBVECTOR [.I] = ..PTR;
848 0979              PTR = .PTR + 4;
849 0980          END;
850 0981
851 0982      ! Fix up the pointer field, fill in the output parameter, and
852 0983      ! return success.
853 0984
854 0985      TEMP_DESC [DBG$L_VALUE_POINTER] = TEMP_DESC [DBG$A_VALUE_ADDRESS];
855 0986      .DESC2 = .TEMP_DESC;
856 0987      RETURN ST$K_SUCCESS;
857 0988      END;
858 0989
859 0990      ! Check for literal constants.
860 0991
861 0992      IF DBG$STA_SYM_IS_LITERAL (.DESC1 [DBG$L_DHDR_SYMID0])
862 0993      THEN
863 0994          BEGIN
864 0995
865 0996              ! PRIM_TO_VAL does what we want with literals, so we just
866 0997              ! call that routine.
867 0998
868 0999              DBG$PRIM TO VAL (.DESC1, DBG$K_VALUE_DESC, .DESC2);
869 1000              RETURN ST$K_SUCCESS;
870 1001              END;
871 1002
872 1003      ! Build a value descriptor of the desired type.
873 1004      ! Fill in the header fields of this
874 1005      ! value descriptor.
875 1006
876 1007      TEMP_DESC = DBG$MAKE SKELETON_DESC (DBG$K_VALUE_DESC, 4);
877 1008      TEMP_DESC [DBG$B_DHDR_LANG] = .DESC1 [DBG$B_DHDR_LANG];
878 1009      TEMP_DESC [DBG$B_DHDR_KIND] = .DESC1 [DBG$B_DHDR_KIND];
879 1010      TEMP_DESC [DBG$B_DHDR_FCODE] = .DESC1 [DBG$B_DHDR_FCODE];
880 1011      TEMP_DESC [DBG$L_DHDR_TYPEID] = .DESC1 [DBG$L_DHDR_TYPEID];
881 1012
882 1013      ! Set up for a call to DBG$MAKE VMS DESC. This routine (in DBGVALUES)
883 1014      ! converts Primary Descriptors to VMS standard descriptors. Since the
884 1015      ! value descriptor has a VMS descriptor inside it, we pass this
885 1016      ! to DBG$MAKE_VMS_DESC.
886 1017
887 1018      IF NOT DBG$MAKE_VMS_DESC (.DESC1, TEMP_DESC[DBG$A_VALUE_VMSDESC])
888 1019      THEN
889 1020          RETURN ST$K_SEVERE;
890 1021
891 1022      ! Normalize the pointer to include the bit offset.
892 1023

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: 893 1024 2
: 894 1025 2
: 895 1026 2
: 896 1027 2
: 897 1028 2
: 898 1029 2
: 899 1030 2
: 900 1031 2
: 901 1032 2
: 902 1033 2
: 903 1034 2
: 904 1035 2
: 905 1036 2
: 906 1037 2
: 907 1038 2
: 908 1039 2
: 909 1040 2
: 910 1041 2
: 911 1042 2
: 912 1043 2
: 913 1044 2
: 914 1045 2
: 915 1046 2
: 916 1047 2
: 917 1048 2
: 918 1049 2
: 919 1050 2
: 920 1051 2
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: 926 1057 2
: 927 1058 2
: 928 1059 2
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: 935 1066 2
: 936 1067 2
: 937 1068 2
: 938 1069 2
: 939 1070 2
: 940 1071 1

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```

IF .TEMP_DESC [DBG$B_VALUE_CLASS] EQL DSC$K_CLASS_UBS
THEN
  BEGIN
    TEMP_DESC [DBG$L_VALUE_POINTER] = .TEMP_DESC [DBG$L_VALUE_POINTER] +
      .TEMP_DESC [DBG$L_VALUE_POS] / %BPUNIT;
    TEMP_DESC [DBG$L_VALUE_POS] = .TEMP_DESC [DBG$L_VALUE_POS] MOD %BPUNIT;
  END;

  ! If this routine was passed a nonzero DTYPE, fill in the desired DTYPE.
  ! Also fill in all the related fields.
  ! Otherwise, leave it the way we got it from MAKE_VMS_DESC.
  IF .DTYPE NEQ 0
  THEN
    ! The only case where we dummy in a different dtype is for address
    ! arithmetic in BLISS. E.g., EVAL F+F, without dots, we want to
    ! treat the address of F as a longword integer.
    ! In this case, the dtype we pass in is L.
    IF .DTYPE EQL DSC$K_DTYPE_L
    THEN
      BEGIN
        TEMP_DESC [DBG$B_DHDR_KIND] = RST$K_DATA;
        TEMP_DESC [DBG$B_DHDR_FCODE] = RST$K_TYPE_DESCR;
        TEMP_DESC [DBG$B_VALUE_CLASS] = DSC$K_CLASS_S;
        TEMP_DESC [DBG$B_VALUE_DTYPE] = DSC$K_DTYPE_L;
        TEMP_DESC [DBG$W_VALUE_LENGTH] = 4;
      END
      ! We do not currently handle other override dtypes here.
    ELSE
      $DBG_ERROR ('DBGADDEXP\DBG$PRIM_TO_ADDR unknown dtype');

    ! Extract the desired information from the value descriptor.
    TEMP_DESC[DBG$L_VALUE_VALUE0] = .TEMP_DESC[DBG$L_VALUE_POINTER];
    TEMP_DESC[DBG$L_VALUE_VALUE1] = .TEMP_DESC[DBG$L_VALUE_POS];

    ! Fix up the pointer field, fill in the output parameter, and
    ! return success.
    TEMP_DESC [DBG$L_VALUE_POINTER] = TEMP_DESC [DBG$A_VALUE_ADDRESS];
    .DESC2 = .TEMP_DESC;
    RETURN ST$K_SUCCESS
  END;

```

```

: 24 47 42 44 5C 50 58 45 44 44 41 47 42 44 28 0000 P.AAA: .ASCII \ (DBGADDEXP\<92>\DBG$PRIM_TO_ADDR unknow\ :
6E 75 20 52 44 44 41 5F 4F 54 5F 4D 49 52 50 0000F
:
: 65 70 79 74 64 20 6E 6B 0001E
: .ASCII \n dtype\
:

```

				.PSECT	DBG\$CODE,NOWRT,	SHR,	PIC,0			
				00FC	00000			.ENTRY	DBG\$PRIM_TO_ADDR, Save R2,R3,R4,R5,R6,R7	0853
	57	00000000G	00	9E	00002			MOVAB	DBG\$MAKE_SKELETON_DESC, R7	
	5E		08	C2	00009			SUBL2	#8, SP	
	53		04	AC	0000C			MOVL	DESC1, R3	0907
	83		02	A3	00010			CMPB	2(R3), #131	
				40	00015			BNEQ	3\$	
	0100		14	A3	00017			CMPW	20(R3), #256	0922
				07	0001D			BLEQU	1\$	
	0C	BC		53	0001F			MOVL	R3, @DESC2	0925
				013C	00023			BRW	14\$	0926
				7E	00026		1\$:	CLRL	-(SP)	0932
			04	AE	00028			PUSHAB	DUMMY	
			0C	AE	0002B			PUSHAB	TEMP_DESC	
				53	0002E			PUSHL	R3	
	00000000G	00		04	00030			CALLS	#4, DBG\$NCOPIY_DESC	
		50	04	AE	00037			MOVL	TEMP_DESC, R0	0933
	02	A0	7A	8F	0003B			MOVB	#122, 2(R0)	
	20	A0	18	A0	00040			MOVL	24(R0), 32(R0)	0934
		0D	17	A0	00045			CMPB	23(R0), #13	0935
				05	00049			BNEQ	2\$	
	24	A0	1C	A0	0004B			MOVL	28(R0), 36(R0)	0937
	18	A0	20	A0	00050		2\$:	MOVAB	32(R0), 24(R0)	0938
				56	00055			BRB	6\$	0939
		0E	06	A3	00057		3\$:	CMPB	6(R3), #14	0953
				56	0005B			BNEQ	7\$	
		50	0C	A3	0005D			MOVL	12(R3), RSTPTR	0956
		50	0C	A0	00061			MOVL	12(RSTPTR), DSTPTR	0957
		52	03	A0	00065			MOVL	3(DSTPTR), COUNT	0962
		51	07	A0	00069			MOVZBL	7(DSTPTR), R1	0963
		56	08	A140	0006D			MOVAB	8(R1)[DSTPTR], PTR	
	7E			02	00072			ASHL	#2, COUNT, -(SP)	0968
		6E		04	00076			ADDL2	#4, (SP)	
		7E	7A	8F	00079			MOVZBL	#122, -(SP)	
		67		02	0007D			CALLS	#2, DBG\$MAKE_SKELETON_DESC	
	04	AE		50	00080			MOVL	R0, TEMP_DESC	
	03	A0	03	A3	00084			MOVB	3(R3), 3(R0)	0969
	06	A0	06	A3	00089			MOVW	6(R3), 6(R0)	0971
	08	A0	08	A3	0008E			MOVL	8(R3), 8(R0)	0972
		55	2C	A0	00093			MOVAB	32(R0), R5	0974
		54		55	00097			MOVL	R5, SUBVECTOR	
		64		52	0009A			MOVL	COUNT, (SUBVECTOR)	0975
				51	0009D			CLRL	I	0976
				04	0009F			BRB	5\$	
		6441		86	000A1		4\$:	MOVL	(PTR)+, (SUBVECTOR)[I]	0978
	F8	51		52	000A5		5\$:	AOBLEQ	COUNT, I, 4\$	0976
		18	A0	55	000A9			MOVL	R5, 24(R0)	0985
		0C	BC	50	000AD		6\$:	MOVL	R0, @DESC2	0986
				1D	000B1			BRB	8\$	0987
			0C	A3	000B3		7\$:	PUSHL	12(R3)	0992
	00000000G	00		01	000B6			CALLS	#1, DBG\$STA_SYM_IS_LITERAL	
		13		50	000BD			BLBC	R0, 9\$	
			0C	AC	000C0			PUSHL	DESC2	0999

	7E	7A	8F	9A	000C3	MOVZBL	#122, -(SP)	
			53	DD	000C7	PUSHL	R3	
	00000000G	00	03	FB	000C9	CALLS	#3, DBG\$PRIM_TO_VAL	
			008F	31	000D0	BRW	14\$	1000
			04	DD	000D3	PUSHL	#4	1007
	7E	7A	8F	9A	000D5	MOVZBL	#122, -(SP)	
	67		02	FB	000D9	CALLS	#2, DBG\$MAKE_SKELETON_DESC	
	04	AE	50	DO	000DC	MOVL	R0, TEMP_DEST	
	52	04	AE	DO	000E0	MOVL	TEMP_DEST, R2	1008
	03	A2	03	A3	90 000E4	MOVB	3(R3), 3(R2)	
	06	A2	06	A3	B0 000E9	MOVW	6(R3), 6(R2)	1010
	08	A2	08	A3	DO 000EE	MOVL	8(R3), 8(R2)	1011
	54	14	A2	9E	000F3	MOVAB	20(R2), R4	1018
			18	BB	000F7	PUSHR	#*M<R3,R4>	
	00000000G	00	02	FB	000F9	CALLS	#2, DBG\$MAKE_VMS_DESC	
		04	50	E8	00100	BLBS	R0, 10\$	
		50	04	DO	00103	MOVL	#4, R0	1020
			04	00106		RET		
		0D	03	A4	91 00107	CMPB	3(R4), #13	1024
			18	12	0010B	BNEQ	11\$	
	50	1C	A2	08	C7 0010D	DIVL3	#8, 28(R2), R0	1028
		18	A2	50	C0 00112	ADDL2	R0, 24(R2)	
	7E	00	1C	A2	01 7A 00116	EMUL	#1, 28(R2), #0, -(SP)	1029
	50		8E	08	7B 0011C	EDIV	#8, (SP)+, R0, R0	
		1C	A2	50	DO 00121	MOVL	R0, 28(R2)	
			08	AC	D5 00125	TSTL	DTYPE	1036
			2A	13	00128	BEQL	13\$	
		08	08	AC	D1 0012A	CMPL	DTYPE, #8	1045
			0F	12	0012E	BNEQ	12\$	
	06	A2	0603	8F	B0 00130	MOVW	#1539, 6(R2)	1049
		64	01080004	8F	DO 00136	MOVL	#17301508, (R4)	1052
				15	11 0013D	BRB	13\$	1045
			00000000'	EF	9F 0013F	PUSHAB	P.AAA	1058
			00028362	01	DD 00145	PUSHL	#1	
				8F	DD 00147	PUSHL	#164706	
	00000000G	00	03	FB	0014D	CALLS	#3, LIB\$SIGNAL	
		20	A2	18	A2 7D 00154	MOVQ	24(R2), 32(R2)	1062
		18	A2	20	A2 9E 00159	MOVAB	32(R2), 24(R2)	1068
		0C	BC	52	DO 0015E	MOVL	R2, @DESC2	1069
		50	01	DO	00162	MOVL	#1, R0	1070
			04	00165		RET		1071

: Routine Size: 358 bytes, Routine Base: DBG\$CODE + 056B

```

: 942      1072 1 ROUTINE DETERMINE_TYPE (ARG, NEW_ARG, TYPE) : NOVALUE =
: 943      1073 1
: 944      1074 1 FUNCTION
: 945      1075 1
: 946      1076 1     Given a pointer to a descriptor, this routine does two things:
: 947      1077 1     1 - Determines which of the address types the descriptor represents
: 948      1078 1     2 - If necessary, calls CONV_TEXT_VALUE and returns a converted arg
: 949      1079 1     in NEW_ARG.
: 950      1080 1
: 951      1081 1 INPUTS
: 952      1082 1
: 953      1083 1     ARG      - Points to either a Primary Descriptor or a Value Descriptor
: 954      1084 1     or a Volatile Value Descriptor
: 955      1085 1     NEW_ARG - The address in which to leave a pointer to the converted
: 956      1086 1     descriptor.
: 957      1087 1     TYPE    - The address in which to leave a type code
: 958      1088 1
: 959      1089 1 OUTPUTS
: 960      1090 1
: 961      1091 1     In TYPE, returns one of the five type codes ADDR$K_LITERAL, ADDR$K_INST,
: 962      1092 1     ADDR$K_DATA, ADDR$K_BITFIELD, ADDR$K_PRIMARY, saying what kind of
: 963      1093 1     address the descriptor represents.
: 964      1094 1
: 965      1095 1     In NEW_ARG, returns the address of the original argument if no conversion
: 966      1096 1     was done on the descriptor. Otherwise, returns the address of the
: 967      1097 1     new descriptor.
: 968      1098 1
: 969      1099 2 BEGIN
: 970      1100 2
: 971      1101 2 MAP
: 972      1102 2     ARG : REF DBG$VALDESC;
: 973      1103 2
: 974      1104 2 LOCAL
: 975      1105 2     DTYPE,           ! Holds a dtype code
: 976      1106 2     NAME: REF VECTOR[,BYTE], ! Holds name of a primary
: 977      1107 2     SYMID;           ! Pointer to a SYMID
: 978      1108 2
: 979      1109 2     ! Turn value descriptors into volatile value descriptors.
: 980      1110 2     !
: 981      1111 2 IF .ARG [DBG$B_DHDR_TYPE] EQL DBG$K_VALUE_DESC
: 982      1112 2 THEN
: 983      1113 3 BEGIN
: 984      1114 3
: 985      1115 3     ! Check for "unconverted" value descriptors as input. These arise from
: 986      1116 3     ! constants, e.g. on EXAMINE 1000 we get a value descriptor with
: 987      1117 3     ! the string "1000".
: 988      1118 3     !
: 989      1119 3 IF .ARG [DBG$V_DHDR_UNCVT]
: 990      1120 3 THEN
: 991      1121 4 BEGIN
: 992      1122 4
: 993      1123 4     ! Check for the correct dtype.
: 994      1124 4     !
: 995      1125 4 SELECT ONE .ARG [DBG$B_VALUE_DTYPE] OF
: 996      1126 4     SET
: 997      1127 4     [DSC$K_DTYPE_L]:
: 998      1128 4     ARG = DBG$CONV_TEXT_VALUE (.ARG, .ARG, 0);

```

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: 999 1129 4
: 1000 1130 4
: 1001 1131 4
: 1002 1132 4
: 1003 1133 4
: 1004 1134 4
: 1005 1135 5
: 1006 1136 5
: 1007 1137 5
: 1008 1138 5
: 1009 1139 6
: 1010 1140 6
: 1011 1141 6
: 1012 1142 6
: 1013 1143 6
: 1014 1144 5
: 1015 1145 5
: 1016 1146 5
: 1017 1147 5
: 1018 1148 4
: 1019 1149 4
: 1020 1150 4
: 1021 1151 4
: 1022 1152 4
: 1023 1153 4
: 1024 1154 4
: 1025 1155 4
: 1026 1156 4
: 1027 1157 4
: 1028 1158 4
: 1029 1159 4
: 1030 1160 4
: 1031 1161 4
: 1032 1162 4
: 1033 1163 4
: 1034 1164 4
: 1035 1165 4
: 1036 1166 4
: 1037 1167 4
: 1038 1168 4
: 1039 1169 3
: 1040 1170 3
: 1041 1171 3
: 1042 1172 3
: 1043 1173 3
: 1044 1174 3
: 1045 1175 3
: 1046 1176 3
: 1047 1177 3
: 1048 1178 3
: 1049 1179 3
: 1050 1180 3
: 1051 1181 2
: 1052 1182 2
: 1053 1183 2
: 1054 1184 2
: 1055 1185 2

```

```

! The number scanner may pick up the constant as pack decimal
! for some languages, take in pack decimal with no '.' and
! treat it as long.
[DSC$K_DTYPE_P]:
  BEGIN
  IF NOT CH$FIND_CH(.ARG [DBG$W_VALUE_LENGTH],
    .ARG [DBG$L_VALUE_POINTER], %C'.')
  THEN
  BEGIN
  ARG [DBG$B_VALUE_DTYPE] = DSC$K_DTYPE_L;
  ARG [DBG$W_VALUE_TOKENCODE] = TOK$N$K_INTEGER;
  ARG = DBG$CONV_TEXT_VALUE (.ARG, .ARG, 0);
  END
  ELSE
  SIGNAL (DBG$_ILLADDCON, 2, .ARG [DBG$W_VALUE_LENGTH],
    .ARG [DBG$L_VALUE_POINTER]);

  END;

[OTHERWISE]:
  SIGNAL (DBG$_ILLADDCON, 2, .ARG [DBG$W_VALUE_LENGTH],
    .ARG [DBG$L_VALUE_POINTER]);

TES:

! Fill in correct dtype and length information.
!
ARG [DBG$V_DHDR_LITERAL] = TRUE;
ARG [DBG$V_DHDR_OVERRIDE] = TRUE;
ARG [DBG$B_VALUE_CLASS] = 0;
ARG [DBG$B_VALUE_DTYPE] = .DBG$GL_DFLT_TYP;
IF .DBG$GL_DFLT_TYP EQL DSC$K_DTYPE_ZI
THEN
  ARG [DBG$W_VALUE_LENGTH] =
  DBG$INS_DECODE(..ARG [DBG$L_VALUE_POINTER],
    FALSE, FALSE) -
  ..ARG [DBG$L_VALUE_POINTER]
ELSE
  ARG [DBG$W_VALUE_LENGTH] = .DBG$GW_DFLTLENG;
END;

! Make the value descriptor into a volatile value descriptor.
!
ARG [DBG$B_DHDR_TYPE] = DBG$K_V_VALUE_DESC;
ARG [DBG$L_VALUE_POINTER] = .ARG [DBG$L_VALUE_VALUE0];
IF .ARG [DBG$B_VALUE_DTYPE] EQL DSC$K_DTYPE_V
OR .ARG [DBG$B_VALUE_DTYPE] EQL DSC$K_DTYPE_SV
OR .ARG [DBG$B_VALUE_DTYPE] EQL DSC$K_DTYPE_VU
OR .ARG [DBG$B_VALUE_DTYPE] EQL DSC$K_DTYPE_SVU
THEN
  ARG [DBG$L_VALUE_POS] = .ARG [DBG$L_VALUE_VALUE1];
END;

! Case on the kind of descriptor.
!

```



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: 1056 1186 2
: 1057 1187 2
: 1058 1188 2
: 1059 1189 2
: 1060 1190 2
: 1061 1191 2
: 1062 1192 2
: 1063 1193 2
: 1064 1194 2
: 1065 1195 2
: 1066 1196 2
: 1067 1197 2
: 1068 1198 2
: 1069 1199 2
: 1070 1200 2
: 1071 1201 2
: 1072 1202 2
: 1073 1203 2
: 1074 1204 2
: 1075 1205 4
: 1076 1206 4
: 1077 1207 4
: 1078 1208 4
: 1079 1209 4
: 1080 1210 4
: 1081 1211 4
: 1082 1212 4
: 1083 1213 4
: 1084 1214 4
: 1085 1215 4
: 1086 1216 4
: 1087 1217 4
: 1088 1218 4
: 1089 1219 4
: 1090 1220 4
: 1091 1221 4
: 1092 1222 4
: 1093 1223 4
: 1094 1224 4
: 1095 1225 4
: 1096 1226 4
: 1097 1227 5
: 1098 1228 5
: 1099 1229 5
: 1100 1230 5
: 1101 1231 5
: 1102 1232 5
: 1103 1233 5
: 1104 1234 4
: 1105 1235 4
: 1106 1236 4
: 1107 1237 4
: 1108 1238 4
: 1109 1239 4
: 1110 1240 4
: 1111 1241 4
: 1112 1242 4

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```

SELECTONE .ARG[DBG$B_DHDR_TYPE] OF
SET
: Primary Descriptors.
[DBG$K_PRIMARY_DESC] :
BEGIN
: Check for Primary representing a literal.
: Example: In PASCAL,
: CONST
:     X = 512;
:     Y = 1.1;
:
: On EXAMINE X, we will construct a Value Descriptor with 512 in
: the value field. On EXAMINE Y, we will signal an error.
IF DBG$STA_SYM_IS_LITERAL (.ARG [DBG$L_DHDR_SYMID0])
THEN
BEGIN
: Save the symid
SYMID = .ARG [DBG$L_DHDR_SYMID0];
: Convert the Primary into a Value Descriptor and obtain the dtype.
DBG$PRIM_TO_VAL (.ARG, DBG$K_VALUE_DESC, ARG);
DTYPE = .ARG[DBG$B_VALUE_DTYPE];
: Check for integer dtype. These are the only kinds of
: literals we can do address arithmetic on.
IF .DTYPE NEQ DSC$K_DTYPE_Z
AND .DTYPE NEQ DSC$K_DTYPE_L
AND .DTYPE NEQ DSC$K_DTYPE_LU
AND .DTYPE NEQ DSC$K_DTYPE_W
AND .DTYPE NEQ DSC$K_DTYPE_WU
AND .DTYPE NEQ DSC$K_DTYPE_B
AND .DTYPE NEQ DSC$K_DTYPE_BU
THEN
BEGIN
: Call the routine that turns a symid into a name, in order
: to be able to signal the error.
DBG$STA_SYMNAME (.SYMID, NAME);
SIGNAL (DBG$_ILLADDCON, 2, .NAME[0], NAME[1]);
END;
: Make the value descriptor into a volatile value descriptor.
ARG [DBG$B_DHDR_TYPE] = DBG$K_V_VALUE_DESC;
ARG [DBG$L_VALUE_POINTER] = .ARG [DBG$L_VALUE_VALUE0];
IF .ARG [DBG$B_VALUE_CLASS] EQL DSC$K_CLASS_UBS
THEN
ARG [DBG$L_VALUE_POS] = .ARG [DBG$L_VALUE_VALUE1];

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! Put in the flag for literal.
ARG [DBG$V_DHDR_LITERAL] = TRUE;
.TYPE = ADDR$K_LITERAL;
END

ELSE

! The value is not a literal. Set the type code to Primary
.TYPE = ADDR$K_PRIMARY;

END;

! Volatile Value Descriptors.
[DBG$K_V_VALUE_DESC] :
BEGIN

! If the descriptor came from a literal constant, fill in
! dtype of literal.
IF .ARG [DBG$V_DHDR_LITERAL]
THEN
.TYPE = ADDR$K_LITERAL

! Else look at the dtype field.
ELSE
SELECTONE .ARG [DBG$B_VALUE_DTYPE] OF
SET
[DSC$K_DTYPE_ZI, DSC$K_DTYPE_ZEM] :
BEGIN

! Change type from entry mask to instruction if
! any operations are to be performed.
ARG [DBG$B_VALUE_DTYPE] = DSC$K_DTYPE_ZI;
.TYPE = ADDR$K_INST;
END;

[DSC$K_DTYPE_VU, DSC$K_DTYPE_V,
DSC$K_DTYPE_SVU, DSC$K_DTYPE_SV] :
.TYPE = ADDR$K_BITFIELD;

[OTHERWISE] :
.TYPE = ADDR$K_DATA;

TES;

! For non-bitfield types, make sure the POS field is zero.
IF ..TYPE NEQ ADDR$K_BITFIELD
THEN
ARG [DBG$L_VALUE_POS] = 0;

```



			02	DD	0005C		PUSHL	#2		
		00028EF8	8F	DD	0005E		PUSHL	#167672		
	64		04	FB	00064		CALLS	#4, LIB\$SIGNAL		
	52	04	AC	D0	00067	5\$:	MOVL	ARG, R2		1157
	A2	CO	8F	88	0006B		BISB2	#192, 4(R2)		1158
04	50	00000000G	00	D0	00070		MOVL	DBG\$GL_DFLTYP, R0		1160
	16		50	9B	00077		MOVZBW	R0, 22(R2)		
	A2		50	D1	0007B		CMPL	R0, #22		1161
	16		14	12	0007E		BNEQ	6\$		
			7E	7C	00080		CLRQ	-(SP)		1164
			18	B2	DD	00082	PUSHL	@24(R2)		
	00000000G		00	03	FB	00085	CALLS	#3, DBG\$INS_DECODE		
14	A2		50	B2	A3	0008C	SUBW3	@24(R2), R0, 20(R2)		1166
				08	11	00092	BRB	7\$		1163
	14	A2	00000000G	00	B0	00094	6\$:	MOVW	DBG\$GW_DFLTLENG, 20(R2)	1168
		50	04	AC	D0	0009C	7\$:	MOVL	ARG, R0	1173
	02	A0	83	8F	90	000A0		MOVB	#-125, 2(R0)	
	18	A0	20	A0	D0	000A5		MOVL	32(R0), 24(R0)	1174
		01	16	A0	91	000AA		CMPB	22(R0), #1	1175
			12	13	000AE		BEQL	8\$		
	29	16	A0	91	000B0		CMPB	22(R0), #41		1176
			0C	13	000B4		BEQL	8\$		
	22	16	A0	91	000B6		CMPB	22(R0), #34		1177
			06	13	000BA		BEQL	8\$		
	2A	16	A0	91	000BC		CMPB	22(R0), #42		1178
			05	12	000C0		BNEQ	9\$		
	1C	A0	24	A0	D0	000C2	8\$:	MOVL	36(R0), 28(R0)	1180
		52	04	AC	D0	000C7	9\$:	MOVL	ARG, R2	1186
	79	8F	02	A2	91	000CB		CMPB	2(R2), #121	1191
			03	13	000D0		BEQL	10\$		
			008D	31	000D2		BRW	14\$		
			0C	A2	DD	000D5	10\$:	PUSHL	12(R2)	1203
	00000000G		00	01	FB	000D8		CALLS	#1, DBG\$STA_SYM_IS_LITERAL	
			7A	50	E9	000DF		BLBC	R0, 13\$	
			53	0C	A2	D0	000E2	MOVL	12(R2), SYMID	1209
				04	AC	9F	000E6	PUSHAB	ARG	1213
			7E	7A	8F	9A	000E9	MOVZBL	#122, -(SP)	
				52	DD	000ED		PUSHL	R2	
	00000000G		00	03	FB	000EF		CALLS	#3, DBG\$PRIM_TO_VAL	
			52	04	AC	D0	000F6	MOVL	ARG, R2	1214
			50	16	A2	9A	000FA	MOVZBL	22(R2), DTYPE	
				3C	13	000FE		BEQL	11\$	1219
			08	50	D1	00100		CMPI	DTYPE, #8	1220
				37	13	00103		BEQL	11\$	
			04	50	D1	00105		CMPL	DTYPE, #4	1221
				32	13	00108		BEQL	11\$	
			07	50	D1	0010A		CMPL	DTYPE, #7	1222
				2D	13	0010D		BEQL	11\$	
			03	50	D1	0010F		CMPL	DTYPE, #3	1223
				28	13	00112		BEQL	11\$	
			06	50	D1	00114		CMPL	DTYPE, #6	1224
				23	13	00117		BEQL	11\$	
			02	50	D1	00119		CMPL	DTYPE, #2	1225
				1E	13	0011C		BEQL	11\$	
			4008	8F	BB	0011E		PUSHR	#*M<R3, SP>	1232
	00000000G		00	02	FB	00122		CALLS	#2, DBG\$STA_SYMNAME	
7E			6E	01	C1	00129		ADDL3	#1, NAME, -(SP)	1233

	7E	04	BE	9A	0012D	MOVZBL	@NAME, -(SP)			
			02	DD	00131	PUSHL	42			
		00028EF8	8F	DD	00133	PUSHL	#167672			
	64		04	FB	00139	CALLS	#4, LIB\$SIGNAL			
02	A2	83	8F	90	0013C	11\$:	MOVB	#-125, 2(R2)	1238	
18	A2	20	A2	D0	00141		MOVL	32(R2), 24(R2)	1239	
	0D	17	A2	91	00146		CMPB	23(R2), #13	1240	
			05	12	0014A		BNEQ	12\$		
1C	A2	24	A2	D0	0014C		MOVL	36(R2), 28(R2)	1242	
04	A2	40	8F	88	00151	12\$:	BISB2	#64, 4(R2)	1246	
0C	BC		01	D0	00156		MOVL	#1, @TYPE	1247	
			6A	11	0015A		BRB	21\$	1203	
0C	BC		02	D0	0015C	13\$:	MOVL	#2, @TYPE	1254	
			64	11	00160		BRB	21\$	1186	
83	8F	02	A2	91	00162	14\$:	CMPB	2(R2), #131	1260	
			4C	12	00167		BNEQ	20\$		
06	04		06	E1	00169		BBC	#6, 4(R2), 15\$	1266	
	0C		01	D0	0016E		MOVL	#1, @TYPE	1268	
			36	11	00172		BRB	19\$		
	50	16	A2	9A	00174	15\$:	MOVZBL	22(R2), R0	1273	
	16		50	91	00178		CMPB	R0, #22	1276	
			0F	1F	0017B		BLSSU	16\$		
	17		50	91	0017D		CMPB	R0, #23		
			0A	1A	00180		BGTRU	16\$		
16	A2		16	90	00182		MOVB	#22, 22(R2)	1282	
0C	BC		03	D0	00186		MOVL	#3, @TYPE	1283	
			1E	11	0018A		BRB	19\$	1273	
	01		50	91	0018C	16\$:	CMPB	R0, #1	1286	
			0F	13	0018F		BEQL	17\$		
	22		50	91	00191		CMPB	R0, #34		
			0A	13	00194		BEQL	17\$		
	29		50	91	00196		CMPB	R0, #41		
			0B	1F	00199		BLSSU	18\$		
	2A		50	91	0019B		CMPB	R0, #42		
			06	1A	0019E		BGTRU	18\$		
0C	BC		05	D0	001A0	17\$:	MOVL	#5, @TYPE	1288	
			04	11	001A4		BRB	19\$		
0C	BC		04	D0	001A6	18\$:	MOVL	#4, @TYPE	1291	
	05	0C	BC	D1	001AA	19\$:	CMPL	@TYPE, #5	1297	
			16	13	001AE		BEQL	21\$		
			1C	A2	D4	001B0		CLRL	28(R2)	1299
			11	11	001B3		BRB	21\$	1186	
		00000000'	EF	9F	001B5	20\$:	PUSHAB	P.AAB	1306	
			01	DD	001BB		PUSHL	#1		
		00028362	8F	DD	001BD		PUSHL	#164706		
	64		03	FB	001C3		CALLS	#3, LIB\$SIGNAL		
08	BC	04	AC	D0	001C6	21\$:	MOVL	ARG, @NEW_ARG	1312	
			04	001CB			RET		1314	

; Routine Size: 460 bytes, Routine Base: DBG\$CODE + 06D1

```

1186 1315 1 ROUTINE GET_DEREFERENCE (PRIMPTR) =
1187 1316 1
1188 1317 1 FUNCTION
1189 1318 1 This routine is called upon seeing the dereference operator
1190 1319 1 in an address expression, when the operand is a primary.
1191 1320 1 E.g., 'EXAM *PTR' in language C.
1192 1321 1
1193 1322 1 If the object being dereferenced is a typed pointer then
1194 1323 1 this routine dereferences it by modifying the Primary
1195 1324 1 Descriptor to refer to the pointed-to object. The value
1196 1325 1 "TRUE" is then returned indicating that the routine
1197 1326 1 was successful.
1198 1327 1
1199 1328 1 This routine is similar to the GET_DEREFERENCE routine
1200 1329 1 in DBGPARSER.
1201 1330 1
1202 1331 1 INPUTS
1203 1332 1 PRIMPTR - A pointer to the Primary Descriptor
1204 1333 1 being dereferenced.
1205 1334 1
1206 1335 1 OUTPUTS
1207 1336 1 If the Primary represents a typed pointer, then
1208 1337 1 it is modified and TRUE is returned.
1209 1338 1
1210 1339 1 If the Primary does not represent a typed pointer then
1211 1340 1 it is not modified and FALSE is returned.
1212 1341 1
1213 1342 2 BEGIN
1214 1343 2 MAP
1215 1344 2 PRIMPTR: REF DBG$PRIMARY;
1216 1345 2
1217 1346 2 LOCAL
1218 1347 2 FCODE, ! Local variable holding fcode info
1219 1348 2 JUNK, ! Dummy output parameter
1220 1349 2 NODEPTR: REF DBG$PRIM_NODE, ! Points to a Primary Sub-node
1221 1350 2 TYPEID; ! Pointer to a RST type entry
1222 1351 2
1223 1352 2
1224 1353 2 ! Check that the object being dereferenced is actually a pointer.
1225 1354 2
1226 1355 2 IF .PRIMPTR[DBG$B_DHDR_FCODE] NEQ RST$K_TYPE_TPTR
1227 1356 2 THEN
1228 1357 2 RETURN FALSE;
1229 1358 2
1230 1359 2
1231 1360 2 ! Obtain a pointer to the bottom level sub-node by following tr
1232 1361 2 back-pointer. Light the EVAL bit in this subnode,
1233 1362 2 which indicates that pointer dereferencing is
1234 1363 2 taking place.
1235 1364 2 Then, obtain the pointer to the RST type entry for the object
1236 1365 2 dereferenced.
1237 1366 2
1238 1367 2 NODEPTR = .PRIMPTR [DBG$L PRIM BLINK];
1239 1368 2 NODEPTR [DBG$V PNODE EVAL] = TRUE;
1240 1369 2 TYPEID = .NODEPTR [DBG$L_PNODE_TYPEID];
1241 1370 2
1242 1371 2

```

```

: 1243      1372  2      | From this typeid, get the typeid for the object being pointed to.
: 1244      1373  2      | For pointer variables, use the routine that extracts the typeid
: 1245      1374  2      | of the pointed-to object.
: 1246      1375  2      | Then obtain the fcode from the typeid.
: 1247      1376  2      |
: 1248      1377  2      | DBG$STA_TYP_TYPEDPTR (.TYPEID, TYPEID);
: 1249      1378  2      | FCODE =DBG$STA_TYPEFCODE (.TYPEID);
: 1250      1379  2      | DBG$BUILD_PRIMARY_SUBNODE (.PRIMPTR, RST$K_DATA, 0, .FCODE, .TYPEID, 0);
: 1251      1380  2      | RETURN TRUE;
: 1252      1381  1      | END;

```

```

                                0004 0000 GET_DEREFERENCE:
                                .WORD   Save R2
                                5E      04  C2 00002   SUBL2   #4, SP
                                52      04  AC 00 00005   MOVL   PRIMPTR, R2
                                06      06  A2 91 00009   CMPB   6(R2), #6
                                38      12  0000D   BNEQ   1$
                                OA      50      18  A2 00 0000F   MOVL   24(R2), NODEPTR
                                A0      01  88 00013   BISB2  #1, 10(NODEPTR)
                                6E      0C  A0 00 00017   MOVL   12(NODEPTR), TYPEID
                                SE      DD 0001B   PUSHL  SP
                                AE      DD 0001D   PUSHL  TYPEID
                                00000000G 00  02  FB 00020   CALLS  #2, DBG$STA_TYP_TYPEDPTR
                                6E      DD 00027   PUSHL  TYPEID
                                00000000G 00  01  FB 00029   CALLS  #1, DBG$STA_TYPEFCODE
                                7E      D4 00030   CLRL  -(SP)
                                04      AE  DD 00032   PUSHL  TYPEID
                                50      DD 00035   PUSHL  FCODE
                                7E      06  7D 00037   MOVQ   #6, -(SP)
                                52      DD 0003A   PUSHL  R2
                                00000000G 00  06  FB 0003C   CALLS  #6, DBG$BUILD_PRIMARY_SUBNODE
                                50      01  D0 00043   MOVL   #1, R0
                                04 00046   RET
                                50  D4 00047 1$:   CLRL  R0
                                04 00049   RET

```

: Routine Size: 74 bytes, Routine Base: DBG\$CODE + 089D

```

: 1253      1382  1 END
: 1254      1383  0 ELUDOM

```

.EXTRN LIB\$SIGNAL

PSECT SUMMARY

Name	Bytes	Attributes
DBG\$CODE	2279	NOVEC, NOWRT, RD, EXE, SHR, LCL, REL, CON, PIC, ALIGN(0)
DBG\$PLIT	83	NOVEC, NOWRT, RD, EXE, SHR, LCL, REL, CON, PIC, ALIGN(0)

Library Statistics

File	Total	Symbols Loaded	Percent	Pages Mapped	Processing Time
_\$255\$DUA28:[SYSLIB]LIB.L32;1	18619	17	0	1000	00:01.8
-\$255\$DUA28:[DEBUG.OBJ]STRUCDEF.L32;1	32	0	0	7	00:00.1
-\$255\$DUA28:[DEBUG.OBJ]DBGLIB.L32;1	1545	173	11	97	00:01.9
-\$255\$DUA28:[DEB 'G.OBJ]DSTRECRDS.L32;1	418	105	25	31	00:00.4
_\$255\$DUA28:[DEBUG.OBJ]DBGMSG.L32;1	386	5	1	22	00:00.3

COMMAND QUALIFIERS

:  
: BLISS/CHECK=(FIELD,INITIAL,OPTIMIZE)/LIS=LIS\$:DBGADDEXP/OBJ=OBJ\$:DBGADDEXP MSRC\$:DBGADDEXP/UPDATE=(ENH\$:DBGADDEXP)  
: Size: 2279 code + 83 data bytes  
: Run Time: 00:47.7  
: Elapsed Time: 02:41.5  
: Lines/CPU Min: 1738  
: Lexemes/CPU-Min: 17509  
: Memory Used: 515 pages  
: Compilation Complete



The image displays a grid of 144 small technical diagrams or code snippets arranged in 12 rows and 12 columns. Each cell contains a small-scale version of a larger diagram or code block. Some larger diagrams are visible in the center of the grid, including:

- STRUCDEF REQ**: A diagram showing a hierarchical structure definition.
- TEMPREQ REQ**: A diagram showing a temporary requirement structure.
- DBGADDEXP LIS**: A diagram showing a list of debug extensions.
- DBGATSGN LIS**: A diagram showing a list of debug activation signs.
- SSIDEF REQ**: A diagram showing a structure definition for a set of symbols.

The diagrams consist of various elements such as text labels, lines, and small graphical symbols, representing different aspects of system configuration or debugging.