

DDDDDDDDDDDD		CCCCCCCCCCCC	XXX		XXX
DDDDDDDDDDDD		CCCCCCCCCCCC	XXX		YXX
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DDD	DDD	CCC	XXX		XXX
DDD	DDD	CCC	XXX		XXX
DDD	DDD	CCC	XXX		XXX
DDD	DDD	CCC	XXX		XXX
DDD	DDD	CCC	XXX		XXX
DDDDDDDDDDDD		CCCCCCCCCCCC	XXX		XXX
DDDDDDDDDDDD		CCCCCCCCCCCC	XXX		XXX
DDDDDDDDDDDD		CCCCCCCCCCCC	XXX		XXX

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EEEEEEEEEE XX XX PPPPPPPP AAAAAA NN NN DDDDDDDD
EEEEEEEEEE XX XX PPPPPPPP AAAAAA NN NN DDDDDDDD
EE XX XX PP PP AA AA NN NN DD DD
EE XX XX PP PP AA AA NN NN DD DD
EE XX XX PP PP AA AA NN NN DD DD
EEEEEEEE XX XX PPPPPPPP AA AA NN NN DD DD
EEEEEEEE XX XX PPPPPPPP AA AA NN NN DD DD
EE XX XX PP AA AAAAAAAAAA NN NNNN DD DD
EE XX XX PP AA AAAAAAAAAA NN NNNN DD DD
EE XX XX PP AA AA NN NN DD DD
EE XX XX PP AA AA NN NN DD DD
EEEEEEEEEE XX XX PP AA AA NN NN DDDDDDDD
EEEEEEEEEE XX XX PP AA AA NN NN DDDDDDDD
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LL IIIIII SSSSSSSS
LL IIIIII SSSSSSSS
LL II SS
LL II SS
LL II SS
LL II SSSSSS
LL II SSSSSS
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LLLLLLLLLL IIIIII SSSSSSSS
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```

0001 C MODULE dcx_expand ( . Data expansion routines
0002 0 LANGUAGE (BLISS32),
0003 0 IDENT = 'V04-000',
0004 0 ) =
0005 1 BEGIN
0006 1
0007 1
0008 1 *****
0009 1 *
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0026 1 * SOFTWARE ON EQUIPMENT WHICH IS NOT SUPPLIED BY DIGITAL.
0027 1 *
0028 1 *
0029 1 *****
0030 1
0031 1 **
0032 1
0033 1 FACILITY:
0034 1
0035 1 DCX -- Data Compression / Expansion Facility
0036 1
0037 1 ABSTRACT:
0038 1
0039 1 The Data Compression / Expansion procedures provide a general
0040 1 method for reducing the storage requirement for a arbitrary data.
0041 1
0042 1 ENVIRONMENT:
0043 1
0044 1 VAX native, user mode.
0045 1
0046 1 --
0047 1
0048 1
0049 1 AUTHOR: David Thiel
0050 1
0051 1 CREATION DATE: July, 1981
0052 1
0053 1 MODIFIED BY:
0054 1
0055 1 --

```

Declarations

```

: 57 0056 1 %SBTTL 'Declarations';
: 58 0057 1
: 59 0058 1 LIBRARY
: 60 0059 1 'sys$library:starlet'; ! System macros
: 61 0060 1 REQUIRE
: 62 0061 1 'prefix'; ! DCX macros
: 63 0204 1 REQUIRE
: 64 0205 1 'dcxdef'; ! DCX public structure definitions
: 65 0299 1 REQUIRE
: 66 0300 1 'dcxprvdef'; ! DCX private structure definitions
: 67 0466 1
: 68 0467 1 EXTERNAL ROUTINE
: 69 0468 1 dcx$ctx_check : lkg_ctx_check, ! Check context block
: 70 0469 1 dcx$map_check : lkg_map_check, ! Check map
: 71 0470 1 dcx$get_vm, ! Allocate memory
: 72 0471 1 dcx$free_vm, ! Deallocate memory
: 73 0472 1 dcx$long_move : lkg_long_move NOVALUE, ! Copy arbitrary length data
: 74 0473 1 lib$copy_r_dx : ! General string copy
: 75 0474 1 ADDRESSING_MODE (GENERAL);
: 76 0475 1
: 77 0476 1 EXTERNAL LITERAL
: 78 0477 1 dcx$_normal,
: 79 0478 1 dcx$_invctx,
: 80 0479 1 dcx$_invdata,
: 81 0480 1 dcx$_trunc,
: 82 0481 1 lib$_strtru;
: 83 0482 1
: 84 0483 1 FORWARD ROUTINE
: 85 0484 1 dcx$expand_init, ! initialize for data expansion
: 86 0485 1 dcx$expand_data, ! expand data record
: 87 0486 1 dcx$do_expansion_0, ! internal expansion routine -- type 0
: 88 0487 1 dcx$expand_done; ! delete expansion context

```

```

90 0488 1 %SBTTL 'dcx$expand_init - Initialization for data expansion'
91 0489 1
92 0490 1 GLOBAL ROUTINE dcx$expand_init (context_addr, map_addr) =
93 0491 1 BEGIN
94 0492 1 +-
95 0493 1
96 0494 1 Initialization for data expansion.
97 0495 1 Allocate and initialize context area.
98 0496 1
99 0497 1 Inputs:
100 0498 1
101 0499 1 context_addr.mz.r Address of context longword
102 0500 1 map_addr.ra.r Address of map
103 0501 1
104 0502 1 Outputs:
105 0503 1
106 0504 1 context_addr.mz.r Address of context block is stored
107 0505 1
108 0506 1 Return value:
109 0507 1
110 0508 1 status.wlc.v
111 0509 1
112 0510 1 dcx$_normal All is well
113 0511 1 dcx$_invmap Invalid map structure
114 0512 1 lib$_insvirmem Error allocating memory
115 0513 1 --
116 0514 1
117 0515 1 BIND
118 0516 1 ctx = .context_addr : REF BBLOCK, ! address of context block
119 0517 1 dcxmap = .map_addr : REF BBLOCK; ! address of map
120 0518 1
121 0519 1 ctx = 0; ! assume failure
122 0520 1 perform (dcx$map_check (.dcxmap)); ! validate map
123 0521 1 perform (dcx$get_vm (ctx$k_fixed_len + exp$k_length, ctx));
124 0522 1 IF true
125 0523 1 THEN
126 0524 1 BEGIN
127 0525 1
128 0526 1 BIND
129 0527 1 exp = ctx [ctx$l_specific] : BBLOCK;
130 0528 1
131 0529 1 LOCAL
132 0530 1 cptr : REF VECTOR [, LONG],
133 0531 1 dcxsbm : REF BBLOCK;
134 0532 1
135 0533 1 ctx [ctx$l_size] = ctx$k_fixed_len + exp$k_length;
136 0534 1 ctx [ctx$b_type] = ctx$c_expnd;
137 0535 1 ctx [ctx$w_version] = ctx$c_version;
138 0536 1 ctx [ctx$l_sanity] = ctx$c_sanity;
139 0537 1 ctx [ctx$l_map] = .dcxmap;
140 0538 1 perform (dcx$get_vm (4 * 4 * .dcxmap [dcxmap$w_nsubs], exp [exp$l_map_segs]));
141 0539 1 cptr = .exp [exp$l_map_segs];
142 0540 1 dcxsbm = .dcxmap + .dcxmap [dcxmap$w_sub0];
143 0541 1 INCR i FROM 0 to .dcxmap [dcxmap$w_nsubs]-1 DO
144 0542 1 BEGIN
145 0543 1 cptr [0] = .dcxsbm + .dcxsbm [dcxsbm$w_flags];
146 0544 1 cptr [1] = .dcxsbm + .dcxsbm [dcxsbm$w_nodes];

```

```

: 147      0545 4      IF .dcxsbm [dcxsbm$w_next] EQL 0
: 148      0546 4      THEN
: 149      0547 4      cptr [2] = 0
: 150      0548 4      ELSE
: 151      0549 4      cptr [2] = .dcxsbm + .dcxsbm [dcxsbm$w_next] - 2 * .dcxsbm [dcxsbm$b_min_char];
: 152      0550 4      cptr [3] = .dcxsbm;
: 153      0551 4      cptr = cptr [4];
: 154      0552 4      dcxsbm = .dcxsbm + .dcxsbm [dcxsbm$w_size];
: 155      0553 3      END;
: 156      0554 2      END;
: 157      0555 2      RETURN dcx$_normal;
: 158      0556 2
: 159      0557 1      END;

```

! of dcx\$expand_init

```

.TITLE DCX_EXPAND
.IDENT \V04-000\

.EXTRN LIB$ANALYZE_SDESC R2
.EXTRN DCX$CTX_CHECK, DCX$MAP_CHECK
.EXTRN DCX$GET_VM, DCX$FREE_VM
.EXTRN DCX$LONG_MOVE, LIB$COPY_R_DX
.EXTRN DCX$_NORMAL, DCX$_INVCTX
.EXTRN DCX$_INVDATA, DCX$_TRUNC
.EXTRN LIB$_STRTRU

```

```
.PSECT $CODE$,NOWRT,2
```

```

.ENTRY DCX$EXPAND_INIT, Save R2,R3,R4,R5
CLRL @CONTEXT_ADDR
MOVL @MAP_ADDR, R3
MOVL R3, R0
BSBW DCX$MAP_CHECK
BLBC STATUS, -1$
PUSHL CONTEXT_ADDR
PUSHL #24
CALLS #2, DCX$GET_VM
BLBC STATUS, 1$
MOVL @CONTEXT_ADDR, R2
MOVL #24, (R2)
MOVB #2, 4(R2)
CLRW 8(R2)
MOVL #1328643173, 12(R2)
MOVL R3, 16(R2)
PUSHAB 20(R2)
MOVZWL 16(R3), R0
ASHL #4, R0, -(SP)
CALLS #2, DCX$GET_VM
BLBC STATUS, 6$
MOVL 20(R2), CPTR
MOVZWL 18(R3), DCXSBM
ADDL2 R3, DCXSBM
MOVZWL 16(R3), R5
MNEGL #1, 1
BRB 5$
MOVZWL 6(DCXSBM), R2
ADDL3 R2, DCXSBM, (CPTR)

```

```

003C 00000
04 BC D4 00002
53 08 BC D0 00005
50 53 D0 00009
37 0000G 30 0000C
50 50 E9 0000F
04 AC DD 00012
18 DD 00015
0000G CF 02 FB 00017
2A 50 E9 0001C
52 04 BC D0 0001F
62 18 D0 00023
04 A2 02 90 00026
0C A2 08 A2 B4 0002A
10 A2 4F317C65 8F D0 0002D
53 D0 00035
14 A2 9F 00039
7E 50 10 A3 3C 0003C
50 04 78 00040
0000G CF 02 FB 00044
58 50 E9 00049 1$:
50 14 A2 D0 0004C
51 12 A3 3C 00050
51 53 C0 00054
55 10 A3 3C 00057
54 01 CE 0005B
52 39 11 0005E
60 52 06 A1 3C 00060 2$:
51 52 C1 00064

```

```

: 0490
: 0519
: 0520
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```

DCX_EXPAND
V04=000

04	A0	52	08	A1	3C	00068	MOVZWL	8(DCXSBM), R2	:	0544		
		51		52	C1	0006C	ADDL3	R2, DCXSBM, 4(CPTR)	:			
		52	0A	A1	3C	00071	MOVZWL	10(DCXSBM), R2	:	0545		
				05	12	00075	BNEQ	3\$:			
			08	A0	D4	00077	CLRL	8(CPTR)	:	0547		
				10	11	0007A	BRB	4\$:			
	53	51		52	C1	0007C	3\$:	ADDL3	R2, DCXSBM, R3	:	0549	
		52	02	A1	9A	00080	MOVZBL	2(DCXSBM), R2	:			
		52		02	C4	00084	MULL2	#2, R2	:			
08	A0	53		52	C3	00087	SUBL3	R2, R3, 8(CPTR)	:			
		OC		A0	51	D0	0008C	4\$:	MOVL	DCXSBM, 12(CPTR)	:	0550
		50		10	C0	00090	ADDL2	#16, CPTR	:	0551		
		52		61	3C	00093	MOVZWL	(DCXSBM), R2	:	0552		
		51		52	C0	00096	ADDL2	R2, DCXSBM	:			
	C3	54		55	F2	00099	5\$:	AOBLSS	R5, 1, 2\$:	0541	
		50	00000000G	8F	D0	0009D	MOVL	#DCX\$_NORMAL, R0	:	0555		
				04	000A4	6\$:	RET		:	0557		

; Routine Size: 165 bytes, Routine Base: \$CODE\$ + 0000

dcx\$expand_data - Expand data record

```

161 0558 1 %SBTTL 'dcx$expand_data - Expand data record'
162 0559 1
163 0560 1 GLOBAL ROUTINE dcx$expand_data (context_addr, in_rec : REF BBLOCK, out_rec : REF BBLOCK, out_len) =
164 0561 BEGIN
165 0562 ++
166 0563
167 0564 Expand data record
168 0565
169 0566 Inputs:
170 0567
171 0568 context_addr.mz.r Address of context longword
172 0569 in_rec.ft.dx Descriptor for input (text) data record
173 0570 out_rec.wt.dx Descriptor for output (text) data buifer
174 0571
175 0572 Outputs:
176 0573
177 0574 context_addr.mz.r Context block accumulates data
178 0575 out_rec.wt.dx Buffer is filled with output record
179 0576 out_len.wwu.r Word in which to store length of
180 0577 output record (optional)
181 0578
182 0579 Return value:
183 0580
184 0581 status.wlc.v
185 0582
186 0583 dcx$_normal All is well
187 0584 dcx$_invctx Invalid context block
188 0585 dcx$_invmap Invalid map
189 0586 --
190 0587
191 0588 BIND
192 0589 ctx = .context_addr : REF BBLOCK, ! context block
193 0590 dcxmap = ctx [ctx$l_map] : REF BBLOCK, ! map address
194 0591 res_len = .out_len : WORD; ! result length
195 0592
196 0593 LOCAL
197 0594 in_len, ! input length (bytes)
198 0595 in_addr, ! input data address
199 0596 status; ! return status
200 0597
201 0598 BUILTIN
202 0599 NULLPARAMETER;
203 0600
204 0601 perform (dcx$ctx_check (.ctx, ctx$c_expand));
205 0602 perform (dcx$map_check (.dcxmap));
206 0603 perform (lib$analyze_sdesc_r2 (.in_rec; status, in_len, in_addr); .status);
207 0604
208 0605 CASE .out_rec [dsc$b_class]
209 0606 FROM MIN (dsc$k_class_z, dsc$k_class_s)
210 0607 TO MAX (dsc$k_class_z, dsc$k_class_s)
211 0608 OF
212 0609 SET
213 0610 [dsc$k_class_z, dsc$k_class_s]:
214 0611 BEGIN
215 0612
216 0613 LOCAL
217 0614 result : LONG;

```


dcx\$expand_data - Expand data record

```

218 0615 3
219 0616 3 status = dcx$do_expansion_0 (
220 0617 3 .ctx, .in_addr, .in_len, .out_rec [dsc$a_pointer], .out_rec [dsc$w_length],
221 0618 3 result);
222 0619 3 CH$FILL (%C, .out_rec [dsc$w_length] - .result, .out_rec [dsc$a_pointer]+.result);
223 0620 3 IF NOT NULLPARAMETER (4)
224 0621 3 THEN
225 0622 3 res_len = .result;
226 0623 3 END;
227 0624 2 [inrange, outrange]:
228 0625 2 BEGIN
229 0626 2
230 0627 2 LOCAL
231 0628 2 result : LONG,
232 0629 2 status1 : LONG,
233 0630 2 res_buf : VECTOR [65535, BYTE]; ! result buffer
234 0631 2
235 0632 2 status = dcx$do_expansion_0 (
236 0633 2 .ctx, .in_addr, .in_len, res_buf, %ALLOCATION (res_buf),
237 0634 2 result);
238 0635 2 status1 = lib$scopy_r_dx (result, res_buf, .out_rec);
239 0636 2 IF .status1 EQL lib$_strtru
240 0637 2 THEN
241 0638 2 BEGIN
242 0639 2 IF NOT NULLPARAMETER (4)
243 0640 2 THEN
244 0641 2 lib$analyze_sdesc_r2 (.out_rec; status, res_len);
245 0642 2 status1 = dcx$_frunc;
246 0643 2 END
247 0644 3 ELSE IF NOT NULLPARAMETER (4)
248 0645 3 THEN
249 0646 3 res_len = .result;
250 0647 3 IF .status AND NOT .status1
251 0648 3 THEN
252 0649 3 status = .status1;
253 0650 2 END;
254 0651 2 TES;
255 0652 2 RETURN .status;
256 0653 2
257 0654 1 END;

```

! Of dcx\$expand_data

			00FC 0000	.ENTRY	DCX\$EXPAND_DATA, Save R2,R3,R4,R5,R6,R7	: 0560
		57 0000000G	00 9E 00002	MOVAB	LIB\$ANALYZE_SDESC_R2, R7	:
		5E FFFEFFFB	EE 9E 00009	MOVAB	-65544(SP), -SP	:
52	04	BC	10 C1 00010	ADDL3	#16, @CONTEXT_ADDR, R2	: 0590
		51	02 D0 00015	MOVL	#2, R1	: 0601
		50	04 BC D0 00018	MOVL	@CONTEXT_ADDR, R0	:
			0000G 30 0001C	BSBW	DCX\$CTX_CHECK	:
		0F	50 E9 0001F	BLBC	STATUS, -1\$:
		50	62 D0 00022	MOVL	(R2), R0	: 0602
			0000G 30 00025	BSBW	DCX\$MAP_CHECK	:
		06	50 E9 00028	RLBC	STATUS, -1\$:
		50	08 AC D0 0002B	MOVL	IN_REC, R0	: 0603

			67	16	0002F	JSB	LIB\$ANALYZE_SDESC_R2		
	01		50	EB	00031	1\$:	BLBS	STATJ3, 2\$	
				04	00034		RET		
	53	0C	AC	DC	00035	2\$:	MOVL	OUT_REC, R3	
01	00	03	A3	8F	00039		CASEB	3(R3), #0, #1	
	0070		0070		0003E	3\$:	.WORD	7\$-3\$, - 7\$-3\$	
			5E	DD	00042		PUSHL	SP	
	7E	FFFF	8F	3C	00044		MOVZWL	#65535, -(SP)	
				10	AE	9F	00049	PUSHAB	RES BUF
			51	DD	0004C		PUSHL	IN_LEN	
			52	DD	0004E		PUSHL	IN_ADDR	
		04	BC	DD	00050		PUSHL	@CONTEXT_ADDR	
	0000V	CF	06	FB	00053		CALLS	#6, DCX\$DO_EXPANSION_0	
		56	50	DD	00058		MOVL	R0, STATUS	
			53	DD	0005B		PUSHL	R3	
			0C	AE	9F	0005D	PUSHAB	RES BUF	
			08	AE	9F	00060	PUSHAB	RESULT	
	00000000G	00	03	FB	00063		CALLS	#3, LIB\$SCOPY_P_DX	
		54	50	DD	0006A		MOVL	R0, STATUS1	
	00000000G	8F	54	D1	0006D		CMPL	STATUS1, #LIB\$STRTRU	
			1F	12	00074		BNEQ	5\$	
		04	6C	91	00076		CMPB	(AP), #4	
			11	1F	00079		BLSSU	4\$	
			10	AC	D5	0007B	TSTL	16(AP)	
			0C	13	0007E		BEQL	4\$	
		50	53	DD	00080		MOVL	R3, R0	
			67	16	00083		JSB	LIB\$ANALYZE_SDESC_R2	
		56	50	DD	00085		MOVL	R0, R6	
	10	BC	51	DD	00088		MOVL	R1, @OUT_LEN	
		54	8F	DD	0008C	4\$:	MOVL	#DCX\$_TRUNC, STATUS1	
			0E	11	00093		BRB	6\$	
		04	6C	91	00095	5\$:	CMPB	(AP), #4	
			09	1F	00098		BLSSU	6\$	
			10	AC	D5	0009A	TSTL	16(AP)	
			04	13	0009D		BEQL	6\$	
	10	BC	6E	B0	0009F		MOVW	RESULT, @OUT_LEN	
		42	56	E9	000A3	6\$:	BLBC	STATUS, 8\$	
		3F	54	EB	000A6		BLBS	STATUS1, 8\$	
		56	54	DD	000A9		MOVL	STATUS1, STATUS	
			3A	11	000AC		BRB	8\$	
		04	AE	9F	000AE	7\$:	PUSHAB	RESULT	
		7E	63	3C	000B1		MOVZWL	(R3), -(SP)	
			04	A3	DD	000B4	PUSHL	4(R3)	
			51	DD	000B7		PUSHL	IN_LEN	
			52	DD	000B9		PUSHL	IN_ADDR	
		04	BC	DD	000BB		PUSHL	@CONTEXT_ADDR	
	0000V	CF	06	FB	000BE		CALLS	#6, DCX\$DO_EXPANSION_0	
		56	50	DD	000C3		MOVL	R0, STATUS	
		51	63	3C	000C6		MOVZWL	(R3), R1	
		51	04	AE	C2	000C9	SUBL2	RESULT, R1	
	50	04	A3	04	AE	C1	000CD	ADL3	
		20	00	2C	000D3		MOVCS	#0, (SP), #32, R1, (R0)	
			60		000D8				
			04	6C	91	000D9	CMPB	(AP), #4	
51			0A	1F	000DC		BLSSU	8\$	
			10	AC	D5	000DE	TSTL	16(AP)	

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DCX_EXPAND
V04=000

dcx\$expand_data - Expand data record

E 15
15-Sep-1984 23:42:43
14-Sep-1984 12:16:02

VAX-11 Bliss-32 V4.0-742
DISK\$VMSMASTER:[DCX.SRC]EXPAND.B32;1 Page 9 (4)

10	BC	04	05	13	000E1	BEG	8\$:
	50		AE	B0	000E3	MOVW	RESULT, @OUT_LEN	: 0622
			56	D0	000EB	MOVL	STATUS, R0	: 0652
			04	000EB	RET			: 0654

; Routine Size: 236 bytes, Routine Base: \$CODE\$ + 00A5

dcx\$do_expansion_0 - Type 0 expansion

```

259 0655 1 %SBTTL 'dcx$do_expansion_0 - Type 0 expansion'
260 0656 1
261 0657 1 ROUTINE dcx$do_expansion_0 (
262 0658 1   ctx : REF BBLOCK; in_addr1 : REF VECTOR [, BYTE], in_bytes, out_addr1 : REF VECTOR [, BYTE], out_byt
263 0659 1 BEGIN
264 0660 1   ++
265 0661 1
266 0662 1   Expand data record using type 0 expansion
267 0663 1
268 0664 1   Inputs:
269 0665 1
270 0666 1       ctx                Address of context longword
271 0667 1       in_addr1           Address of input record
272 0668 1       in_bytes          Length of input record
273 0669 1       out_addr1        Address of output buffer
274 0670 1       out_bytes       Length of output buffer
275 0671 1
276 0672 1   Outputs:
277 0673 1
278 0674 1       ctx                Context block accumulates data
279 0675 1       res_addr          Address in which to store result length
280 0676 1
281 0677 1   Status value:
282 0678 1
283 0679 1       dcx$_normal        All is well
284 0680 1       dcx$_invdata      Invalid encoded data
285 0681 1       dcx$_trunc        Result buffer too small - output truncated
286 0682 1   --
287 0683 1
288 0684 1 BIND
289 0685 1   dcxmap = ctx [ctx$l_map] : REF BBLOCK;      ! map address
290 0686 1
291 0687 1   .res_addr = 0;                               ! initialize resulting length
292 0688 1   IF .dcxmap [dcxmap$w_nsubs] NEQ 0
293 0689 1   THEN
294 0690 1     BEGIN
295 0691 1
296 0692 1     LOCAL
297 0693 1       res_len : LONG,
298 0694 1       in_addr : REF VECTOR [, BYTE],
299 0695 1       out_addr : REF VECTOR [, BYTE],
300 0696 1       cptr : REF VECTOR [4, LONG],
301 0697 1       offset : LONG;
302 0698 1
303 0699 1     BIND
304 0700 1       exp = ctx [ctx$l_specific] : BBLOCK,
305 0701 1       base_ptr = exp [exp$l_map_segs] : REF VECTOR [, LONG];
306 0702 1
307 0703 1       in_addr = .in_addr1;
308 0704 1       out_addr = .out_addr1;
309 0705 1       res_len = 0;
310 0706 1       offset = 0;
311 0707 1       cptr = base_ptr [0];
312 0708 1       DECR byte_counter FROM .in_bytes - 1 TO 0 DO
313 0709 1         BEGIN
314 0710 1
315 0711 1         LOCAL

```

```

dcx$do_expansion_0 - Type 0 expansion
: 316      0712      4      byte_full_of_bits : BITVECTOR [8];
: 317      0713      4
: 318      0714      4      byte_full_of_bits = CHRCHAR_A (in_addr);
: 319      0715      4      INCR_bit_counter FROM 0 TO 7 DO
: 320      0716      5      BEGIN
: 321      0717      5
: 322      0718      5      BIND
: 323      0719      5      flags = cptr [0] : REF BITVECTOR,
: 324      0720      5      nodes = cptr [1] : REF VECTOR [, BYTE],
: 325      0721      5      next = cptr [2] : REF VECTOR [, WORD];
: 326      0722      5
: 327      0723      5      IF .byte_full_of_bits [.bit_counter]
: 328      0724      5      THEN
: 329      0725      5      offset = .offset + 1;
: 330      0726      5      IF NOT .flags [.offset]
: 331      0727      5      THEN
: 332      0728      6      BEGIN
: 333      0729      6      offset = .nodes [.offset];
: 334      0730      6      IF (offset = .offset + .offset) EQL 0
: 335      0731      6      THEN
: 336      0732      6      RETURN dcx$_normal;
: 337      0733      6      END
: 338      0734      5      ELSE IF .res_len LSS .out_bytes
: 339      0735      5      THEN
: 340      0736      6      BEGIN
: 341      0737      6      out_addr [.res_len] = .nodes [.offset];
: 342      0738      6      res_len = .res_len + 1;
: 343      0739      6      .res_addr = .res_addr + 1;
: 344      0740      6      IF .next NEQA 0
: 345      0741      6      THEN
: 346      0742      6      cptr = base_ptr [4 * .next [.nodes [.offset]]];
: 347      0743      6      offset = 0;
: 348      0744      6      END
: 349      0745      5      ELSE
: 350      0746      5      RETURN dcx$_trunc;
: 351      0747      4      END;
: 352      0748      3      END;
: 353      0749      3      RETURN dcx$_invdata;
: 354      0750      3      END
: 355      0751      2      ELSE IF .in_bytes GTRU .out_bytes
: 356      0752      2      THEN
: 357      0753      2      BEGIN
: 358      0754      2      dcx$long_move (.out_bytes, .in_addr1, .out_addr1);
: 359      0755      2      res_addr = .out_bytes;
: 360      0756      2      RETURN dcx$_trunc;
: 361      0757      2      END
: 362      0758      2      ELSE
: 363      0759      2      BEGIN
: 364      0760      2      dcx$long_move (.in_bytes, .in_addr1, .out_addr1);
: 365      0761      2      .res_addr = .in_bytes;
: 366      0762      2      RETURN dcx$_normal;
: 367      0763      2      END;
: 368      0764      1      END;
! Of dcx$do_expansion_0

```

03FC 0000 DCX\$DO_EXPANSION_0:									
50	04	AC	10	C1	00002	.WORD	3ave R2,R3,R4,R5,R6,R7,R8,R9		0657
			18	BC	D4	00007	ADDL3	#16, CTX, R0	0685
		50	60	D0	0000A	CLRL	@RES_ADDR		0687
			10	A0	B5	0000D	MOVL	(R0), R0	0688
			6B	13	00010	TSTW	16(R0)		
56	04	AC	14	C1	00012	BEQL	8\$		0700
		59	08	AC	D0	00017	ADDL3	#20, CTX, R6	0703
		54	10	AC	D0	0001B	MOVL	IN_ADDR1, IN_ADDR	0704
			57	D4	0001F	MOVL	OUT_ADDR1, OUT_ADDR		0705
			52	D4	00021	CLRL	RES_LEN		0706
		50	66	D0	00023	CLRL	OFFSET		0707
		55	0C	AC	D0	00026	MOVL	(R6), CPTR	0708
			46	11	0002A	MOVL	IN_BYTES, BYTE_COUNTER		
		58	89	90	0002C	BRB	7\$		0714
			53	D4	0002F	MOVB	(IN_ADDR)+, BYTE_FULL_OF_BITS		0715
02		58	53	E1	00031	CLRL	BIT_COUNTER		0723
			52	D6	00035	BBC	BIT_COUNTER, BYTE_FULL_OF_BITS, 3\$		0725
0C	00	B0	52	E0	00037	INCL	OFFSET		0726
		52	04	B042	9A	0003C	BBS	OFFSET, @0(CPTR), 4\$	0729
		52	52	C0	00041	MOVZBL	@4(CPTR)[OFFSET], OFFSET		0730
			28	12	00044	ADDL2	OFFSET, OFFSET		
			6C	11	00046	BNEQ	6\$		0732
		14	57	D1	00048	BRB	11\$		0734
			4A	18	0004C	CMP	RES_LEN, OUT_BYTES		
		51	04	B042	9A	0004E	BGEQ	9\$	0737
		874	51	90	00053	MOVZBL	@4(CPTR)[OFFSET], R1		
			18	BC	D6	00057	MOVB	R1, (RES_LEN)+[OUT_ADDR]	0739
			08	A0	D5	0005A	INCL	@RES_ADDR	0740
			0D	13	0005D	TSTL	8(CPTR)		
		51	08	B041	3C	0005F	BEQL	5\$	0742
		51	04	C4	00064	MOVZWL	@8(CPTR)[R1], R1		
		50	00	B641	DE	00067	MULL2	#4, R1	
			52	D4	0006C	MOVAL	@0(R6)[R1], CPTR		
BF		53	07	F3	0006E	CLRL	OFFSET		0743
		87	55	F4	00072	AOBLEQ	#7, BIT_COUNTER, 2\$		0715
		50	00000000G	8F	D0	00075	SOBGEQ	BYTE_COUNTER, 1\$	0708
				04	0007C	MOVL	#DCX\$_INVDATA, R0		0751
		14	AC	0C	AC	D1	RET		
				1C	1B	00082	CMP	IN_BYTES, OUT_BYTES	
		52	10	AC	D0	00084	BLEQU	10\$	0754
		51	08	AC	D0	00088	MOVL	OUT_ADDR1, R2	
		50	14	AC	D0	0008C	MOVL	IN_ADDR1, R1	
				0000G	30	00090	MOVL	OUT_BYTES, R0	
		18	BC	14	AC	D0	BSBW	DCX\$LONG_MOVE	0755
		50	00000000G	8F	D0	00093	MOVL	OUT_BYTES, @RES_ADDR	0756
				04	0009F	MOVL	#DCX\$_TRUNC, R0		
		52	10	AC	D0	000A0	RET		0760
		51	08	AC	D0	000A4	MOVL	OUT_ADDR1, R2	
		50	0C	AC	D0	000A8	MOVL	IN_ADDR1, R1	
				0000G	30	000AC	MOVL	IN_BYTES, R0	
		18	BC	0C	AC	D0	BSBW	DCX\$LONG_MOVE	0761
		50	00000000G	8F	D0	000AF	MOVL	IN_BYTES, @RES_ADDR	0762
				04	000B8	MOVL	#DCX\$_NORMAL, R0		0764
				04	000BB	RET			

DCX_EXPAND
V04=000

dcx\$do_expansion_0 - Type 0 expansion

; Routine Size: 188 bytes, Routine Base: \$CODE\$ + 0191

1 15
15-Sep-1984 23:42:43
14-Sep-1984 12:16:02

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DISK\$VMSMASTER:[DCX.SRC]EXPAND.B32;1 Page 13
(5)

```

: 370 0765 1 %SBTTL 'dcx$expand_done -- Release data expansion context'
: 371 0766 1
: 372 0767 1 GLOBAL ROUTINE dcx$expand_done (context_addr) =
: 373 0768 2 BEGIN
: 374 0769 2 +-
: 375 0770 2
: 376 0771 2 Release data expansion context
: 377 0772 2
: 378 0773 2 Inputs:
: 379 0774 2
: 380 0775 2 context_addr.mz.r Address of context longword
: 381 0776 2
: 382 0777 2 Outputs:
: 383 0778 2
: 384 0779 2 context_addr.mz.r Context block accumulates data
: 385 0780 2
: 386 0781 2 Return value:
: 387 0782 2
: 388 0783 2 status.wlc.v
: 389 0784 2
: 390 0785 2 dcx$_normal All is well
: 391 0786 2 dcx$_invctx Invalid context block
: 392 0787 2 dcx$_invmap Invalid map structure
: 393 0788 2 --
: 394 0789 2
: 395 0790 2 BIND
: 396 0791 2 ctx = .context_addr : REF BBLOCK, ! address of context block
: 397 0792 2 exp = ctx [ctx$l_specific] : BBLOCK,
: 398 0793 2 dcxmap = ctx [ctx$l_map] : REF BBLOCK; ! address of map
: 399 0794 2
: 400 0795 2 perform (dcx$ctx_check (.ctx, ctx$c_expnd));
: 401 0796 2 perform (dcx$free_vm (4 * 4 * .dcxmap [dcxmap$w_nsubs], .exp [exp$l_map_segs]));
: 402 0797 2 perform (dcx$free_vm (.ctx [ctx$l_size], .ctx));
: 403 0798 2 ctx = 0; ! mark context as gone
: 404 0799 2 RETURN dcx$_normal;
: 405 0800 2
: 406 0801 1 END; ! Of dcx$expand_done

```

			0004 0000	.ENTRY	DCX\$EXPAND_DONE, Save R2	: 0767
	52	04	BC D0 00002	MOVL	@CONTEXT_ADDR, R2	: 0792
	51		02 D0 00006	MOVL	#2, R1	: 0795
	50		52 D0 00009	MOVL	R2, R0	
			0000G 30 0000C	BSBW	DCX\$CTX_CHECK	
	2D		50 E9 0000F	BLBC	STATUS, 1\$	
		14	A2 DD 00012	PUSHL	20(R2)	: 0796
	50	10	A2 D0 00015	MOVL	16(R2), R0	
	51	10	A0 3C 00019	MOVZWL	16(R0), R1	
			04 78 0001D	ASHL	#4, R1, -(SP)	
7E			02 FB 00021	CALLS	#2, DCX\$FREE_VM	
	0000G	CF	50 E9 00026	BLBC	STATUS, 1\$	
			52 DD 00029	PUSHL	R2	: 0797
			62 DD 0002B	PUSHL	(R2)	
	0000G	CF	02 FB 0002D	CALLS	#2, DCX\$FREE_VM	

2
)

DCX_EXPAND
V04=000

dcx\$expand_done -- Release data expansion conte
K 15
15-Sep-1984 23:42:43 VAX-11 Bliss-32 v4.0-742 Page 15
14-Sep-1984 12:16:02 DISK\$VMSMASTER:[DCX.SRC]EXPAND.B32;1 (6)

0A		50	E9	00032	BLBC	STATUS, 1\$:
	04	BC	D4	00035	CLRL	@CONTEXT_ADDR	: 0798
50	00000000G	8F	D0	00038	MOVL	#DCX\$_NORMAL, R0	: 0799
			04	0003F	RET		: 0801

; Routine Size: 64 bytes, Routine Base: \$CODE\$ + 0240

3
)

DCX_EXPAND
V04=000

dcx\$expand_done -- Release data expansion conte

L 15
15-Sep-1984 23:42:43
14-Sep-1984 12:16:02

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DISK\$VMSMASTER:[DCX.SRC]EXPAND.B32;1

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: 408 0802 1 END
: 409 0803 0 ELUDOM

! Of module expand

PSECT SUMMARY

Name	Bytes	Attributes
\$CODES	653	NOVEC,NOWRT, RD , EXE,NOSHR, LCL, REL, CON,NOPIC,ALIGN(2)

Library Statistics

File	Total	Symbols Loaded	Percent	Pages Mapped	Processing Time
_\$255\$DUA28:[SYSLIB]STARLET.L32;1	9776	11	0	581	00:01.0

COMMAND QUALIFIERS

BLISS/CHECK=(FIELD,INITIAL,OPTIMIZE)/LIS=LIS\$:EXPAND/OBJ=OBJ\$:EXPAND MSRC\$:EXPAND/UPDATE=(ENH\$:EXPAND)

: Size: 653 code + 0 data bytes
: Run Time: 00:15.9
: Elapsed Time: 00:49.8
: Lines/CPU Min: 3030
: Lexemes/CPU-Min: 24418
: Memory Used: 130 pages
: Compilation Complete

The grid contains 100 small terminal window screenshots, each showing a different VAX/VMS command or utility interface. The windows are arranged in a 10x10 grid. Each window contains text-based data, including lists, tables, and command prompts. Some windows have large, bold titles in the top left corner.

Visible titles include:

- STACINT LIS
- STATUS LIS
- PREFIX REQ
- ANALYZE LIS
- DCXMSG LIS
- STASTUB LIS
- DCXDEF MDL
- EXPAND LIS
- SYSOUTPUT LIS
- DCXPRVDEF MDL
- STATEMENT LIS
- DCX
- COMPRESS LIS
- TRANSFER LIS
- SYMBOL LIS
- DCXSHR MAP
- SUBS LIS