

# RSX-11D

# **Executive Reference Manual**

Order No. DEC-11-OXERA-B-D



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RSX-11D Version 6

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Refer to RSX-11D Documentation Directory, DEC-11-OXUGA-B-D.

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#### PREFACE

The <u>RSX-11D Executive Reference Manual</u> provides information required to prepare user programs written in MACRO-11 Assembler Language for execution under the RSX-11D Operating System. It touches only briefly on preparing FORTRAN programs, because the <u>FORTRAN Special Subroutines</u> Reference Manual covers this material.

The manual is organized as follows.

Chapters 1 and 2 present overview information, first on the whole RSX-11D system, then on the RSX-11D Executive, respectively. Details on the philosophy of RSX-11D can be obtained by reading the introductory chapters of the RSX-11D User's Guide.

Chapters 3 and 4 deal with specific Executive functions and how to use them.

Appendixes A, B and C cover system list formats, a glossary, and a summary of the file QIOMAC.MAC. QIOMAC.MAC provides all symbolic values for QUEUE I/O function codes as well as symbolic definitions of status return codes. Appendix D contains directive parameter block formats for the directives.

#### CHAPTER 1

#### INTRODUCTION

RSX-11D is an event-driven multiprogramming operating system with features that make it appropriate for a wide range of applications involving real-time operations. RSX-11D provides the basis for process control systems, online business systems, and communications systems.

The modular construction of RSX-11D allows the user to configure available hardware and software resources to fit a particular processing requirement. The use of memory partitions and priority scheduling facilitates user control over the execution of many parallel real-time functions.

RSX-11D features include:

Fast interrupt response and servicing,

Simultaneous monitoring of multiple activities,

250 priority levels for task execution,

Priority servicing of I/O requests,

Convenient storage and recall of disk-resident programs,

Efficient, convenient task scheduling facilities,

Dynamic memory partitions to contain tasks of varying sizes,

Event flags for task synchronization and notification,

Checkpointing (roll-in/roll-out), a form of memory sharing,

Online program development, concurrent with task execution,

FORTRAN, COBOL\*, and MACRO-11 programming languages and utilities,

Asynchronous execution of I/O-dependent code,

Support of multiuser programs and re-entrant code,

Dynamic shared global areas.

\*Separate license

#### 1.1 SYSTEM EXECUTIVE SOFTWARE

The following paragraphs provide a brief description of RSX-11D software.

#### 1.1.1 Tasks

The basic program unit under RSX-11D is called a task. A task consists of one or more programs that have been written in FORTRAN or MACRO-11 Assembly Language. Relocatable object modules are generated and installed into the system online, making them available in memory-image format on the disk. A task can initiate another task's execution in various ways. The following are examples:

- 1. Request immediate execution,
- 2. Request execution contingent upon available memory,
- 3. Schedule at a future time, with optional rescheduling at periodic intervals.

All these task initiation functions can be accomplished from an operator's console as well as from a currently executing task.

#### 1.1.2 Memory Management

There are three basic functional uses for which memory is allocated. The amount of memory allocated to each function is specified by the user at SYSGEN time.

The three functional memory areas follow.

- 1. RSX-11D Executive code.
- 2. Partitions space for tasks and shared global areas.
  - a. User-controlled partitions in which only one task or shared global area can occupy the partition at a time
  - b. System-controlled partitions in which the Executive controls allocation of memory in the partition and allows multiple tasks or shared global areas within the partition at one time
- 3. System lists and system tables.

#### 1.1.3 Significant Events and System Traps

RSX-11D is an event-driven system in which task execution is governed by the occurrence of significant events. A significant event is any change in system status that affects the execution of a task. For example, completion of an I/O operation is a significant event.

One of the ways that significant events are signalled is through event flags. There are 64 event flags. Flags 1 through 32 are local to the task, while event flags 33 through 64 are common to all tasks. A task can set, clear, test, and wait for any event flag or combination of event flags, to achieve efficient synchronization between itself and other tasks in the system.

When a significant event occurs, the Executive scans an active task list, described in Chapter 2, seeking the highest priority task that can be executed. When an eligible task is found, it is run until it exits, suspends execution, waits for a significant event, or a significant event occurs.

System traps are another means of governing task execution. While significant events have a system-wide scope, traps are local to a task. Traps interrupt the sequence of instruction execution in the task and cause control to be transferred to a prespecified point in the program. Traps can be either synchronous or asynchronous.

Synchronous system traps (SSTs) allow servicing of fault conditions, such as memory protection violation, that can occur internally in a task.

Asynchronous system traps (ASTs) are executed as the result of significant events. Because the task has no control over when the significant event is to occur, ASTs execute asynchronously with respect to the task.

Trap service routines may or may not be provided by the user to handle the synchronous and asynchronous traps. If no synchronous trap service routine is provided, the faulting task is aborted. If no asynchronous trap service routine is provided, the task continues to execute without interruption.

#### 1.1.4 System Directives

System directives are instructions to RSX-11D to perform functions for an executing task. System directives allow tasks to perform the following:

> Schedule other tasks, Communicate with other tasks, Measure time intervals, Perform I/O functions, Suspend execution,

Exit.

Directives are generated by MACRO-11 programs via macro calls and are supported for FORTRAN by library routines supplied by DIGITAL. Refer to Chapter 3 of this manual for details of directive usage in MACRO-11 programs.

#### 1.2 DEVICE HANDLERS

Device Handlers are tasks that support I/O devices. These tasks are similar to normal tasks within the system with the following additional features:

They usually contain an interrupt service routine to respond to hardware interrupts,

They are allowed to gain access to any memory areas including privileged ones.

By convention, device handler task names consist of two alphabetic characters followed by four dots. For example, the line printer handler is named as follows.

LP....

Device handler tasks are loaded into memory on command from the operator as needed. Requests from user tasks are queued by the Executive to the device handler according to the priority of the I/O request. If no priority is specified, that of the requesting task is used by default. When necessary, however, the requesting task can reserve a device for its exclusive use for a period of time by attaching it using the ATTACH function.

#### CHAPTER 2

#### RSX-11D EXECUTIVE

This chapter acquaints the user with the basic design elements of the RSX-11D Executive. It is not intended to provide the detail required by a systems programmer for modification of RSX-11D.

The primary functions of the Executive include memory and disk management, supervision of task execution and scheduling, intertask communication, I/O queuing, console command monitoring, and maintenance of system integrity.

The basic program unit under RSX-11D is called a task and consists of a program or set of programs that have been written in FORTRAN and/or MACRO-11 assembly language. Relocatable task modules are created either online or offline and are installed into an RSX-11D system. This process results in the recording of the task on the system disk in memory image form; i.e., the task is in executable form.

## 2.1 MEMORY MANAGEMENT

There are three different functional memory spaces. The size of each is specified during system generation. These areas are:

- 1. The Executive,
- 2. Partitions,
  - a. System-controlled partitions
  - b. User-controlled partitions
- 3. System communication area (SCOM).

#### 2.1.1 Partitions

Partitions are areas of contiguous real memory that are used for task execution. There are two modes of partition usage: user-controlled where only one task at a time can occupy the partition and system-controlled where the system controls allocation of memory within the partition for execution of one or more tasks. The name, base address, size, and mode of each partition are specified during system generation and cannot be changed online. Tasks are installed to run in a particular partition but, upon specific request, can run in any partition that is large enough.

Normally, an active task remains resident in its memory space until its execution is completed. A checkpointable task, however, can be forced to relinquish its memory for execution of a higher priority task.

#### 2.1.2 Shared Global Areas

Shared global areas (libraries, global common blocks, and pure areas of multiuser tasks) require space in partitions. They can be fixed in memory or can be assigned memory by the Executive when tasks that use them are activated.

Libraries normally are read-only and are used for code.

Global common blocks can be addressed on a per-task basis as read/write or read-only. This is a characteristic of the task rather than the global area and is specified during task building. The global area includes FORTRAN COMMON space and, normally, is used for intertask exchange of large amounts of data. SEND and SEND AND REQUEST also can be used to exchange small amounts of data among tasks. They should not be used for large amounts of data.

Multiuser tasks consist of a pure area and an impure area. The pure area of multiuser task is the area that is not modified during task execution and can be shared among multiple versions of the task. The impure area changes during execution; one copy of the impure area exists for each simultaneous user of the task.

#### 2.1.3 System Communication Area (SCOM)

This memory space contains the tables, lists, system subroutines, and other information required by the Executive to perform its functions and maintain control of the system. It consists of a number of fixed tables or lists and code, with the remaining space being available in variable-length nodes. These nodes are used by the Executive and for intertask communication.

#### 2.2 CONTROL OF TASK EXECUTION

RSX-11D is event-driven, in contrast to systems which use a time slice mechanism for determining a task's eligibility to execute. Under RSX-11D, the highest priority task can run continuously until some event or condition in the system causes it to be suspended. Another event or change in system status can reactivate the task.

Tasks can be activated either by the operator or by another task. Activation can be conditional, based on currently available partition space (EXECUTE), or it can occur as soon as possible (REQUEST). Also it can occur as soon as possible after some future time (SYNC, SCHEDULE, and RUN).

#### 2.2.1 Multiprogramming

Effective multiprogramming is achieved when many tasks reside in memory simultaneously, spending some of their residency waiting for I/O completion, waiting for synchronization with other tasks, or in some way being unable to continue execution. While one or more tasks are waiting, another task can utilize the central processor's resources.

Under RSX-11D, tasks are run at a software priority level ranging from a low of 1 through a high of 250. The Executive grants central processor resources to the highest priority task capable of execution. When a task becomes ready to execute and it has a higher priority than the currently executing task, the Executive interrupts the lower priority task and allows the higher priority task to run. Execution of the interrupted task continues when it once again becomes the highest priority task capable of execution. The environment of an interrupted task is preserved; except for elapsed time, interruption is transparent to an interrupted task.

This multiprogramming scheme normally applies only to memory-resident tasks. Once a task is in memory, the Executive allows it to run to completion in a multiprogramming fashion even if its memory becomes required for the execution of a higher priority, non-resident task. However, if it is desirable to free memory for execution of a higher priority task, a task can be declared checkpointable when it is task built. A checkpointable task is swapped out when its memory is required for a higher priority task and swapped in when it once again becomes the highest priority task requiring its memory.

Normally, a task is brought into memory when requested, executes, and is removed from memory upon completion. This process frees memory for another task to execute. However, a task can be fixed in memory to permit faster response to requests for its execution. It remains in the partition until it is explicitly removed by an UNFIX directive. Tasks fixed in a system-controlled partition have no effect on the rest of the partition which remains available for execution of other tasks.

#### 2.2.2 Significant Events

A task is considered active from the time its execution starts until the time it has exited. While the task is active, it is included in a priority-ordered list of active tasks called the active task list (ATL). The system uses the ATL in the following way.

When a significant event is declared, the Executive interrupts the executing task and scans the active task list examining the status of tasks until a task capable of execution is found. Execution of that task is then initiated, or continued, until one of the following occurs.

- 1. The task exits.
- 2. The task must wait for another event (e.g., I/O completion).
- 3. A significant event occurs and a higher priority task is capable of execution.

Task switching occurs as a result of a significant event, and significant events occur only when declared explicitly or implicitly by tasks.

NOTE

Task switching occurs implicitly when a lower priority task is eligible for execution and the currently executing task performs one of the following actions:

> Suspends itself, Waits for an event (e.g., I/0 completion), Exits.

Event flags are associated with significant events. Declaration of a significant event indicates that something has happened in the system, and the possible setting of a particular event flag indicates what has happened. For example, upon completion of I/O requests, a device handler task normally sets a requester-indicated event flag and declares a significant event. If a requesting task instructs the system that it cannot run until an event flag is set (signalling task I/O completion), other eligible tasks of lower priority may run. In the scan of the active task list, a task that is awaiting I/O completion is by passed until a significant event is declared through the setting of a event flag upon task I/O completion.

Each task has access to 64 event flags of which 1 through 32 are unique to each task and 33 through 64 are common to all tasks. The use of event flags is detailed in Chapter 4.

#### 2.2.3 System Traps

The ability to service certain conditions without continuously testing for their existence is provided via system traps. As discussed in Chapter 1, two types of traps are defined: synchronous and asynchronous (also see Chapter 4). A trap is a linkage method for optional in-task service routines. Service routines must be included as a part of the task, limited by the same restrictions as the task, and run at the task's priority as a result of a system trap condition, (e.g., fault, I/O completion). This facility also provides a means of responding to the execution of privileged instructions and non-RSX-11D EMT's.

If the system is not explicitly notified of the existence of a system trap service routine, the system trap does not occur.

#### 2.3 SYSTEM TABLES AND SYSTEM LISTS

RSX-11D uses linked lists and fixed-length tables to maintain system information. Fixed-length tables are lists with elements that reside in consecutive memory locations. This format is used when lists are static, when list scan time is critical, or both.

Most linked lists are linked as double-ended queues and are called deques (pronounced "decks"). Deques allow list elements to be added or deleted from either end, since they include backward and forward pointers. An RSX-11D deque consists of a listhead and list elements (nodes), circularly linked by both backward and forward pointers. The first word of a node (or listhead) is a forward pointer, i.e., the address of the next node (or listhead) looking forward. The second word of a node (or listhead) is a backward pointer, i.e., the address of the next node (or listhead) looking forward.

A listhead is a node that consists of only a forward and a backward pointer, and is used as a reference point. Hence, a listhead identifies a deque, and indicates both the beginning and end of the circularly linked list. All nodes are a multiple of eight words in length. Usually the first two words contain pointers and the third defines the node's owner. The following paragraphs describe the major lists used in RSX-11D. Their formats and contents are described in Appendix A.

#### 2.3.1 Clock Queue

The clock queue is a linked list with its listhead in SCOM. It consists of one node for each operation to be performed at some time in the future. A ticks-till-due count in the first node of the clock queue is decremented at each clock tick until the node becomes due (i.e., until the count is zero). Then the indicated operation is performed. The nodes are linked in the order in which they come due. Each node is 16 words.

#### 2.3.2 I/O Request Queue

The I/O request queue is a linked list with a listhead in the physical unit directory entry for the unit to which the request has been queued. Each entry is 16 words.

#### 2.3.3 Global Common Directory

The global common directory (GCD) is a linked list of entries for each global common block and library installed in the system. The GCD listhead is in SCOM. Each entry is 16 words.

The GCD entries are created by INSTALL for the pure area of multiuser tasks and for global common areas (libraries and global common). GCD entries are linked into the GCD at run time and are pointed to by the task's STD.

#### 2.3.4 Physical Unit Directory

The physical unit directory (PUD) is a table of entries for each physical unit specified during system generation. When a logical unit number is assigned to a physical unit, the physical unit is represented by the address of the corresponding PUD entry. Each entry is 25 words.

#### 2.3.5 System Task Directory

The system task directory (STD) is a table that provides information about each task installed in the system. The information recorded in a task's STD entry includes the following:

- Information required when the task is not active (viz., receive linked list listhead),
- Information required to load a task into memory (viz., task name, disk address of image).

Under RSX-11D, tasks are referred to by name, and the STD is searched for an indicated task name at each reference. The STD is structured to enable this search to be performed rapidly, without imposing naming conventions, order of installation, or the dedication of a large memory area.

The STD consists of a table of entry pointers (alpha table) for the maximum number of installed tasks and a 16-word entry for each task that is installed. The table is maintained by the programs that install and remove tasks such that the number of entries is known and consecutive table words point to task STD entries ordered alphabetically by task name. Thus, a task name can be found rapidly using a binary search and memory is not dedicated for STD entries until it is needed. The maximum size of the STD is specified during system generation.

The 16-word block of memory for an STD entry is taken from the pool when a task is installed and returned when a task is removed.

#### 2.3.6 Send/Receive Queues

The send/receive queues are linked lists with listheads in the STD entries for each task. Entries are created and queued in priority order by the SEND directives and removed by the RECEIVE directives. Entries are variable in length up to 255 words.

## 2.3.7 Task Partition Directory

The task partition directory (TPD) is a table of entries for each task partition defined during system generation. Each entry is 10 words.

#### 2.3.8 Node Pool

A node is a block of memory that is a multiple of eight words in length. Empty nodes for use in any deque are initially provided by the system generation routine in the form of a long block called the pool. When a node is needed to expand a list, it is taken from the pool. When a node is no longer needed, it is returned to the pool.

#### 2.3.9 Interrupt Connect Node

Interrupt connect nodes connect the trap vector to the interrupt service routine of a device handler task. Each node is 16 words.

### 2.3.10 Memory Required List (MRL)

The memory required list is a priority-ordered linked list of active task list nodes for active tasks that require memory in a partition. Its listhead is in the task partition directory (TPD). There is an MRL for each partition. Whenever a nonfixed task exits, the MRL associated with that partition is scanned, and an attempt is made to assign memory to the highest-priority task in the list. If the attempt is successful, the task's node is moved from the MRL to the active task list. Each node is 24 words.

#### 2.3.11 Checkpointable Task List (CTL)

For each partition, there is a priority-ordered list of checkpointable tasks that are active in that partition. Actually, this list is a set of links threaded through the ATL and not a distinct physical set of nodes. The CTL listhead is in the TPD. Each entry is 24 words.

#### 2.3.12 Fixed Task List (FTL)

The fixed task list (FTL) is a deque of active task list nodes for tasks that have been fixed in memory but are not active. The FTL listhead is in SCOM. When a fixed task is made active, its node is relinked from the FTL to the ATL. When the task exits it is relinked into the FTL. Each node is 24 words.

#### 2.3.13 MCR Command Buffer

The MCR command buffer is 96-byte buffer that holds the data for a requested MCR function task. The buffer is set up by the MCR dispatch task. The nodes required for the buffer are returned to the pool after the GET MCR COMMAND LINE directive passes the command line to the MCR function task.

#### 2.3.14 Batch Command Buffer

The batch command buffer is a 96-byte buffer that holds data for the batch processor. It functions in the same manner as the MCR command buffer.

#### 2.3.15 Asynchronous System Trap Queue

The asynchronous system trap queue (ASQ) is a linked list that operates on a first-in/first-out basis. Its listhead is in ATL entries. It consists of one node for each AST (asynchronous system trap) to be executed for the task as defined by the STD entry. Each node is eight words.

#### 2.4 I/O OPERATIONS

The Executive's main function in I/O operations is to handle I/O requests from tasks and pass the requests to the appropriate device handler task. The general method follows.

 A QIO directive (see Chapter 3) is issued by a task. The task specifies a number of parameters that are required in processing the I/O request. One of these parameters is the logical unit number (LUN) assigned to a device by the task.

The directive is issued by means of a normal software emulated trap. It follows the normal PDP-11 trap sequence.

- 2. The Executive examines the LUN parameter of the QIO directive to determine which device handler is to process the request. The particular device handler is chosen by mapping the LUN of a particular task into an entry in the physical unit directory using the logical unit table.
- 3. The I/O request is put in the request queue of a device handler (one of a set of special tasks).

The requesting task can either suspend operation until the I/O request is completed or continue to operate until interrupted by an asynchronous system trap (see Chapter 4). RSX-11D permits parallel I/O requests to be issued by the same task. That is, the task continues executing after issuing a QIO; subsequently the task can issue further QIO requests without waiting for the previous request to be completed.

Some device handlers operate in conjunction with the file control primitives (FCP) to manipulate files. When an FCP routine is required, the device handler issues a SEND/REQUEST which initiates operation of the specified FCP routine.

I/O requests are queued for each unit by priority at requester task priority unless otherwise specified. The handler tasks pick requests from the top of request queues. Thus, preferential service is given to high priority requests. However, when appropriate, devices can be attached to a task, in which case only requests from the attached task or express request are dequeued. This continues until a detach-unit-from-task request is dequeued, causing requests to be dequeued by priority from the top of the I/O request queue once again.

The right to attach and detach devices is controlled by access privileges defined for each device. Requests to attach a device are rejected if the requester does not have the proper access rights. Because device handler tasks can service many units, they are not themselves attached.

The interface between a device handler task and the RSX-llD system is accomplished by directives and system subroutines which attach and detach devices and dequeue I/O requests.

#### CHAPTER 3

#### SYSTEM DIRECTIVES

#### 3.1 INTRODUCTION

System directives are instructions to the Executive to perform an indicated operation. The applications programmer uses them to control the execution and interaction of tasks. The system macro library contains macro calls, which the programmer can use to execute directives. The FORTRAN programmer invokes system directives through a subroutine call, as described in the FORTRAN Special Subroutines Reference Manual.

Directives are implemented solely through the EMT 377 instruction. By using only EMT 377, programs using EMT 0 through EMT 376 can be run via a non-RSX system trap. Any EMT other than EMT 377 traps to a task-contained service routine that can be written to simulate another environment to whatever degree is desired.

By using macro calls, instead of executing the directive, the programmer need only reassemble his program if changes are made in the directive specifications, rather that being required to edit the source code.

#### 3.2 DIRECTIVE IMPLEMENTATION

A brief discussion of how directives are implemented will help the programmer understand and use the macro calls associated with the directives.

Directive processing consists of five parts.

1. The user issues a directive. The directive identifier and the directive parameters are placed in the directive parameter block (DPB).

The DPB can either be on the stack or be in the user task space.

2. An EMT 377 is issued.

- 3. The Executive traps the instruction and performs three steps:
  - 1. Determines whether the instruction is an EMT 377.
  - 2. If it is a 377, transfers it to the directive processor.
  - 3. If it is not a 377, determines whether user is capable of handling the directive. If yes, user is given control. If not, an error is returned.
- 4. The Executive processes the directive.
- 5. The Executive returns to the issuing task through "common exit," if necessary, and processes any system events.

The EMT 377 is issued with the address of a directive parameter block (DPB), or a DPB itself, on the top of the issuing task's stack. When the stack contains a DPB pointer (address), the pointer is removed (popped) after the directive is processed. When the stack contains a DPB, the entire DPB is removed as the directive is processed. In either case, the DPB is not altered when the directive is processed.

With the exception of EXIT directives, control is returned to the instruction following the EMT, with the C condition code set or cleared and the directive status word (DSW) set to indicate performance or rejection. The DSW is at virtual location zero of each task.

In the case of EXIT directives, control is not returned, but the C condition code and DSW are set.

When a directive is performed properly, the C condition code in the PS (CC-C) is cleared and the directive status word (DSW) contains a positive number, unless otherwise noted in the directive description. When a directive is rejected, the C condition code is set and the DSW contains a negative number. The number's value indicates the reason for rejection.

The first word of all DPB's contains a directive identification code (DIC) byte and a DPB size byte. The DIC indicates which directive is to be performed. The size byte indicates the DPB length in words. The DIC is in the low-order byte of the word and the size is in the high-order byte.

#### 3.3 CONVENTIONS

The following conventions and assumptions are standard for all directives.

- 1. Task and partition names can be up to six characters long and are always represented as two words in radix-50 form.
- 2. Device names are two characters long and are represented by one word in ASCII form.
- 3. Time unit indicators, used for initial and repeated requests, are 1 for clock ticks, 2 for seconds, 3 for minutes, and 4 for hours.
- 4. The term "background task" indicates a task that is initiated from batch.

#### 3.4 ERROR RETURNS

Directive rejections are divided into two classes: those where a programmed recovery would be common and those where it would be unlikely. The error code, always negative, is returned in the DSW at virtual location 0. Rejections with expected programmed recoveries (i.e., where a branch is taken to an error routine) have values between -1 and -19. Error codes indicating errors for which programmed recoveries are not feasible are in the range from -20 through -99.

The error codes that can be received for a particular directive are listed with the individual directive descriptions.

#### 3.5 USING THE DIRECTIVE MACROS

This discussion applies to MACRO-11 programs. FORTRAN programmers should refer to the <u>FORTRAN Special Subroutines Reference Manual</u> for a description of the library subroutines which support the directives for that language.

Directives are issued by including appropriate macro calls in the program. Macro names consist of up to four letters followed by a dollar sign and, optionally, one letter. The optional letter specifies which of three possible expansions of the macro is desired.

If the optional letter is S, the macro produces code to push a DPB onto the stack, followed by an EMT 377.

If the optional letter is omitted, the macro produces only the directive's DPB. The macro expansion is inserted at the point of macro invocation, but it does not contain executable code.

When the user uses the \$ or \$C form of the macro call, specifying the generation of a DPB at assemble time, it is assumed that the parameters required for DPB construction are valid expressions to be used in MACRO-11 data storage instructions (e.g., .BYTE, .WORD, .RAD50).

If, however, the \$S form is used, specifying the generation of code to store the DPB in the stack, the parameters must be valid source operands to be placed directly in MOV instruction.

Only the \$S form produces the DPB dynamically. The other two forms produce the DPB at assembly time.

If the user has a predefined DPB, i.e., has used the \$ or \$C form of the macro, and wishes to avoid the creation of another one, the DIR\$ macro should be used instead of one that identifies the function. This macro pushes the DPB address onto the stack using MOV SSS,-(SP), where the macro parameter (shown here as SSS), is any valid representation of the DPB address. The instruction is followed by EMT 377.

In addition to the macro routines that correspond to the directives, the DIR\$ macro is useful to the programmer, particularly in cases where the DPB has been defined independently of the execution of the directive.

DIR\$ generates an RSX-11D Executive trap with a predefined DPB.

Macro Call: DIR\$ adr,err

Three forms are possible, with the following interpretation:

- DIR\$ Assumes that the address of the DPB or the DPB itself has already been pushed onto the stack.
- DIR\$ adr Generates the code to push the parameter adr onto the stack.
- DIR\$ adr,err Generates the code to push the parameter adr onto the stack, executes an EMT 377, generates a branch on carry clear to the address of the branch +4 (or +6 if necessary) and generates a JSR PC to the err address.

The argument adr is optional but, if present, must be a valid assembler source operand used to push the DPB address onto the stack.

The argument err is optional. If defined, it must be a valid assembler destination operand to permit a Jump to Subroutine (JSR) instruction to an error handler if the directive is rejected.

If the optional letter is C, the macro generates a DPB in a separate program section, called \$DPB\$\$. The DPB is followed by a return to the original program section, an instruction to push the DPB address onto the stack, and an EMT 377. To ensure that the correct program section is re-entered, the user must specify its name in the argument list immediately following the required DPB parameters. If the argument is not specified, the blank (unnamed) program section is assumed.

The \$C form of the macro call accepts an optional argument, PSECT. This argument allows return to the PSECT specified rather than to the default PSECT.

The \$C and \$S forms of macro calls and DIR\$ accept an optional final argument. If included, the argument must be a valid assembler destination operand to call a user error routine. The argument generates the following code.

OPEN\$W #FDBADR,,,,,OPEERR

BCC .+6 ; BRANCH ON DIRECTIVE ACCEPTANCE

JSR PC, OPEERR ; ELSE, CALL ERROR SERVICE ROUTINE

This option is ignored when the user specifies the generation of the DPB only.

#### 3.5.1 Symbolic Offsets

Most system directive macro calls generate local symbolic offsets. The symbols are unique to each directive and are assigned the values of the byte offset from the start of the directive's DPB to the DPB elements.

Because the offsets are defined symbolically, the programmer who must refer to or modify DPB elements can do so without calculating the offsets. Symbolic offsets also preclude the necessity of rewriting programs to accommodate changes in DPB specifications.

All \$ and \$C forms of macros that generate DPB's longer than one word generate local offsets. All informational directives, including the \$S, form generate local symbolic offsets for the parameter block in question.

If any of the \$ or \$C forms of the macros is invoked and the user defined symbol \$\$\$GLB is included, the DPB is not expanded. Furthermore, if the macro produces symbolic offsets, they are generated as global symbols, unless previously defined. The symbol \$\$\$GLB has no effect on the expansion of \$S macros.

# 3.5.2 Examples of Macro Calls

1.	MRKT\$ .BYTE .WORD .WORD .WORD .WORD	1,5,2,MTRAP 23.,5. 1 5 2 MTRAP	[generate DPB only in current PSECT] ;"MARK-TIME" DIC AND DPB SIZE ;EVENT FLAG NUMBER ;TIME INTERVAL MAGNITUDE ;TIME INTERVAL UNIT ;AST ENTRY POINT
2.	MRKT\$C	1,5,2,MTRAP,PR	OG1,ERR [generates DPB in separate PSECT]
	.PSECT	\$DPB\$\$	
\$\$ <b>\$</b>	=.		; DEFINE TEMPORARY SYMBOL
	. BYTE .WORD .WORD .WORD .WORD	23.,5. 1 5 2 MTRAP	;"MARK TIME" DIC AND DPB SIZE ;EVENT FLAG NUMBER ;TIME INTERVAL MAGNITUDE ;TIME INTERVAL UNIT ;AST ENTRY POINT
	.PSECT	PROGL	[return to the original PSECT]
	MOV EMT BCC JSR	#\$\$\$,-(SP) 377 .+6 PC,ERR	;PUSH DPB ADDRESS ON STACK ;TRAP TO THE MONITOR ;BRANCH ON DIRECTIVE ACCEPTANCE ;ELSE, CALL ERROR SERVICE ROUTINE
3.	MRKT \$S	<b>#1,</b> #5,#2,R2	,ERR [push DPB onto stack]
	MOV MOV MOV MOV .BYTE EMT BCC JSR	R2,-(SP) #2,-(SP) #5,-(SP) #1,-(SP) (PC)+,-(SP) 23.,5. 377 .+6 PC,ERR	; PUSH AST ENTRY POINT ; TIME INTERVAL UNIT ; TIME INTERVAL MAGNITUDE ; EVENT FLAG NUMBER ; AND MARK TIME" DIC & DPB SIZE ; ON THE STACK ; TRAP TO THE MONITOR ; BRANCH ON DIRECTIVE ACCEPTANCE ; ELSE, CALL ERROR SERVICE ROUTINE
4.	DIR\$ MOV EMT BCC JSR	@Rl,(R3) @Rl,-(SP) 377 .+4 PC,(R3)	[DPB already defined. DPB address in Rl.] ;PUSH DPB ADDRESS ONTO STACK ;TRAP TO THE MONITOR ;BRANCH ON DIRECTIVE ACCEPTANCE ;ELSE, CALL ERROR SERVICE ROUTINE

#### 3.6 DIRECTIVE SUMMARIES

Each directive description consists of a narrative explanation of its function and use, the name of the macro associated with it and its parameters, and the possible measurement values of the directive status word (DSW), which is virtual memory location zero.

Only the \$ form of the macro name is given, although all three options are available unless otherwise specified.

The directive descriptions are presented in alphabetic order by macro call for ease of reference. However, the directives can be categorized according to their function. Table 3-1 lists the directives by function and describes them briefly.

DIRECTIVE	ASSOCIATED MACRO CALL	
DIRECTIVE	DIR\$	Generates an RSX-11D trap with a predefined DPB.
Task Execution Control		
EXECUTE	EXEC\$	Executes a task only if sufficient memory is available at the present time. If memory is not available, the request is not queued.
REQUEST	RQST\$	Runs a task contingent upon priority and memory availability. If the task cannot be run immediately, the request is queued.
SCHEDULE	SCHD\$	Requests a task using the RQST\$ directive at a specific future time and, optionally, repeats the request periodically; e.g., the request can be scheduled for 11:35 a.m.
RUN	RUN\$	Requests a task using the RQST\$ directive at a specified interval from the current time and, optionally, repeats the request periodically.
SYNCHRONIZE	SYNC\$	Requests a task using RQST\$ at a specific interval from a specified future time and, optionally, repeats the request periodically.
CANCEL SCHEDULED REQUESTS	CSRQ\$	Cancels scheduled requests for task executions.
SUSPEND	SPND\$	Suspends execution of the task issuing the directive.

#### Table 3-1 RSX-11D Directives

# Table 3-1 (Cont.) RSX-11D Directives

DIRECTIVE	ASSOCIATED MACRO CALL	
RESUME	RS UM\$	Resumes the execution of a task that has suspended itself.
TASK EXIT	EX <b>IT\$</b>	Terminates execution of the task issuing the directive and causes a significant event.
ABORT TASK	ABRT\$	Terminates execution of another task.
Task Status Control		
FIX IN MEMORY	FIX\$	Fixes in memory (makes permanently resident) an inactive, installed task.
UNFIX	UFIX\$	Negates a FIX\$ directive and frees the memory allocated to the task.
DISABLE	DSBL\$	Rejects future attempts to execute or fix an indicated task using any of the following directives: RQST\$, EXEC\$, SCHD\$, RUN\$, SYNC\$, or FIX\$.
ENABLE	ENBL\$	Makes a specified disabled task capable of being run.
DISABLE CHECKPOINTING	DSCP\$	Disables the checkpointability of the issuing task.
ENABLE CHECKPOINTING	ENCP\$	Makes the issuing task checkpointable if checkpointing was previously disabled.
ALTER PRIORITY	ALTP\$	Alters the priority of the specified active task to the new priority indicated in the directive.
Informational Directives		
GET TIME PARAMETERS	GTIM\$	Fills an indicated 8-word buffer with current time and date information.
GET COMMON BLOCK PARAMETERS	GCOM\$	Fills an indicated 8-word buffer with information for a specific common block.
GET PARTITION PARAMETERS	GPRT\$	Fills an indicated 3-word buffer with information for a specific partition.
GET TASK PARAMETERS	GTSK\$	Fills an indicated 16-word buffer with information about the task issuing the directive.

# Table 3-1 (Cont.) RSX-11D Directives

DIRECTIVE	ASSOCIATED MACRO CALL	
GET SENSE SWITCHES	gssw\$	Obtains the status of the console sense switches and stores it in the issuing task's directive status word.
GET LUN	GLUN\$	Fills a 6-word buffer with information about a physical unit to which the task is assigned
Event-Associated Directives		
DECLARE SIGNIFICANT EVENT	DECL\$	Declares a significant event and, optionally, sets an event flag and reports the status of the flag before it was set. DECL\$ causes an ATL scan.
SET EVENT FLAG	SETF\$	Sets an indicated event flag and reports the status of the flag before it was set. SETF\$ does not cause a significant event.
CLEAR EVENT FLAG	CLEF\$	Clears an indicated event flag and reports the flag's status before clearing. CLEF\$ does not cause a significant event.
READ EVENT FLAG	RDE <b>F\$</b>	Reads a specified event flag and indicates by the return code in the directive status word whether the flag is set or cleared.
READ ALL FLAGS	RDAF\$	Reads all 64 event flags and records their status by setting or clearing corresponding bits in a 64-bit (4-word) buffer.
WAIT FOR SINGLE EVENT FLAG	<b>W</b> TSE\$	Suspends execution of the issuing task until the indicated event flag is set.
WAIT FOR LOGICAL OR OF FLAGS	WTLO\$	Suspends execution of the issuing task until any indicated event flag in one of five groups is set.
WAIT FOR SIGNIFICANT EVENT	WSIG\$	Suspends execution of the issuing task until the next significant event occurs.
EXIT IF	EXIF\$	Terminates the execution of the issuing task if an indicated event flag is not set.

# Table 3-1 (Cont.) RSX-11D Directives

DIRECTIVE	ASSOCIATED MACRO CALL	
Trap-Associated Directives		
MARK TIME	MRKT\$	Declares a significant event after an indicated time interval starting at issuance of the directive. An event flag also can be set when the significant event is declared.
CANCEL MARK TIME REQUESTS	CMKT\$	Cancels any mark time requests made by the issuing task.
INHIBIT AST RECOGNITION	IHAR\$	Inhibits recognition of asynchronous system traps for the issuing task.
ENABLE AST RECOGNITION	ENAR\$	Recognizes asynchronous system traps for the issuing task.
SPECIFY POWER Recovery Ast	SPRA\$	Tells the system whether or not power recovery AST's are desired for the issuing task. If desired, this directive indicates where control is to be transferred when the AST occurs.
SPECIFY FLOATING POINT EXCEPTION AST	sfpa\$	Tells the system whether or not PDP-11/45 floating point exception AST's are desired for the task. If desired, the directive indicates where control is to be transferred when the AST occurs.
SPECIFY RECEIVE AST	SRDA\$	Allows a task to determine whether another task has sent data to it without waiting for an event flag or continually checking the buffer. When data is detected, an AST is executed.
SPECIFY SST VECTOR TABLE FOR DEBUGGING AID	SVDB\$	Specifies the virtual address of a table of synchronous system trap service routine entry points for use by ODT or other debugging aids. SVDB\$ takes precedence over SVTK\$.
SPECIFY SST VECTOR TABLE FOR TASK	svtk\$	Specifies the virtual address of a table of synchronous system trap service routine entry points for use by the issuing task.
TERMINATE AST Execution	ASTX\$	Terminates execution of an asynchronous system trap service routine.

### Table 3-1 (Cont.) RSX-11D Directives

DIRECTIVE	ASSOCIATED MACRO CALL			
I/O Related Directives				
QUEUE I/O	Q10\$	Places an I/O request for an indicated device in a priority-ordered queue of requests for that unit.		
QUEUE I/O AND WAIT	QIOW\$	Performs the functions of both WTSE\$ (see above) and QIO\$.		
ASSIGN LUN	<b>ALUN</b> \$	Assigns a logical unit number (LUN) to a physical device unit.		
SEND DATA	VSDA\$	Queues a variable-length data block by priority for a task to receive.		
SEND DATA AND RESUME OR REQUEST RECEIVER	VSDR\$	Queues a variable-length data block by priority for a task to receive and resumes or requests execution of the receiving task.		
RECEIVE DATA	VRCD\$	Receives a variable-length data block that was queued by another task.		
RECEIVE DATA OR EXIT	VRCX\$	Receives a variable-length data block that has been queued by another task or exits if none is queued.		
RECEIVE DATA OR SUSPEND	VRCS \$	Receives a variable-length data block if one is queued for the task or suspends the task.		
GET MCR COMMAND LINE	GMCR\$	Transfers an 80-byte command line to the issuing task.		
NOTE				
For Information About: Refer To:				
SDATS	; ;	VSDA\$		
SDRQS	;	VSDR\$		
RCVD\$		VRCD\$		
RCVX	;	VRCX\$		

#### 3.6.1 SEND and RECEIVE Directives

The following additional information about SEND and RECEIVE directives should be noted.

- 1. Variable-length data up to 255 words can be sent and received.
- 2. If the receiver's buffer is too small to hold all the data sent, the excess is lost and the receiver is notified by an error code in the directive status word (DSW).
- 3. The TI of a task is the same as the TI of the terminal from which the task was initiated. If the task was initiated by another task, the TI of the requesting task becomes the TI of the requested task also.

Certain directives can be issued only to affect tasks with the same TI: RESUME, ABORT TASK, ALTER PRIORITY, and CANCEL SCHEDULED REQUEST.

- 4. Multiuser tasks issuing receive directives are passed only that data sent with the same TI as the multiuser task. This approach ensures the proper flow of data among several multiuser tasks and a single-user task when the single-user task is receiving from and sending to the multiuser tasks.
- 5. Up to three data words can be transmitted in a single 8-word node; the other five words are used for SEND/RECEIVE overhead.
- 6. The default buffer length (13 words) requires a 24-word node, rather than 16 words.

3.7 ABRT\$ (ABORT TASK)

The ABORT TASK directive terminates the execution of the indicated task. This directive is intended for use as an emergency exit or fault exit. It causes a printout at the console each time it is invoked. A task can abort any other task. If the task being aborted is a multiuser task, it is aborted only if its TI matches that of the task issuing the ABORT directive. ABORT TASK cannot be issued by a background task.

Macro Call: ABRT\$ task

task = Task name.

The following symbol is locally defined with its assigned value equal to the byte offset from the start of the DPB to the DPB element.

A.BTTN = (Length is 4 bytes) Task name

DSW return codes:

+1	 Successful completion
-02	 Task not installed
-07	 Task not active
-08	 Task loading or exiting
-10	 Task is not abortable
-80	 Directive issued by background task
-98	 Part of DPB is out of issuing task's address space
-99	 DIC or DPB size is invalid

Macro Expansion:

ABRTS YOUPS .BYTE 83.,5 .FAD50 /YOURS/

#### 3.8 ALTP\$ (ALTER PRIORITY)

The ALTER PRIORITY directive alters the priority of the specified active task to the new priority indicated in the directive. If the task is multiuser, its priority is altered only if its TI matches that of the calling task.

Macro Call: ALTP\$ task, pri

- task = the name of the task whose priority is to be changed. If task is not specified, the calling task's priority is changed to its default priority.
- pri = the new priority (1-250) for the task. If pri is not specified, the new priority is the priority specified at task build or installation. If a priority was not specified during task build or installation, a system default of 50 (decimal) is used.

The following symbols are defined locally with their assigned values equal to the byte offset of the DPB.

A.LTTN = task name (4 bytes)

A.LTPR = priority (2 bytes)

DSW return codes:

+1		Successful completion
-02		Task not installed
-07	-	Task not active
-08		Task in process of being loaded or exiting
-80		Directive issued by background task
-95		Invalid priority
-98		Part of DPB is out of issuing task's address space

```
ALTPR TASK10,100

.FYTE 9.,4

.SADSO /TASK10/

.FORD 100
```

## ALUN\$

### 3.9 ALUN\$ (ASSIGN LUN)

The ASSIGN LUN directive assigns a logical unit number (LUN) to a physical device unit.

Macro Call: ALUN\$ lun, devnam, devnum

lun = Logical unit number devnam = Physical device name (two characters) devnum = Physical device unit number

If devnam and devnum are omitted, the LUN is assigned as null. If it has previously been assigned, omitting devnam and devnum deassigns the LUN.

The following symbols are locally defined with their assigned values equal to the byte offset from the start of the DPB to the respective DPB elements.

A.LULU = (Length 2 bytes) logical unit number A.LUNA = (2) Physical device name A.LUNU = (2) Physical device unit number

DSW return codes:

+01	 Successful completion
-90	 LUN usage interlocked (via file open)
-92	 Invalid device and/or unit
-96	 Invalid logical unit number
-98	 Part of DPB is out of issuing task's address space
-99	 DIC or DPB size is invalid

### 3.10 ASTX\$ (AST SERVICE EXIT)

The AST SERVICE EXIT directive terminates execution of an asynchronous system trap service routine. ASTs are described in Section 4.2.

If another AST is queued, and ASTs are not inhibited, the next AST is executed. Otherwise, the task's pre-AST state is restored.

When an AST service routine is entered, the stack contains certain information. This information is required because the AST routine runs with the same ATL node, task header, and DSW as the main part of the task. Those portions of control areas that can be used to effect requests from AST service routines are saved on the stack. The following information is on the stack.

> SP+14 -- Event flag mask word for flags 1-16 SP+12 -- Event flag mask word for flags 17-32 SP+10 -- Event flag mask word for flags 33-48 SP+06 -- Event flag mask word for flags 49-64 SP+04 -- The pre-AST task's program status (PS) SP+02 -- The pre-AST task's program counter (PC) SP+00 -- The pre-AST directive status (virtual zero) Additional parameters, if any

The stack can contain additional information as well. For power recovery AST's, no information is added, but for I/O done the stack contains the address of the I/O status block; for MARK TIME, the stack contains the event flag number; and for an 11/45 floating point exception, the stack contains the exception code, and the exception address.

AST service routines must save and restore all registers used.

The AST service routine must remove any information on the stack that is additional to the seven words shown above before issuing an AST SERVICE EXIT directive. The following example shows how this is done when an AST routine is used on I/O completion:

;MAIN B START:	ODY OF PI	ROGRAM	; PROCESS
	QIO\$S	#IO.WVB	<pre>,#2,,,#IOSTBK,#ASTSER,&lt;#BUFFER,#60.,#40&gt; ;PROCESS &amp; WAIT</pre>
	EXIT\$S		;EXIT TO MONITOR
IOSTBK: BUFFER:		2 60.	
;AST SE ASTSER:			
	TST(SP)- ASTX\$S	+	;REMOVE ADDR OF I/O STATUS BLOCK ;AST EXIT MACRO

Macro Call: ASTX\$

DSW return codes:

+01	 Successful completion	
-80	 Directive not issued from an AST service routine	
-98	 Part of DPB or table is out of task's address space	
-99	 DIC or DPB size is invalid	

ASTXS	
.BYTE	115.,1

3.11 CLEF\$ (CLEAR EVENT FLAG)

The CLEAR EVENT FLAG directive clears an indicated event flag and reports the flag's polarity before clearing in the DSW. Clearing an event flag does not cause a significant event.

Macro Call: CLEF\$ efn

efn = Event flag number

The following symbol is locally defined with its assigned value equal to the byte offset from the start of the DPB to the DPB element.

C.LEEF = (Length 2 bytes) event flag number

DSW return codes:

+00 -- Flag was already clear +02 -- Flag was set -97 -- Event flag number <1 or >64 -98 -- Part of DPB is out of issuing task's address space -99 -- DIC or DPB size is invalid

Macro Expansion:

CLEFS 4 .5YTE 31.,2 .WORD 4

# CMKT\$

3.12 CMKT\$ (CANCEL MARK TIME REQUESTS)

The CANCEL MARK TIME REQUEST directive cancels MARK TIME requests that have been made by the issuing task. If no parameters are supplied with the macro call, all MARK TIME requests that have been made by the issuing task are cancelled. Parameters are specified to cancel only mark time requests that set an indicated event flag or cause an AST at an indicated location.

Macro Call: CMKT\$ efn,ast

efn = event flag number (0 implies no event flag)
ast = AST service routine entry address

The following symbols are locally defined with their assigned values equal to the byte offset from the start of the DPB to respective DPB elements.

> C.MKEF = (length 2 bytes) Event flag number C.MKAE = AST service routine entry address

DSW return codes:

+1 -- Successful completion -90 -- No EFN or AST entry specified -97 -- Invalid event flag number (event flag number <1 or >64) -98 -- Part of DPB is out of issuing task's address space -99 -- DIC or DPB size is invalid

Macro Expansion:

CMKTS 5,CMTAST .5YTE 27.,5 .word 5 .word cmtast 3.13 CSRQ\$ (CANCEL SCHEDULED REQUESTS)

The CANCEL SCHEDULED REQUESTS directive cancels scheduled requests for task executions. All requests to run a specified task can be cancelled, or only those issued for a specified task by another specified task. If the task to be cancelled is a multiuser task, it is cancelled only if its TI matches that of the scheduler. This directive cannot be issued by a background task.

Macro Call: CSRQ\$ ttask,rtask
ttask = Scheduled (requested) task name,
rtask = Scheduler (requester) task name.

The following symbols are locally defined with their assigned values equal to the byte offset from the start of the DPB to the respective DPB elements.

> C.SRTN = (length 4 bytes) target task name C.SRRN = (4) Requester task name

The issuing task is taken as the scheduler task when not specified.

DSW return codes:

+1 -- Successful completion -02 -- Task not installed -80 -- Directive issued by background task -98 -- Part of DPB is out of issuing task's address space -99 -- DIC or DPB size is invalid

Macro Expansion:

CSRDS T45K1,T45K2 .DYTE 25..5 .F4050 /T45K1/ .F4052 /T45K2/ DIR\$

3.14 DIR\$ (DIRECTIVE)

DIR\$ generates an RSX-11D Executive trap with a predefined DPB.

Macro Call: DIR\$ adr,err

Three forms are possible, with the following interpretation:

- DIR\$ assumes the address of the DPB or the DPB itself has already been pushed onto the stack.
- DIR\$ adr generates the code to push the parameter adr onto the stack.
- DIR\$ adr,err generates the code to push the parameter adr onto the stack, executes an EMT 377, generates a branch on carry clear to the address of the branch +4 (or +6 if necessary) and generates a JSR PC to the err address.

The argument adr is optional but, if present, must be a valid assembler source operand used to push the DPB address on the stack.

The argument err is optional. If defined, it must be a valid assembler destination operand to permit a jump to subroutine (JSR) instruction to an error handler if the directive is rejected.

n168	#Δ
MGV	#A,-(SP)
EMT	377

3.15 DECL\$ (DECLARE SIGNIFICANT EVENT)

The DECLARE SIGNIFICANT EVENT directive declares a significant event and, optionally, sets an event flag and reports its state before it was set. Declaring a significant event causes a scan of the active task list (ATL). The directive performs four functions:

- 1. Tests an event flag,
- 2. Sets the event flag,
- 3. Declares a significant event,
- 4. Reports the flag's polarity prior to being set in the DSW.
- Macro Call: DECL\$ efn
- efn = event flag number (an event flag number of 0 implies no event flag number)

The following symbol is locally defined with its assigned value equal to the byte offset from the start of the DPB to the DPB element.

D.CLEF = (Length 2 bytes) event flag number

DSW return codes:

+1	No event flag specified
+0	 Specified flag was cleared
+2	 Specified flag was set
-97	 Event flag number <1 or >64
-98	 Part of DPB is out of issuing task's address space
-99	 DIC or DPB size is invalid

05015	49
.2YTE	35,,2
.korn	40

## DSBL\$

3.16 DSBL\$ (DISABLE)

The DISABLE directive instructs the system to reject future attempts to run or fix an indicated task (REQUEST, EXECUTE, SCHEDULE, RUN, SYNCHRONIZE, and FIX-IN-MEMORY directives). It is used effectively to remove a task from a system without actually deleting its STD entry. DISABLE cannot be issued by a background task. If the task to be disabled is active, it is not disabled until it becomes inactive.

When a task is installed into a system, it is not disabled, i.e., it is runnable.

Macro Call: DSBL\$ task

task = Task name,

The following symbol is locally defined with its assigned value equal to the byte offset from the start of the DPB to the DPB element.

D.SBTN = (Length 4 bytes) task name

DSW return codes:

+1		Successful completion
-02		Task not installed
-08		Task is already disabled
-10	-	Task is not to be disabled
-80		Directive issued by a background task
-98		Part of DPB is out of issuing task's address space
-99		DIC or DPB size is invalid

Macro Expansion:

08468	DOLL
.BYTE	91.,5
.54050	100111

### 3.17 DSCP\$ (DISABLE CHECKPOINTING)

The DISABLE CHECKPOINTING directive disables the checkpointability of an issuing task that has been task built as being checkpointable. This directive cannot be issued by a background task.

When a checkpointable task's execution is started, checkpointing is not disabled, i.e., the task can be checkpointed.

#### Macro Call: DSCP\$

DSW return codes:

+1		Successful completion
-08		Task checkpointing already disabled
-10		Issuing task not installed as checkpointable
-80		Directive issued by a background task
-98	-	Part of DPB is out of issuing task's address space
-99	~-	DIC or DPB size is invalid

Macro Expansion:

NSCR9 .5715 95.,1

# ENAR\$

3.18 ENAR\$ (ENABLE AST RECOGNITION)

The ENABLE AST RECOGNITION directive allows recognition of asynchronous system traps for the issuing task, i.e., nullifies an INHIBIT AST RECOGNITION directive. AST's that have occurred while recognition was inhibited are effected at issuance. When a task's execution is started, AST recognition is not disabled. ASTs are described in Section 4.2.

AST service routines must save and restore all registers used.

Macro Call: ENAR\$

DSW return codes:

+1	 Successful completion
-08	 AST recognition not inhibited
-98	 Part of DPB is out of issuing task's address space
-99	 DIC or DPB size is invalid

Macro Expansion:

ENARS .BYTE 171..1

#### 3.19 ENBL\$ (ENABLE)

This directive instructs the system to make an indicated disabled task runnable, i.e., to nullify a DISABLE directive. ENABLE cannot be issued by a background task.

When a task is installed into a system, it is not disabled, i.e., it is runnable.

Macro Call: ENBL\$ task

task = Task name,

The following symbol is locally defined with its assigned value equal to the byte offset from the start of the DPB to the DPB element.

E.NBTN = (Length 4 bytes) Task name

DSW return codes:

+1 -- Successful completion
-02 -- Task not installed (No STD entry found)
-08 -- Task is not disabled
-80 -- Directive issued by a background task
-98 -- Part of DPB is out of issuing task's address space
-99 -- DIC or DPB size is invalid

Macro Expansion:

ENGLE TASKS2 "BYTE 93.,5 "RADS9 /TASK53/

# ENCP\$

#### 3.20 ENCP\$ (ENABLE CHECKPOINTING)

The ENABLE CHECKPOINTING directive makes the issuing task checkpointable after its checkpointability has been disabled, i.e., to nullify a DISABLE CHECKPOINTING directive. Checkpoint cannot be enabled for a task that was not built as being checkpointable.

Macro Call: ENCP\$

DSW return codes:

+1	 Successful completion
-08	 Checkpointing not disabled
-80	 Directive issued by background task
-98	 Part of DPB is out of issuing task's address space
-99	 DIC or DPB size is invalid

Macro Expansion:

#### 3.21 EXEC\$ (EXECUTE)

The EXECUTE directive activates a task only if the memory required for its execution is presently available. It cannot be issued by a background task.

Macro Call: EXEC\$ task,part,pri,ugc,upc

task = Task name, part = Partition name, pri = Priority, ugc = UIC group code, upc = UIC programmer code.

A partition cannot be specified for a multiuser task; i.e., the task must be requested to execute in its default partition.

The following symbols are locally defined with their assigned values equal to the byte offset from the start of the DPB to the respective DPB elements.

> E.XCTN = (length 4 bytes) task name E.XCPN = (4) Partition name E.XCPR = (2) Priority E.XCGC = (1) UIC group code E.XCPC = (1) UIC programmer code

DSW return codes:

+1 -- Successful completion -01 -- Insufficient pool nodes available to requester -02 -- Task not installed -03 -- No memory for execution -06 -- Handler task not resident to load task -07 -- Task is active -08 -- Task is disabled -80 -- Directive issued by background task -91 -- invalid UIC -94 -- Partition not in system -95 -- Invalid Priority specified (<0 or >250) -98 -- Part of DPB is out of issuing task's address space -99 -- DIC or DPB size is invalid

Macro Expansion:

EXECS MYTASK, PART, 30, 200, 200 .byte 13.,7 .fad50 /mytask/ .Rad50 /part/ .word 30 .fyte 200,200

# EXIF\$

3.22 EXIF\$ (EXITIF)

The EXITIF directive terminates the execution of the issuing task if an indicated event flag is NOT set. Control is returned if the specified event flag is set.

Macro Call: EXIF\$ efn

efn = event flag number

The following symbol is locally defined with its assigned value equal to the byte offset from the start of the DPB to the DPB element.

E.XFEF = (length 2 bytes) event flag number

DSW return codes:

+1 -- Indicated event flag cleared, task exited
+2 -- Indicated event flag set, task not exited
-97 -- No event flag specified in mask word(s), or invalid event flag number (event flag number <1 or >64)
-98 -- Part of DPB is out of issuing task's address space
-99 -- DIC or DPB size is invalid

EXIFS	50
BYTE	53.,2
.WORD	50

### 3.23 EXIT\$ (TASK EXIT)

The TASK EXIT directive terminates the execution of the issuing task.

Macro Call: EXIT\$

DSW return codes:

The DSW return codes can be tested only if the EXIT\$ directive fails.

+1	 Successful completion
-98	 Part of DPB is out of issuing task's address space
-99	 DIC or DPB size is invalid

Macro Expansion:

 3.24 FIX\$ (FIX IN MEMORY)

The FIX IN MEMORY directive fixes an inactive, installed task in memory. Once fixed in memory, the task cannot be checkpointed, and it does not relinquish its memory space until removed by the UNFIX directive.

The use of this directive is desirable when the timing of a task's execution is critical, or when it is requested very frequently. when fixed, a fresh copy of the task is not loaded for each request. FIX-IN-MEMORY may not be issued by a background task.

Macro Call: FIX\$ task

task = Task name,

The following symbol is locally defined with its assigned value equal to the byte offset from the start of the DPB to the DPB element.

F.IXTN = (length 4 bytes) task name

DSW return codes:

+1 -- Successful completion -01 -- Insufficient pool nodes available to requester -02 -- Task not installed -03 -- Partition too small for task -06 -- Handler task not resident to load task -07 -- Task is active -08 -- Task is disabled -09 -- Task is already fixed -10 -- Task not fixable -11 -- Task is checkpointable -80 -- Directive issued by background task -98 -- Part of DPB is out of issuing task's address space -99 -- DIC or DPB size is invalid.

FIXS	TASK2
.PYTE	A53
.PAD50	/TASK2/

### 3.25 GCOM\$ (GET COMMON BLOCK PARAMETERS)

The GET COMMON BLOCK PARAMETERS directive fills an indicated 8-word buffer with common block parameters.

The 8-word buffer is filled as follows:

Wd.	00		1/64th base address of common block
Wd.	01		1/64th size of common block
wd.	02		Creation year
Wd.	03		Creation month
Wd.	04		Creation day
Wd.	05		Owner identification code (UIC)
Wd.	06		Starting ASR (APR)
Wd.	07		Common block flags byte (low order) and status byte (high order)
Macro Call:		11:	GCOM\$ blknam,bufadr

blknam = Name of the common block bufadr = Adress of 8-word buffer

The following symbols are locally defined with their assigned value equal to the byte offset from the start of the DPB element.

G.COBN - (4) Common block name G.COBA - (2) Buffer address

The following offsets are assigned relative to the start of the common block parameters buffer.

C CORR -	(2)	1/64 common block base address
		1/64 common block base address
G.COBS -	(2)	1/64 common block size
G.COYR -	(2)	Creation year
G.COMO -	(2)	Creation month and day
G.COSA -	(2)	Status and starting APR
G.COUI -	(2)	Owner identification (UIC)
G.COTP -	(2)	TPD
G.COFW -	(2)	Common block flags byte address

The flags byte and the status byte are in the same word (G.COFW). The flags byte is in the low-order position and the status byte is in the high-order position.

The following bits are defined for the flags byte.

.

#### Bit

Meaning When Set

0	Library or common area loaded
1	1 = 1 ibrary, $0 = common area$
2	GCA is position independent
3	Nonowner has write access
4	Nonowner has read access

The following values are defined for the status byte.

Meaning

## Value

0	Global area not in use
•	
2	Load request queued
4	Load request succeeded
6	Load request failed
52	Record request queued
54	Record request succeeded
56	Record request failed

### DSW Status:

+1	 Successful completion
-02	 Indicated common block not in system
-98	 Part of DPB or buffer is out of task's address space
-99	 DIC or DPB size is invalid

# Macro Expansion:

SYSRES, COMBUE
67.,4
/SYSRES/
COMBUE

.

3.26 GLUN\$ (GET LUN INFORMATION)

The GET LUN INFORMATION directive fills a 6-word buffer with information about a physical unit to which the specified logical unit is assigned for the requesting task. If requests to the physical unit have been redirected to another unit, the information returned describes the effective assignment.

The 6-word buffer of LUN information comprises the first six words of the PUD. See Appendix A.

Macro Call: GLUN\$ lun, bufadr

lun = Logical unit number bufadr = Address of 6-word buffer which holds LUN information

The following symbols are locally defined with their assigned values equal to the byte offset from the start of the DPB to the respective DPB elements.

G.LULU - (length 2 bytes) Logical unit number G.LUBA - (2) Buffer address

DSW return codes:

+1	 Successful completion
-05	 Unassigned LUN
-96	 Invalid logical unit number
-98	 Part of DPB or buffer is out of task's address space
-99	 DIC or DPB size is invalid

JL INS	3,LUN30F
HYTE	5,5
. 4JRO	5
. ~ ) ? )	LUNBUR

### 3.27 GMCR\$ (GET MCR COMMAND LINE)

The GET MCR COMMAND LINE directive instructs the system to transfer a command line to the issuing task. The issuing task is an MCR function task requested by the MCR dispatcher task. The command line can be 1 through 80 bytes in length. It was placed in a list in SCOM by the MCR dispatcher. The GMCR\$ macro call should be issued as soon as possible by MCR function tasks (...xxx) in order to free pool space.

If the command line is terminated by a carriage return, a flag is set in the task's header to cause the MCR dispatcher task to be requested upon exit.

The command line is read into a buffer that is picked from the pool by the MCR dispatcher task. It is linked into the MCR queue or the batch queue by the batch processor.

The format of the buffer is a standard MCR buffer.

The DSW contains the character count for the command line excluding the carriage control characters if the directive executes successfully.

Macro Call: GMCR\$

The following symbol is locally defined with its assigned value equal to the byte offset from the start of the DPB to the respective DPB element.

G.MCRB = (Length 80 bytes) MCR line buffer

DSW return codes.

+nn -- Character count
-80 -- No buffer exists or illegal task name
-98 -- Part of DPB or buffer is out of issuing task's
address space
-99 -- DIC or DPB size is invalid

NOTE

No "\$S" macro form exists for this directive.

Macro Expansion:

GMCRS .3YTE 127..41. 3.28 GPRT\$ (GET PARTITION PARAMETERS)

The GET PARTITION PARAMETERS directive fills an indicated 3-word buffer with partition parameters. If a partition is not specified, the partition of the issuing task is assumed.

The 3-word buffer is filled as follows:

Wd. 00 -- 1/64th base address of partition
Wd. 01 -- 1/64th size of partition
Wd. 02 -- partition flags byte
Macro call: GPRT\$ prtnam,bufadr
prtnam = Partition name
bufadr = Address of buffer

The following symbols are locally defined with their assigned value equal to the byte offset from the start of the DPB to the DPB element.

G.PRPN - (4) Partition name G.PRBA - (2) Buffer address

The following offsets are assigned relative to the start of the partition parameters buffer:

G.PRPB - (2) 1/64 partition base address G.PRPS - (2) 1/64 partition size G.PRFW - (2) Partition flags byte

The following bits are defined for the flags byte.

Bit	Meaning When Set
0	User-controlled partition
1	Occupied user-controlled partition
2	System-controlled partition
3	Active system-controlled partition
•	

DSW:

+0	 Successful completion
-02	 Indicated partition not in system
-98	 Part of DPB or buffer is out of task's address
	space
-99	 DIC or DPB size is invalid

GPRTS	GENEL, PARBUE
BYTE	65.,4
.HADS0	/GENRL/
• × TRI)	PARBUE

# GSSW\$

### 3.29 GSSW\$ (GET SENSE SWITCHES)

This directive instructs the system to get the status of the console sense switches and store it in the issuing task's directive status word.

Macro call: GSSW\$

DSW Codes:

Successful completion is indicated if CC-C is clear. Switch values will be found in the DSW. Unsuccessful completion is indicated by CC-C set and one of the following codes in the DSW.

-98 -- Part of DPB is out of issuing task's address space -99 -- DIC or DPB size is invalid

Macro Expansion:

GSSWS .EYTE 125.,1 3.30 GTIM\$ (GET TIME PARAMETERS)

The GET TIME PARAMETERS directive fills an indicated 8-word buffer with current time and date parameters. All values are in binary. The 8-word buffer is filled as follows.

WD.	Q		Year (since 1900),
WD.	1		Month of year,
WD.	2		Day of month,
WD.	3		Hour of day,
WD.	4		Minute of hour,
WD.	5		Second of minute,
WD.	6		Tick of second,
WD.	7		Ticks per second (depends on frequency of clock).
Macr	o Ca	all:	GTIM\$ bufadr

bufadr = Address of 8-word buffer

The following symbol is locally defined with its assigned value equal to the byte offset from the start of the DPB to the DPB element.

G.TIBA = (length 2 bytes) Buffer address

The following offsets are assigned relative to the start of the time parameters buffer.

G.TIYR	-	(2)	Year
G.TIMO	=	(2)	Month
G.TIDA	Ħ	(2)	Day
G.TIHR	Ħ	(2)	Hour
G.TIMI	=	(2)	Minute
G.TISC	=	(2)	Second
G.TICT	=	(2)	Clock tick
G.TICP	-	(2)	Clock ticks per second

DSW return codes:

+1	 Successful completion
-98	 Part of DPB or buffer is out of task's address
	space
-99	 DIC or DPB size is invalid

Macro Expansion:

ç

GIIMS	TIMBUF
EYTE	61.,2
WORD	TIMBUE -

# **GTSK\$**

3.31 GTSK\$ (GET TASK PARAMETERS)

The GET TASK PARAMETERS directive fills an indicated 16-word buffer with parameters relating to the issuing task.

The 16-word buffer is filled as follows:

WD.	00	 Issuing task's name (first half),
WD.	01	
		Partition name (first half),
		Partition name (second half),
	•	node is accounted; usually task name of
		requester of issuing task (first half),
LID	0 E	
WD.	05	 
		accounted; usually task name of requester
		of issuing task (second half),
WD.	06	 Run priority
WD.	07	 User identification code (UIC for file
		control services)
WD.	10	 Number of logical I/O units (LUN's)
WD.	11	
-		
WD.		[Address of task SST vector tables],
-		[Size of task SST vector table (in words)],
-		 • • •
-		
WD.	17	 Zero (reserved)

Macro Call: GTSK\$ bufadr

bufadr = Address of a 16-word buffer.

The following symbol is locally defined with its assigned value equal to the byte offset from the start of the DPB to the DPB element.

G.TSBA = (length 2 bytes) Buffer Address,

The following offsets are assigned relative to the start of the task parameters buffer:

G.TSTN = (4) Task name, G.TSTN = (4) Partition name, G.TSPN = (4) Name of task's requester, G.TSPR = (2) Priority, G.TSGC = (1) UIC Group code G.TSPC = (1) UIC Programmer code G.TSNL = (2) Number of logical units, G.TSMT = (2) Machine type, G.TSFW = (2) STD flags word G.TSVA = (2) Task's SST vector address G.TSVL = (2) Task's SST vector (word) length

# The following bits are defined for the flags word.

Bit	Meaning When Set
0	STD is 24 words long rather than 16 words long
1	Task is fixed in memory
3	Task is disabled
4	Task is being fixed in memory
6	Task is multiuser
7	Task is privileged
8	Network attribute bit
9	Restricted usage level one (background batch jobs)
10	Restricted usage level two
11	Task cannot be aborted
12	Task cannot be disabled
13	Task cannot be fixed in memory
14	Task cannot be checkpointed

DSW:

-----

+01	 Successful completion
-98	 Part of DPB or buffer is out of task's address
	space
-99	 DIC or DPB size is invalid

Macro Expansion:

GISKS	TSKRUF
BYTE	63.,8
	TSKBUF

,

# **IHAR\$**

### 3.32 IHAR\$ (INHIBIT AST RECOGNITION)

The INHIBIT AST RECOGNITION directive inhibits recognition of asynchronous system traps for the issuing task. The AST's are queued as they occur and are effected when AST recognition is enabled. There is an implied AST inhibit whenever an AST service routine is executing. When a task's execution is started, AST recognition is not disabled. ASTs are described in Section 4.2.

It is the recognition only which is inhibited. The ASTs are still queued by the system. They are queued FIFO and occur in that order.

AST service routines must save and restore any registers used.

Macro Call: IHAR\$

DSW return codes:

+1 -- Successful completion -08 -- AST recognition already inhibited -98 -- Part of DPB is out of issuing task's address space -99 -- DIC or DPB size is invalid

Macro Expansion:

IHARS .BYTE 99.,1

### 3.33 MRKT\$ (MARK TIME)

The MARK TIME directive declares a significant event after an indicated time interval. The interval begins at issuance of the directive. If an event flag is specified, it is cleared at issuance and set at the time of the significant event. If an AST entry point is specified, an asynchronous system trap occurs at the time of the significant event. At the AST, the task's PS, PC, virtual zero (directive status), and the event flag mask are pushed onto the task's (user) stack. If neither an event flag number nor an AST service entry point is specified, the significant event still occurs after the indicated time interval. ASTs are described in Section 4.2.

Macro Call: MRKT\$ efn,timmag,timunit,ast

efn	8	Event flag number (0 implies no event flag)
timmag	#	Time interval magnitude; i.e., how many time interval units
timunit	=	Time interval unit 1 = clock ticks 2 = seconds 3 = minutes 4 = hours
ast	-	AST entry address

The following symbols are locally defined with their assigned values equal to the byte offset from the start of the DPB to the respective DPB elements.

M <b>.</b> KTEF	-	(Length 2 bytes) event flag
M. KTMG	-	(2) time magnitude
M.KTUN	=	(2) Time unit
M.KTAE	<b>2</b>	(2) AST entry address

DSW return codes:

+1	-	Successful completion	
-01		Unavailable pool node	
-93	-	Invalid time specified	
-97		Invalid event flag number (event flag number <1 or >64)	
-98		Part of DPB is out of issuing task's address space	
-99		DIC or DPB size is invalid	

MRKTS	4,20,2,MTAST
.BYTE	51.12
.woRD	4
.WORD	20
.W390	5
. HORD	MTAST

## **QIO\$**

3.34 QIO\$ (QUEUE I/O)

The QUEUE I/O directive places an I/O request for an indicated device in a queue of priority-ordered requests for that device unit. The device unit is specified as a logical unit number (LUN).

Normally, a significant event is declared by handler tasks (via system subroutines) upon I/O completion. If an event flag is specified, it is cleared when the request is queued, and set at the significant event. The I/O Status block is also cleared when the request is queued.

If an AST service entry point is specified, the AST occurs upon I/O completion with the task's PS, PC, virtual zero (directive status), and the address of the I/O status block is pushed onto the task's (user) stack. Refer to Section 4.2.

Macro Call: QIO\$ fnc,lun,efn,pri,iost,ast,prmlst

Three error indicators are used in conjunction with QIOS:

- 1. The C bit,
- 2. The directive status word,

3. The I/O status block.

The C bit is set to indicate that the format of the macro call was incorrect; i.e., it indicates that the Executive rejected the macro call because it resulted in an incorrect DPB. If the C bit is clear, format of the macro call is correct, but the I/O has not necessarily been successful.

The directive status word can be tested to determine whether the Executive successfully queued the I/O request. It is tested following the WTSE\$ macro call associated with the QIO\$. If the request was not queued, the directive status word contains one of the error codes listed below.

The I/O status block can be tested upon I/O completion to determine the success or failure of the actual transfer. The format of the I/O status block is illustrated below. I/O status error codes are provided in Appendix C. The first call of this macro defines the following symbols and assigns, as values, their byte offset from the beginning of the DPB.

(length 2 bytes) I/O function Q.IOFN = (2) Logical unit number Q.IOLU = Q.IOEF = (1) Event flag number (1) Priority Q.IOPR = Q.IOSB = (2) Address of I/O status block Q.IOAE (2) Address of I/O done AST entry point = Q.IOPL = (12) Parameter list (up to 6 words).

DSW return codes:

+1	 Successful completion
-01	 Unavailable pool node for request queue
-05	 Unassigned LUN
-06	 Handler task not resident
-95	 Invalid Priority (>250)
-96	 Invalid LUN
-97	 Invalid event flag number (<1 or >64)
-98	 Part of DPB is out of issuing task's address space
-99	 DIC or DPB size is invalid

Figure 3-1 illustrates the content of the I/O status block.

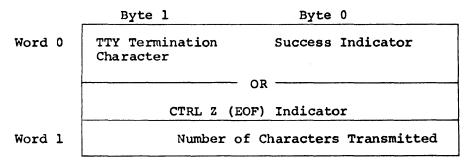


Figure 3-1 I/O Status Block

Word 0 can contain two 1-byte entries (TTY termination character and success indicator) or it can contain a -10 to indicate CTRL Z (EOF). If two entries are placed in word 0, the success indicator contains a 1 to indicate that the operation was successful or an error code, as described in Appendix C.

The TTY termination character is used by teletype handler tasks and similar handler tasks (e.g., DECwriter) and can be one of the following:

15(8) = CR,

33(8) = ALT,

0 = maximum number of characters received.

Word 1 contains the number of characters transmitted to or from the I/O device.

### Macro Expansion:

 QI0S
 IJ.WV8.3,5,50,IQSB.QIDAST,<BUF,SIZ,40>

 .3YTE
 1,333ARG

 .WORD
 ID.WV8

 .WORD
 3

 .BYTE
 3,50

 .WORD
 IDSB

 .WORD
 QIDAST

 .WORD
 SIZ

 .WORD
 40

.

3.35 QIOW\$ (QUEUE I/O AND WAIT)

The QUEUE I/O AND WAIT directive performs the functions of both QUEUE I/O and WAIT FOR SINGLE EVENT FLAG (WTSE\$). The format of the call and other related information is identical with that of QIO\$, with the following exception: if event flag 0 is specified, the queue I/O is performed, but the wait is not performed.

The user should check the C bit and the I/O status block immediately after the macro call.

Macro Call: QIOW\$

DSW return codes:

All those returned for QIO -97 -- Event flag >64

Macro Expansion:

TO.RLA.5, 3, 200, IOSB1, RDAST, <INBUF, SIZE> 31348 3, 85 SARG .BYTE .WORD IU.RLS .WORD 5 .HYTE 3.220 WIRD 10531 . WORD ROAST INBUF . 3020 SILE .WORD

3.36 RDAF\$ (READ ALL FLAGS)

The READ ALL FLAGS directive reads all 64 event flags for the issuing task and records their polarities in a 64-bit (4-word) buffer.

Macro Call: RDAF\$ bufadr

bufadr = Address of 4-word buffer.

The following symbol is locally defined with its assigned value equal to the byte offset from the start of the DPB to the DPB element.

R.DABA = (Length 2 bytes) buffer address

DSW return codes:

+1 -- Successful completion
-98 -- Part of DPB or buffer is out of issuing task's
address space
-99 -- DIC or DPB size is invalid

RDAFS	FLAGS
.EYTE	39.,2
- FORD	FLAGS

**RDEF\$** 

3.37 RDEF\$ (READ EVENT FLAG)

The READ EVENT FLAG directive tests an indicated event flag and reports its polarity in the DSW.

Macro Call: RDEF\$ efn

efn = Event flag number

The following symbol is locally defined with its assigned value equal to the byte offset from the start of the DPB to the DPB element.

R.DEEF = (Length 2 bytes) event flag number

DSW return codes:

+0 -- Flag was cleared +2 -- Flag was set -97 -- Event flag number <1 or >64 -98 -- Part of DPB is out of issuing task's address space -99 -- DIC or DPB size is invalid

Macro Expansion:

RDEFS 6 .BYTE 37...2 .WDRD 6

.

### 3.38 ROST\$ (REQUEST)

The REQUEST directive activates a task, i.e., enables and runs the task contingent upon priority and memory availability. If the task cannot be run immediately, the request is retained so that the task executes when conditions change, i.e., when more memory is available. It cannot be issued by a background task.

Macro call: RQST\$ task,part,pri,ugc,upc

task	= Task name,
part	= Partition name,
pri	= Priority
ugc	= UIC group code,
upc	= UIC programmer code.

A partition cannot be specified for a multiuser task; i.e., the task must be requested to execute in its default partition.

The following symbols are locally defined with their assigned values equal to the byte offset from the start of the DPB to the respective DPB elements.

> R.QSTN = (length 4 bytes) task name in radix 50 R.QSPN = (4) Partition name R.QSPR = (2) Priority R.QSGC = (1) UIC group code R.QSPC = (1) UIC programmer code

DSW return codes:

+1 -- Successful completion -01 -- Insufficient pool nodes available to requester -02 -- Task not installed -03 -- Partition too small for task -06 -- Handler task not resident to load task -07 -- Task is active -08 -- Task is disabled -80 -- Directive issued by background task -91 -- Invalid UIC -94 -- Partition not in system (no TPD entry found) -95 -- Invalid priority specified (<0 or >250) -98 -- Part of DPB is out of issuing task's address space -99 -- DIC or DPB size is invalid

Macro Expansion:

ROSTS MYTASK, GENRL, 5, 100, 50 .SYTE 11.,7 . FAD50 /MYTASK/ .RADSØ /GENRL/ . 0080 5 .EYTE 50,100

### 3.39 RSUM\$ (RESUME)

The RESUME directive instructs the system to resume the execution of a task that has issued a suspend directive. If the task being resumed is a multiuser task, it is resumed only if its TI matches that of the task issuing the RESUME directive. The RESUME directive cannot be issued by a background task.

### NOTE

It is possible for a task to RESUME itself using the asynchronous trap feature.

Macro Call: RSUM\$ task

task = Task name.

The following symbol is defined locally with its assigned value equal to the byte offset from the start of the DPB to the DPB element.

R.SUTN = (length 4 bytes) task name

DSW return codes:

+1	 Successful completion
-02	 Task not installed
-07	 Task not active
-08	 Task not suspended
-80	 Directive issued by background task
-98	 Part of DPB is out of issuing task's address space
-99	 DIC or DPB size is invalid

REUMS	TASKS
.BYTE	47.,3
.64050	1125551

### 3.40 RUN\$ (RUN)

The RUN directive causes a task to be requested at a specified future time, and optionally repeated periodically. The schedule time is specified in terms of delta time from issuance. The RUN directive cannot be issued by a background task.

Macro Call: RUN\$ tsk,prt,pri,ugc,upc,smg,snt,rmg,rnt

tsk = Task name, prt = Partition name, pri = Priority ugc = upc = smg = UIC group code UIC programmer code, Schedule delta magnitude i.e., how many schedule units, snt = Schedule delta unit, l= clock ticks 2 = seconds3= minutes 4 = hoursrmg = Reschedule interval magnitude, rnt = Reschedule interval unit

A partition cannot be specified for a multiuser task; i.e., the task must be requested to execute in its default partition.

The following symbols are locally defined with their assigned values equal to the byte offset from the start of the DPB to the respective DPB elements.

 $R_{\bullet}UNTN =$ (length 4 bytes) task name (4) Partition name R.UNPN = (2) Priority  $R_{\bullet}UNPR =$  $R_{\bullet}UNGC =$ (1) UIC group (1) UIC programmer  $R_{\bullet}UNPC =$ (2) Schedule magnitude  $R_{\bullet}UNSM =$ (2) Schedule unit R.UNSU = R.UNRM = (2) Reschedule magnitude R.UNRU = (2) Reschedule unit

DSW return codes:

+1 -- Successful completion -01 -- Insufficient pool nodes available to requester -02 -- Task not installed -03 -- Partition too small for task -80 -- Directive issued by background task -91 -- Invalid UIC -93 -- Invalid time parameter specified -94 -- Partition not in system -95 -- Invalid priority specified (<0 or >250) -98 -- Part of DPB is out of issuing task's address space -99 -- DIC or DPB size is invalid

RUNS	DOIT, MOSPAR, 25, 31, 40, 10, 3, 15, 3
BYTE	17.,11.
.RA050	/0011/
RAD50	/MOSPAR/
.W330	25
_BYTE	47,31
.*383	10
,WORD	3
. NORO	15
• M (1 R D	5

# SCHD\$

#### 3.41 SCHD\$ (SCHEDULE)

The SCHEDULE directive causes a task to be requested at a specified future time, and optionally, repeated periodically. The schedule time is specified in terms of absolute time-of-day. The SCHEDULE, RUN, and SYNC directives are the same, differing only in the form in which the schedule data is presented. SCHEDULE cannot be issued by a background task.

Macro Call: SCHD\$ tsk,prt,pri,ugc,upc,hrs,min,sec,tck,rmag,rnt

tsk = Task name, prt = Partition name, pri = Priority, ugc = UIC group code, upc = UIC programmer code, hrs = Schedule hours, min = Schedule minutes, sec = Schedule minutes, sec = Schedule seconds, tck = Schedule clock ticks, rmag = Reschedule interval magnitude (how many of the units defined by rnt), rnt = Reschedule interval unit. l = clock ticks 2 = seconds 3 = minutes 4 = hours

A partition cannot be specified for a multiuser task; i.e., the task must be requested to execute in its default partition.

The following symbols are locally defined with their assigned values equal to the byte offset from the start of the DPB to the respective DPB elements.

> S.CHTN = (length 4 bytes) Task name  $S_{CHPN} =$ (4) Partition name  $S_CHPR =$ (2) Priority  $S_CHGC =$ (1) UIC group UIC programmer
>  Hours
>  Minutes
>  Seconds S.CHPC =S.CHHR =S.CHMI =S.CHSC = (2) Clock ticks(2) Reschedule magnitude(2) Reschedule unit S.CHCT = S.CHRM =S.CHRU =

DSW return codes:

+1 -- Successful completion -01 -- Insufficient pool nodes available to requester -02 -- Task not installed -03 -- Partition too small for task -80 -- Directive issued by background task -91 -- Invalid UIC -93 -- Invalid time parameter specified -94 -- Partition not in system -95 -- Invalid priority specified (<0 or >250) -98 -- Part of DPB is out of issuing task's address space -99 -- DIC or DPB size is invalid

PODL, CORE, 200, 40, 40, 10, 30,00,00, 5, 3
15,,13,
/P00L/
/CORE/
298
40.47
1.0
30
62
79
5
3

# SETF\$

3.42 SETF\$ (SET EVENT FLAG)

The SET EVENT FLAG directive sets an indicated event flag and reports the flag's polarity before setting in the DSW. Setting an event flag does not cause an ATL scan.

Macro Call: SETF\$ efn

efn = event flag number

The following symbol is locally defined with its assigned value equal to the byte offset from the start of the DPB to the DPB element.

S.ETEF = (Length 2 bytes) event flag number

DSW:

+0	 Flag was cleared
+2	 Flag was set
-97	 Event flag number <1 or >64
- 98	 Part of DPB is out of issuing task's address space
-99	 DIC or DPB size is invalid

### NOTE

It is recommended that the user avoid specifying event flag numbers from 59 to 64, which are reserved for RSX-11D.

SETFS	33
.BYTE	33.,2
.XORD	33

### 3.43 SFPA\$ (SPECIFY FLOATING POINT EXCEPTION AST)

The SPECIFY FLOATING POINT EXCEPTION AST directive instructs the system to record one of the following two items:

- 1. That floating point exception ASTs for the issuing task are desired and where control is to be transferred when a floating point exception AST occurs,
- 2. The floating point exception ASTs for the issuing task are no longer desired.

When an AST service entry point is specified, future floating point exception ASTs occur for the issuing task, and control is transferred to the indicated location whenever a floating point exception AST occurs.

When an AST service entry point is not specified (zero in the second DPB word), future floating point exception ASTs do not occur until an AST entry point is specified again. ASTs are described in Section 4.2.

If SFPA\$ is not used, no AST is queued.

AST service routines must save and restore all registers used.

Macro Call: SFPA\$ ast

ast = AST service entry address.

The following symbol is locally defined with its assigned value equal to the byte offset from the start of the DPB to the DPB element.

S.FPAE = (Length 2 bytes) AST entry address

DSW return codes:

+1	 Sucessful completion
-01	 No pool node available
-08	 AST entry already un-specified
-80	 Directive issued during AST service
-98	 Part of DPB is out of issuing task's address space
-99	 DIC or DPB size is invalid

NOTE

This directive applies only for PDP-11/45. It has no effect on PDP-11/40. For the PDP-11/40, refer to SVTK\$.

5	F	2	A	5	i i	F	P	4	5	٢		
				E		1	1	1		,	2	
	•	ð	R	9	1	F	p	4	S	T		

# SPND\$

## 3.44 SPND\$ (SUSPEND)

The SUSPEND directive suspends the execution of the issuing task. A task can suspend only itself and not another task. The SUSPEND directive cannot be issued by a background task.

Macro Call: SPND\$

DSW return codes:

+2	 Successful completion
-80	 Directive issued by background task
-98	 Part of DPB is out of issuing task's address space
-99	 DIC or DPB size is invalid

Macro Expansion:

SPNDS BYTE 45.1

#### 3.45 SPRA\$ (SPECIFY POWER RECOVERY AST)

The SPECIFY POWER RECOVERY AST directive instructs the system to record either of the following:

- 1. That power recovery AST's for the issuing task are desired, and where control is to be transferred when a power recovery AST occurs, or
- 2. That power recovery AST's for the issuing task are no longer desired.

When an AST service routine entry point is specified, future power recovery ASTs occur for the issuing task, and control is transferred to the indicated location whenever a power recovery occurs. When an AST service entry point is not specified (zero in the second DPB word), future power recovery ASTs do not occur until an AST entry point is specified again. ASTs are described in Section 4.2.

If SPRA\$ is not used, no AST is queued.

AST service routines must save and restore all registers used.

Macro Call: SPRA\$ ast

ast = AST service routine entry address or zeros if none is desired.

The following symbol is locally defined with its assigned value equal to the offset from the start of the DPB to the DPB element.

S.PRAE = (Length 2 bytes) AST entry address

DSW return codes:

+1		Successful completion
-01		No pool node available
-08	<b></b> `	AST entry already unspecified
-80		Directive issued during AST service
-98		Part of DPB is out of issuing task's address space
-99		DIC or DPB size is invalid

SPZAS	PWRAST
.SYTE	109.,2
.WORD	PHRAST

# SRDA\$

#### 3.46 SRDA\$ (SPECIFY RECEIVE AST)

The SPECIFY RECEIVE AST directive allows a task to determine whether another task has sent data to it. When the sending of data is detected, the AST is executed. Use of this directive eliminates the need for the receiving task to check a buffer constantly for the presence of data. ASTs are described in Section 4.2.

- Macro Call: SRDA\$ ast
- ast = AST service entry point. If ast is specified, receive occurs for the issuing task and control is transferred to the entry point. If ast is not specified, ASTs do not occur.

The following symbol is defined locally with its assigned value equal to the byte offset from the start of the DPB.

S.RDAE = AST entry address (two bytes)

DSW return codes:

+1	 Successful completion
-01	 No pool node available
-08	 AST entry already unspecified
-80	 Directive issued during AST service
-98	 Part of DPB is out of issuing task's address space
-99	 DIC or DPB size is invalid

AST service routines must save and restore all registers used.

SROAS	RCVAST
BYTE	1072
.WDR0	PCVAST

### 3.47 SVDB\$ (SPECIFY SST VECTOR TABLE FOR DEBUGGING AID)

The SPECIFY SST VECTOR TABLE FOR DEBUGGING AID directive specifies the virtual address of a table of synchronous system trap service routine entry points for use by an intratask debugging aid (e.g., ODT). SSTS are described in Section 4.2.

When both the issuing task table and the ODT exist and both contain an entry for a particular trap, the ODT table takes precedence.

The table can contain up to eight entry points. Each entry point corresponds to a type of error that could occur or an instruction. The table is in the following format.

00 -- Odd address error or other trap through 4 WD. 01 -- Segment fault WD. 02 -- T-bit trap or execution of a BPT instruction WD. WD. 03 -- Execution of an 'IOT' instruction WD. 04 -- Execution of a reserved instruction WD. 05 -- Execution of non-RSX EMT. WD. 06 -- Execution of a trap instruction, WD. 07 -- PDP-11/40 floating point exception

If the table does not exist and one of the errors or instructions listed in the table above occurs, the task is aborted. Likewise, the task is aborted if the table exists but does not contain an entry point that corresponds to a particular error or instruction and that error or instruction occurs.

The table does not exist if SVDB\$ was not specified or if the macro call contained incorrect parameters.

SST service routines must save and restore all registers used.

Macro Call: SVDB\$ adr, len adr = Address of SST vector table len = Number of entries in table

The following symbols are locally defined with their assigned values equal to the byte offset from the start of the DPB to the respective DPB elements.

> S.VDTA = (Length 2 bytes) table address S.VDTL = (2) Table length

DSW return codes:

+01	 Successful completion
-98	 Part of DPB or table is out of task's address
	<pre>space, or table address not specified (zero address)</pre>
-99	 DIC or DPB size is invalid

Macro Expansion:

SV )33 SS113L,4 .AYTE 175..3 .4089 SSITAL . 4020 1

#### 3.48 SVTK\$ (SPECIFY SST VECTOR TABLE FOR TASK)

The SPECIFY SST VECTOR TABLE FOR TASK directive specifies the virtual address of a table of synchronous system trap service routine entry points for use by the issuing task. When both the issuing task table and the ODT table exist and both contain an entry for a particular trap, the ODT table takes precedence. SSTs are described in Section 4.2.

The table can contain up to eight entry points. Each entry point corresponds to a type of error that could occur or an instruction. The table is in the following format.

WD.00 -- Odd address error,
WD.01 -- Segment fault
WD.02 -- T-Bit trap or execution of a BPT instruction
WD.03 -- Execution of an IOT instruction
WD.04 -- Execution of a reserved instruction
WD.05 -- Execution of non-RSX EMT,
WD.06 -- Execution of a trap instruction
WD.07 -- PDP-11/40 floating point exception.

If the table does not exist and one of the errors or instructions listed in the table above occurs, the task is aborted. Likewise, the task is aborted if the table exists but does not contain an entry point that corresponds to a particular error or instruction and that error or instruction occurs.

The table does not exist if SVTK\$ was not specified or if the macro call contained incorrect parameters.

SST service routines must save and restore all registers used.

Macro Call: SVTK\$ adr, len

adr = Address of SST vector table len = Length of (number of entries in) table

The following symbols are locally defined with their assigned values equal to the byte offset from the start of the DPB to the respective DPB elements.

S.VTTA = (Length 2 bytes) table address S.VTTL = (2) Table length

DSW return codes:

+1	 Successful completion
-98	 Part of DPB or table is out of task's address space, or table address is not specified (zero
	address)
-99	 DIC or DPB size is invalid

SVTKS	SST14L.2
.BYTE	105.,3
,WORD	SSTTAL
. 9080	2

#### 3.49 SYNC\$ (SYNCHRONIZE)

The SYNCHRONIZE directive requests a task at a specified future time, and optionally, repeats it periodically. The schedule time is specified in terms of delta time from clock unit synchronization. Clock unit synchronization is specified for a future time. SYNCHRONIZE differs from RUN in that RUN requests task execution for delta time from the present time, not delta time from a future time. The SYNCHRONIZE directive cannot be issued by a background task.

#### Macro Call: SYNC\$ tsk,prt,pri,ugc,upc,smg,snt,sync,rmg,rnt

tsk = Task name, prt = Partition name, pri = Priority, ugc = UIC group code, upc = UIC programmer code, smg = Schedule delta magnitude; i.e., how many delta units snt = Schedule delta unit, 1 = clock ticks 2 = seconds 3 = minutes 4 = hours sync = Synchronization unit, rmg = Schedule interval magnitude, rnt = Schedule interval unit.

A partition cannot be specified for a multiuser task; i.e., the task must be requested to execute in its default partition.

The following symbols are locally defined with their assigned values equal to the byte offset from the start of the DPB to the respective DPB elements.

S.YNTN = (length 4 bytes) task name S.YNPN = (4) Partition name S.YNPR = (2) Priority S.YNGC = (1) UIC group code S.YNPC = (1) UIC programmer code S.YNSM = (2) Schedule magnitude S.YNSU = (2) Schedule unit S.YNSY = (2) Synchronization S.YNRM = (2) Reschedule magnitude S.YNRU = (2) Reschedule unit

DSW return codes:

+1	 Successful completion
-01	 Insufficient pool nodes available to requester
-02	 Task not installed
-03	 Partition too small for task
-80	 Directive issued by background task
-91	 Invalid UIC
-93	 Invalid time parameter specified
-94	 Partition not in system
-95	 Invalid priority specified (<0 or >250)
-98	 Part of DPB is out of issuing task's address space
-99	 DIC OR DPB size is invalid

### Macro Expansion:

 SYNCS
 THIS,GENRL,55,100,100,6,2,3,10,2

 HYTE
 19.,12.

 RAD50
 /THIS/

 RAD50
 /GENRL/

 WORD
 55

 HYTE
 100,100

 WORD
 6

 WORD
 2

 WORD
 3

 WORD
 10

 WORD
 2

#### 3.50 UFIX\$ (UNFIX)

The UNFIX directive negates a FIX directive that has been issued to make a task permanently memory-resident. The task specified in the UNFIX directive must have the same TI as the issuing task. The task then can be removed from memory. UNFIX cannot be issued by a background task.

Macro Call: UFIX\$ task

task = Task name,

The following symbol is locally defined with its assigned value equal to the byte offset from the start of the DPB to the DPB element.

U.FXTN = (length 4 bytes) task name

DSW return codes:

+1 -- Successful completion -02 -- Task not installed -09 -- Task is already unfixed -80 -- Directive issued by background task -98 -- Part of DPB is out of issuing task's address space -99 -- DIC or DPB size is invalid

UFTXS	POOL
PATE	87.,5
.84050	/POGL/

### 3.51 VRCD\$ (RECEIVE DATA)

The RECEIVE DATA directive receives a variable-length data block that has been queued for it according to priority. The SEND DATA or SEND DATA AND RESUME OR REQUEST RECEIVER directives queue data for a receiver.

When a sender task is specified, only data sent by the indicated task is received.

If the buffer size is not specified, a default size of 13 words is used. A 2-word sending task name and the data block are placed in the indicated buffer. The task name is in the first two words. The buffer length should not include these two words.

If the location to store the TI is specified, the TI indicator is transferred from the SEND/RECEIVE node to this location.

If the receiver task is multiuser, only data with the same TI assignment is received.

Data is transferred from the sending task to the receiving task by means of nodes picked from the pool. The number of nodes picked is charged to the sender. When the data is received, the nodes are returned and subtracted from sender's usage count.

Macro call: VRCD\$ task, bufadr, buflen, ti

task = sender task name bufadr = buffer address buflen = buffer length ti = address to store TI

The following symbols are locally defined with their assigned values equal to the byte offset from the start of the DPB to the respective DPB elements.

R.VDTN = (length 4 bytes) task name R.VDBA = (2) buffer address R.VDBL = (2) buffer length R.VDTI = (2) address in which to store TI

Condition codes:

CC-C - cleared to indicate successful completion. CC-C - set (with CC-V unaltered) if rejection occurs. CC-V - set if sender task is privileged

### DSW return codes:

\_\_\_\_

+01	successful completion
-02	sender task not installed
-08	no data queued (sent)
-15	receiver's buffer too small, data truncated
-89	invalid buffer size (>255)
-98	part of DPB or buffer is out of task's address
-99	space DIC or DPB size is invalid.

An alternate macro call is RCVD\$ which receives a 13-word data block.

Macro call: RCVD\$ task, bufadr

Macro Expansion of VRCD\$:

VRCDS OTHER, DATAIN, 25., TIADDR .8YTE 75., 585T9 .RADSO /OTHER/ .WORD DATAIN .1IF GE 565T9-5, WORD 25. .TIF EQ 55ST9-6, WORD TIADDR

### 3.52 VRCS\$ (RECEIVE DATA OR SUSPEND)

The RECEIVE DATA OR SUSPEND directive receives a variable-length data block that has been queued according to priority for it or suspends if no data blocks can be received. The SEND DATA and SEND DATA AND RESUME OR REQUEST RECEIVER are used to queue data for a receiver. This directive cannot be issued by background task.

When a sender task is specified, only data sent by the indicated task is received.

If the buffer size is not specified, a default size of 13 words is used. A 2-word sending task name and the data block are returned in the indicated buffer. The task name is in the first two words. The buffer length should not include these two words.

If the location at which to store TI is specified, the TI indicator is transferred from the SEND/RECEIVE node to this specified location.

If the receiving task is multiuser, only data with the same TI assignment is received.

Data is transferred from the sending task to the receiving task by means of nodes picked from the pool. The number of nodes picked is charged to the sender. When the data is received, the nodes are returned and subtracted from sender's usage count.

Macro call: VRCS\$ task, bufadr, buflen, ti

task = sender task name bufadr = buffer address buflen = buffer length ti = address in which to store TI

The following symbols are defined locally with their assigned values equal to the byte offset from the start of the DPB to the respective DPB elements.

R.VSTN = (length 4 bytes) task name R.VSBA = (2) buffer address R.VSBL = (2) buffer length R.VSTI = (2) address in which to store TI

Condition codes:

CC-C - cleared to indicate successful completion. CC-C - set (with CC-V unaltered) if rejection occurs. CC-V - set if sender task is privileged

DSW return codes:

+01	successful completion
-02	sender task not installed
-15	receiver's buffer too small, data truncated
-80	directive issued by background task
-89	invalid buffer size (>255).
-98	part of DPB or buffer is out of task's address space
-99	DIC or DPB size is invalid.

An alternate macro call is RCVS\$ which receives a 13-word data block.

Macro call: RCVS\$ task, bufadr

Macro Expansion of VRCS\$:

VRCSS TASK2, UATAIN, 10,, TIADDH .SYTE 79., \$\$\$T9 .RAD52 /TASK2/ .WORD DATAIN .LIF GE \$3\$T9=5, WOPD 10, .IIF EN 355T9=6, WORD TIADDR

# **VRCX\$**

### 3.53 VRCX\$ (RECEIVE DATA OR EXIT)

The RECEIVE DATA OR EXIT directive receives a variable-length data block that has been queued for it according to priority or exits if no data block can be received. The SEND DATA and SEND DATA AND RESUME OR REQUEST RECEIVER directives queue data for a receiver.

When a sender is specified, only data sent by the indicated task is received.

If the buffer size is not specified, a default size of 13 words is used. A 2-word sending task name and the data block are returned in the indicated buffer. The task name is in the first two words. The buffer length should not include these two words.

If the location to store TI is specified, the TI indicator is transferred from the SEND/RECEIVE node to this specified location.

If the receiving task is multiuser, only data with the same TI assignment is received.

Data is transferred from the sending task to the receiving task by means of nodes picked from the pool. The number of nodes picked is charged to the sender. When the data is received, the nodes are returned and subtracted from sender's usage count.

Macro call: VRCX\$ task, bufadr, buflen, ti

task = sender task name bufadr = buffer address buflen = buffer length ti = address at which to store TI

The following symbols are locally defined with their assigned values equal to the byte offset from the start of the DPB to the respective DPB elements.

R.VXTN = (length 4 bytes) task name
R.VXBA = (2) buffer address
R.VXBL = (2) buffer length
R.VXTI = (2) address in which to store TI

Condition codes:

CC-C - cleared to indicate successful completion. CC-V - set (with CC-V unaltered) if rejection occurs. CC-C - set if sender task is privileged

DSW return codes:

+01	successful completion
-02	sender task not installed
-15	receiver's buffer too small, data truncated
-89	invalid buffer size (>255)
-98	part of DPB or buffer is out of task's address
	space
-99	DIC or DPB size is invalid.

An alternate macro call is RCVX\$ which receives a 13-word data block.

Macro call: RCVX\$ task, bufadr

## Macro Expansion of VRCX\$:

VRCXS TASK9, HUFIN, B., TIADDR BYTE 77., S5ST9 RAD50 /TASK9/ WORD BUFIN ITF GE S3ST9-5, WORD B. ITF EQ S3ST9-5, WORD FIADDR

# VSDA\$

#### 3.54 VSDA\$ (SEND DATA)

The SEND DATA directive queues a variable-length data block according to priority for a task to receive.

If the buffer size is not specified, a default of 13 words is used.

When an event flag is specified, a significant event is declared if the directive is performed; the indicated event flag is set. Normally, the event flag is used to trigger the receiver into some action. The user must distinguish between the task's event flags (1 through 32) and the systems's event flags (33 through 64). To be effective, the task must set a system event flag.

If no priority is specified, the priority of the sending task is used.

If a TI is specified for the receiving task, the specified TI is inserted in the SEND/RECEIVE node; an AST is declared if an active task with the same TI is found. If no TI is specified, the sender's TI is inserted in the SEND/RECEIVE node; an AST is declared if the task is active regardless of its TI.

Data is transferred from the sending task to the receiving task by means of nodes picked from the pool. The number of nodes picked is charged to the sender. When the data is received, the nodes are returned and subtracted from sender's usage count.

Macro Call: VSDA\$ task, bufadr, buflen, efn, sndpri, ti

task = receiver task name bufadr = address of data block buflen = length of buffer (1 through 255 words) efn = event flag number (0 implies no event flag) sndpri = priority of send (1 through 250) ti = TI indicator

The following symbols are defined locally with their assigned values equal to the byte offset from the start of the DPB to the respective DPB elements.

S.DATN = (length 4 bytes) task name S.DABA = (2) buffer address S.DAEF = (2) event flag number S.DABL = (2) buffer length S.DASP = (2) send priority S.DATI = (2) TI indicator DSW return codes:

+01 -- successful completion -02 -- receiver task not installed -04 -- insufficient pool nodes for SEND -88 -- invalid TI -89 -- invalid buffer size (size > 255) -97 -- invalid event flag number (event flag number < 0 or > 64) -98 -- part of DPB or data block is out of task's address space -99 -- DIC or DPB size is invalid

An alternate macro call is SDAT\$ which sends a 13-word data block.

Macro call: SDAT\$ task, bufadr, efn

Macro Expansion of VSDA\$:

V3045 TA3(4,0ATA,24,36,200,TIADDR .3YTE 71.,355T9 .34050 /TA3(4/ .4020 0ATA .4020 36 .ITF GE 355T9-6, .4020 24 .ITF GE 535T9-7, .4020 240 .ITF EQ 355T9-8, .4020 TIADDR

#### 3.55 VSDR\$ (SEND DATA AND RESUME OR REQUEST RECEIVER)

The SEND DATA AND RESUME OR REQUEST RECEIVER queues a variable-length data block according to priority for a task to receive and to request or resume the execution of the receiver. This directive cannot be issued by a background task.

This directive has the effect of issuing a SEND DATA directive followed by a REQUEST directive with the following exceptions:

- 1. No task switching occurs between the SEND and the RECEIVE or RESUME,
- 2. If the SEND is not performed, neither the RESUME nor the REQUEST is attempted.

If the buffer size is not specified, a default buffer size of 13 words is used.

When an event flag is specified, a significant event is declared if the directive is performed; the indicated event flag is set. The event flag is used commonly to trigger the receiver into some action. The user must distinguish between task event flags (1 through 32) and system event flags (33 through 64).

To be effective, the task must set a system event flag. If the send priority is not specified, the priority of the sender is used to insert the SEND/RECEIVE node into the receiver's queue.

If a TI is specified for the receiving task, the specified TI is inserted in the SEND/RECEIVE node. An AST is declared if an active task is found with the same TI and the task has specified RECEIVE AST. If no TI is specified, the TI of the sending task is inserted in the SEND/RECEIVE node; an AST is declared if the task is active regardless of its TI.

Data is transferred from the sending task to the receiving task by means of nodes picked from the pool. The number of nodes picked is charged to the sender. When the data is received, the nodes are returned and subtracted from sender's usage count.

Macro call: VSDR\$ task,part,pri,ugc,upc,bufadr,buflen,efn,sndpri,ti

task = receiver task name = partition part = priority pri = UIC group code = UIC programmer code uge upc bufadr = address of data block buflen = length of data block in words = event flag number (0 implies no event flag) efn sndpri = priority of send = TI indicator ti

The following symbols are locally defined with their assigned values equal to the byte offset for the start of the DPB to the respective DPB elements.

S.DRTN = (length 4 bytes) task name) S.DRPN = (4) partition name S.DRPR = (2) request priority S.DRGC = (1) UIC group S.DRPC = (1) UIC programmer S.DRBA = (2) buffer address S.DREF = (2) event flag S.DRBL = (2) buffer length S.DRSP = (2) send priority S.DRTI = (2) TI indicator

DSW return codes:

In the following code descriptions, R indicates that the REQUEST or RESUME was rejected, and B indicates that both the SEND and REQUEST or RESUME were rejected.

+01 - data sent and task requested +02 - data sent and task resumed +03 - data sent to a non-suspended task -01 - [R] insufficient pool nodes available for REQUEST -02 - [B] receiver task not installed -03 - [R] partition too small for receiver task -04 - [B] insufficient pool nodes for SEND -06 - [R] handler task not resident to load task -08 - [R] receiver task is disabled -80 - [B] directive issued by background task -88 - [B] invalid TI indicator -89 - [B] invalid buffer size (>255) -91 - [R] invalid uic -94 - [R] partition not in system -95 - [R] invalid priority specified (<0 or >250) -97 - [B] invalid event flag number (efn<0 or >64) -98 - [B] part of DPB or data block is out of the task's address space -99 - [B] DIC or DPB size is invalid.

NOTE

The SEND portion of this directive can complete and the REQUEST portion fail.

An alternate macro call is SDRQ\$ which sends a 13-word data block.

Macro call: SDRQ\$ task,part,pri,ugc,upc,bufadr,efn

### Macro Expansion of VSDR\$:

```
15)25
         YOJ, PART. 40, 200, 200, MYDATA, 30, 60, 30, TIADDR
BYTE
         73., $5519
-RAD50 /YOU/
.IIF LT $5$71-4,
                           .WORD
                                     Ø
RADSO /PART/
.HORD
BYTE
         40
         237,203
. #180
         14 YOATA
. אספא
         51
ITF
         SE
                  $$$T9=10., HORD 30
                  SSST9-11., NORD 30
SSST9-12., NORD FIADUR
,1(F
         GF
,ITF
        E J
```

3.56 WSIG\$ (WAIT FOR SIGNIFICANT EVENT)

The WAIT FOR SIGNIFICANT EVENT directive suspends execution of the issuing task until the next significant event occurs. It is an especially effective way to suspend a task which cannot continue because of a lack of pool nodes.

Macro Call: WSIG\$

DSW return codes:

+1 -- Successful completion

Macro Expansion:

WSTGR EYTE 49.,1

# WTLO\$

3.57 WTLO\$ (WAIT FOR LOGICAL OR OF FLAGS)

The WAIT FOR LOGICAL OR OF FLAGS directive suspends the execution of the issuing task until any indicated event flag of one of the following groups of event flags is set.

GR 0 -- FLAGS 1-16 GR 1 -- FLAGS 17-32 GR 2 -- FLAGS 33-48 GR 3 -- FLAGS 49-64 GR 4 -- FLAGS 1-64

If the indicated condition is met at issuance, task execution is effectively not suspended.

Mask word bits from right-to-left represent increasing event flag numbers. A set mask word bit indicates that the task is to wait for the corresponding event flag.

> > If zero is specified in the \$S form of the macro, do not use a number sign (#) preceding it.

Example: The following macro is used to wait for flag 19, or flag 20, or flag 21, or flag 32.

WTLO\$ 1,100034

Example: This macro waits for 1, 19, 20, 21, 32 or 64.

WTLO\$ 4,<000001,100034,0,100000>

DSW return codes:

+1	 Successful completion
-97	 No event flag specified in mask word(s)
	or; Flag set indicator other than 0,1,2,3, or 4
-98	 Part of DPB is out of issuing task's address space
-99	 DIC or DPB size is invalid

Macro Expansion:

WTLOS 4,<1,500,2,343> .EYTE 43.,5 .WORD 1 .WORD 502 .WOPD 2 .WORD 343 3.58 WTSE\$ (WAIT FOR SINGLE EVENT FLAG)

The WAIT FOR SINGLE EVENT FLAG directive suspends the execution of the issuing task until an indicated event flag is set. If the flag is set at issuance, task execution is effectively not suspended.

Macro Call: WTSE\$ efn

efn = event flag number

The following symbol is locally defined with its assigned value equal to the byte offset from the start of the DPB to the DPB element.

W.TSEF = (Length 2 bytes) event flag number

DSW return codes

+1 -- Successful completion
-97 -- Invalid event flag number (<1 or >64)
-98 -- Part of DPB is out of issuing task's address space
-99 -- DIC or DPB size is invalid

۰,	T	SES	17.
	٤	YTE.	41.,2
•	þ	280	19.

### CHAPTER 4

### SIGNIFICANT EVENTS AND SYSTEM TRAPS

Understanding of trap logic and EMT handling as described in the PDP-11 Processor Handbook is a prerequisite to understanding this chapter.

Significant events and system traps are the means by which communication is effected among various parts of the system. Events and traps appear to overlap in some areas, but three points may help to clarify their function and use.

- 1. A significant event causes a change in system status; it causes the Executive to re-evaluate the eligibility for execution of all tasks.
- 2. System Traps are exclusive to a single task; they are useful for intra-task communication and control.
- 3. The occurrence of an event can only change the eligibility of a task to run. A trap, however, is a real interrupt; the sequence of instructions being executed by the task is interrupted and control is transferred to another place in the program. This process may be invisible to the user in some cases. However, it is a difference between events and traps.

### 4.1 SIGNIFICANT EVENTS

Significant events provide a mechanism for achieving dynamic control of task execution. Tasks are able to declare and recognize significant events through event-associated directives. The declaration and occurrence of significant events provide dynamic control over the execution of tasks. Waiting for an event, such as the waiting for an I/O request, can suspend a high priority program until that event occurs. Meanwhile, lower priority programs are allowed to run.

Event flags are the means by which RSX-11D and tasks distinguish one event from another. Associated with each task are 64 event flags. The first 32 flags (1-32) are unique to each task, and are set or cleared only as a result of that task's operation. The integrity of these flags is maintained by the task. They are stored in the task's ATL and are not available to other tasks. The second 32 flags (33-64) are common to all tasks, and can be set or cleared as a result of any task's operation. Therefore, they require system management to preserve their significance and are stored in SCOM.

Event flags usually are set when significant events occur, and tasks can read and/or clear them using system directives. Also, task execution can be suspended until a particular event flag or a logical combination of event flags is set.

Some system processes running for the user need event flags. The last eight local (25-32) and global (57-64) event flags are reserved by convention for use by RSX-11D. Accounting uses flags 61 and 62.

In addition, device handler tasks use event flag 1 for express requests and event flag 2 for normal requests.

All significant events occur as the result of a task's having issued a system directive; some directives have the event explicitly noted, while in others it is implicit. Refer to Chapter 3 for a complete discussion of the directives.

- 1. The DECLARE SIGNIFICANT EVENT directive allows any task to stimulate the event-driven system whenever necessary;
- 2. The SEND, SEND AND RESUME OR REQUEST, or RESUME directives also cause a significant event at issuance;
- 3. The directives that cause task execution (REQUEST, EXECUTE, SCHEDULE, RUN, and SYNCHRONIZE) also cause a significant event whenever task execution is initiated;
- 4. The MARK TIME directive causes a significant event at a specified time interval after issuance;
- 5. Most I/O operations (initiated by the QUEUE I/O directive) cause a significant event upon completion. Successful completion of the QUEUE I/O directive itself causes a significant event.

The following examples show the usage of the common event flags for task synchronization.

Example 1 -- Global Flags

Task B specifies a global event flag (e.g., event flag number 35) in a WAITFOR directive, and task A specifies the same event flag in a SET EVENT FLAG directive at the time it is appropriate for task B to proceed.

Example 2 -- Global Flags

Task A specifies task B and a global event flag in a SEND directive. Task B has specified the same common event flag in a WAITFOR directive and issues a RECEIVE directive (to dequeue a block of data sent by task A) when activated because the WAITFOR is satisfied.

Following are examples of event flag usage to detect I/O completion, and to indicate that a specified period of time has elapsed.

#### Example 3 -- Local Flags

If an event flag is specified in QUEUE I/O and associated WAITFOR directives, the flag is cleared when the request is queued. When the task executes a WAITFOR predicated on the same event flag and the requested action is not yet completed, execution of the task is suspended.

The task performing the requested service sets the specified event flag when the request is completed, and the task's execution is resumed.

Example 4 -- Local Flags

If an event flag is specified in MARK TIME and WAITFOR directives, the flag is cleared at MARK TIME issuance and set after the indicated time has elapsed, and the task's execution is suspended for the indicated interval of time.

In the above examples, computation and/or event flag testing are not precluded prior to, or instead of, the WAITFOR directive, i.e., specifying an event flag does not imply that a WAITFOR directive must be used. Event flag testing can be performed at any time. The purpose of a WAITFOR directive is to stop execution until an indicated significant event occurs. It is not necessary to issue a WAITFOR directive immediately following the issuance of a QUEUE I/O or a MARK TIME directive.

#### 4.2 SYSTEM TRAPS

System traps are task interrupts initiated by the Executive to allow servicing of certain conditions or situations that can occur.

When a task plans to use the system trap facility, it must contain a trap service routine. This routine is automatically entered when the trap occurs with the task's normal priority and privilege. The action taken by the Executive if a service routine is not supplied is dependent upon the type of trap and is described below.

There are two types of system traps: synchronous system traps (SST's) and asynchronous system traps (AST's).

SST's provide a means of servicing fault conditions within a task, such as memory protection violation and PDP-11/40 floating point exceptions. These conditions, which are internal to a task and are not significant events, occur synchronously with respect to task execution. In these cases, if an SST service routine is not included in the task, the task's execution is aborted. AST's commonly occur as the result of a significant event and thus occur asynchronously with respect to a task's execution, i.e., a task does not have direct or complete control over when AST's occur. A characteristic of AST's is that they are for information purposes, such as signifying an I/O completion that a task wants to know about immediately and PDP-11/45 floating point exceptions. If an AST service routine is not provided, a trap does not occur and task execution is not interrupted.

It should be emphasized that SST's only are initiated by the Executive; no further action is taken. That is, they appear to the Executive just like normal task execution. The Executive having initiated an SST, cannot determine that the task is in the SST service routine. Thus, an SST service routine can be interrupted by another SST or an AST. SST's can be nested.

SST's are caused by activities internal to the task, while AST's occur as a result of an external event. The Executive keeps track of all AST's, queues them (FIFO) and is aware of when a task is executing an AST.

## 4.2.1 Synchronous System Traps

Before reading this section, it is advisable to review the vector interrupt logic as described in the PDP-11 Processor Handbook.

When an SST occurs, the task's PSW (program status word) and PC (program location counter) are pushed onto its stack, and return from the SST routine is accomplished by issuing an RTI or RTT instruction.

Execution of an SST service routine is indistinguishable from task execution, and an SST service routine can perform any operation that the task can. However, if a service routine for a system trap is to cause that same system trap to occur, it must be re-entrant.

SST service routine entry points are provided in a trap vector table which is contained in the task. The trap vector table is described in Section 3.41.

A zero address in the vector table is interpreted as if no entry point were specified. If an SST occurs and no entry point is specified, the task's execution is aborted. If an invalid address is specified as an SST service address, an attempt is made to transfer control to the address specified, which probably results in an odd-address or segment fault. The SST vector table is given to the monitor by the task by use of the DECLARE SST VECTOR TABLE Directive.

On entry to an SST service routine, the stack contains the following standard PDP-11 trap vector information:

SP+02	PS
SP+00	PC

At the start of SST service, the stack can contain additional information depending on the cause of the trap, as follows.

1. Segment Fault -

Segmentation Status Register 1

Segmentation Status Register 2

Segmentation Status Register 0

2. EMT Other Than 377 -

Instruction Operand (low-order byte) times two

Items 1 and 2, above, must be removed from the stack before the SST exit is taken. Exit from SST's is by means of an RTI or RTT instruction.

The RTI or RTT instruction removes PC and PS from the stack.

The additional data is pushed on the stack by the RSX-11D interrupt service routines. Seven trap service routines are provided as part of the RSX-11D Executive. Table 3-2 lists the ISRs and describes their functions.

	Tabl	le 4-1	
Executive	Trap	Service	Routines

ISR	FUNCTION
Memory Parity Interrupt	Traps at location 114. If a parity error occurs at other than processor priority zero (Executive code), the system is crashed. If a parity error occurs at processor priority zero, the task's status is changed to task parity error and the task is effectively locked in memory and can execute no move.
Odd Address and Miscellaneous Error	Traps at location 4. If the cause of the trap is a red or yellow trap violation, the system crashes. If the trap was in task code and the SST vector table is defined, the SST service routine executes. If the SST vector table is not defined, the task is aborted. For other traps refer to Section 4.2.3.
Segment Faults	Traps at location 250. If the segment fault occurs at other than task level, the system is crashed. If the segment fault was caused by task-level code, the user task is notified by means of an SST or aborted.
EMTs and Traps	Traps at locations 30 and 34. If the instruction is an EMT 377, control is transferred to the RSX-11D EMT handler. If the instruction was a trap and the previous mode was kernel, control is transferred to the directive status return routine in the Executive. For other EMTs or traps, the user task is notified by an SST or is aborted.
T Bit and BPT	Traps at location 14.
IOT and Reserved Instruction	Traps at locations 20 and 10.
Floating Point Exception	Traps at location 244. SST for 11/40 and AST for 11/45.

### 4.2.2 Asynchronous System Traps

When an AST occurs, the task's PSW and PC are pushed onto its stack. There also can be other parameters pushed onto the stack depending upon the cause of the AST.

After processing an AST, the trap dependent parameters must be removed from the task's stack, and an EXIT AST SERVICE directive is issued with the task's stack set as indicated in the description of the AST SERVICE EXIT directive.

Upon AST service exit, control is returned to one of three places:

- 1. Another (queued) AST;
- 2. The task;
- 3. Another task (e.g., the corresponding task was not in execution).

Five variations on the stack format, depending upon the AST origin, are as follows:

1. If a task is to be notified of power failure recoveries, a SPECIFY POWER RECOVERY AST directive is issued.

SP+04 -- PS of task at AST (interrupt), SP+02 -- PC of task at AST (interrupt), SP+00 -- Task's directive status (virtual zero).

It is not necessary to pop any of this from the stack.

2. When an I/O request is queued, an AST service entry point may be specified in the macro. If specified, an AST occurs upon completion of the request with the task's stack containing the following information:

> SP+16 -- Event flag mask word for flags 1-16 SP+14 -- Event flag mask word for flags 17-32 SP+12 -- Event flag mask word for flags 33-48 SP+10 -- Event flag mask word for flags 49-64 SP+06 -- PS of task at AST (interrupt), SP+04 -- PC of task at AST (interrupt), SP+02 -- Task's directive status (virtual zero), SP+00 -- Address of I/O status block for request (or zero if none specified). Must be popped off stack before AST exit.

3. When a MARK TIME directive is issued, an AST service entry point can be specified in the DPB. If specified, when the indicated time interval has elapsed, an AST occurs with the task's stack as follows:

> SP+16 -- Event flag mask word for flags 1-16 SP+14 -- Event flag mask word for flags 17-32 SP+12 -- Event flag mask word for flags 33-48 SP+10 -- Event flag mask word for flags 49-64 SP+06 -- PS of task at AST (interrupt), SP+04 -- PC of task at AST (interrupt), SP+02 -- Task's directive status (virtual zero), SP+00 -- Event Flag number (or zero if none specified). Must be popped off stack before AST exit.

4. If a task is to be notified of PDP-11/45 floating point hardware exceptions, a SPECIFY FLOATING POINT EXCEPTION AST directive is issued, and following floating point exceptions will cause AST's to occur with the task's stack as follows:

> SP+20 -- Event flag mask word for flags 1-16 SP+16 -- Event flag mask word for flags 17-32 SP+14 -- Event flag mask word for flags 33-48 SP+12 -- Event flag mask word for flags 49-64 SP+10 -- PS of task at AST (interrupt), SP+06 -- PC of task at AST (interrupt), SP+04 -- Task's directive status (virtual zero), SP+02 -- Floating point exception code, must be popped off stack before AST exit, SP+00 -- Floating point exception address, must be popped off stack before AST exit.

5. For SPECIFY RECEIVE AST, nothing is placed on the stack. The actual RECEIVE can be performed by the AST service routine.

The following are some general notes on the characteristics and use of AST's.

- 1. Two directives, INHIBIT AST RECOGNITION and ENABLE AST RECOGNITION, allow AST's to be queued for subsequent execution during critical periods. If AST's occur while AST recognition is inhibited, they are queued (FIFO), and are processed when AST recognition is enabled.
- 2. If an AST occurs while another AST is being processed, it is queued (FIFO), and is processed when the preceding AST service is completed unless AST recognition was disabled by an AST service routine.
- 3. If an AST occurs while an SST is being processed, the SST service routine execution is not distinguished from task execution, and is interrupted for execution of the AST service routine.
- 4. If an AST occurs while the related task is suspended, the task remains suspended after execution of the AST service routine unless explicitly resumed by the AST.
- 5. If an AST occurs while the related task is waiting for an event flag setting (WAITFOR directive), the task remains in a wait state after execution of the AST service routine unless an appropriate event flag is set by the AST or other routine.
- 6. If an AST occurs while the related task is in execution, the task is interrupted for the execution of the AST service routine. This interrupt is transparent to the task unless the trap service routine changes the context of the task.
- 7. If an AST occurs while a task is checkpointed, the task remains checkpointed and the AST is queued for execution after the task is reloaded.

### 4.2.3 Processor Priorities

Seven processor priorities are used by the PDP-11, as described in the PDP-11 Processor Handbook. Peripheral device interrupt service routines run at processor priority levels four through seven. In addition, software modules that cannot be interrupted run at priority level seven for short periods of time.

Priority levels zero through three are used by the system as operation indicators; i.e., no software interrupts occur, and therefore, no precedence is implied or invoked by levels zero through three.

Level zero is used exclusively for task execution. If a segment fault occurs at level zero, a system trap is caused if the task has a service routine to handle it. Otherwise, the task is aborted.

Level one is used for the servicing of trap type instructions, e.g., EMT IOT, TRAP, and is used for the following:

- 1. The processing of system directives (EMT 377s),
- 2. The causing of a synchronous system trap if a trap-type other than BPT trap executes at priority one or the priority of the interrupt service routine, whichever is higher.

Level two is used for the recognition of system events. These events are indicated in the system event recognition flag called .SERFG and are recognized only when returning to task execution, e.g., from an interrupt or from a directive. System events are significant event declarations, clock ticks, and power failure recoveries.

Level three is used for execution of routines that cannot be interrupted by significant event recognition or clock tick recognition, but that can be interrupted by peripheral device interrupts, e.g., the system subroutine to dequeue a request for a device handler task. In these cases, the level can be set to three by any means, but it must be lowered to zero by transferring control to subroutine ..ENBO to allow recognition of system events that might have occurred while running at priority level three. The system trace debugging routine also runs at level three.

APPENDIX A SYSTEM LISTS AND TABLES RSX11D -- RESIGENT EXECUTIVE MACRO D0710 24-APR-75 12:56 PAGE 10 EXEC MODULE ONE -- SYMBOLIC DEFINITIONS

478		1 TPD TASK PARTITION DIRECTORY
479		The two function directions
480		THE TTAN IS A STYRD LIST OF ENTRIPS APPARENTED AND AND APPARENT A
481		# THE "TPD" IS A FIXED LIST OF ENTRIES DESCRIBING EACH PARTITION IN A
482		I SYSTEM. THIS DIRECTORY IS CREATED BY THE SYSTEM CONFIGURATION
483		E ROUTINE (SYSGEN) CONSISTING OF ENTRIES OF THE FOLLOWING FORMATE
484	000000	T.PN==00 ; WD. 00 (B 00) PARTITION NAME (FIRST HALF)
485		J WD. 01 (B 02) PARTITION NAME (SECOND HALF)
486	666664	T. BATTON (IN BYTES)
487	66666	T.PZ==06 ; WD. 03 (8 06) 1/64TH SIZE OF PARTITION (IN BYTES)
488	000010	T.FWEE10 ; WD, 04 (B 10) PARTITION FLAGS WORD
489	000012	T. HP==12 J WD. 05 (B 12) 1/64TH BASE ADR OF FIRST HOLE, OR ZERO IF NO HOLFS
490	000014	T.RF==14 / WD. 06 (B 12) MRL LISTHEAD (FORWARD LINKAGE)
491	000016	T.RB==16 \$ WD. 07 (B 14) MRL LISTHEAD (BACKWARD LINKAGE)
492	000020	T.CF==20 J WD. 10 (B 20) CTL LISTHEAD (FORWARD LINKAGE)
493	000022	T.CB==22 J WD. 11 (B 22) CTL LISTHEAD (BACKWARD LINKAGE)
494		
495	000024	T, SZ==24 ISIZE (IN BYTES) OF TPD ENTRIES
496		I I I I I I I I I I I I I I I I I I I
497		FLAGS WORD BIT DEFINITIONS:
498		
499	600001	TF.UC==000001 (00) SET IF USER CONTROLLED PARTITION.
500	000002	TF. CU==000002 (01) SET IF OCCUPIED USER CONTROLLED PARTITION.
501	666664	TF. TS==000004 1(02) SET IF A TIME SHARED PARTITICN
502	006010	TF.AC==000010 1033 SET IF A TIME SHARED PARTITION IS ACTIVE

504		; GCD GLOBAL COMMON DIRECTORY
505		I CODE COMPON DIRECTORY
506		I GLOBAL COMMON AREAS ARE SHARABLE AREAS OF MEMORY FOR USE AS
507		1 LIBRARIES, OR FOR COMMON DATA STORAGE.
508		1 THE "GCD" IS A LINKED LIST OF ENTRIES DESCRIBING EACH GLOBAL COMMON
589		BLOCK IN A SYSTEM. THIS LIST IS CREATED BY INSTALL
510		AND CONSISTS OF ENTRIES OF THE FOLLOWING FORMAT, NOTE THAT GCD TYPE NODE
511		I ARE CREATED BY INSTALL FOR THE PURE AREAS OF MULTI-USER TASKS, BUT
512		I THESE NODES ARE NOT LINKED INTO THE GCD, BUT ARE POINTED TO BY THE
513		I TASKS" STD NODES.
514		1
515		) WD. 00 (B 00) FORWARD LINK
516		3 WD. 01 (B 02) BACKWARD LINK
517	000004	G.BN==04 J WD. 02 (B 04) COMMON BLOCK NAME (6 CHAR IN RADIX=50, 2=WCRDS)
518	620210	G.BAIII0 ; WD. 04 (B 10) 1/64TH BASE ADDRESS OF COMMON BLOCK
519	000012	G.CZ==12 J WD, 05 (B 12) 1/64TH SIZE OF COMMON BLOCK
520	000014	G.CT==14 ; WD. 06 (B 14) CREATION TIME (TWO WORDS: YEAR, MONTH/DAY)
521	600020	G.GS==N.SBIWD, 10 (B 20) GLOBAL AREA STATUS
522 523	000021	G.SATT21 J (B 21) STARTING APR
524	000022 000024	G.OIE=22 J WD. 11 (B 22) OWNER IDENTIFICATION (UIC)
525	000026	G.PD==24 ; WD, 12 (B 24) == GLOBAL AREA TPD ADDRESS
526	000027	G.FB==26 J WD, 13 (B 26) == FLAGS BYTE G.DI==27 J (B 27) == DISK INDICATOR
527	000027	G.AC==30 J WD. 14 (B 30) ACTIVE REFERENCE COUNT (BYTE)
528	000031	G,IC==31 J (B 31) == INSTALLED REFERENCE COUNT (BYTE)
529	000032	G.NATTIZZ J WD. 15 (B 32) - I/O NODE ADDRESS
530	000034	G.DA==34 J WD. 16 (B 34) GLOBAL AREA DISK ADDRESS
531		I I I I I I I I I I I I I I I I I I I
532		THE FOLLOWING ARE GLOBAL AREA STATUSES:
533		
534	000000	GS.NUL≖≢00 IGLOBAL AREA NOT IN USE
535	000002	GS.LRG==02 ILOAD REQUEST QUEUED
536	000004	GS.LRS==04 ILOAD REQUEST SUCCEEDED
537	000006	GS.LRF==006 JLOAD REQUEST FAILED
538	000052	GS.RRQ==Ø52 IRECORD REQUEST QUEUED
539	000054	GS.RRS==054 IRECORD REQUEST SUCCEEDED
540	000056	GS.RRF==056 #RECORD REQUEST FAILED
541		1
542		
543	666640	G.SZ==40 ISIZE (IN BYTES) OF GCD ENTRIES
544		1
545		I FLAGS WCRD BIT DEFINITIONS
546		1
547	000001	GF.EI==000001 1[00] EXISTENCE INDICATOR (SET WHEN LIB OR COMMON LOADED)
548	000002	GF.LIE=000002 1(01) LIBRARY COMMCN INDICATOR ++ 11LIB 01COM
549	000004	GF.RI==000004 (02) LIBRARY RELOCATABILITY INDICATOR SET FOR PIC CODE
550	000010	GF.NWE=000010 (03) SET WHEN NON-OWNER HAS WRITE ACCESS
551	000020	GF.NR≖≖000020     ;[04] SET WHEN NON⇔OWNER HAS READ ACCESS

# RSX11D -- RESIDENT EXECUTIVE MACRO D0710 24-APR-75 12:56 PAGE 12 EXEC MODULE ONE -- SYMBOLIC DEFINITIONS

553		
554		# PUD PHYSICAL UNIT DIRECTORY
555		I THE "PUD" IS A FIXED LIST OF ENTRIES DESCRIBING EACH PHYSICAL DEVICE"
556		I UNIT IN A SYSTEM. THIS LIST IS CREATED BY THE SYSTEM CONFIGURATION
557		\$ ROUTINE (SYSGEN) AND CONSISTS OF ENTRIES OF THE FOLLOWING FORMATE
558		
559	000000	U.DN==00 ; WD. 00 (B 00) DEVICE NAME (2_ASCII CHARS)
560	000002	U.UN==02 J WD. 01 (B 02) UNIT NUMBER (BYTE)
561	N69603	U.FB==03 ( B 03) FLAGS (BYTE)
562	00000u	U.C1==04 ; WD. 02 (B 04) CHARACTERISTICS WORD ONE (DEVICE INDEPENDENT INDICATORS)
563	000006	U.C2==06 ; WD, 03 (B 06) CHARACTERISTICS WORD TWO (DEVICE DEPENDENT, INDICATORS)
564	606010	U.C3==10 / WD. 04 (B 10) CHARACTERISTICS WORD THREE (DEVICE DEPENDENT INDICATORS)
565	000012	U.C4==12 ; WD. 05 (B 12) CHARACTERISTICS WORD FOUR (SIZE OF BLOCK, BUEFER. IINE)
566	000014	U.AF==14 J WD. 06 (B 14) ATTACH FLAG (ATL NODE ADDRESS OF ATTACHING TASK)
567	600016	U.RP==16 J WD. 07 (8 16) REDIRECT POINTER
568	000020	U.HA==20 J WD. 10 (B 20) HANDLER TASK ATL NODE ADDRESS
569	000022	U.XC==22 ; WD. 11 (B 22) COUNT OF EXPRESS REQUESTS IN QUEUE
570	000024	U.RF==24 ; WD. 12 (B 24) UNIT REQUEST DEQUE LISTHEAD (FWD PNTR)
571	000026	U.RB==26 ; WD, 13 (B 26) UNIT REQUEST DEQUE LISTHEAD (BKD PNTR)
572	000030	U.TV==30 ; WD. 14 (B 30) INTERRUPT TRAP VECTOR ADDRESS
573	000032	U, IP==32 ; WD, 15 (B 32) INTERRUPT PRIORITY (IN BITS 5-7)
574	000034	U.DA==34 J WD. 16 (B 34) {DEVICE PAGE ADDRESS1
575		
576		1 PHYSICAL UNITS ARE CONSIDERED "VOLUMES" BY THE FILES SYSTEM, AND THE
577		1 REMAINDER OF THE PUD ENTRY IS A "VOLUME CONTROL BLOCK".
578		
579	606036	U.VA==36 ; WD. 17 (B 36) ADDRESS OF VOLUME CONTROL BLOCK EXTENSION
580	000040	U.UI==40 ; WD. 20 (B 40) USER IDENTIFICATION CODE (UIC)
581	000040	U.PC==40 ; (B 40) UIC PROGRAMMER CODE
582	006641	$U_{\bullet}GC = = 41$ ; (8 41) == UIC GROUP CODE
583	000742	U, VP==42 ; WD, 21 (B 42) VOLUME PROTECTION WORD
584	000042	U.CHEE42 J (B 42) CHARACTERISTICS FLAGS
585		(E 43) RESERVED BYTE
586	606344	U,ARE=44 ; WC, 22 (B 44) ACCESS RIGHTS FLAGS WORD
587	000046	U.DACP==46 ; WD. 23 (B 46) DEFAULT ACP NAME, RAC50 (FIRST_WORD)
588	000050	U.ACP==50 ; WD. 24 (B 50) STD ENTRY ADDRESS OF CURRENT ACP
589	000052	U.TF==52 ; WD. 25 (B 52) TERMINAL FLAGS WORD
590	000052	U,PR==52 J WD. 25 (B 52) TERMINAL PRIVILEGE BYTE
591	000053	U,FO==53 J (B 53) TERMINAL FORMS BYTE
592	000054	U.LBH==54 ; WD. 26 (B 54) HIGH ORDER - TOTAL # OF BLKS FOR DEVICE
593	000056	U.LBASES6 & WD. 27 (B 56) LOW ORDER- TOTAL # OF BLOCKS FOR DEVICE
594		1 WD, 30 (B 60) - RESERVED WORD
595		
596	000062	U.SZ##62 ISIZE (IN BYTES) OF PUD ENTRIES

597		I
598		FLAGS BYTE BIT DEFINITIONS
599		
630	666640	UF.RD==040 ; *******TEMPORARY**************
601	000200	UF.RH==200 J [7] SET WHEN HANDLER TASK IS DECLARED RESIDENT.
602	000100	UF.TL==100 ; [6] SET WHEN HANDLER TASK RECOGNIZES LOAD AND RECORD
683	000040	UF.CFL==040 ; (5) SET WHEN DEVICE IS OFFLINE
684		
685		BIT DEFINITIONS FOR CHARACTERISTICS WORD ONE
686		1
607	696601	UC.REC==000001 ;[00] SET IF RECORD ORIENTED DEVICE (VIZ., TT. LP. CR)
608	000002	UC.CCL==000002 (01) SET IF CARRIAGE CONTROL DEVICE (VIZ., TT LP)
669	00000 00000 00000	UC.TTY==000004 (02) SET IF TTY DEVICE (VIZ., KSR, LA30)

## RSX11D -- RESIDENT EXECUTIVE MACRO DØ710 24-APR-75 12:56 PAGE 12-1 Exec module one -- symbolic definitions

010	000010	UC.DIR==000010 +[03] SET IF DEVICE IS A DIRECTORY DEVICE
611	000020	UC.SDI==000020 : [04] SET IF DEVICE IS A SINGLE DIRECTORY DEVICE
612	000040	UC.SQD==000040 (05) SET IF DEVICE IS A SEQUENTIAL DEVICE
613	600400	UC.INB==000400 1+003 [08] SET IF THE DEVICE IS INTERMEDIATE BUFFERED
614	001000	UC.SWL==001000 (09) SET IF THE DEVICE IS SOFTWARE WRITE LOCKED
615	002200	UC.ISP==002000 (10) SET IF DEVICE IS INPUT SPOCLED
616	004000	UC. OSP==004000 I[11] SET IF DEVICE IS OUTPUT SPOOLED
617	010000	UC.PSE==010000 (12) SET IF DEVICE IS PSEUDO DEVICE
618	900050	UC.COM==020000 (13) SET IF DEVICE IS COMMUNICATIONS CHANNEL
619	040000	UC.F11==040000 1(14) SET IF DEVICE IS FILES-11
620	100000	UC.MNT==10000 (15) SET IF DEVICE IS MOUNTABLE
621	• • • • •	
022		BIT DEFINITIONS FOR CHARACTERISTICS WORD TWO
623		
624	000004	UC.WCK==000004 1(02) SET IF A READ AFTER WRITE CHECK IS REQUIRED
625		
626		BIT DEFINITIONS FOR VOLUME CHARACTERISTICS BYTE U.CH
627		
628	600200	CH.OFF==200 IVOLUME IS OFF-LINE
629	000100	CH.FOR==100 IVOLUME IS FOREIGN
630	000040	CH.UNL==40 JDISMOUNT PENDING
631	000020	CH.NAT==20 JATTACH/DETACH NOT PERMITTED
632	000010	CH.NDC==10 JDEVICE CONTROL FUNCTIONS NOT PERMITTED
633	000001	CH.LABEE1 IVOLUME IS LABELED TAPE
634		
635		
636		BIT DEFINITIONS FOR TERMINAL PRIVILEGE BYTE
637		
638	000001	UT.PR##1 ISET IF TTY IS PRIVLEGED
639	000002	UT.SL==2 ISET IF TTY IS SLAVED
640	000004	UT.LG##4 JSET IF TERMINAL IS LOGGED ON
- · •		

# RSX11D -- RESIDENT EXECUTIVE MACRO D0710 24-APR-75 12:56 PAGE 13 EXEC MODULE UNE -- SYMBOLIC DEFINITIONS

642		
		; STD SYSTEM TASK DIRECTORY
643		1
644		I THE SYSTEM TASK DIRECTORY IS A MEMORY RESIDENT DIRECTORY OF ALL TASKS
645		J WHICH HAVE BEEN INSTALLED INTO A SYSTEM. THIS DIRECTORY CONSISTS OF TWO
646		9 PARTS: (1) A FIXED SIZE AREA OF ONE WORD FOR EACH TASK THAT MAY
647		BE INSTALLED AT ANY TIME, AND (2) AN STO ENTRY FOR EACH TASK THAT IS
648		I INSTALLED, THE FIXED SIZED AREA IS CALLED THE "ALPHA TABLE" AND
649		I PROVIDES SPACE FOR AN ALPHABETICALLY ORDERED CONTIGUOUS LIST OF POINTERS
650		I TO STD ENTRIES TO FACILITATE SEACH FOR STD ENTRY BY TASK NAME.
051		I EACH STD ENTRY IS OF THE FOLLOWING FORMATI
652		
653	000000	S.TN==00 ; WD. 00 (B 00) TASK NAME (6 CHAR IN RADIX=50, 2 WORDS)
654		J WD. 01 (B 02) (SECOND HALF OF TASK NAME)
655	606004	S.TD==04 ; WD. 02 (B 04) DEFAULT TASK PARTITION (TPD ADDRESS)
056	000006	S,FWEED6 ; WD, 03 (B 06) == FLAGS WORD
657	000010	S.DP==10 ; WD. 04 (8 10) DEFAULT PRIORITY (BYTE)
658	000011	S.DI==11 J (B 11) SYSTEM DISK INDICATOR (BYTE)
659	000012	S.LZ==12 J WD. 05 (B 12) 1/64TH SIZE OF LOAD IMAGE
662	000014	S.TZ==14 ; WD. 06 (8 16) -= 1/64TH MAX JASK SIZE
661	202016	S.AV==16 J WD. 07 (B 16) NUMBER OF ACTIVE VERSIONS OF TASK (BYTE)
662	000017	S.PV==17 ; (B 17) TASK POOL LIMIT PER VERSION (BYTE)
663	000020	S,PU==20 ; WD. 10 (B 20) TASK POOL UTILIZATION
664	000022	S.RF==22 ; WD. 11 (B 22) RECEIVE DEQUE LISTHEAD (FWD PNTR)
665	000024	S.RB==24 ; WD. 12 (B 24) RECEIVE DEGUE LISTMEAD (BKG PNJR)
666	200026	S.DL==26 ; WD. 13 (B 26) LOAD IMAGE FIRST BLOCK NUMBER (37-BITS)
667		J WD. 14 (B 30) (SECOND HALF OF DISK ADDRESS)
668	000032	
669		I I I I I I I I I I I I I I I I I I I
670		
671		THE SYSTEM DISK INDICATOR SPECIFIES WHICH I/O REQUEST QUEUE 18
672		I TO RECEIVE A "LOAD TASK IMAGE" REQUEST, BY PROVIDING A "PUD ENTRY INCEX".
673		I E.G., A ZERO WOULD INDICATE THE REQUEST QUEUE FOR THE DEVICE-UNIT
674		REPRESENTED BY THE FIRST (ENTRY ZERO) PUD ENTRY.
675		A REPORTED DI THE FINDI (ENTRI LERU) PUD ENTRI.
676		FLAGS WORD BIT DEFINITIONS
677		
678	000001	SF.24==000001 +[00] STD IS 24 WORDS LONG (DEFAULT IS 16)
679	000002	SF.FX==000002 (01) SET WHEN TASK IS FIXED IN MEMORY
680	000004	SF.RM==000004 = 1 (02) SET WHEN STD IS TO BE REMOVED
681	000010	SF.TD==000010 + [03] SET WHEN TASK IS DISABLED
682	000020	SF.BF==000020 \$ (04) SET WHEN A TASK IS BEING FIXED IN MEMORY
683	000040	SF_XT==000040 + [05] SET WHEN A TASK IS TO BE REMOVED ON EXIT
684	000100	SF. MUE=000100 1(06) SET WHEN A TASK IS TO BE REMOVED ON EXIT SF. MUE=000100 1(06) SET WHEN TASK IS MULTI=USER
685	000200	SF.PT==000200 1(07) SET WHEN TASK IS A PRIVILEGED TASK
		GTATIDUDLED FLUTT SET MMEN TASK IS A PRIVILEGED TASK

686	000400	SF.NT==000400	1 (08) NETWORK ATTRIBUTE BIT
687	601000	SF_R1==001000	1 [09] RESTRICTED USAGE LEVEL ONE (BACKGROUND BATCH JOBS)
688	002000	SF.R2==002000	(10) RESTRICTED USAGE LEVEL TWO (UNIMPLEMENTED)
689	004000	SF, XA==004000	(11) SET WHEN TASK IS NEVER TO BE ABORTED
690	010000	SF. XD==010000	I [12] SET WHEN TASK IS NEVER TO BE DISABLED
691	0066550	SF.XF==020000	I [13] SET WHEN TASK IS NEVER TO BE FIXED IN MEMORY
692	840882	SF.XC==040000	1 [14] SET WHEN TASK IS NEVER TO BE CHECKPOINTED
693			1[15] UNUSED BIT
694		1	
695	000040	S.SIZ==32.	ISIZE OF STD IN BYTES

## RSX11D -- RESIDENT EXECUTIVE MACRO D0710 24-APR-75 12156 PAGE 14 EXEC MODULE UNE -- SYMBOLIC DEFINITIONS

697		) ATL ACTIVE TASK LIST
698		
699		THE "ATL" IS A PRIORITY ORDERED DEQUE OF "ATL" NODES FOR ACTIVE TASKS
700		I THAT HAVE MEMORY ALLOCATED FOR THEIR EXECUTION. THE TASKS REPRESENTED
701		BY ENTRIES IN THE ATL ARE EITHER MEMORY RESIDENT, OR A REQUEST FOR THEIR
702		I LOADING HAS BEEN QUEUED, THE LISTHEAD FOR THIS DEQUE IS IN THE SYSTEM
703		COMPUNICATIONS AREA (SCOM), AND THE NODES ARE OF THE FOLLOWING FORMAT:
704		COPPORTURITIONS AREA (SCOP), AND THE NUCES ARE UP THE FULLOWING FURMATE
705		
706		; WD. 00 (B 00) FORWARD LINKAGE
707		J WD, Ø1 (B Ø2) BACKWARD LINKAGE
708	00004	I WD. 02 (B 04) NODE ACCOUNTING WORD (STD ENTRY ADR OF REQUESTOR)
709		A, ROBEN, AW
•	69696	A.TI==N.TI; WD. 03 (B 06) TI IDENTIFICATION - PUD ADDRESS
710	000010	A.RP==10 ; WD. 04 (B 10) TASK'S RUN PRIORITY (BYTE)
711	000011	A. IREE11 J (B 11) TASK I/O IN PROCESS COUNT (BYTE)
712	000012	A. INFE12 ; WD. 05 (B 12) TASK I/O PENDING COUNT (BYTE)
713	000013	A.CS==13 J (B 13) SAVED STATUS OF CHECKPOINTED TASK
714	000014	A. MT#=14 J HD. 06 (B 14) == TASK MARK TIME PENDING COUNT (BYTE)
715	000215	A.CP==15 J (B 15) SAVED PRIORITY OF CHECKPOINTED TASK (BYTE)
716	090016	A.HA==16 ; WD. 07 (B 16) 1/64TH REAL ADR OF LOAD IMAGE
717	600016	A.NATTA.HA ; I/O NODE ADDRESS WHEN TASK IS IN MRL
718	000020	A.TSEEN.SBJWD. 10 (B 20) == TASK STATUS (BYTE)
719	000021	A.AS==21 ; (B 21) AST INDICATOR (PREVIOUS STATUS) BYTE
720	00022	A.TD==22 J WD. 11 (B 22) == SYSTEM TASK DIRECTORY (STD) ENTRY ADDRESS
721	ØØØØ24	A.EF##24 J WD. 12 (B 24) +- TASK'S EVENT FLAGS (1-32)
722		J WD. 13 (B 26) == (SECOND HALF OF TASK'S EVENT FLAGS)
723	000930	A.FM==30 J WD. 14 (B 30) TASK'S EVENT FLAGS MASKS (64-BITS)
724		F WD. 15 (B 32) (SECOND WORD OF FLAGS MASK)
725		J WD, 16 (B 34) (THIRD WORD OF FLAGS MASK)
726		J WD. 17 (B 36) (FOURTH WORD OF FLAGS MASK)

727	666949	
728	000042	A.PD==40 ; WD. 20 (B 40) TASK'S RUN PARTITION (TPD ADDRESS)
729	000044	A.AF==42 1 WD. 21 (B 42) AST DEQUE LISTHEAD (FWD POINTER)
730		A.AB==44 ; WD. 22 (8 44) - AST DEQUE LISTHEAD (BKWD POINTER)
	000046	A.IA##40 / WD. 23 (B 46) TASK IMAGE DISK ADDRESS
731		J ₩D. 24 (SECOND WORD OF IMAGE ADDRESS)
732		J INITIALLY = S.DL
733		J CONTAINS CHECKPOINT ADRS IF TASK IS CHECKPOINTED
734	000052	A. TF==52 ; HD. 25 (B 52 TASK FLAGS
735	000054	A.CF==54 ; HD. 26 (B 54) CHECKPOINT TASK LIST FORWARD POINTER
736	000056	A.CB==56 J WD. 27 (B 56) CHECKPOINT TASK LIST BACKWARD POINTER
737		I A A A A A A A A A A A A A A A A A A A
738		I TASK STATUS VALUES ARE DESCRIBED AT JASXDT
739		A A A A A A A A A A A A A A A A A A A
740		BEFORE EXECUTION , THE
741		
742		1 FLAGS MASK WORDS ARE USED AS FOLLOWS'
743		
744		A.FM+0 ADDRESS OF TASK LOAD I/O REQUEST DEQUE LISTHEAD,
		A.FM+4 TASK UIC.
745		1
746		I AFTER TASK EXECUTION, "A.FM+0" IS SET AS FOLLOWS!
747		1
748		BIT-8 IS SET WHEN THE LOW ORDER BYTE (BITS 8-7) CONTAIN &
749		TERMINATION NOTIFICATION CODE (CODES DESCRIBED AT "SC. CAC")
750		i
751		BIT-9 IS SET WHEN I/O RUNDOWN MESSAGE IS REQUIRED.
752		
753		FLAGS WORD BIT DEFINITIONS

RSX11D -- RESIDENT EXECUTIVE MACRO DØ710 24-APR-75 12:56 PAGE 14-1 EXEC MODULE UNE -- SYMBOLIC DEFINITIONS

750			
754		1	
755	860601	AF.CP==001	1 SET WHEN TASK IS CHECKPOINTED
756	000002	AF. RD==002	SET WHEN TASK'S I/O IS BEING RUN DOWN
757	606064	AF. AD==004	SET WHEN TASK AST RECOGNITION IS INHIBITED
758	edde10	AF.CD==010	SET WHEN CHECKPOINTING IS DISABLED
759	669659	AF.MC==020	I SET WHEN TASK IS MARKED FOR CHECKPOINTING
760	666646	AF.GC==040	I SET WHEN TASK IS HOLDING A COMMAND BUFFER
761	000100	AF.IC==100	I SET WHEN TASK HAS AN I/O COMPLETION EVENT IN ITS AST QUEUE
762	000200	AF.IT==200	I SET WHEN TASK HAS AN INTERMEDIATE TRANSFER IN PROGRESS
763	606400	AF.GR==400	I SET WHEN TASK'S SHARED GLOBAL AREAS HAVE BEEN RELEASED
764	001000	AF.8F==1000	I SET WHEN A TASK IS TO BE FIXED
765	002000	AF.FX==2000	1 SET WHEN A TASK IS FIXED
766	004000	AF.AS==4000	I SET WHEN AN AST HAS BEEN DECLARED
767	010000	AF.RA==10000	I SET WHEN THERE IS A POTENTIAL RECEIVE AST
768		1	
769	000000	A.SIZ==48.	ISIZE OF ATL IN BYTES

RSX11D -- RESIDENT EXECUTIVE MACRO D0710 24-APR-75 12:56 PAGE 15 Exec module one -- symbolic definitions

771	
772	MRL MEMORY REQUIRED LIST
773	
774	I THE "MRL" IS A PRIORITY ORDERED DEQUE OF "ATL" NODES FOR ACTIVE TASKS
775	I THAT REQUIRE MEMORY IN A PARTITION. EACH PARTITION HAS ITS OWN MRL.
776	I WHENEVER A NON-FIXED TASK RUNNING IN A PARTITION EXITS, AN
777	I ATTEMPT IS MADE TO ASSIGN MEMORY TO THE FIRST (HIGHEST PRIORITY)
778	I TASK IN THE LIST. IF MEMORY IS FOUND, THE TASK'S NODE IS
779	3 MOVED FROM THE "MRL" TO THE "ATL" DEQUE. THE MRL LISTHEAD IS IN
780	1 THE TPD ENTRY FOR THE CORRESPONDING PARTITION.
781	1

RSX11D -- RESIDENT EXECUTIVE MACRO DØ710 24-APR-75 12:56 PAGE 16 EXEC MODULE ONE -- SYMBOLIC DEFINITIONS

783		•
784		
785		I CTL == CHECKPOINTABLE TASK LIST
786		
787		THE "CTL" IS A PRICRITY ORDERED DEQUE OF ENTRIES FOR CHECKPOINTABLE
788		I TASKS THAT ARE ACTIVE IN A PARTITION. EACH PARTITION HAS ITS OWN
789		I CTL. THE CTL LISTMEAD IS IN THE TPD ENTRY FOR THE CORRESPONDING
790		PARTITION.
791		I THE CTL IS REALLY JUST A RELINKING OF THE ATL, MOWEVER, AND HAS
792		I NO UNIQUE NODES OR FORMAT OF ITS OWN.
793		I THE CTL FORWARD AND BACKWARD POINTERS ARE THE LAST TWO WORDS IN
794		THE ATL NODE, AND , CONSEQUENTLY, NEGATIVE OFFSETS MUST BE DEFINED
795		I SO THAT PARAMETERS MAY BE REFERENCED WITH RESPECT TO THE CTL FORWARD
796		POINTER,
797		- FOINTERS
798	177730	, RQ==A, RQ=A, CF
799	177764	K, PD==A, PD=A, CF
800	177734	K, RP==A, RP=A, CF
801	177735	
802		K, IR==A, IR=A, CF
883	177736	K, INEFA, IN-A, CF
864	177740	
	177741	K. CP==A. CF
805	177742	K.NAEBA.NA-A.CF
806	177742	K. HA==A. HA=A. CF
807	177744	K.TS=#A.TS=A.CF
868	177745	K.ASEEA.AS=A.CF
809	177746	K.TDEEA.TD=A.CF

810	177750	K.EF==A.EF=A.CF
811	177754	K.FMEEA.FM-A.CF
812	177732	K.TI==A.TI=A.CF
813	177766	K.AF==A.AF=A.CF
814	177770	K.ABEEA.AB-A.CF
815	177772	K.IA≡≡A.IA⇒A.CF
816	17776	K.TF==A.TF=A.CF
817	177737	K.CS==A.CS=A.CF
818		1

RSX11D -- RESIDENT EXECUTIVE MACRO DØ710 24-APR-75 12:56 PAGE 18 EXEC MODULE UNE -- SYMBOLIC DEFINITIONS

821		1 IRQ I/C REQUEST QUEUE
822		
823		THE "IRQ" IS A PRIORITY ORDERED DEQUE OF I/O REQUEST NODES WITH ITS
824		I LISTHEAD IN THE PUD ENTRY OF THE PHYSICAL UNIT FOR WHICH THE I/O
825		REQUEST WAS QUEUED. EACH PHYSICAL UNIT HAS ITS CWN I/O REQUEST QUEUE.
826		1 I/O REQUEST NODES ARE CREATED AND QUEUED PRIMARILY BY THE "QUEUE I/O"
827		DIRECTIVE. HOWEVER, THE EXEC ALSO CREATES I/O REQUESTS TO:
828		(1) LOAD A TASK IMAGE, (2) RECORD A TASK IMAGE [CHECKPOINTING], AND
829		I (3) TO RUNDOWN I/O ON AN EXIT'ED TASK. I/O REQUEST NODES ARE OF
830		THE FOLLOWING FORMAT.
831		
832		; WD. 00 (B 00) FORWARD LINKAGE
833		J WD. 01 (B 02) BACKWARD LINKAGE
834		J WD. 02 (B 04) NODE ACCOUNTING WORD (STD ENTRY ADR OF REQUESTOR)
835	600004	R.TDEEN.AW
836	000006	R.AT==06 J WD. 03 (B 06) ATL NODE OF REQUESTOR ***
837	606010	R.PR==10 J WD. 04 (B 10) PRIORITY (BYTE)
838	000011	R.DP==11 / (B 11) DPB SIZE (BYTE) ***
839	000012	R.LUE=12 ; WD. 05 (B 12) LOGICAL UNIT NUMBER (BYTE)
840	000713	R.FN==13 J (8 13) EVENT FLAG NUMBER (BYTE)
841	000014	R.FC==14 / WD. 06 (B 14) I/O FUNCTION CODE
842	000016	R.SBEE16 ; WD. 07 (B 16) VIRTUAL ADDRESS OF STATUS BLOCK
843	600020	R.AE==20 ; WD. 10 (B 20) == VIRTUAL ADDRESS OF AST SERVICE ENTRY
844	000022	R.UI==22 J WD. 11 (B 22) USER IDENTIFICATION CODE
845	000022	R.PC==22 J (B 22) PROGRAMMER CODE
846	000023	R, GC = 23 ; (B 23) GROUP CODE
847	000024	R.PB==24 ; WD. 12 (B 24) PARAMETER #1
848		1 WD. 13 (8 26) PARAMETER #2
849		5 WD. 14 (B 30) PARAMETER #3
850		1 WD. 15 (B 32) PARAMETER #4
851		1 WD. 16 (B 34) PARAMETER #5
852		3 WD. 17 (B 36) PARAMETER #6
853	600040	R.PD==40 ; WD. 20 (8 40) PUD POINTER FOR THIS REQUEST

854 855 856 857	236845 236844	R.EL==42 ; HD. 21 (B 42) ERROR LOG BUFFER POINTER/FLAG R.HA==44 ; HD. 22 (B 44) FLAG BYTE FOR EXEC ; HD. 22 (B 45) WORK AREA FOR DEVICE HANDLERS ; HD. 23 (B 46) WORK AREA FOR DEVICE HANDLERS
858 859 860 861 862	020054 000056	J WD. 24 (B 50) WORK AREA FOR DEVICE HANDLERS J WD. 25 (B 52) WORK AREA FOR DEVICE HANDLERS R.IB==54 J WD. 26 (B 54) INTERMEDIATE BUFFER ADDRESS R.UB==56 J WD. 27 (B 56) USER BUFFER ADDRESS(INTERMEDIATE TRANSFER)
863 864 865 866 867	888801 888802	THE LOW ORDER THREE-BITS OF THE I/O FUNCTION CODE ARE USED BY THE SYSTEM AS FCLLCWS: RF.IT==000001 :[0] RESERVED FOR FUTURE USE
868 869 870 871	00004	RF.XR==000002 ;[1] "EXPRESS REQUEST" RF.IR==000004 ;[2] RESERVED FOR FUTURE USE ; ; *** WHENEVER AN I/O REQUEST IS QUEUED BY THE "QUEUE I/O" DIRECTIVE. THE ; DPB SIZE AND THE REQUESTOR'S ATL NODE ADDRESS ARE RECORDED IN THE I/O
872 873 874 875 876		; REQUEST NODE. WHENEVER AN I/O REQUEST IS QUEUED AS A RESULT OF ANOTHER ; DIRECTIVE (VIZ., "REQUEST" CAUSING A TASK IMAGE TO BE LOADED), THE DPB ; SIZE AND THE REQUESTOR'S ATL NODE ADDRESS ARE SET TO ZERO. THUS. BOTH ; BOTH THE DPB SIZE AND THE ATL NODE ADDRESS ARE ALSO "EXEC REQUEST" ; INDICATORS.

# RSX11D -- RESIDENT EXECUTIVE MACRO DØ710 24-APR-75 12156 PAGE 19 Exec module one -- symbolic definitions

878 879	S CKO CLOCK QUEUE
689 681 882 883 884 885 885	THE CLOCK QUEUE IS A DEQUE CONSISTING OF ONE NODE FOR EACH OPERATION SCHEDULED TO BE PERFORMED AT SOME FUTURE TIME. A "SCHEDULE DELTA- TIME" IN THE FIRST NODE (IF ANY) OF THE CLOCK QUEUE IS DECREMENTED AT EACH CLOCK TICK UNTIL THE NODE "COMES DUE", AT WHICH TIME THE INDICATED OPERATION IS PERFORMED. CLOCK QUEUE NODES ARE LINKED IN THE ORDER IN WHICH THEY WILL COME DUE, AND THE SCHEDULE DELTA-TIME IN EACH NODE (EXCEPT THE FIRST) IS RELATIVE TO THE SCHEDULE TIME
687 888 889 890	3 OF THE PREVIOUS CLOCK QUEUE NODE. CLOCK QUEUE NODES ARE OF THE 3 FOLLOWING FORMAT. 3 WD. 00 == FORWARD LINKAGE
891 892 893 000004 894 000006	} WD. 01 BACKWARD LINKAGE } WD. 02 NODE ACCOUNTING WORD (STD ENTRY ADR OF REQUESTOR) C.TD==N.AW
895 000010	C.AT==06 ; WD. 03 == ATL NODE ADDRESS OF REQUESTOR C.SD==10 ; WD. 04 == SCHEDULE DELTA IN TICKS (64=BITS)

896		J WD. 05 (LOWER ORDER HALF OF SCHEDULE DELTA)
897	000014	C.RT==14 / WD. 06 REQUEST TYPE INDICATOR & UNUSED BYTE
898		
899	000016	C.FM==16 ; WD. 07 [MT] FLAG MASK (BIS SRC)
980	000020	C.FA==20 ; WD, 10 [MT] FLAGS WORD ADR (BIS DST ADR)
901	006022	C.FN==22 ; WD. 11 [MT] EVENT FLAG NUMBER
902	000024	C.AE==24 / WD. 12 [MT] VIRTUAL ADDRESS OF AST SERVICE ENTRY
903		(5 UNUSED WORDS)
904		
905	000016	C.RI==16 ; WD. 07 [TS] RESCHEDULE INTERVAL IN TICKS (64-BITS)
986		1 WD. 10 [TS] (LOW ORDER HALF OF RESCHEDULE INTERVAL)
907	000022	C.R2==22 ; WD. 11 [TS] STD ENTRY ADR OF REQUESTED TASK (RP FOR ".REQS")
908	000024	C.R3==24 J WD. 12 [TS] TPD ENTRY ADR, OR ZERO, (R3 FOR , REGS!)
989	000026	C.R4==26 J WD, 13 [TS] RUN PRIORITY, OR ZERO (R4 FOR ".REGS")
910	000030	C.UISE30 ; WD. 14 [TS] UIC INDICATOR FOR ".REQS"
911	006032	C.TI==32 / WD. 15 [TS] TI IDENTIFICATION FOR ".REGS"
912		(2 UNUSED WORDS)
913		
914		9 [MT] == MARK TIME NODE ENTRIES
915		# [TS] TASK SCHEDULING NODE ENTRIES
916		
917		1 REQUEST TYPE INDICATORS:
918		3
919		S 0 MARK TIME
920	000400	TM.SL==400 JSUB CODE FOR AN INTERNAL TIME SLICE
921		1 TASK SCHEDULING (SINGLE SHOT)
922		1 2 TASK SCHEDULING WITH PERIODIC RESCHEDULING
923		3
924		NOTE THE CLOCK QUEUE SCAN ROUTINE IN "CANCEL SCHEDULED REQUESTS"
925		ASSUMES TASK SCHEDULING IF NON-ZERO REQUEST TYPE INDICATOR.

RSX11D -- KESIDENT EXECUTIVE MACRO D0710 24-APR-75 12:56 PAGE 20 EXEC MODULE ONE -- SYMBOLIC DEFINITIONS

927		; ASQ == AYNCHRONOUS SYSTEM TRAP QUEUE
928		1
929		) THE "ASQ" IS A DEQUE (FIFO), WITH LISTHEAD IN ATL ENTRIES, CONSISTING
930		; OF CNE NODE FOR EACH AST (ASYNCHRONOUS SYSTEM TRAP) TO BE EXECUTED FOR
931		THE TASK DEFINED BY THE STD ENTRY. ASG NODES ARE OF THE FOLLOWING
932		I FORMAT.
933		
934		; WD. 00 FORWARD LINKAGE
935		
		1 WD. 01 == BACKWARD LINKAGE
936		; WD. 02 ACCOUNTING WORD (STD ENTRY ADDRESS OF CHARGED TASK)
937	69666	Y_TT==06 ; WD. 03 AST TYPE & NUMBER OF PARAMETERS ++
938	000010	Y.AE≠≖10 ; WD. 04 +- AST ENTRY POINT
939	000012	Y.PI==12 ; WD. 05 AST PARAMETER 1
940		1 WD. 05 AST PARAMETER 2
941		1 WD. 06 AST PARAMETER 3
942		J ETC.
943		
944		* * THE AST TYPE & NUMBER OF PARAMETER DEFINITIONS ARE AS FOLLOWS:
945		
946	000400	YF.MT==0+<400+1> IMARK=TIME (PARAMETERI EVENT FLAG NUMBER)
947	000401	YF.IC==1+<400+1> II/O COMPLETION (PARAMETER: STATUS BLOCK ADDRESS)
948		YF.FE==2+<400+2> IF.P. EXCEPTION (PARAMETERS) EXCEPTION CODE & ADDRESS)
	021002	
949	000003	YF.PR==3+<400+0> IPOWER RECOVERY (NO PARAMETERS)
950	000004	YF.RE==4+<400+0> IRECEIVE QUEUE'D (NO PARAMETERS)
951	001407	YF.PC==7+<400+3> ICOMMUNICATIONS AST
952		

R\$X11D -- RESIDENT EXECUTIVE MACRO DØ710 24-APR-75 12:56 PAGE 21 EXEC MODULE ONE -- SYMBOLIC DEFINITIONS

954		1
955		1 SRQ SEND/RECEIVE QUEUE
956		9
9.57		1 THE "SRG" IS A DEQUE (FIFO), WITH LISTHEAD IN STD ENTRIES, CONSISTING
958		3 ONE NODE FOR EACH BLOCK OF DATA "SENT" (VIA "SEND" OR "SEND & REGUEST"
959		1 DIRECTIVES) TO THE TASK DEFINED BY THE STD ENTRY. ROS NODES ARE OF
968		1 THE FOLLOWING FORMAT.
961		
962		I WD. 00 FORWARD LINKAGE
963		9 WD. 01 BACKWARD LINKAGE
964	000004	D.SIEEN.AW 9 WD. 02 (8 04) SENDER ID (NAW)
965	000006	D.TI==A.TI ; WD. 03 (8 06) TI INDICATOR
966	666610	D.PR==10 ; WD. 04 (B 10) PRIORITY OF SEND
967	000011	D.BS==11 / (B 11) == BUFFER SIZE (WORDS)
968	000012	D.D1==12 ; WD. 05 (B 12) FIRST WORD OF DATA BLOCK

	SIDENT EXECUTIVE ONE SYMBOLIC DE	MACRO D0710 24-APR-75 12:56 PAGE 22 FINITIONS
970		: MCR MCR COMMAND BUFFER
971		1
972		I THE MCR COMMAND BUFFER IS A 96 BYTE BUFFER THAT HOLDS THE DATA
973		FOR A REQUESTED MCR FUNCTION. THE BUFFER IS SET UP BY THE MCR
974		1 DISPATCH FUNCTION AND IS RETURNED TO THE POOL BY THE "GET MCR
975		# COMMAND LINE DIRECTIVEAFTER THE INFORMATION HAS BEEN PASSED
976		I TO THE MCR FUNCTION, THE BUFFERS ARE LINKED TO THE MCR BUFFER
977		1 LIST BY THE MCR DISPATCH FUNCTION.
978		1
979		1 9 WD. 00 FORWARD LINKAGE
980		I WD. 01 BACKWARD LINKAGE
981		# WD. 02 == NODE ACCOUNTING WORD
982	000006	MATNEES I WD. 03 SECOND HALF OF MCR TASK NAME
983	600010	M.TI==10 ; WD. 84 TI ADDRESS OF MCR PUNCTION
984	000012	M.BC==12 # WD. 05 NO. OF BYTES IN COMMAND LINE
985	000014	M.BF==14 I WD. 06 START OF DATA AREA IN BUFFER

#### APPENDIX B

#### GLOSSARY

ATL (Active Task List) A priority-ordered list of Active Tasks used to drive the system. The ATL is a deque consisting of one node for each Active Task in the system.

CLOCK QUEUE The Clock Queue is a deque consisting of one node for each item to be done at some time in the future, such as scheduling of Tasks (Via the SCHEDULE and MARK TIME Directives), and rescheduling of Tasks (Clock interrupt service routine). The nodes are linked in the order in which they come due.

COMMON BLOCK, INTERNAL An area of contiguous memory within a partition, available only to the Task in the partition during its residency.

COMMON BLOCK, SYSTEM An area of contiguous memory, defined at System Generation time, where data can be stored and referenced by all Tasks. A SYSTEM COMMON BLOCK is referenced by using a COMMON name matching a SYSTEM COMMON BLOCK name and declaring that COMMON as SYSTEM COMMON to the Task Builder.

DEFAULT PRIORITY A priority given to a Task during Task Building or Task Installation that is used when a priority is not specified and the Task's execution is requested or scheduled.

DEQUE A double-ended queue consisting of a listhead and list elements (nodes), circularly linked by both forward and backward pointers. Deques or linked lists are used to store system information.

DEVICE HANDLER A Task in the RSX-11D system which drives or services an I/O device. Handler tasks are activated using the Queue I/O directive.

DIRECTIVE See SYSTEM DIRECTIVE

DISK-RESIDENT TASK A Task which normally resides on the disk and is brought into a memory partition to execute. DPB (DIRECTIVE PARAMETER BLOCK)

EVENT FLAG

EXECUTIVE

I/O RUNDOWN

LISTHEAD

LINKED LIST

LUN (LOGICAL UNIT NUMBER)

LUT (LOGICAL UNIT TABLE)

MCR (MONITOR CONSOLE ROUTINE)

A block of up to 12 (decimal) contiguous words containing information needed in processing a System Directive.

One of 64 bits associated with a Task, which is set or cleared to indicate that a particular Significant Event has occurred.

The Executive coordinates all activities in the system including Task scheduling, I/O supervision, resource allocation, and interactive operator communication.

A process which delays the availability of a partition until all transfers to and from that partition have been stopped or have been allowed to complete. I/O RUNDOWN is invoked when a Task is terminated by the Executive or by the ABORT MCR Function Task and has outstanding transfers pending to/from its partition.

A 2-word memory block with forward and backward pointers pointing to the next and previous list node or to itself if empty. The listhead is a reference point in a circularlylinked list.

A deque consisting of nodes and a listhead used to store system information. An empty list consists of only a listhead.

Logical Unit Numbers are used to represent logical I/O device units rather than physical units. Each Logical Unit Number is represented by an entry in the Logical Unit Table.

A block of contiguous memory with a 1-word entry, or slot, for each Logical Unit Number. When a LUN is assigned to a physical device unit, the corresponding LUT slot contains the address of the appropriate Physical Unit Directory node.

The MCR allows the user to communicate on-line with the system from the console Teletype. The MCR consists of the Resident MCR Task, which accepts user's commands, and the MCR Functions, which actually carry out the indicated requests. MEMORY-RESIDENT TASK A Task which has been fixed-in-memory or which is assembled as part of the Executive.

MONITOR CONSOLE The control Teletype of the RSX-11D system where MCR Function requests may be issued by the operator.

NODES The list elements of a deque. All nodes (of dynamic lists) consist of the listhead, followed by data (list elements).

> An area of contiguous memory within which Tasks are executed; defined at System Generation time.

PUD (PHYSICAL UNIT DIRECTORY) A table constructed during System Generation to describe the I/O devices and units in the system. When a logical I/O number is assigned to a physical unit, the device and unit are set in a LUT entry corresponding to the LUN.

> Empty 17-word nodes for use in any deque. The pool is generated by System Generation from a core area not specified for other use.

SIGNIFICANT EVENT An event which results in the scanning of the active task list.

The following events are considered significant events: 1) I/O queuing, 2) normal I/O request completion, 3) a task request, 4) a scheduled RUN, SCHEDULE, or SYNC coming due, 5) a Mark Time expiration, 6) a task resumption (Resume directive), and 7) a task exit(Exit directive).

A directory of all tasks installed in the system.

The process through which the user tailors the RSX-11D system to best fit his requirements.

Instructions to the RSX-11D Executive to perform special functions, such as I/O, etc.

A program written by the user or supplied by Digital which is built via the Task Builder, installed in the system via the Monitor Console Routine, and scheduled and executed on a priority basis.

The Task Builder program is used to build Task files from relocatable binary files.

STD (SYSTEM TASK DIRECTORY)

POOL (POOL OF EMPTY NODES)

SYSTEM GENERATION

PARTITION

SYSTEM DIRECTIVES

TASK

TKB (TASK BUILDER)

-

APPENDIX C QIOMAC.MAC

1		.TITLE GIOMAC - GIOSYM MACRO DEFINITION
5		J ALTERED SUNDAY 24-NOV-74 13:00
3		J ALTERED TUESDAY 28-JAN-75 13:50:00
5		J ALTERED THURSDAY 06-FEB-75 15150 J Altered Monday 24-FeB-75 15140100 By ED Marison
6		J ALTERED TUE 25-MAR-75 15:30 EDIT # +801
7		
8		3 ***** ALWAYS UPDATE THE FOLLOWING TWO LINES TUGETHER
9	_	.IDENT /0304/
10	0v0304	QI.VER=0304
11 12		
13		J COPYRIGHT 1974,1975, DIGITAL EQUIPMENT CORP., MAYNARD MASS.
14		I THIS SOFTWARE IS FURNISHED TO PURCHASER UNDER A LICENSE FOR USE
15		3 ON A SINGLE COMPUTER SYSTEM AND CAN BE COPIED (WITH INCLUSION
16		3 OF DEC'S COPYRIGHT NOTICE) ONLY FOR USE IN SUCH SYSTEM, EXCEPT
17		J AS MAY OTHERWISE BE PROVIDED IN WRITING BY DEC.
18 19		
22		} THE INFORMATION IN THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT } notice and should not be construed as a commitment by digital
21		FOULPMENT CORPORATION.
22		
23		J DEC ASSUMES NO RESPONSIBILITY FOR THE USE OR RELIABILITY
24		3 OF ITS SOFTWARE ON EQUIPMENT WHICH IS NOT SUPPLIED BY DEC.
25		3
26 27		
28		; ; •
29		MACRO TO DEFINE STANDARD QUEUE I/O DIRECTIVE FUNCTION VALUES
310		3 AND IOSH RETURN VALUES. TO INVOKE AT ASSEMBLY TIME (WITH LOCAL
31		J DEFINITION) USE:
32		1
33 34		J GINSYS JDEFINE SYMBOLS
35		, I TO OBTAIN GLOBAL DEFINITION OF THESE SYMBOLS USE:
36		
37		3 GIOSYS DEFSG JSYMBOLS DEFINED GLOBALLY
38		<b>i</b>
39		3 THE MACRO CAN BE CALLED ONCE ONLY AND THEN
42 41		7 REDEFINES ITSELF AS NULL,
42		,-
43		.MACRU GIOSYS \$\$\$GBL,\$\$\$MSG
44		, IIF IDN, <\$\$\$GBL>, <def\$g>, ,GLOBL QI, VER</def\$g>
45		IF IDN, <\$\$\$M9G>, <def\$s></def\$s>
46		SSSMAX=0
47 48		\$\$M8G=1
49		,IFF \$\$MSG=0
50		,ENDC
51		MCALL IDEKRS
52		TOEHRS SSSGBL JI/U ERROR CODES FROM HANDLERS, FCP, FCS
53		"MCALL DRERPS
54		DHERRS SSSGBL JDIRECTIVE STATUS WORD ERROR CODES
55 55		, IF D1F, <\$\$\$MSG>, <def\$s></def\$s>
57		.MCALL FILIOS FILIOS \$\$\$GBL JDEFINE GENERAL GI/O FUNCTION CODES
		STERNA AAAAAA AAAAAA AAAAAAAAAAAAAAAAAAAAA

## GIOMAC - UTOSYM MACRO DEFINITIO MACRO DU710 25-MAR-75 14123 PAGE 1-1

58	.MCALL SPCIOS
59	SPCIOS SSSBL JDEVICE DEPENDENT I/O FUNCTION CODES
66	"MACRO QIOSY\$ AKG,AKG1,ARG2 JRECLAIM MACRO STORAGE
61	ENDM GIOSYS
62	, ENDC
63	ENDM GLOSYS

65		
66	3	
-		ROR CODES RETURNED BY DEVICE HANDLER AND FILE PRIMITIVES
		WORD OF THE I/O STATUS BLOCK
		RE ALSO RETURNED BY FILE CONTROL SERVICES (FCS) IN THE
-		THE FILE DESCRIPTOR BLOCK (FDB)
		E F.ERR+1 IS Ø IF F.ERR CONTAINS A HANDLER OR FCP ERROR CODE,
72 73	; MACRO	IDERRS SSSGBL
74	•	IOER., DEFINS
75	.IF	IDN, <\$\$\$GBL>, <defsg></defsg>
76	GBL=	
77	.IFF	•
78	GBL=	0
79	ENDC	
80	.IIF	NDF, SSMSG, SSMSG=0
•••	3	
•		RD CODES, USED BY ALL FUNCTIONS
•••	3	
84	,IOER,	IE, BAD, -01,, <bad parameters=""></bad>
85		IE. IFC, -02., <invalid code="" function=""></invalid>
86		IE, DNR, -03,, <device not="" ready=""></device>
87		IE.VER,-Ø4., <parity device="" error="" on=""> IE.ONP,-Ø5.,<hardware not="" option="" present=""></hardware></parity>
68 89		IE.SPC,-06., <illegal buffer="" user=""></illegal>
90 90		IE, DNA, -07., <device attached="" not=""></device>
91		IE. DAA, -08., <device already="" attached=""></device>
92		IE, DUN, -09., <device attachable="" not=""></device>
93		IE.EOF, -10,, <end detected="" file="" of=""></end>
94		IE.EOV,-11., <end detected="" of="" volume=""></end>
95		IE, WLK, -12,, <write attempted="" locked="" to="" unit=""></write>
96		IE, DAD, -13., <data overrun=""></data>
97		IE, SRE, -14,, <send failure="" receive=""></send>
98	.IOER.	IE,ABO,-15,, <request terminated=""></request>
99		IE.PRI,-16., <privilege violation=""></privilege>
100		IE,RSU,-17., <sharable in="" resource="" use=""></sharable>
101		IE, UVR, =18., <1LLEGAL OVERLAY REQUEST>
102		IE, BYT, -19,, <odd (or="" address)="" byte="" count="" virtual=""></odd>
103		IE.BLK, -20,, <logical block="" large="" number="" too=""></logical>
104		IE.MOD,-21., <invalid #="" module="" udc=""></invalid>
105		IE.CON,-22,, <udc connect="" error=""></udc>
106 107		IE.BBL,-56., <bad bluck="" device="" on=""> IE.STK,-58.,<not (fcs="" enough="" fcp)="" or="" space="" stack=""></not></bad>
108	• •	IE.FHE,-59., <fatal device="" error="" hardware="" on=""></fatal>
109		IE.EOT, -62., <end detected="" of="" tape=""></end>
110		IE.OFL,-65., <device line="" off=""></device>
111		IE.BCC, -66, , < BLOCK CHECK OR CRC ERROR>
112		
113		
-	1	
	<b>FILE PRIMITIV</b>	E CODES
-	;	
117		
118	,IOER,	IE,NOD,-23., <caller's exhausted="" nodes=""></caller's>
119	• • •	IE,DFU,-24,, <device full=""></device>
120	•	IE, IFU, -25,, <index file="" full=""></index>
121	, IOER,	IE.NSF,-26., <no file="" such=""></no>

122	IDER. IE.LCK, -27., <locked access="" from="" write=""></locked>
123	.IOER. IE.HFU,-28., <file full="" header=""></file>
124	.IOER. IE.WAC,-29., <accessed for="" write=""></accessed>
125	,IGER, IE,CKS,+30,, <file checksum="" failure="" header=""></file>
126	IDER. IE.WAT,-31., <attribute control="" error="" format="" list=""></attribute>
127	.IOER. IE.RER,-32., <file device="" error="" processor="" read=""></file>
128	IDER. IE.WER,-33., <file device="" error="" processor="" write=""></file>
129	IDER, IE, ALN, -34., <file accessed="" already="" lun="" on=""></file>
130	IDER. IE.SNC,-35., <file check="" file="" id,="" number=""></file>
131	.IOER. IE.SQC,-30., <file check="" id,="" number="" sequence=""></file>
132	.IOER. IE.NLN,-37., <no accessed="" file="" lun="" on=""></no>
133	.IOER. IE,CLO,-36., <file closed="" not="" properly="" was=""></file>
134	.IOER. IE,DUP,-57., <enter -="" directory="" duplicate="" entry="" in=""></enter>
135	IDER. IE, EVR, -63, < BAD VERSION NUMBER>
136	.IOER. IE.BHD,-64., <bad file="" header=""></bad>
	.IOER, IE.EXP,-75., <file date="" expiration="" nut="" reached=""></file>
137	
138	.IOER, IE.BTF,-76., <bad format="" tape=""></bad>
139	
140	3
141	; FILE CONTROL SERVICES CODES
142	;
143	
144	.IDER. IE.NBF,-39., <open -="" available="" buffer="" file="" for="" no="" space=""></open>
145	IDER. IE.RBG,-40., <illegal record="" size=""></illegal>
	.IOER. IE.NBK,-41., <file allucated,="" blocks="" exceeds="" no="" space=""></file>
146	
147	, IOER, IE, ILL, -42., <illegal block="" descriptor="" file="" on="" operation=""></illegal>
148	.IOER. IL.BTP,-43., <bad record="" type=""></bad>
149	.IOER. IE.RAC,-44., <illegal access="" bits="" record="" set=""></illegal>
150	,IDER. IE,RAT,-45., <illegal attributes="" bits="" record="" set=""></illegal>
151	.IOER. IE.RCN,-46., <illegal -="" large="" number="" record="" too=""></illegal>
152	_INER. IE_MRK,+47., <multiple -="" block="" implemented="" not="" read="" write="" yet=""></multiple>
153	IDER. IE.2DV,-48,, <rename -="" 2="" devices="" different=""></rename>
154	.IOER. IE.FEX,-49,, <rename -="" already="" file="" in="" name="" new="" use=""></rename>
155	IDER. IE.BDR,-50., <bad directory="" file=""></bad>
156	, IOER, IE, RNM, -51, , <can't file="" old="" rename="" system=""></can't>
157	IDER. IL.BDI,-52., SBAD DIRECTORY SYNTAX>
158	.IOER. IE,FOP,-53., <file already="" open=""></file>
159	,ICER, IE,BNM,-54,, <bad file="" name=""></bad>
160	,IDER. IE,BDV,+55,, <bad device="" name=""></bad>
161	.IOEP. IE.NFI,-60,, <file id="" not="" specified="" was=""></file>
162	IDER. IE, ISQ, -61., <illegal operation="" sequential=""></illegal>
163	.IDER. IE.NNC,-77., <not 'd'="" ansi="" byte="" count="" format=""></not>
164	
165	3 NETWORK ACP CODES
166	
167	.IDER. IE.AST,-67., <nd ast="" connect="" in="" specified=""></nd>
168	.IOER. IE.NNN,+68., <nd node="" such=""></nd>
169	,IDER. IE,NFW,=69,, <path lost="" partner="" to=""> }+001 THIS CODE MUST BE ODD</path>
170	IDER, IE, BLB, -70., <bad buffer="" logical=""> 1+001</bad>
171	IDER. IE.TMM, -71., <tod many="" messages="" outstanding=""></tod>
172	.IDER, IE.NDR, -72., <no available="" dynamic="" space=""></no>
173	, IOER, IE, CNR, -73, , <connection rejected=""></connection>
174	, ICER. IL.TMO, -74., <timeout on="" request=""></timeout>
175	.INER. IE.NNL,-78., <not a="" lun="" network=""> }+001</not>
176	
177	3
178	3 SUCCESSFUL RETURN CODES

GIOMAC - GIOSYM MACRO DEFINITIO MACHO DØ710 25-MAR-75 14123 PAGE 2-2

179	1
180	
181	DEFINS IS, PNU, +00. JOPERATION PENDING
182	DEFINS IS, SUC, +01. JOPERATION CONPLETE, SUCCESS
183	DEFINS IS. BV, +05. JON A/D READ, AT LEAST ONE BAD VALUE
184	JWAS READ (REMAINDER MAY BE GODD).
185	JBAD CHANNEL IS INDICATED BY A
185	INEGATIVE VALUE IN THE BUFFER.
187	JAEGALIZE TALUE IN THE DUFFER,
188	
189	
190	TTY SUCCESS CODES:
191	
	,
192 193	
	DEFINS IS, CR, <15+400+1> ;CARRIAGE RETURN WAS TERMINATOR
194	DEFIN\$ IS.ESC,<33+400+1> JESCAPE (ALTMODE) WAS TERMINATOR
195	
196	
197	; ****
198	3
199	J THE NEXT AVAILABLE ERROR NUMBER IS: -79.
200	J ALL LOWER NUMBERS ARE IN USELL
201	3
202	3 ****
263	.IF EQ.SSMSG
204	MACRO IDERRS A
205	LNDM IDERRS
205	.ENDC
247	ENDM IDERRS
	echan Tackwa

# GIOMAC - GIOSYM MACRO DEFINITIO MACRO DØ710 25-MAR-75 14:23 PAGE 3

209	1		
210	DEFINE THE DI	RECTIVE ERROR CON	DES RETURNED IN THE DIRECTIVE STATUS WORD
211			
212	FILE CO	NTROL SERVICES (F	FCS) RETURNS THESE CODES IN THE BYTE F.ERR
213	OF THE FILE DI	ESCRIPTOR BLOCK	(FDB), TO DISTINGUISH THEM FROM THE
			R AND FILE PRIMITIVES, THE BYTE
			GATIVE FOR A DIRECTIVE ERROR CODE.
216	•		
217	MACRO	DRERRS SSSGBL	
218	-	,GIOE, DEFINS	
219	.IF	IDN, <\$\$\$GUL>, <de< td=""><td>FSG&gt;</td></de<>	FSG>
220	GBL=		
221	.IFF	•	
222	GBL=	0	
223	ENDC	-	
224	.IIF	NDF, SSMSG, SSMSG	a Ø
225			• •
226	STANDARD ERRO	R CODES RETURNED	BY DIRECTIVES IN THE DIRECTIVE STATUS WORD
227			
228		IE_UPN01	SUFFICIENT DYNAMIC STORAGE>
220			ECIFIED TASK NUT INSTALLED>
230		IE. ULN, -05. , <un< td=""><td></td></un<>	
231			NDLER TASK NOT RESIDENT>
232		IE.ACT, -07., «TA:	
233			RECTIVE INCONSISTENT WITH TASK STATE>
234			SUING TASK NOT CHECKPOINTABLE>
235	•	10,000 / 20,000	SOINS THUR ADT CHECKFOINTABLES
236			
237			
238		TE. 45180 CDT	RECTIVE ISSUED/NOT ISSUED FROM AST>
239		IE.LNL,-90., <lu< td=""><td></td></lu<>	
240			VALID DEVICE OR UNIT>
241			VALID TIME PARAMETERS>
242			VALID PRIORITY ( .GT. 250.)>
243		IE.ILU,-96., <in< td=""><td></td></in<>	
244			
245			VALID EVENT ( ,GT, 64,)> RT OF DPB OUT OF USER'S SPACE>
246			C OR DPB SIZE INVALID>
247	• WIUE •	16.2061-33.14010	L OR DEB SIZE INVALIDE
	SUCCESS CONCO	FROM INTRECTIVES	- BLACED IN THE DIRECTIVE STATUS OHD
249		PROM DIRECTIVES	- PLACED IN THE DIRECTIVE STATUS WORD
259		TO CLD A	
251	UEFINS	IS,CLR,0	JEVENT FLAG WAS CLEAR
252	ACCTUC		JFROM CLEAR EVENT FLAG DIRECTIVE
	DEFINS	IS.SET,2	JEVENT FLAG WAS SET
253 254	AP - 1		JFROM SET EVENT FLAG DIRECTIVE
		IS,SPD,2	ITASK WAS SUSPENDED
255			
256			
257	.IF	EG,5\$MSG	
258		DRERRS A	
259	. ENDM	DRERPS	
260	.ENDC		
261	, ENDM	DRERRS	

263				
		-		ION CODES - DEVICE INDEPENDENT
264		- 175 001	NERAL GIVO PUNCI	ION CODES - DEVICE INDEPENDENT
265 1	F			
266		.MACRO	FILIOS SSSGBL	
267		.MCALL	,WORD, DEFINS	
268		,IF	IDN,<\$\$\$GBL>, <d< td=""><td>EF\$G&gt;</td></d<>	EF\$G>
269		G8L=:	1	
270		.IFF		
271		GBL=	8	
272		ENDC		
273	1	•		
274	GENERA	L GIZO	QUALIFIER BYTE D	EFINITIONS
275				
276		.word.	IG.X,001,000	ING ERROR RECOVERY
277		•	IQ.Q.002,000	JOUEUE REQUEST IN EXPRESS QUEUE
		wORD.	10.,004,000	JRESERVED
		· WORD ·	10.,004,000	JREDERVED
			6 0 M M A N D 0	
		SS GUEUE	COMMANDS	
281 3	1			
282				
283				JKILL CURRENT REQUEST
284			IO.RDN,022,000	
285		,WORD,	IO.UNL,042,000	JUNLOAD I/O HANDLER TASK
286		.WORD.	IO.LTK,050,000	JLOAD A TASK IMAGE FILE
287		.word.	IO.RTK,060,000	FRECORD A TASK IMAGE FILE
288 1	1			
289	GENERA	AL DEVIC	E HANDLER CODES	
290				
291		.word.	IC.WLB,000,001	JWRITE LOGICAL BLOCK
292				JREAD LOGICAL BLOCK
293				JLOAD OVERLAY (DISK DRIVER)
294		WORD,		JATTACH A DEVICE TO A TASK
295		WORD	IO.DET,000,004	JDETACH A DEVICE FROM A TASK
296 1			10.00170007004	
297			MITIVE CODES	
298		IUNI PALI		
			TO ENA 000 011	ACTNO ETTE NAME IN OTHECTOOM
299		.WORD.	IO.FNA,000,011	
300		WORD.	···	JREMOVE FILE NAME FROM DIRECTORY
301		.word.	IO.ENA,000,014	JENTER FILE NAME IN DIRECTORY
365				
		PRIMITIV	E CODES	
304				
305		,WORD,	IO.CLN,000,007	
306				JACCESS FOR READ
307		.WORD.	IU.ACW,000,016	JACCESS FOR WRITE
308		.word.	10.ACE,000,017	JACCESS FOR EXTEND
309		.word.	10.DAC,000,020	JDE-ACCESS FILE
310		WORD	IO.RVB,000,021	FREAD VIRITUAL BLOCK
311				JWRITE VIRITUAL BLOCK
312		WORD.	IU.EXT,000,023	
313		WORD.	10.CRE,000.024	
314			IO.DEL,000,025	
-			-	
315				JREAD FILE ATTRIBUTES
316				JWRITE FILE ATTRIBUTES
317		• •	•	JPRIVILEGED ACP CONTROL
318		.word.	IO,APC,000,030	JACP CONTROL
319 7				

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320	3				
321		MACRO	FILIOS	A	
322		ENDM	FILIOS		
323		ENDM	FILIOS		

325	7		
326	J DEFINE THE QI	10 FUNCTION CODE	S THAT ARE SPECIFIC TO INDIVIDUAL DEVICES
327	1		
328	.MACRO	SPCIOS SSSGBL	
329	MCALL	.WORD., DEFINS	
330	.IF	IDN, <\$\$\$GBL>, <d< th=""><th>FFSGD</th></d<>	FFSGD
331	GBL=		
332	.IFF	•	
333		0	
334	GBL=		
	.ENDC		
335	,		
336	J GI/O FUNCTION	CODES FOR SPECI	FIC DEVICE DEPENDENT FUNCTIONS
337	1		
338	.word.	IO.WLV,100,001	JWRITE LOGICAL REVERSE (DECTAPE)
339	.word.	IO.WLS,010,001	J(COMMUNICATIONS) WRITE PRECEDED BY SYNC TRAIN
340	.WORD.		J(COMMUNICATIONS) WRITE, NO SYNC TRAIN
341	WORD.		JREAD REVERSE (DECTAPE)
342	NORD.		FREAD - NO LOWER CASE CONVERT (TTY)
343	WORD.		JREAD PASSING ALL CHARACTERS (TTY)
344	.WORD.		
345			
346	.WORD.		JREAD BINARY MODE (CARD READER)
347	.WORD.		J(COMMUNICATIONS) READ, STRIP SYNC
•	.WOND.		J(COMMUNICATIONS) READ, DON'T STRIP SYNC
348	.WORD.		J(COMMUNICATIONS) READ, DON'T CLEAR CHC
349	.WORD.	IO.R1C,000,005	JREAD SINGLE CHANNEL (AFC, AD01, UDC)
350	,WORD,	IO,INL,000,005	J(COMMUNICATIONS) INITIALIZATION FUNCTION
351	"WOKD,	10,TRM,010,005	J(COMMUNICATIONS) TERMINATION FUNCTION
352	WORD		IREAD MULTICHANNELS (BUFFER DEFINES CHANNELS)
353	.WORD.		J(CUMMUNICATIONS) SETMODE FUNCTION FAMILY
354	WORD.		F(COMMUNICATIONS) SET UNIT HALF DUPLEX
355	WORD.		ICOMMUNICATIONS) SET UNIT FULL DUPLEX
356	.WORD.		J(COMMUNICATIONS) SPECIFY SYNC CHARACTER
357	.WORD.		
358			JREAD CHANNEL - TIME BASED
359	.WORD.		JREWIND (MAGTAPE, DECTAPE)
	.WORD.		JMAGTAPE, SPACE "N" BLOCKS
360	.word,		· · · · · · · · · · · · · · · · · · ·
361	.WORD.	IO.EOF,000,005	IMAGTAPE, WRITE EOF
362	.word.	IO.STC,100,005	JMAGTAPE, SET CHARACTERISTIC
363	•WORD•	IO.SEC,120,005	IMAGTAPE, SENSE CHARACTERISTIC
364	.word.	IO.RWU,140,005	JREWIND AND UNLOAD (MAGTAPE, DECTAPE)
365	.WORD.		IMAGTAPE, MOUNT & SET CHARACTERISTICS
366	WORD.		JUDC SINGLE CHANNEL ANALOG OUTPUT
367	NORD	IU.SS0,000,011	JUDC SINGLE SHOT, SINGLE POINT
368	.WORD.		JUDC SINGLE SHOT, MULTI-POINT
369	,WORD,	IO.SL0,000,013	JUDC LATCHING, SINGLE POINT
370			
	.WORD.	IO, ML0,000,014	JUDC LATCHING, MULTI-POINT
371	.WORD.	IO.LED,000,024	JLPS11 WRITE LED DISPLAY LIGHTS
372	.WORD.		JLPS11 WRITE DIGITAL OUTPUT REGISTER
373	.word.		JLPS11 READ DIGITAL INPUT REGISTER
374	.WORD.	IO.SCS,000,026	JUDC CONTACT SENSE, SINGLE POINT
375	.word.	IO.REL,000,027	
376	WORD.		JUDC CONTACT SENSE, MULTI-POINT
377	WORD		JLPS11 SYNCHRONOUS A/D SAMPLING
378	WORD.		JUDC CONTACT INT - CONNECT
379	.WORD.		JLPS11 SYNCHRONOUS DIGITAL INPUT
380			
381	.WORD.	IO,DCI,000,031	JUDE CONTACT INT - DISCONNECT
44 L	.WORD,	IO.XMT,000,031	J(COMMUNICATIONS) TRANSMIT SPECIFIED BLOCK WITH ACK

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	582			
	183			J(COMMUNICATIONS) TRANSMIT WITHOUT ACK
	384		÷ ÷ · ·	JLPS11 SYNCHRONOUS HISTOGRAM SAMPLING
	385	.WORD.		JUDC CONTACT INT - READ
-				F(COMMUNICATIONS) RECEIVE DATA IN BUFFER SPECIFIED
	386		IO,MDO,000,033	
	187		IO.CTI,000,033	
	388		IO,CON,000,033	
	189		IO.CPR,010,033	
-	90		IO,CAS,020,033	J(COMMUNICATIONS) COMMUNICATIONS CONNECT WITH AST
	91	"WORD,	IO_CRJ,040,033	I(COMMUNICATIONS) COMMUNICATIONS CONNECT REJECT
	92	.word.	IO.C80,110,033	3+001 (COMMUNICATIONS) COMMUNICATIONS BOOT CONNECT
	93	.WORD.	IO.CTR,210,033	3+001 (COMMUNICATIONS) COMMUNICATIONS TRANSPARENT CONNECT
	94	,WORD,	IO.GNI,010,035	J(COMMUNICATIONS) COMMUNICATIONS GET NODE INFO
3	195	WORD,	IO,GLI,020,035	J(COMMUNICATIONS) COMMUNICATIONS GET LINK INFO
3	96	.WORD.	IO.GLC,030,035	J(COMMUNICATIONS) GET LINK INFO CLEAR COUNTERS
:	97	WORD.	IO.GRI,040,035	J(COMMUNICATIONS) GET REMOTE NODE INFO
	98			1+001 (COMMUNICATIONS) GET REMOTE NODE ERROR COUNTS
3	399			1+001 (COMMUN.) GET REMOTE NODE NAME
4	100			1+801 (COMMUNICATIONS) CHANGE SOLU MODE
	01			1+001 (COMMUN,) CHANGE CONNECTION INHIBIT
	102			1+001 (COMMUNICATIONS) CIRCULAR BUFFER NCS
	103			3+001 (COMMUNICATIONS) CIRCULAR BUFFER DDCMP
	104	• •	• • • • • • •	JUDC TIMER - DISCONNECT
	105			J(COMMUNICATIONS) COMMUNICATIONS DISCONNECT FUNCTION
	066			JLPS11 SYNCHRONOUS D/A OUTPUT
	07			JUDC TIMER - READ
				J(COMMUNICATIONS) NETWORK CONTROL FUNCTION
	109	• •		JLPS11 STOP IN PROGRESS FUNCTION
	10			JUDC TIMER - INITIALIZE
	11			1 RX01 - FLOPPY DISK WRITE PHYSICAL BLOCK
	12			J RX01 - FLOPPY DISK READ PHYSICAL BLUCK
	13			SET MORIZONTAL TAB POSITIONS
	14			
	15		•	ISET SPECIAL TERMINATOR CHARACTERS
	-		• • •	JSET TERMINAL MODE (CHARACTERISTICS)
	16			ISENSE TERMINAL MODE
	17			ICONNECT TO REMOTE TERMINAL (AUTO DIALOUT)
	18	.WORD,		IDISCONNECT FROM REMOTE TERMINAL (HANGUP)
	19	"WORD,	IO.ESA,100,005	JENABLE STATUS AST
	29			
	21 )			
	22	•	SPCIOS A	
	23	, ENDM	SPCIOS	
	24	.ENDM	SPCIOS	

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426	1
427	J HANDLER ERROR CODES RETURNED IN I/O STATUS BLOCK ARE DEFINED THROUGH THIS
428	J MACRO WHICH THEN CONDITIONALLY INVOKES THE MESSAGE GENERATING MACRO
429	FOR THE GIOSYM MSG FILE
430	
431	MACRO .IDER. SYM,LO,MSG
432	DEFINS SYM,LO
433	, IF GT, S\$MSG
434	MCALL . IOMG.
435	IDMG. SYM,LO, <msg></msg>
436	ENDC
437	ENDM . IDER.
438	
439	J GI/O ERROR CODES ARE DEFINED THOUGH THIS MACRO WHICH THEN INVOKES THE
440	J ERROR MESSAGE GENERATING MACRO, ERROR CODES =129 THROUGH =256
441	J ARE USED IN THE GIOSYM.MSG FILE
442	J ARE USED IN THE WIDSTMOND FILE
443	,MACRO .QIOE, SYM,LO,MSG
444	DEFINS SYMALO
445	
440	IF GT,SSMSG
	MCALL , IOMG,
447	,IOHG. SYM, <lo-128,>,<msg></msg></lo-128,>
448	, ENDC
449	,ENDM ,QIOE,
450	3
451	J CONDITIONALLY GENERATE DATA FOR WRITING A MESSAGE FILE FOR MO
452	1
453	"MACRO "IOMG" SYM,LO,MSG
454	.word -^o <lo></lo>
455	.ASCIZ AMSGA
456	. EVEN
457	IIF LT, AO <s\$\$max+<lu>&gt;,\$\$\$MAX==AO<lo></lo></s\$\$max+<lu>
458	ENDM JOMG.
459	
460	3 DEFINE THE SYMBOL SYM WHERE LO IS IS THE LOW ORDER BYTE, HI IS THE HIGH BYTE
461	
462	MACRO .WORD. SYN,LO,HI
463	DEFINS SYM, <a0<hi+400+l0>&gt;</a0<hi+400+l0>
464	ENDM .WORD.
	eran erane

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GIOSYS DEFSG .END GIOMAC - GIOSYM MACRO DEFINITIO MACRO DØ710 25-MAR-75 14:23 PAGE 7-1 Symbol table

IE.ABO= 177761 G	IE_IFU= 177747 G	IE_UPN= 177777 G	IO,FNA= 004400 G	IO,RWU= 002540 G
IE.ACT= 177771 G	IE.ILL= 177726 G	IE.VER= 177774 G	IO.GLC= 016430 G	10.R1C= 002400 G
IE, ADP= 177636 G	IE, ILU= 177640 G	IE WAC= 177743 G	IO.GLI= 016420 G	IO,SAO= 004000 G
IE.ALN= 177736 G	IE.INS= 177776 G	IE, WAT= 177741 G	IO.GNI= 016410 G	
IE.AST= 177660 G	IE.IPR= 177641 G	IE, WER= 177737 G		10,8C9= 013000 G
IE.BAD= 177777 G	<b>-</b>	•	IQ,GRC= 016450 G	IO.SDI= 013000 G
IE.BBE= 177710 G	IE, ISQ= 177703 G	IE, WLK= 177764 G	IO,GRI= 016440 G	IO.3D0= 012400 G
	IE.ITI= 177643 G	IE.2DV= 177720 G	IO.GRN= 016460 G	IO.SEC= 002520 G
IE,BCC= 177676 G	IE, ITS= 177770 G	IO,ACE= 007400 G	IO.HDX= 003010 G	IO, SEM= 002440 G
IE.BD1= 177714 G	IE.LCK= 177745 G	IO,ACR= 006400 G	10,HIS= 015000 G	IO,SHT= 002410 G
IE.BDR= 177716 G	IE.LNL= 177646 G	IO.ACW= 007000 G	IO.INL= 002400 G	IO,SLO= 005400 G
IE.BDV= 177711 G	IE, MBK= 177721 G	IO.ADS= 014000 G	IO,ITI= 017000 G	IO, SMO= 002560 G
IE.8HD= 177700 G	IE, MOD= 177753 G	IO.APC= 014000 G	IO.KIL= 000012 G	IO,SNM= 002450 G
IE.BLB= 177672 G	IE.NBF= 177731 G	IO,APV= 014010 G	IO.LED= 012000 G	IO <b>,</b> SP8= 002420 G
IE.BLK= 177754 G	IE,NBK= 177727 G	IO,ATT= 001400 G	IO.LOV- 001010 G	IO <b>,</b> SPF= 002440 G
IE.BNM= 177712 G	IE,NDR= 177670 G	IO,CAS= 015420 G	IO.LTK= 000050 G	IO.SSO= 004400 G
IE.BTF= 177664 G	IE.NFI= 177704 G	IO,CBD= 016520 G	IO.MCS= 013400 G	IO_SST= 002430 G
IE.8TP= 177725 G	IE,NFW= 177673 G	IO,CBN= 016510 G	IO.MDA= 016000 G	IO.STC= 002500 G
IE.BVR= 177701 G	TE,NLN= 177733 G	IO,CBO= 015510 G	IO.MDI= 014400 G	IO.STP= 016400 G
IE.8YT= 177755 G	IE,NNC= 177063 G	IO,CCI= 014000 G	IO_MDO= 015400 G	IO.SYN= 003040 G
IE.CKP= 177766 G	IE.NNL= 177662 G	IO_CCT= 002460 G	IO_MLO= 006000 G	IO, TRM= 802410 G
IÉ.CKS= 177742 G	IE.NNN= 177674 G	IO,CIN= 016500 G	IO, MOD= 003000 G	IO.UNL= 000042 G
IE.CLO= 177732 G	IE,NOD= 177751 G	IO,CLN= 003400 G	IO_MSO= 005000 G	IO.WAT= 013400 G
IE.CNR= 177667 G	IE,NSF= 177746 G	IO,CON= 015400 G	IO,RAL= 001010 G	IO. WLB= 000400 G
IE.CON# 177752 G	IE,OFL= 177677 G	IO.CPR= 015410 G	IO,RAT= 013000 G	IO, WLS= 000410 G
IE.DAA= 177770 G	IE, ONP= 177773 G	IO.CRC= 001040 G	10, RBC= 003000 G	IO.WLV= 000500 G
IE.DAU= 177763 G	IE_OVR= 177756 G	10.CRE= 012000 G	IO, RCI= 015000 G	IO. WNS= 000420 G
IE,DFU= 177750 G	IE_PRI= 177760 G	IU,CRJ= 015440 G	IO_RCV= 015000 G	IO, WP8= 000440 G
IE.DNA= 177771 G	IE.RAC= 177724 G	IO.CSM= 016470 G	IO.RDB= 001200 G	IO.WVB- 011000 G
IE.DNR= 177775 G	IE_RAT= 177723 G	IO,CTI= 015400 G	IO. RDN= 000022 G	IO.XMT= 014400 G
IE.DUN= 177767 G	IE_R8G= 177730 G	IO,CTL= 016400 G	IO,REL= 013400 G	IO.XNA= 014410 G
IE.DUP= 177707 G	JE.RCN= 177722 G	IO.CTR= 015610 G	IO, RHD= 001010 G	IQ.Q = 000002 G
IE.EOF* 177766 G	IE.RER= 177740 G	IO.DAC= 010000 G	IO.RLB= 001000 G	IQ.X = 000001 G
IE.E01= 177702 G	IE.RNM= 177715 G	IO.DCI= 014400 G	IO.RLV= 001100 G	IS.8V - 000005 G
IE.EOV= 177765 G	IE RSU= 177757 G	IO.DCT= 002470 G	IO, RNA= 005400 G	IS.CLR= 000000 G
IE.EXP= 177665 G	IE.SDP= 177635 G	IO.DEL= 012400 G	IO.RNC= 001040 G	$IS_{CR} = 006401 G$
IE.FEX= 177717 G	IE SNC= 177735 G	10.DET= 002000 G	IO.RNE= 001020 G	IS.ESC= 015401 G
IE.FHE= 177705 G	IE.SPC= 177772 G	IO.UIS= 016000 G	IG.RNS= 001020 G	IS.PNO= 000000 G
IE.FOP= 177713 G	IE.SQC= 177734 G	IO,UTI= 016000 G	IO, RPB= 001040 G	IS, SET = 000002 G
IE.HFU= 177744 G	IE, SRE= 177762 G	IO.ENA= 006000 G	IO.RTC= 003400 G	IS, SPD= 000002 G
IE.HWH# 177772 G	IE.STK= 177706 G	10,EOF= 003000 G	IO.RTI= 016400 G	IS.SUC= 000001 G
IE.IDU= 177644 G	IE.TMM= 177671 G	IU,ESA= 002500 G	IG_RTK= 000060 G	QI_VER= 808384 G
IE.IEF. 177637 G	TE.TMO= 177666 G	IO.EXT= 011400 G	IO.RV8= 010400 G	SSMSG = 000000
IE.IFC= 177776 G	IE_ULN= 177773 G	IO.FDX= 003020 G	IO,RWD= 002400 G	
		·····	ANSUMAL DECADE A	GBL= 000001

. ABS, 000000 000 00000 001

ERRORS DETECTED: 0

FREE CORE: 5669, WORDS ,LP:=(156,133)@IOMAC,TI:

## APPENDIX D

#### DIRECTIVE PARAMETER BLOCKS

#### D.1 QUEUE I/O

A 6- to 12-word DPB of the following format is used for the Queue I/O.

```
wD. 00 -- DIC (01.) & DPB SIZE (6-12),
wD. 01 -- I/U FUNCTION CODE,
wD. 02 -- LUN,
wD. 03 -- [EFN] & [PRIORITY],
wD. 04 -- [ADDRESS OF I/U STATUS BLOCK],
wD. 05 -- [I/U DUNE AST SERVICE ENTRY POINT],
wD. 05 -- [I/U DUNE AST SERVICE ENTRY POINT],
wD. 06 -- PARAMETER #1,
wD. 07 -- PARAMETER #2,
wD. 10 -- PARAMETER #4,
wD. 11 -- PARAMETER #4,
wD. 12 -- PARAMETER #5,
wD. 13 -- PARAMETER #6.
```

#### D.2 QUEUE I/O AND WAIT

A 6- to 12-word DPB of the following format is used.

```
AD. 00 -- DIC (03.) & DPB SIZE (6-12),

WD. 01 -- I/U FUNCTION CUDE,

WD. 02 -- LUN,

WD. 03 -- (EFN) & (PRIURITY),

WD. 04 -- LADDRESS OF I/U STATUS BLOCK),

WD. 05 -- (I/U DUNE AST SERVICE ENTRY POINT),

WD. 05 -- PARAMETER #1,

WD. 07 -- PARAMETER #2,

WD. 10 -- PARAMETER #3,

WD. 11 -- PARAMETER #4,

WD. 12 -- PARAMETER #5,

WD. 13 -- PARAMETER #6.
```

#### D.3 GET LUN INFORMATION

WD. UU -- DIC (US.) & DPB SIZE (3.), WD. UI -- LUN, AD. UZ -- ADDRESS OF SIX-WURD BUFFER.

## D.4 ASSIGN LUN

WD. 00 -- DIC (07.) & DPB SIZE (4.), WD. 01 -- LUGICAL UNIT NUMBER, ND. 02 -- PHYSICAL DEVICE NAME, ND. 03 -- PHYSICAL DEVICE UNIT NUMBER.

## D.5 ALTER PRIORITY

A 4-word DPB in the following format is used.

WD. UU -- DIC (009.) & DPB SIZE (4.). WD. UI -- TASK NAME (FIRST HALF), WD. UZ -- TASK NAME (SECUND HALF). WD. U3 -- TASK PRIORITY.

## D.6 REQUEST

A 7-word DPB in the following format is used.

wD. UU -- DIC (11.) & DPB SIZE (7.), wD. UI -- TASK NAME (FIRST HALF), AD. U2 -- TASK NAME (SECUND HALF), wD. U3 -- [PARTITIUN NAME (FIRST HALF)], wD. U4 -- [PARTITIUN NAME (SECUND HALF)], wD. U5 -- [PRIORITY], wD. U5 -- [UIC].

## D.7 EXECUTE

A 7-word DPB in the following format is used.

WD. 0J -- DIC (13.) & DPB SIZE (7.), WD. 01 -- TASK NAME (FIRST HALF), WD. 02 -- TASK NAME (SECOND HALF), WD. 03 -- [PARTITION NAME (FIRST HALF)], WD. 04 -- [PARTITION NAME (SECOND HALF)], WD. 05 -- [PRIORITY], WD. 05 -- [JIC].

#### D.8 SCHEDULE

A 13-word DPB in the following format is used.

wD. 00 -- DIC (15.) & DPB SIZE (13.), wD. 01 -- TASK NAME (FIRST HALF), wD. 02 -- TASK NAME (SECUND HALF), wD. 03 -- [PARTITION NAME (FIRST HALF)], wD. 04 -- [PARTITION NAME (SECUND HALF)], wD. 05 -- [PRIORITY], wD. 05 -- [PRIORITY], wD. 06 -- [UIC], wD. 07 -- SCHEDULE HUURS (0-23), wD. 10 -- SCHEDULE HUURS (0-59), wD. 11 -- SCHEDULE MINUTES (0-59), wD. 11 -- SCHEDULE SECUNDS (0-59), wD. 12 -- SCHEDULE FICKS (0-59), wD. 13 -- [RE-SCHEDULE INTERVAL MAGNITUDE], wD. 14 -- [RE-SCHEDULE INTERVAL UNITS (1-4)].

## D.9 RUN

An ll-word DPB in the following format is used.

WD. 00 -- DIC (17.) & DPB SIZE (11.),
WD. 01 -- TASK NAME (FIRST HALF),
WD. 02 -- TASK NAME (SECUND HALF),
WD. 03 -- (PARTITION NAME (FIRST HALF)),
WD. 04 -- (PARTITION NAME (SECUND HALF)),
WD. 05 -- (PRIORITY),
WD. 05 -- (UIC),
WD. 07 -- SCHEDULE DELTA MAGNITUDE,
WD. 10 -- SCHEDULE DELTA UNITS (1-4),
WD. 11 -- (RE-SCHEDULE INTERVAL UNITS (1-4)].

#### D.10 SYNC

A 12-word DPB in the following format is used.

ND. 00 -- DIC (19.) & DPB SIZE (12.), ND. 01 -- TASK NAME (FIRST HALF), ND. 02 -- TASK NAME (SECUND HALF), ND. 03 -- [PARTITION NAME (FIRST HALF)], ND. 04 -- [PARTITION NAME (SECUND HALF)], ND. 05 -- [PRIURITY], ND. 05 -- [JIC], ND. 06 -- [JIC], ND. 07 -- SCHEDULE DELTA MAGNITUDE, ND. 10 -- SCHEDULE DELTA UNITS (1-4), ND. 11 -- SYNCHRUNIZATION UNITS (1-4), ND. 12 -- [RE-SCHEDULE INTERVAL MAGNITUDE], ND. 13 -- [RE-SCHEDULE INTERVAL UNITS (1-4)].

#### D.11 MARK TIME

A 5-word DPB in the following format is used.

WD. UU -- DIC (23.) & DPB SIZE (5.), ND. UI -- LEVENT FLAG NUMBER (EFN)], ND. U2 -- TIME INTERVAL MAGNITUDE, ND. U3 -- TIME INTERVAL UNITS, ND. U4 -- [SYSTEM TRAP ENTRY PUINT].

#### D.12 CANCEL SCHEDULED REQUESTS

A 3-word DPB in the following format is used to cancel all scheduled requests for an indicated task.

WD. 00 -- DIC (25.) & DPB SIZE (3.), WD. 01 -- SCHEDULED TASK NAME (FIRST HALF), WD. 02 -- SCHEDULED TASK NAME (SECOND HALF). A 5-word DPB in the following format is used to cancel only those requests issued for an indicated task by an indicated task.

WD. 00 -- DIC (25.) & DPB SIZE (5.), WD. 01 -- SCHEDULED TASK NAME (FIRST HALF), WD. 02 -- SCHEDULED TASK NAME (SECUND HALF), WD. 03 -- (SCHEDULER TASK NAME (FIRST HALF)), WD. 04 -- (SCHEDULER TASK NAME (SECOND HALF)).

#### D.13 CANCEL MARK TIME REQUESTS

A 1-word DPB of the following format is used to cancel all Mark Time requests made by the issuing task.

WD. UU -- DIC (27.) & DPB SIZE (1.).

A 3-word directive in the following format is used to cancel only Mark Time request made by the issuing task and that set an indicated event flag or cause an AST at an indicated location.

ND. UU -- UIC (27.) & DPB SIZE (3.), ND. UI -- [EVENI FLAG NUMBER (EFN)], ND. UZ -- [AST SERVICE RUUTINE ENIRY].

## D.14 CLEAR EVENT FLAG

A 2-word DPB of the following format is used.

WD. UU -- DIC (31.) & DPB SIZE (2.), WD. UI -- EVENI FLAG NUMBER (EFN).

### D.15 SET EVENT FLAG

A 2-word DPB in the following format is used.

WD. UU -- DIC (33.) & DPB SIZE (2.), WD. UI -- EVENI FLAG NUMBER (EFN).

## D.16 DECLARE SIGNIFICANT EVENT

A 2-word DPB of the following format is used to read an event flag, set an event flag, declare a significant event, and to report the pre-event flag polarity.

wp. UU -- DIC (35.) & DPB SIZE (2.), wd. UI -- Eveni flag number (Efn).

A 1-word DPB in the following format is used to declare a significant event.

WU. UU -- UIC (35.) & DPB SIZE (1.).

## D.17 READ EVENT FLAG

A 2-word DPB of the following format is used.

WD. UU -- DIC (37.) & DPB SIZE (2.), WD. UI -- EVENI FLAG NUMBER (EFN).

### D.18 READ ALL FLAGS

A 2-word DPB of the following format is used.

WD. UU -- DIC (39.) & DPB SIZE (2.), WD. UI -- ADDRESS (VIRTUAL) UF 64-BIT BUFFER.

#### D.19 WAIT FOR SINGLE EVENT FLAG

A 2-word DPB of the following format is used.

WD. UU -- DIG (41.) & DPH SIZE (2.), WD. UI -- EVENT FLAG NUMBER (EFN).

## D.20 WAIT FOR LOGICAL OR OF FLAGS

A 3-word DPB of the following format is used to wait for event flags of sets 0, 1, 2, or 3.

wD. UU -- UIC (43.)  $\approx$  DPB SIZE (3.), wD. U1 -- SET INDICATUR (0, 1, 2, 3), wD. U2 -- SIXIEEN FLAG MASK WURD.

A 5-word DPB of the following format is used to wait for event flags of set 4.

ND. UU -- DIC (43.) & DPB SIZE (5.), ND. UI -- MASK WURD FUR FLAGS 1-10, WD. UZ -- MASK WURD FUR FLAGS 17-32, WD. U3 -- MASK WURD FUR FLAGS 33-48, ND. U4 -- MASK WURD FUR FLAGS 49-64.

#### D.21 WAIT FOR NEXT SIGNIFICANT EVENT

A l-word DPB of the following format is used.

WD. UU -- DIC (49.) & DPB SIZE (1.).

#### D.22 SUSPEND

A 1-word DPB of the following format is used.

WD. 00 -- DIC (45.) & DPB SIZE (1.).

#### D.23 RESUME

A 3-word DPB of the following format is used.

WD. UU -- DIC (47.) & DPB SIZE (3.), WD. UI -- TASK NAME (FIRST HALF), WD. UZ -- TASK NAME (SECUND HALF).

## D.24 EXIT

A 1-word DPB of the following format is used.

WD. UU -- DIC (51.) & DPB SIZE (1.).

## D.25 EXITIF

A 2-word DPB of the following format is used.

WD. 00 -- DIC (53.) & DPB SIZE (2.), WD. 01 -- EVENT FLAG NUMBER.

## D.26 GET TIME PARAMETERS

A 2-word DPB of the following format is used.

wD. UU -- DIC (61.) & DPB SIZE (2.), wp. U1 -- ADDRESS UF 8-WURD BUFFER.

#### D.27 GET TASK PARAMETERS

A 2-word DPB of the following format is used.

WD. 00 -- DIC (63.) & DPB SIZE (2.), WD. 01 -- ADDRESS OF SIXIEEN WORD BUFFER.

#### D.28 GET PARTITION PARAMETERS

A 4-word DPB of the following format is used.

WD. UU -- UIC (65.) & DPB SIZE (4.), WD. UI -- LPARTITION NAME (FIRST HALF)], WD. UZ -- (PARTITION NAME (SECOND HALF)], WD. U3 -- ADDRESS UF THREE WURD BUFFER.

#### D.29 GET COMMON BLOCK PARAMETERS

A 4-word DPB of the following format is used.

WD. 00 -- DIC (67.) & DPB SIZE (4.), WD. 01 -- CUMMON BLOCK NAME (FIRST HALF), WD. 02 -- CUMMON BLOCK NAME (SECOND HALF), WD. 03 -- ADDRESS OF EIGHT WORD BUFFER.

## D.30 SEND DATA

A 5- to 8-word DPB of the following format is used. wD. 00 -- DIC (71.) & DPB SIZE (5.-8.), wD. 01 -- RECEIVER TASK NAME (FIRST HALF), wD. 02 -- RECEIVER TASK NAME (SECOND HALF), wD. 03 -- ADDRESS OF DATA BLOCK, wD. 04 -- (EVENT FLAG NUMBER), wD. 05 -- (BUFFER SIZE - 1-255.), wD. 06 -- (PRIORITY OF SEND), wD. 07 -- (RECEIVER T1).

## D.31 SEND AND REQUEST OR RESUME

A 9- to 12-word DPB of the following format is used. wD. 00 -- DIC (73.) & DPB SIZE (9.-12.), wD. 01 -- RECEIVER TASK NAME (FIRST HALF), wD. 02 -- RECEIVER TASK NAME (SECOND HALF), wD. 03 -- IPARTITION NAME (FIRST HALF)], wD. 04 -- IPARTITION NAME (SECOND HALF)], wD. 05 -- IPRIORITY], wD. 05 -- IPRIORITY], wD. 06 -- IUIC], wD. 07 -- ADDRESS OF DATA BLUCK, wD. 10 -- IEVENT FLAG NUMBER], wD. 11 -- (BUFFER SIZE - 1-255.), wD. 12 -- IPRIORITY OF SEND), wD. 13 -- IRECEIVER TIJ.

# D.32 RECEIVE DATA

A 4- to 6-word DPB of the following format is used.

WD. 00 -- DIC (75.) & DPB SIZE (4-6), WD. 01 -- [SENDER TASK NAME (FIRST HALF)], WD. 02 -- [SENDER TASK NAME (SECOND HALF)], WD. 03 -- ADDRESS OF BUFFER. WD. 04 -- [BUFFER SIZE - 1-255.], WD. 05 -- [LOC. TO STORE TI].

## D.33 RECEIVE DATA OR EXIT

A 4- to 6-word DPB of the following format is used.

WD. UU -- DIC (77.) & DPB SIZE (4-6), WD. UI -- ISENDER TASK NAME (FIRST HALF)], WD. U2 -- [SENDER TASK NAME (SECUND HALF)], WD. U3 -- ADDRESS OF BUFFER. WD. U4 -- [BUFFER SIZE - 1-255.], WD. U5 -- [LUC. TU STURE II].

## D.34 RECEIVE DATA OR SUSPEND

A 4- to 6-word DPB of the following format is used.

WD. UU -- DIC (79.) & DPB SIZE (4-6), WD. UI -- (SENDER TASK NAME (FIRST HALF)], WD. U2 -- (SENDER TASK NAME (SECUND HALF)), WD. U3 -- ADDRESS OF BUFFER. WD. U4 -- (BUFFER SIZE - 1-255.), WD. U5 -- (LUC. TU STORE TI).

## D.35 ABORT

A 3-word DPB of the following format is used.

WD. 00 -- DIC (83.) & DPB SIZE (3.), WD. 01 -- PASK NAME (FIRST HALF), WD. 02 -- PASK NAME (Secund Half).

## D.36 FIX-IN-MEMORY

A 3-word DPB of the following format is used.

WD. UU -- DIC (85.) & DPB SIZE (3.), AD, 01 -- TASK NAME (FIRST HALF), WD. UZ -- TASK NAME (SECUND HALF).

## D.37 UNFIX

A 3-word DPB of the following format is used.

WD. UU -- DIC (87.) & DPB SIZE (3.), WD. UI -- IASK NAME (FIRST HALF), WD. UZ -- FASK NAME (SECUND HALF).

# D.38 DISABLE

A 3-word DPB of the following format is used.

wD. UU -- DIC (91.) & DPB SIZE (3.), wD. U1 -- TASK NAME (FIRST HALF), wU. U2 -- TASK NAME (SECUND HALF).

#### D.39 ENABLE

A 3-word DPB of the following format is used.

WD. 00 -- DIC (93.) & DPB SIZE (3.), WD. 01 -- TASK NAME (FIRST HALF), WD. 02 -- TASK NAME (SECOND HALF).

## D.40 DISABLE CHECKPOINTING

A 1-word DPB of the following format is used.

#### D.41 ENABLE CHECKPOINTING

A 1-word DPB of the following format is used. wD. 00 -- DIC (97.) & DPB SIZE (1.).

## D.42 INHIBIT AST RECOGNITION

A 1-word DPB of the following format is used.

## D.43 ENABLE AST RECOGNITION

A 1-word DPB of the following format is used. wD. 00 -- DIC (101.) & DPb SIZE (1.).

## D.44 SPECIFY SST VECTOR TABLE FOR DEBUGGING AID

A 3-word DPB of the following format is used.

WD. UU -- DIC (103.)  $\leftarrow$  DPB SIZE (3.), WD. U1 -- ADDRESS UF SST VECTUR TABLE, WD. U2 -- NUMBER UF TABLE ENTRIES (8).

#### D.45 SPECIFY SST VECTOR TABLE FOR TASK

A 3-word DPB of the following format is used.

WD. OU -- DIC (105.) & DPB SIZE (3.), WD. UI -- ADDRESS UF SST VECTUR TABLE, WD. UZ -- NUMBER OF TABLE ENTRIES (8).

# D.46 SPECIFY RECEIVE AST

A 2-word DPB of the following format is used.

ND. UU -- DIC (107.) & DPB SIZE (2.), ND. UI -- LAST SERVICE ENTRY PUINT].

### D.47 SPECIFY POWER FAIL AST

A 2-word DPB of the following format is used.

WD. 00 -- UIC (109.) & DPB SIZE (2.), WD. 01 -- (AST SERVICE ENTRY PUINT).

## D.48 SPECIFY FLOATING POINT EXCEPTION AST

A 2-word DPB of the following format is used.

wD. UU -- DIC (111.) & DPB SIZE (2.), wD. UI -- LAST SERVICE ENTRY POINT].

## D.49 AST SERVICE EXIT

A 1-word DPB of the following format is used.

WD. 00 -- DIC (115.) & DPB SIZE (1.).

D.50 GET SENSE SWITCHES

A 1-word DPB of the following format is used.

WU. UU -- DIC (125.) & DPB SIZE (1.).

## D.51 GET MCR COMMAND LINE

A 41-word DPB of the following format is used.

wD. 00 -- DIC (127.) & DPB SIZE (41.), wD. 01 -- FIRST WURD UF 80-BYTE BUFFER.

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