

RP11E

FUNCT LOGIC + READ/WRITE
MD-11-DZRPY-C

EP-DZRPY-C-DL-C
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The main body of the document is a grid of 12 columns and 12 rows of small, illegible technical diagrams or data tables. Each cell in the grid contains a small, dark image that is too faint to read. The diagrams appear to be related to the 'FUNCT LOGIC + READ/WRITE' title, possibly showing circuit logic or data flow. The grid is the primary content of the document, occupying most of the page area.

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IDENTIFICATION

PRODUCT CODE: MAINDEC-11-DZRPY-C-D
PRODUCT NAME: RP11E FUNCTIONAL LOGIC & READ/WRITE TEST
DATE: SEPTEMBER 1977
MAINTAINER: DIAGNOSTIC GROUP
AUTHOR: C. HESS

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CONTENTS

1. ABSTRACT
2. REQUIREMENTS
 - 2.1 EQUIPMENT
 - 2.2 STORAGE
 - 2.3 PRELIMINARY PROGRAMS
3. STARTING PROCEDURE
 - 3.1 LOADING PROCEDURES
 - 3.2 STARTING ADDRESSES
 - 3.3 OPERATOR ACTION
 - 3.4 'CONTROL C' OPTION
 - 3.5 DEFAULT RP11E UNIBUS ADDRESS
4. OPERATING PROCEDURES
 - 4.1 OPERATIONAL SWITCH SETTINGS
 - 4.2 TELETYPE KEYBOARD AND PRINTER UNIBUS ADDRESSES
 - 4.3 TTY KEYBOARD ENTRY CONTROL CODES AND CONVENTIONS
 - 4.4 RP11E UNIBUS ADDRESS CHANGE ROUTINE
 - 4.5 CONVERSATION MODE
 - 4.6 OPERATION OF TESTS 15, 16, & 17
5. ERROR HANDLING
 - 5.1 ERROR REPORTING
 - 5.2 ERROR TYPES
6. RESTRICTIONS
 - 6.1 SUBSYSTEMS WITH MIXED DRIVE TYPES
 - 6.2 FORMATTED PACKS
 - 6.3 'CONTROL C' TERMINATION DURING TESTS 5 & 13
 - 6.4 CONTROLLER INTERRUPT
7. MISCELLANEOUS
 - 7.1 RUN TIME
 - 7.2 STACK POINTER
 - 7.3 END OF PASS/END OF TEST
 - 7.4 SUBROUTINE CALLS
 - 7.5 DISK SURFACE USAGE
 - 7.6 TEST EXECUTION SEQUENCE
 - 7.7 DRIVE STATUS TIMEOUT
 - 7.8 DATA COMPARISON USING MEMORY MANAGEMENT
 - 7.9 PATTERN SELECTION WORD
 - 7.10 CONTROLLER/DRIVE OPERATION TIME LIMIT
8. TEST DESCRIPTIONS
9. PROGRAM LISTING

1. ABSTRACT

THE RP11E FUNCTIONAL LOGIC & READ/WRITE TEST PERFORMS A CURSORY CHECK OF THE RP11E CONTROLLER LOGIC AND PERFORMS DATA STORAGE/RETRIEVAL TESTING OF THE SUBSYSTEM. INCLUDED IN THE PROGRAM ARE UTILITY TESTS WHICH ASSIST IN PERFORMING HEAD ALIGNMENT, WHICH CHECK SUBSYSTEM POWER FAIL OPERATION, AND WHICH CHECK THE RP11E AND DRIVE CONTROL SWITCHES.

THE PROGRAM CONTAINS A 'CONVERSATION MODE' WHICH ALLOWS THE OPERATOR TO MODIFY THE OPERATION PARAMETERS FOR THE DATA STORAGE/RETRIEVAL TEST. THE PROGRAM WILL USE MEMORY MANAGEMENT IN THE OPERATION OF THE DATA TEST IF THE PROGRAM IS BEING RUN ON A MEMORY MANAGEMENT SYSTEM.

THIS PROGRAM COMPLETES THE TESTING STARTED IN THE DISKLESS CONTROLLER TEST (MD-11-DZRPW). TESTING IN THE FUNCTIONAL LOGIC PROGRAM IS PERFORMED WITH 1 - 8 RP02, RPR02, OR RP03 DISK DRIVES.

2. REQUIREMENTS

2.1 EQUIPMENT

PDP-11 COMPUTER WITH 12K OF MEMORY; CONSOLE TELETYPE; RP11E DISK CONTROLLER; 1 TO 8 RP02, RPR02, OR RP03 DISK DRIVES WITH FORMATTED PACKS.

2.2 STORAGE

THIS PROGRAM WILL LOAD AND RUN IN 12K.

2.3 PRELIMINARY PROGRAMS

MAINDEC-11-DZRPW - RP11E DISKLESS LOGIC TEST

3. STARTING PROCEDURES

3.1 LOADING PROCEDURES

THE PROGRAM MAY BE LOADED FROM EITHER PAPER TAPE OR AN 'XXDP' DEVICE. REFER TO EITHER THE STANDARD PROCEDURES FOR LOADING 'ABS' FORMAT PAPER TAPES OR TO THE 'XXDP' SYSTEM REFERENCE DOCUMENT.

THIS PROGRAM MAY BE INCLUDED IN AN 'XXDP' CHAIN. IF THE PROGRAM IS PART OF A CHAIN AND IS LOADED FROM AN RP02, RPR02, OR AN RP03, THE PROGRAM WILL NOT USE DRIVE 0 FOR TESTING.

3.2 STARTING ADDRESSES

200 NORMAL STARTING ADDRESS
 204 SELECT OPERATING PARAMETERS
 210 SELECT RP11 ADDRESS
 214 COMBINATION OF 204 AND 210

3.2.1 START FROM LOCATION 200

WHEN THE PROGRAM IS STARTED FROM LOCATION 200, ALL ONLINE DRIVES WILL BE USED (SEE SECTION 3.1 FOR DRIVE 0 EXCEPTION) USING TESTS 0 - 14. ALL PARAMETERS FOR TEST 14 WILL BE RETURNED TO THEIR DEFAULT VALUES.

3.2.2 START FROM LOCATION 204

WHEN THE PROGRAM IS STARTED FROM LOCATION 204, PARAMETERS FOR TEST 14 ARE RESET TO THEIR DEFAULT VALUES, AND THE PROGRAM ENTERS CONVERSATIONAL MODE.

3.2.3 START FROM LOCATION 210

WHEN THE PROGRAM IS STARTED FROM LOCATION 210, OPERATION IS THE SAME AS THE START FROM 200, EXCEPT THAT THE PROGRAM ASKS FOR AN RP11E ADDRESS. THE OPERATOR MAY ENTER A NEW ADDRESS VALUE OR HE MAY RETAIN THE CURRENT VALUE. THE PARAMETERS FOR TEST 14 ARE RESET TO THEIR DEFAULT VALUES.

3.2.4 START FROM LOCATION 214

PROGRAM START FROM LOCATION 214 IS THE SAME AS THE START FROM LOCATION 204 EXCEPT THAT THE PROGRAM ASKS FOR A NEW RP11E ADDRESS. THE PARAMETERS FOR TEST 14 ARE RESET TO THEIR DEFAULT VALUES.

3.3 OPERATOR ACTION

BEFOR STARTING REFER TO SECTION 4.1

1. LOAD PROGRAM INTO MEMORY (SEE SECTION 3.1)
2. CHANGE TELETYPE KEYBOARD AND PRINTER ADDRESSES AS REQUIRED (SEE SECTION 4.2)
3. PLACE A FORMATTED PACK ON DRIVE(S) TO BE USED
4. BRING DRIVE(S) TO ONLINE STATE
5. PLACE 'FORMAT' AND 'MAINTENANCE' SWITCHES ON THE RP11E CONTROL PANEL TO 'NORMAL'
6. SET CONTROLLER AND DRIVE WRITE LOCKOUT SWITCHES AS REQUIRED
7. LOAD ADDRESS 200, 204, 210, OR 214.
8. SET CPU SWITCH REGISTER SWITCHES (SEE SECTION 4.1)
9. PRESS START ON THE CPU CONTROL PANEL

3.4 'CONTROL C' OPTION

THE OPERATOR MAY TERMINATE THE CURRENT TEST AT ANY TIME BY TYPING A 'CONTROL C' ON THE TELETYPE KEYBOARD. TYPING A 'CONTROL C' CAUSES THE PROGRAM TO ENTER THE 'CONVERSATION MODE' ROUTINE. ALL THE PARAMETERS FOR TEST 14 WILL BE RETURNED TO THEIR DEFAULT VALUES.

3.5 DEFAULT RP11E UNIBUS ADDRESS

THE DEFAULT RP11E UNIBUS ADDRESS IS 176710. WHEN THE PROGRAM IS STARTED FROM EITHER LOCATION 200 OR LOCATION 204, ADDRESS 176710 WILL BE CHECKED AND IF NO RESPONSE IS RECEIVED FROM THAT ADDRESS THE PROGRAM WILL ENTER THE RP11E ADDRESS CHANGE ROUTINE. ADDRESS CHANGE OPERATION WILL BE IDENTICAL TO THE OPERATION WHEN THE PROGRAM IS STARTED FROM EITHER LOCATION 210 OR 214. (REFER TO SECTION 4.4)

4. OPERATING PROCEDURES

IF THE DIAGNOSTIC IS RUN ON A CPU WITHOUT A SWITCH REGISTER THEN A SOFTWARE SWITCH REGISTER IS USED WHICH ALLOWS THE USER THE SAME SWITCH OPTIONS AS THE HARDWARE SWITCH REGISTER. IF THE HARDWARE SWITCH REGISTER DOES NOT EXIST OR IF ONE DOES AND IT CONTAINS ALL ONES (177777) THEN THE SOFTWARE SWITCH REGISTER (LOC. 176) IS USED.

CONTROL:

THIS PROGRAM ALSO SUPPORTS THE DYNAMIC LOADING OF THE SOFTWARE SWITCH REGISTER (LOC. 176) FROM THE TTY. THIS CAN BE ACCOMPLISHED BY DOING THE FOLLOWING:

- 1) TYPE CONTROL G (<↑G>); THIS WILL ALLOW THE TTY TO ENTER DATA INTO LOC. 176 AT SELECTED POINTS WITHIN THE PROGRAM.
- 2) THE MACHINE WILL THEN TYPE: SWR=XXXXXXNEW= (XXXXXX IS THE OCTAL CONTENTS OF THE SOFTWARE SWITCH REGISTER.)
- 3) AFTER THE ''NEW='' HAS BEEN TYPED THEN THE OPERATOR CAN DO ONE OF THE FOLLOWING AT THE TTY:
 - A) TYPE A NUMBER TO BE LOADED INTO LOC. 176 FOLLOWED BY A <CR>. (ONLY NUMBERS BETWEEN 0-7 WILL BE ACCEPTED AND ONLY 6 NUMBERS WILL BE ALLOWED) IF A <CR> IS THE FIRST KEY DEPRESSED THE SOFTWARE SWITCH REGISTER CONTENTS WILL NOT BE CHANGED.
 - B) IF A CONTROL U (<↑U>) IS DEPRESSED THEN THE PROGRAM WILL SEND YOU BACK TO STEP 2.

4.1 OPERATIONAL SWITCH SETTINGS

WITH SW<15:0>=1, THE PROGRAM WILL TYPEOUT ERRORS AND CONTINUE WITH THE TEST.

THE SWITCH SETTINGS ARE:

SW<15>=1...HALT ON ERROR
 SW<14>=1...LOOP ON TEST
 SW<13>=1...INHIBIT ERROR TIMEOUTS
 SW<11>=1...INHIBIT ITERATIONS
 SW<10>=1...RING BELL ON ERROR
 SW<09>=1...LOOP ON ERROR
 SW<07>=1...DON'T TYPE DRIVE STATUS WHEN PROGRAM STARTED OR WHEN
 ENTERING CONVERSATION MODE.
 CHANGE HEAD SELECTION IN TEST 16.
 SW<06>=1...DON'T USE MEMORY MANAGEMENT IN TEST 14
 SW<05>=1...DISPLAY ALL READ RETRY ATTEMPTS IN TEST 14
 SW<04>=1...TYPE ALL DATA COMPARE ERRORS IN TESTS 13 & 14
 SW<03>=1...INHIBIT DATA COMPARE IN TESTS 13 & 14
 SW<02>=1...BYPASS READ & DATA COMPARE IN TEST 14
 SW<01>=1...BYPASS WRITE CHECK IN TEST 14
 SW<00>=1...BYPASS WRITE IN TEST 14

4.2 TELETYPE KEYBOARD AND PRINTER UNIBUS ADDRESSES

THE PROGRAM ASSUMES THE FOLLOWING UNIBUS ADDRESSES. THESE ADDRESSES MAY BE CHANGED AT THE INDICATED LOCATION BEFORE STARTING THE PROGRAM.

MEMORY LOCATION	CONTENTS	FUNCTION
1136	177560	TTY KEYBOARD STATUS REG
1140	177562	TTY KEYBOARD BUFFER REG
1142	177564	TTY PRINTER STATUS REGISTER
1144	177566	TTY PRINTER BUFFER REG

(THE RP11E UNIBUS ADDRESS IS CHANGED IN RESPONSE TO THE PROGRAM'S TYPED REQUEST IF THE PROGRAM IS STARTED FROM EITHER LOCATION 210 OR 214 OR IF THERE IS NO RESPONSE WHEN LOCATION 176710 IS ADDRESSED.)

4.3 TTY KEYBOARD ENTRY CONTROL CODES AND CONVENTIONS

THE FOLLOWING CONTROL CODES AND CONVENTIONS ARE USED BY THE PROGRAM:

CARRIAGE RETURN (CR) - ALL ENTRIES MUST BE TERMINATED BY A CARRIAGE RETURN.

CONTROL U (↑U) - IF THIS CONTROL CODE IS TYPED BEFORE AN ENTRY IS TERMINATED, THE ENTRY IS CLEARED AND ANOTHER VALUE MAY BE ENTERED. A ↑U IS TYPED BY PRESSING THE 'CONTROL' AND 'U' KEYS TOGETHER.

RUBOUT (RO) - IF THIS CONTROL CODE IS TYPED, THE CHARACTER TYPED BEFORE THE 'RO' WILL BE DELETED FROM THE ENTRY. EACH TIME A 'RO' IS TYPED, THE LAST CHARACTER IN THE INPUT ENTRY IS DELETED. WHEN A 'RO' IS TYPED, THE ENTRY ROUTINE WILL TYPE A BACKSLASH ('\') CHARACTER AND THE CHARACTER BEING DELETED. WHEN A CHARACTER OTHER THAN A 'RO' IS TYPED, THE ENTRY ROUTINE WILL TYPE ANOTHER BACKSLASH CHARACTER AND TYPE THE NEW CHARACTER ENTERED. THE 'RO' CANNOT BE USED TO DELETE A 'CR'.

CONTROL C (IC) - THE OPERATION OF THE 'IC' CHARACTER IS DESCRIBED IN SECTION 3.4. 'IC' MAY ALSO BE USED TO ABORT THE CONVERSATION MODE AND TO RETURN TO THE BEGINNING OF CONVERSATION MODE. 'IC' IS GENERATED BY PRESSING THE 'CONTROL' KEY AND THE 'C' KEY.

IF ONLY A 'CR' IS ENTERED IN RESPONSE TO AN ENTRY REQUEST FROM THE PROGRAM, THE PROGRAM WILL USE THE DEFAULT VALUE FOR THE REQUESTED ENTRY.

LEADING ZEROS ARE NOT REQUIRED ON ANY ENTRY.

4.4

RP11E UNIBUS ADDRESS CHANGE ROUTINE

WHEN THE PROGRAM IS STARTED FROM LOCATION 200 OR LOCATION 204, THE PROGRAM WILL ASSUME THAT THE RP11E IS AT UNIBUS ADDRESS 176710. WHEN THE PROGRAM IS STARTED FROM EITHER LOCATION 210 OR LOCATION 214, THE PROGRAM ENTERS A ROUTINE WHICH ALLOWS THE OPERATOR TO CHANGE THE RP11E UNIBUS ADDRESS TO BE USED BY THE PROGRAM.

WHEN THE ADDRESS CHANGE ROUTINE IS ENTERED, THE PROGRAM TYPES THE PRESENT RP11E UNIBUS ADDRESS:

RPADR = AAAAAA/

NOTE: 'AAAAAA' REPRESENTS THE PRESENT ADDRESS

THE ADDRESS TYPED OUT MAY BE USED BY TYPING A 'CR' ONLY. A NEW ADDRESS IS ENTERED BY TYPING THE NEW ADDRESS AND TERMINATING THE ENTRY WITH A 'CR'. (THE NEW ADDRESS WILL BE USED UNTIL ANOTHER ADDRESS IS ENTERED.)

AFTER THE OPERATOR HAS RESPONDED TO THE ADDRESS ENTRY REQUEST, THE PROGRAM WILL VERIFY THAT THE RP11E ADDRESS RESPONDS. IF NO RESPONSE IS RECEIVED FROM THE RP11E ADDRESS, THE FOLLOWING ERROR MESSAGE IS TYPED:

RP11E DIDN'T RESPOND TO ADDRESSING
RPADR
AAAAA

THE PROGRAM RETURNS TO THE ADDRESS ENTRY ROUTINE. THE PROGRAM WILL NOT CONTINUE UNTIL THE OPERATOR ENTERS AN ADDRESS FOR WHICH THERE IS A UNIBUS RESPONSE.

4.5 CONVERSATION MODE

THE PROGRAM CONTAINS A 'CONVERSATION MODE' ROUTINE WHICH ALLOWS THE OPERATOR TO SELECT AND EXECUTE INDIVIDUAL TESTS AND TO SPECIFY THE DRIVE TO BE USED. THE CONVERSATION MODE ALSO ALLOWS THE OPERATOR TO CHANGE THE OPERATING PARAMETERS OF THE 'DATA STORAGE/RETRIEVAL TEST' (TEST 14).

WHEN THE PROGRAM ENTERS CONVERSATION MODE, THE FOLLOWING MESSAGE IS TYPED:

ENTER TEST #:

A 'CR' MAY BE ENTERED OR A VALID TEST NUMBER (E.G., 0 - 17) MAY BE ENTERED. THE DEFAULT ENTRY ('CR') WILL CAUSE THE PROGRAM TO SELECT TESTS 0 - 14. IF A SINGLE TEST NUMBER IS ENTERED, ONLY THAT TEST WILL BE EXECUTED.

THE PROGRAM WILL THEN ASK FOR A DRIVE ASSIGNMENT.

ENTER DRIVE #:

A 'CR' ENTRY WILL CAUSE THE PROGRAM TO SELECT ALL ONLINE DRIVES FOR TESTING. IF A VALID DRIVE NUMBER IS ENTERED (0 - 7), THE PROGRAM WILL CHECK THE STATE OF THE DRIVE: IF THE DRIVE IS NOT ONLINE, THE DRIVE'S STATUS WILL BE TYPED AND THE PROGRAM WILL ASK FOR ANOTHER DRIVE ENTRY.

IF TEST 15, 16, OR 17 IS SELECTED, A 'CR' RESPONSE TO THE DRIVE NUMBER REQUEST WILL NOT BE ACCEPTED; A SPECIFIC DRIVE NUMBER MUST BE ENTERED FOR TEST 15, 16, OR 17.

IF TESTS 0 - 13, 15, 16 OR 17 HAVE BEEN SELECTED, OR IF THE ENTIRE TEST SEQUENCE WERE SPECIFIED, THE PROGRAM WILL BEGIN EXECUTING THE TEST OR SEQUENCE IMMEDIATELY. IF TEST 14 IS SPECIFIED, THE PROGRAM WILL ASK IF THE OPERATOR WANTS TO CHANGE PARAMETERS FOR TEST 14. THE FOLLOWING MESSAGE IS TYPED.

CHANGE PARAMETERS FOR TEST 14 ?

EITHER A 'CR' OR A 'Y' MAY BE ENTERED. IF A 'CR' IS ENTERED, THE PROGRAM WILL BYPASS THE TEST 14 PARAMETER ENTRY ROUTINE AND START TEST 14. IF A 'Y' IS ENTERED, THE PROGRAM STARTS THE TEST 14 PARAMETER CHANGE DIALOG.

THE PROGRAM WILL THEN TYPE THE WORD LENGTH FOR ALL TEST 14 TRANSFERS.

MAXIMUM WORD COUNT FOR TEST 14 (IN OCTAL): SSSSSS

NOTE: 'SSSSSS' = THE OCTAL VALUE OF THE WORD COUNT

THE PROGRAM WILL THEN ASK FOR A NEW WORD COUNT.

ENTER NEW WORD COUNT:

EITHER A VALUE (LESS THAN THE MAXIMUM TYPED ABOVE) OR A 'CR' MAY BE ENTERED. IF A VALUE IS ENTERED, THE NEW VALUE WILL BE USED FOR ALL DATA TRANSFER OPERATIONS IN TEST 14. WHEN THE PROGRAM IS RESTARTED OR A 'CONTROL C' IS TYPED, THE WORD COUNT VALUE WILL BE RETURNED TO THE SYSTEM DEFAULT VALUE. IF A 'CR' IS TYPED, THE PROGRAM WILL USE THE DEFAULT WORD COUNT VALUE.

THE PROGRAM WILL THEN ASK FOR A PATTERN SELECTION WORD ENTRY.

ENTER PATTERN SELECTION CODE FOR TEST 14:

ENTER THE OCTAL VALUE OF THE PATTERN SELECTION WORD OR A 'CR' FOR THE DEFAULT VALUE. REFER TO SECTION 7.9 FOR A DESCRIPTION OF THE PATTERN SELECTION WORD AND ITS USE IN TEST 14. IF TESTING IS TO BE AT A SPECIFIC DISK LOCATION, ENTER THE CODE FOR A SINGLE PATTERN. (IF TESTING IS AT A SPECIFIC ADDRESS, THE PROGRAM WILL NOT ROTATE THE DATA PATTERNS BUT WILL USE THE LOWEST NUMBERED PATTERN SPECIFIED BY THE PATTERN SELECT WORD.)

THE PROGRAM WILL ASK IF THE OPERATOR WANTS TO TEST AT A SPECIFIC DISK ADDRESS.

USE A SPECIFIC DISK ADDRESS FOR TEST 14 ?

ENTER EITHER A 'CR' OR A 'Y'. IF A 'Y' IS ENTERED, THE PROGRAM WILL TYPE THE FOLLOWING REMINDER MESSAGE ABOUT DATA PATTERN USAGE.

(A SINGLE DATA PATTERN MUST HAVE BEEN SPECIFIED
IF NOT, PATTERN 0 (ALL ZEROS) WILL BE USED)

THE PROGRAM WILL ASK FOR CYLINDER, TRACK, AND SECTOR ADDRESSES. AN ADDRESS MUST BE ENTERED. THE PROGRAM WILL NOT ACCEPT DEFAULT ENTRIES FOR THESE ADDRESSES. THE ENTRY REQUESTS FOR THE ADDRESSES ARE GIVEN BELOW.

ENTER CYLINDER ADDRESS:

ENTER TRACK ADDRESS:

ENTER SECTOR ADDRESS:

IF A SPECIFIC ADDRESS IS ENTERED FOR TESTING, TESTING WILL TAKE PLACE ONLY AT THAT ADDRESS WITH THE WORD COUNT AND PATTERN ENTERED ABOVE. THE TEST WILL MAKE ONE PASS AND GO TO END OF TEST. SW<14> SHOULD BE SET TO INHIBIT THE END OF TEST MESSAGE TYPEOUT.

TO TERMINATE THE TEST OR SEQUENCE, EITHER TYPE A 'CONTROL C' OR HALT THE PROCESSOR AND RESTART AT THE APPROPRIATE STARTING ADDRESS.

4.6 OPERATION OF TESTS 15, 16, & 17

TEST 15 AND TEST 17 CONTAIN IMBEDDED OPERATING INSTRUCTIONS. SETUP AND OPERATING INSTRUCTIONS ARE TYPED OUT AS THE TEST PROCEEDS.

TO OPERATE TEST 16, 'HEAD ALIGNMENT ROUTINE', REFER TO THE APPLICABLE MAINTENANCE MANUALS FOR THE DRIVE SETUP PROCEDURES. THE PROGRAM WILL TYPEOUT THE CONTROLLER SETUP PROCEDURES. TO CHANGE HEAD SELECTION, SW<07> MUST BE TOGGLED (TURNED ON, THEN OFF).

TO SETUP THE RP11E FOR HEAD ALIGNMENT, CONNECT A JUMPER BETWEEN PINS J13M2 AND J13C2 ON THE BACKPLANE.

5. ERROR HANDLING

5.1 ERROR REPORTING

WHEN THE PROGRAM DETECTS AN ERROR, THE ERROR ROUTINE IS CALLED; IF SW<13> IS NOT SET, THE ERROR MESSAGE FOR THAT ERROR WILL BE TYPED. EACH ERROR TYPEOUT CONTAINS THE FOLLOWING:

- A. AN ERROR MESSAGE
- B. A DATA HEADER LINE
- C. A DATA LINE CONTAINING
 - 1. THE TEST NUMBER
 - 2. THE ADDRESS (PROGRAM COUNTER CONTENTS) WHERE THE ERROR CALL WAS MADE
 - 3. CONTENTS OF THE APPROPRIATE REGISTERS
 - 4. OTHER SPECIFIC DATA

5.2 ERROR TYPES

THE ERRORS DETECTED ARE DIVIDED INTO 2 CATEGORIES: FATAL AND NON-FATAL.

FATAL ERRORS ARE DRIVE UNSAFE ERRORS WHICH OCCUR ON THE DRIVE BEING USED FOR TESTING OR DRIVES WHICH GO OFFLINE DURING TESTING. IF EITHER OF THESE TWO CONDITIONS OCCUR, THE PROGRAM WILL TERMINATE TESTING USING THE DRIVE WHICH GAVE THE FATAL ERROR.

ALL OTHER ERROR TYPES ARE NON-FATAL, E.G., TESTING CONTINUES AFTER THE ERROR IS REPORTED.

6. RESTRICTIONS

6.1 SUBSYSTEMS WITH MIXED DRIVE TYPES

THE PROGRAM ASSUMES THAT ONLY ONE KIND OF DRIVE IS ATTACHED TO A SYSTEM (E.G. RPO2'S & RPRO2'S OR RPO3'S). IF BOTH RPO2'S (AND RPRO2'S) OR RPO3'S ARE ON THE SAME SYSTEM, THE PROGRAM WILL ASSUME THAT ALL OF THE DRIVES ARE RPO3'S. TO TEST DRIVES ON A MIXED SYSTEM, TEST ALL OF THE SAME KIND OF DRIVE AT ONE TIME. THE OTHER DRIVES ON THE CONTROLLER MUST BE POWERED DOWN.

6.2 FORMATTED PACKS

EACH OF THE DRIVES BEING USED MUST HAVE A FORMATTED DISK PACK.

6.3 'CONTROL C' TERMINATION DURING TESTS 5 & 13

IF TESTS 5 OR 13 ARE TERMINATED WITH A 'CONTROL C', CYLINDER 0, TRACK 0, SECTOR 0 MAY BE LEFT WITH EITHER AN INVALID HEADER OR AN INVALID DATA FIELD. IF TEST 5 IS STOPPED, THE HEADER FOR SECTOR 0 MAY STILL BE MISFORMATTED; IF TEST 13 IS STOPPED, SECTOR 0 MAY BE STILL RECORDED IN 10/15 MODE.

6.4 CONTROLLER INTERRUPT

THE PROGRAM DOES NOT USE CONTROLLER INTERRUPT. THE PROGRAM WAITS FOR THE CONTROLLER 'READY' OR DRIVE 'READY' BITS TO SET TO INDICATE ORDER TERMINATION.

7. MISCELLANEOUS

7.1 RUN TIME

TO MAKE ONE PASS OF THE PROGRAM (TESTS 0 - 14) TAKES APPROXIMATELY 20 MINUTES FOR EACH RPO2 OR RPRO2 USED AND APPROXIMATELY 40 MINUTES FOR EACH RPO3 USED.

7.2 STACK POINTER

STACK IS INITIALLY SET TO 1100 AND EXTENDS DOWNWARD IN MEMORY.

7.3 END OF PASS/END OF TEST

THE PROGRAM WILL PERFORM ONE PASS FOR EACH DRIVE BEING USED FOR TESTING. END OF TEST WILL OCCUR WHEN EACH DRIVE BEING USED FOR TESTING HAS HAD ONE PASS.

7.4 SUBROUTINE CALLS

THE SUBROUTINE CALLS USED BY THE PROGRAM ARE:

- A. 'SCOPE' (IOT INSTRUCTION). THIS CALL IS PLACED BETWEEN EACH TEST IN THE PROGRAM. THIS ROUTINE ESTABLISHES THE TEST ITERATION COUNT AND LOOP ON TEST AND LOOP ON ERROR ADDRESSES.
- B. 'ERROR' (EMT INSTRUCTION). THIS CALL IS USED TO REPORT ALL ERRORS. A CALL TO THIS ROUTINE IS FOLLOWED BY A NUMBER WHICH IDENTIFIES THE ERROR MESSAGE WHICH WILL BE TYPED.

THE TRAP INSTRUCTION IS USED FOR THE FOLLOWING SUBROUTINE CALLS:

TYPE - TTY TYPEOUT ROUTINE
TYPOC - TYPE OCTAL NUMBER (WITH LEADING ZEROS)
TYPOS - TYPE OCTAL NUMBER (NO LEADING ZEROS)
TYPDS - TYPE DECIMAL NUMBER
RDCHR - READ A CHARACTER FROM THE TTY KEYBOARD
RDLIN - READ A LINE FROM THE TTY KEYBOARD
SAVREG - ROUTINE TO SAVE R0 - R5
RESREG - ROUTINE TO RESTORE R0 - R5

7.5 DISK SURFACE USAGE

THE PROGRAM USES THE FOLLOWING DISK LOCATIONS FOR TESTING.

<u>TEST</u>	<u>DISK ADDRESS</u>
0	CYL 0, TRK 0, SEC 0
1	CYL 0, TRK 0, SEC 0
2	CYL 0, TRK 0, SEC 0
3	CYL 0, TRK 0, SEC 0 MAXIMUM CYL, TRK 19, SEC 9
4	CYL 0, TRK 0, SEC 0
5	CYL 0, TRK 0, SEC 0
6	CYL 192, TRK 0, SEC 0
7	CYL 0, TRK 0, SEC 0
10	CYL 0, TRK 0, SEC 0 - 9
11	CYL 0, TRK 0 - 19, SEC 0
12	CYL 0, TRK 0, SEC 0
13	CYL 0, TRK 0, SEC 0
14	ENTIRE PACK
15	MAXIMUM CYL. TRK 0, SEC 0
16	N/A
17	NONE

7.6 TEST EXECUTION SEQUENCE

THE TEST SEQUENCE IS TESTS 0 - 14. TESTS 15, 16, & 17 ARE ONLY PERFORMED IF DIRECTED BY THE OPERATOR FROM CONVERSATION MODE. TESTS 15, 16, OR 17 CANNOT BE EXECUTED IN SEQUENCE.

7.7 DRIVE STATUS TYPEOUT

WHEN THE PROGRAM IS STARTED FROM LOCATION 200, 204, 210, OR 214 OR ENTERS THE CONVERSATION ROUTINE FROM A 'CONTROL C', THE PROGRAM WILL TYPE THE STATUS OF DRIVES 0 - 7. THIS DRIVE STATUS TYPEOUT CAN BE INHIBITED BY SETTING SW<07> BEFORE THE PROGRAM IS STARTED OR A 'CONTROL C' IS TYPED.

7.8 DATA COMPARISON USING MEMORY MANAGEMENT

IF THE SYSTEM HAS MORE THAN 28K, TEST 14 WILL ROTATE THE READ BUFFERS THROUGH ALL OF MEMORY. IF A DATA COMPARISON ERROR IS DETECTED, THE PROGRAM WILL TYPEOUT THE 'GOOD' AND THE 'BAD' DATA AND THE V I R T U A L ADDRESS OF THE COMPARISON ERROR LOCATION. THE CONTENTS OF THE APR'S IN USE WILL ALSO BE TYPED. IF MEMORY MANAGEMENT IS ENABLED, THE PROGRAM PERFORMS ALL DATA COMPARISONS FROM VIRTUAL LOCATIONS 60000 - 137776 USING APR'S 3, 4, & 5. THE PHYSICAL ADDRESS OF THE COMPARISON ERROR CAN BE FORMED BY ADDING THE APPLICABLE 'KIPAR' REGISTER CONTENTS (THE REGISTERS ARE DISPLAYED) TO THE VIRTUAL ADDRESS OF THE ERROR.

7.9 PATTERN SELECTION WORD

THE DATA PATTERNS USED BY TEST 14 ARE SELECTED FROM A SELECTION WORD WHICH IS LOADED BY THE PROGRAM OR LOADED BY THE OPERATOR IN CONVERSATION MODE. THE PATTERN SELECTION WORD CONTAINS A BIT FOR EACH PATTERN TO BE USED; BIT00 REPRESENTS PATTERN 0, BIT01 REPRESENTS PATTERN 1, ETC. REFER TO SECTION 8.13 FOR THE CONTENTS OF EACH PATTERN.

THE OCTAL VALUES FOR EACH DATA PATTERN ARE AS FOLLOWS.

PATTERN #	OCTAL SELECTION CODE
0	100000
1	000000
2	000000
3	000000
4	000000
5	000000
6	000000
7	000000
8	000000
9	000000
10	000000
11	000000
12	000000
13	000000
14	000000
15	000000

THE INDIVIDUAL OCTAL CODES ARE 'OR ED' TOGETHER TO FORM THE PATTERN SELECTION WORD. TO SELECT PATTERNS 0, 6, & 13, THE PATTERN SELECTION WORD COULD BE THE FOLLOWING OCTAL VALUE: 20101.

7.10 CONTROLLER/DRIVE OPERATION TIME LIMIT

ALL CONTROLLER AND DRIVE OPERATIONS ARE TIMED BY THE PROGRAM. IF AN OPERATION EXCEEDS THE TIME LIMIT, THE PROGRAM WILL REPORT THE TIMEOUT, RESET THE CONTROLLER, TERMINATE THE CURRENT TEST, AND GO TO THE NEXT TEST. THE TIME LIMIT IS 150 MS TO 1.5 SEC (DEPENDING ON THE PROCESSOR).

B. TEST DESCRIPTIONS

B.1 TEST 0 - 'CLEAR' COMMAND TERMINATION TEST

VERIFY THAT 'CLEAR' TERMINATES AN OPERATION AND CAUSES CONTROLLER READY TO SET.

B.2 TEST 1 - WRITE CHECK COMMAND TEST

THIS TEST VERIFIES THE WRITE CHECK LOGIC AND TO VERIFY THAT A WRITE CHECK ERROR CAN BE DETECTED. 'FLOATING ONES' AND 'FLOATING ZEROS' PATTERNS ARE USED TO TEST THE WRITE CHECK COMPARE LOGIC.

B.3 TEST 2 - PARTIAL SECTOR WRITE TEST

CHECK THE ABILITY OF THE RP11E TO CLEAR THE REMAINDER OF A SECTOR ON A PARTIAL WRITE OPERATION. A SECTOR OF ALL ONES IS WRITTEN AND THEN A TWO WORD WRITE OPERATION IS PERFORMED. THE SECTOR IS THEN READ BACK AND VERIFIED. THE FIRST TWO WORDS SHOULD BE ONES AND THE REST SHOULD BE ZEROS.

B.4 TEST 3 - 'EOP' TEST

TEST THAT 'EOP' SETS WHEN THE CONTROLLER TRIES TO WRITE BEYOND THE LIMITS OF THE DRIVE. THE FIRST SECTOR OF THE PACK IS WRITTEN WITH ZEROS. THEN A TWO SECTOR WRITE OF ALL ONE'S IS ISSUED FOR THE MAXIMUM BE SET. THE FIRST SECTOR OF THE PACK IS CHECKED TO MAKE SURE THAT IS STILL CONTAINS ZEROS.

B.5 TEST 4 - 'PROG' ERROR TEST

VERIFY THAT THE RP11E GENERATES A 'PROG' ERROR IF A COMMAND IS ISSUED WHILE THE CONTROLLER IS BUSY.

B.6 TEST 5 - 'HEADER NOT FOUND' TEST

MISFORMAT THE FIRST SECTOR ON THE PACK AND THEN READ IT BACK. VERIFY THAT READ AND WRITE HEADER OPERATIONS WILL TRANSFER DATA CORRECTLY. ISSUE A WRITE COMMAND TO SECTOR ZERO. THIS SHOULD CAUSE 'HEADER NOT FOUND' TO SET. THE TEST SECTOR IS REFORMATTED.

B.7 TEST 6 - COMMAND BUFFERING TEST

ISSUE A SEEK COMMAND AND WAIT FOR DONE TO SET. THEN ISSUE A READ COMMAND WHILE THE HEADS ARE STILL MOVING. THE RP11E SHOULD HOLD THE READ COMMAND UNTIL THE SEEK IS COMPLETE.

B.8 TEST 7 - 'NXME' ERROR TEST

THE NON-EXISTENT MEMORY ERROR BIT IS TESTED BY ATTEMPTING TO READ 1 WORD INTO LOCATION 760000. 'NXME', 'HE', & 'ERR' SHOULD ALL BE SET.

8.9 TEST 10 - SECTOR ADDRESSING TEST

THE SECTOR ADDRESSING TEST TESTS THE CAPABILITY OF THE RP11E TO LOCATE A SECTOR BY USING THE 'SOT' COUNTER (FOR 'HOR' OPERATIONS) AND TO LOCATE A SECTOR BY COMPARING THE HEADER CONTENTS AGAINST THE CYLINDER AND TRACK/SECTOR ADDRESS REGISTERS (NORMAL MODE). THE TEST IS PERFORMED IN 3 PARTS.

1. WRITE ALL HEADERS ON TRACK 0, CYLINDER 0 IN ASCENDING SEQUENCE FROM INDEX (0, 1, 2 ... 9).

2. READ EACH HEADER, BEGINNING WITH SECTOR 0, AND VERIFY THAT THE SECTOR FIELD IN THE HEADER IS CORRECT FOR THE HEADER EXPECTED.

VERIFY THAT THE 'SOT' COUNTER AND THE SECTOR ADDRESS IN 'RPDA' ARE EQUAL AND ARE THE EXPECTED VALUE. THE EXPECTED VALUE IS 1 GREATER THAN THE SECTOR READ. THE SECTOR ADDRESS REGISTER AND THE 'SOT' WILL BE INCREMENTED BY THE TIME THE PROGRAM IS ABLE TO READ THE REGISTER.

WRITE THE SECTOR'S ADDRESS IN THE DATA FIELD. (FOR SECTOR 0, AN OCTAL 12 IS WRITTEN.)

CONTINUE THIS SEQUENCE FOR THE REMAINING SECTORS ON THE TRACK.

3. READ THE DATA FROM EACH SECTOR USING A NORMAL (I.E. NON-HEADER) READ AND VERIFY THAT THE DATA IS CORRECT FOR THE EXPECTED SECTOR.

8.10 TEST 11 - TRACK ADDRESSING TEST

TRACK ADDRESSING IS TESTED BY WRITING THE TRACK'S ADDRESS AS DATA IN SECTOR 0 OF EACH TRACK IN CYLINDER 0. SECTOR 0 IS THEN READ BACK AND THE DATA IS CHECKED TO VERIFY THAT THE PROPER HEAD WAS SELECTED.

8.11 TEST 12 - EXTENDED MEMORY ADDRESS BIT TEST

THIS TEST TESTS THE OPERATION OF THE EXTENDED MEMORY ADDRESS BITS. IF THE SYSTEM DOES NOT HAVE MEMORY MANAGEMENT OR HAS MEMORY MANAGEMENT AND ONLY 32K, THE TEST WILL NOT BE PERFORMED.

1. THE PROGRAM WRITES A 2 WORD TEST SECTOR OF ALL ONES.

2. EXTENDED ADDRESS BIT 'MEX00' IS TESTED BY CLEARING LOCATION 200000 AND READING THE TEST SECTOR INTO LOCATION 177776. LOCATION 200000 IS CHECKED TO VERIFY THAT THE DATA IS CORRECT (ONES). THE PROGRAM VERIFIES THAT 'MEX00' HAS SET AND THAT 'MEX01' IS NOT SET.

3. IF THE SYSTEM HAS AT LEAST 64K, 'MEX01' IS TESTED. LOCATION 400000 IS CLEARED AND THE TEST SECTOR IS READ INTO LOCATION 377776 ('MEX00' IS SET FOR THE READ). LOCATION 400000 IS CHECKED FOR THE PROPER CONTENTS (ONES). 'MEX00' SHOULD HAVE RESET AND 'MEX01' SHOULD BE SET.

8.12 TEST 13 - RP11E DATA BUFFER REGISTER BIT TEST

THE DATA BUFFER REGISTER TEST VERIFIES THAT ALL 36 BITS OF THE DATA BUFFER REGISTER CAN BE SET AND CLEARED.

1. THE TEST WRITES THE FOLLOWING TEST PATTERN IN CYLINDER 0, TRACK 0, SECTOR 0 USING 10/15 MODE ('MODE' BIT SET).

```
052525  
052525  
000005  
125252  
125252  
000012  
052525  
052525  
000005  
125252
```

ETC

2. THE TEST SECTOR IS THEN READ AND THE DATA IS COMPARED. AT THE COMPLETION OF THE TEST, THE PROGRAM RESTORES THE TEST SECTOR TO PDP-11 MODE.

8.13 TEST 14 - DATA STORAGE/RETRIEVAL TEST

THE DATA TEST PERFORMS DATA STORAGE AND RETREIVAL TESTING USING THE CONTROLLER AND THE CURRENTLY SELECTED DRIVE. UNLESS ALTERED BY THE OPERATOR, THE TEST WILL WRITE AND WRITE CHECK THE ENTIRE DISK PACK USING ALL 16 DATA PATTERNS IN A ROTATING MANNER. THE BUFFER SIZE USED FOR THE WRITE AND WRITE CHECK ORDERS IS DETERMINED BY THE AVAILABLE MEMORY ON THE SYSTEM (MINUS THE SPACE REQUIRED BY THE PROGRAM LOADER). THE PROGRAM WILL USE THE AVAILABLE MEMORY SIZE OR 8K, WHICHEVER IS LESS, AS THE BUFFER SIZE.

AFTER THE PACK HAS BEEN WRITTEN, THE TEST WILL READ AND COMPARE THE DATA. THE BUFFER SIZE USED FOR THE READ IS THE SAME SIZE AS THAT USED FOR THE WRITE/WRITE CHECK SEGEMENT OF THE TEST. FOR THE READ SEGEMENT OF THE TEST, THE PROGRAM ROTATES THE READ BUFFER THROUGH MEMORY SO THAT ALL OF AVAILABLE MEMORY IS USED BY THE READ AND COMPARE OPERATIONS. THE TEST USES MEMORY MANAGEMENT ON SYSTEMS WITH MORE THAN 28K. (MEMORY MANAGEMENT WILL NOT BE USED IF THE TEST IS RUN ON MEMORY MANAGEMENT SYSTEMS WITH 28K OR LESS.)

IF AN ERROR OCCURS DURING A READ OPERATION (EXCEPT DATA COMPARSION ERRORS), THE PROGRAM WILL RETRY THE READ ORDER UP TO 3 TIMES. WRITE AND WRITE CHECK ORDERS ARE NOT RETRIED.

THE DATA PATTERNS USED BY THE DATA TEST ARE GIVEN BELOW:

PAT 0	PAT 1	PAT 2	PAT 3	PAT 4	PAT 5	PAT 6	PAT 7
000000	000001	177776	000000	000000	052525	007417	026455
000000	000003	177774	000000	010421	052525	007417	026455
000000	000007	177770	000000	021042	052525	007417	026455
000000	000017	177760	177777	031463	052525	170360	151322
000000	000037	177740	177777	042104	052525	170360	151322
000000	000077	177700	177777	052525	052525	170360	151322
000000	000177	177600	000000	063146	052525	007417	026455
000000	000377	177400	000000	073567	052525	007417	026455
000000	000777	177000	177777	104210	052525	170360	151322
000000	001777	176000	177777	114631	052525	170360	151322
000000	003777	174000	000000	125252	052525	007417	026455
000000	007777	170000	177777	135673	052525	170360	151322
000000	017777	160000	000000	146314	052525	007417	026455
000000	037777	140000	177777	156735	052525	170360	151322
000000	077777	100000	177777	167356	052525	170360	151322
000000	177777	000000	000000	177777	052525	007417	026455
PAT 8	PAT 9	PAT 10	PAT 11	PAT 12	PAT 13	PAT 14	PAT 15
077577	000001	177776	172666	077777	177777	000000	177777
077577	000002	177775	155555	137777	177777	000000	177777
077577	000004	177773	172666	157777	177777	177777	000000
077577	000010	177767	155555	167777	177777	177777	000000
077577	000020	177757	172666	173777	177777	177777	000000
077577	000040	177737	155555	175777	177777	177777	000000
077577	000100	177677	172666	176777	177777	177777	000000
077577	000200	177577	155555	177377	177777	177777	000000
077577	000400	177377	172666	177577	177777	177777	000000
077577	001000	176777	155555	177677	177777	177777	000000
077577	002000	175777	172666	177737	177777	177777	000000
077577	004000	173777	155555	177757	177777	177777	000000
077577	010000	167777	172666	177767	177777	177777	000000
077577	020000	157777	155555	177773	177777	177777	000000
077577	040000	137777	172666	177775	177777	177777	000000
077577	100000	077777	155555	177776	177777	177777	000000

B.14 TEST 15 - POWER FAIL TEST

TEST THE ABILITY OF THE RP11E TO SENSE POWER FAILURE AND FOR THE DRIVES TO RETRACT THEIR HEADS. WHEN POWER IS TURNED ON AGAIN THE CYLINDER ADDRESS IS TESTED FOR ZERO. AFTER TYPING THE MESSAGE REQUESTING POWER TO BE TURNED OFF, THE PROGRAM GOES INTO A LOOP READING FROM THE SELECTED DISK DRIVE. AFTER POWER IS RESTORED, MEMORY IS CHECKED TO SEE THAT THE DISK DID NOT TRANSFER ANY DATA TO MEMORY WHILE POWER WAS GOING DOWN.

B.15 TEST 16 - HEAD ALIGNMENT ROUTINE

THIS ROUTINE PERFORMS HEAD SELECTION AS DIRECTED FROM THE KEYBOARD; ALIGNMENT OF THE SELECTED HEAD MAY BE CHECKED OR ADJUSTED.

8.16 TEST 17 - RP11E/DRIVE CONTROL PANEL SWITCH TEST

THIS TEST TESTS THE 'ENABLE/DISABLE' AND 'READ ONLY' SWITCHES ON THE DRIVE CONTROL PANEL AND TESTS THE 'WRITE LOCKOUT' AND 'LOA' SWITCHES ON THE RP11E CONTROL PANEL.

9. PROGRAM LISTING

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.TITLE MD-11-DZRPY-C, RP11-E FUNCTIONAL LOGIC & R/W TEST
.*COPYRIGHT (C) 1975, 1977
.*DIGITAL EQUIPMENT CORP.
.*MAYNARD, MASS. 01754
.*
.*PROGRAM BY C. HESS/F. ROEMER
.*
.*THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
.*PACKAGE (MAINDEC-11-DZQAC-C3), JAN 19, 1977.
.SBTTL OPERATIONAL SWITCH SETTINGS
.*
.*      SWITCH          USE
.*      -----          -
.*      15             HALT ON ERROR
.*      14             LOOP ON TEST
.*      13             INHIBIT ERROR TYPEOUTS
.*      11             INHIBIT ITERATIONS
.*      10             BELL ON ERROR
.*      9              LOOP ON ERROR
.*      7              DON'T TYPE DRIVE STATUS WHEN PROGRAM STARTED OR WHEN
.*                    ENTERING CONVERSATION MODE
.*                    CHANGE HEAD SELECTION IN TEST 16
.*      6              DON'T USE MEMORY MANAGEMENT IN TEST 14
.*                    DISPLAY ALL READ RETRY ATTEMPTS IN TEST 14
.*                    PRINT ALL COMPARE ERRORS IN TESTS 13 & 14
.*                    INHIBIT DATA COMPARE IN TESTS 13 & 14
.*      5              BYPASS READ AND DATA COMPARE IN TEST 14
.*                    BYPASS WRITE CHECK IN TEST 14
.*      4              BYPASS WRITE IN TEST 14
.*      3              BYPASS WRITE IN TEST 14
.*      2              BYPASS WRITE IN TEST 14
.*      1              BYPASS WRITE IN TEST 14
.*      0              BYPASS WRITE IN TEST 14

```

.SBTTL TRAP CATCHER

000000

```

.=0
.*ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A "+2,HALT"
.*SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS
.*LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS

```

000174 000174
000176 000000

```

.=174
DISPREG: .WORD 0      ;; SOFTWARE DISPLAY REGISTER
SWREG:   .WORD 0      ;; SOFTWARE SWITCH REGISTER

```

.SBTTL ACT11 HOOKS

000200
000046 017050
000052 000000
000200

```

.******
.*HOOKS REQUIRED BY ACT11
.$SVPC=.          ;SAVE PC
.=46             ;;1)SET LOC.46 TO ADDRESS OF $ENDAD IN .SEOF
.$ENDAD          ;;2)SET LOC.52 TO ZERO
.=52             ;; RESTORE PC
.WORD 0
.$SVPC

```

.SBTTL STARTING ADDRESSES

000200

.=200

STARTING ADDRESSES

```

977 000200 000137 003654
978
979 000204 000137 003676
980
981 000210 000137 003644
982
983 000214 000137 003666
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```

:*200 = NORMAL START
      JMP START1
:*204 = SELECT OPERATING PARAMETERS
      JMP START2
:*210 = SELECT RP11E ADDRESS
      JMP START3
:*214 = COMBINATION OF 204 AND 210
      JMP START4

```

.SBTTL BASIC DEFINITIONS

;*INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***

```

STACK= 1100
.EQUIV EMT,ERROR      ;;BASIC DEFINITION OF ERROR CALL
.EQUIV IOT,SCOPE      ;;BASIC DEFINITION OF SCOPE CALL

```

;*MISCELLANEOUS DEFINITIONS

```

HT= 11      ;;CODE FOR HORIZONTAL TAB
LF= 12      ;;CODE FOR LINE FEED
CR= 15      ;;CODE FOR CARRIAGE RETURN
CRLF= 200   ;;CODE FOR CARRIAGE RETURN-LINE FEED
PS= 177776  ;;PROCESSOR STATUS WORD
.EQUIV PS,PSW
STKLMT= 177774 ;;STACK LIMIT REGISTER
PIRQ= 177772  ;;PROGRAM INTERRUPT REQUEST REGISTER
DSWR= 177570  ;;HARDWARE SWITCH REGISTER
DDISP= 177570 ;;HARDWARE DISPLAY REGISTER

```

;*GENERAL PURPOSE REGISTER DEFINITIONS

```

R0= %0      ;;GENERAL REGISTER
R1= %1      ;;GENERAL REGISTER
R2= %2      ;;GENERAL REGISTER
R3= %3      ;;GENERAL REGISTER
R4= %4      ;;GENERAL REGISTER
R5= %5      ;;GENERAL REGISTER
R6= %6      ;;GENERAL REGISTER
R7= %7      ;;GENERAL REGISTER
SP= %6      ;;STACK POINTER
PC= %7      ;;PROGRAM COUNTER

```

;*PRIORITY LEVEL DEFINITIONS

```

PR0= 0      ;;PRIORITY LEVEL 0
PR1= 40     ;;PRIORITY LEVEL 1
PR2= 100    ;;PRIORITY LEVEL 2
PR3= 140    ;;PRIORITY LEVEL 3
PR4= 200    ;;PRIORITY LEVEL 4
PR5= 240    ;;PRIORITY LEVEL 5
PR6= 300    ;;PRIORITY LEVEL 6
PR7= 340    ;;PRIORITY LEVEL 7

```

;*SWITCH REGISTER" SWITCH DEFINITIONS

```

SW15= 100000
SW14= 40000
SW13= 20000
SW12= 10000
SW11= 4000

```

BASIC DEFINITIONS

1033 002000
1034 001000
1035 000400
1036 000200
1037 000100
1038 000040
1039 000020
1040 000010
1041 000004
1042 000002
1043 000001

SW10= 2000
SW09= 1000
SW08= 400
SW07= 200
SW06= 100
SW05= 40
SW04= 20
SW03= 10
SW02= 4
SW01= 2
SW00= 1
.EQUIV SW09,SW9
.EQUIV SW08,SW8
.EQUIV SW07,SW7
.EQUIV SW06,SW6
.EQUIV SW05,SW5
.EQUIV SW04,SW4
.EQUIV SW03,SW3
.EQUIV SW02,SW2
.EQUIV SW01,SW1
.EQUIV SW00,SW0

1055 100000
1056 040000
1057 020000
1058 010000
1059 004000
1060 002000
1061 001000
1062 000400
1063 000200
1064 000100
1065 000040
1066 000020
1067 000010
1068 000004
1069 000002
1070 000001

DATA BIT DEFINITIONS (BIT00 TO BIT15)

BIT15= 100000
BIT14= 40000
BIT13= 20000
BIT12= 10000
BIT11= 4000
BIT10= 2000
BIT09= 1000
BIT08= 400
BIT07= 200
BIT06= 100
BIT05= 40
BIT04= 20
BIT03= 10
BIT02= 4
BIT01= 2
BIT00= 1
.EQUIV BIT09,BIT9
.EQUIV BIT08,BIT8
.EQUIV BIT07,BIT7
.EQUIV BIT06,BIT6
.EQUIV BIT05,BIT5
.EQUIV BIT04,BIT4
.EQUIV BIT03,BIT3
.EQUIV BIT02,BIT2
.EQUIV BIT01,BIT1
.EQUIV BIT00,BIT0

1083 000004
1084 000010
1085 000014
1086 000014
1087 000014
1088 000014

BASIC "CPU" TRAP VECTOR ADDRESSES

ERRVEC= 4 :: TIME OUT AND OTHER ERRORS
RESVEC= 10 :: RESERVED AND ILLEGAL INSTRUCTIONS
TBITVEC= 14 :: "T" BIT
TRTVEC= 14 :: TRACE TRAP
BPTVEC= 14 :: BREAKPOINT TRAP (BPT)

BASIC DEFINITIONS

```

1089      000020      IOTVEC= 20      :: INPUT/OUTPUT TRAP (IOT) **SCOPE**
1090      000024      PWRVEC= 24      :: POWER FAIL
1091      000030      EMTVEC= 30      :: EMULATOR TRAP (EMT) **ERROR**
1092      000034      TRAPVEC=34      :: "TRAP" TRAP
1093      000060      TKVEC= 60      :: TTY KEYBOARD VECTOR
1094      000064      TPVEC= 64      :: TTY PRINTER VECTOR
1095      000240      PIRQVEC=240     :: PROGRAM INTERRUPT REQUEST VECTOR
1096      .SBTTL      MEMORY MANAGEMENT DEFINITIONS
1097
1098      ;*KT11 VECTOR ADDRESS
1099
1100      000250      MMVEC= 250
1101
1102      ;*KT11 STATUS REGISTER ADDRESSES
1103
1104      177572      SRO= 177572
1105      177574      SR1= 177574
1106      177576      SR2= 177576
1107      172516      SR3= 172516
1108
1109      ;*KERNEL "I" PAGE DESCRIPTOR REGISTERS
1110
1111      172300      KIPDR0= 172300
1112      172302      KIPDR1= 172302
1113      172304      KIPDR2= 172304
1114      172306      KIPDR3= 172306
1115      172310      KIPDR4= 172310
1116      172312      KIPDR5= 172312
1117      172314      KIPDR6= 172314
1118      172316      KIPDR7= 172316
1119
1120      ;*KERNEL "I" PAGE ADDRESS REGISTERS
1121
1122      172340      KIPAR0= 172340
1123      172342      KIPAR1= 172342
1124      172344      KIPAR2= 172344
1125      172346      KIPAR3= 172346
1126      172350      KIPAR4= 172350
1127      172352      KIPAR5= 172352
1128      172354      KIPAR6= 172354
1129      172356      KIPAR7= 172356
1130
1131      ;RP11E REGISTER INDEX EQUATES
1132
1133
1134      000000      RPDS=00
1135      000002      RPER=02
1136      000004      RPCS=04
1137      000006      RPWC=06
1138      000010      RPBA=10
1139      000012      RPCA=12
1140      000014      RPOA=14
1141      000016      RPM1=16
1142      000020      RPM2=20
1143      000022      RPM3=22
1144      000024      SUCA=24

```



```

1145      000026      SILO=26
1146
1147      ;RP11 OP CODE DEFINITIONS
1148
1149      000001      CLEAR=1      ;CLEAR THE CONTROLLER
1150      000003      WRTSEK=3     ;WRITE WITH IMPLIED SEEK AND HEAD SELECTION
1151      000005      RDSEK=5      ;READ WITH IMPLIED SEEK AND HEAD SELECT
1152      000007      WRCHK=7     ;WRITE CHECK WITH IMPLIED SEEK AND HEAD SELECT
1153      000011      SEEK=11     ;SEEK
1154      000013      WRITE=13    ;WRITE (NO IMPLIED SEEK OR HEAD SELECT)
1155      000015      HOMSEK=15   ;HOME SEEK
1156      000017      READ=17    ;READ (NO IMPLIED SEEK OR HEAD SELECT)
1157
1158
1159      ;RPDS REGISTER BIT DEFINITIONS
1160
1161      000400      SUWP=400     ;SELECTED UNIT WRITE PROTECTED
1162      001000      SUFU=1000   ;SELECTED UNIT FILE UNSAFE
1163      004000      SUSI=4000   ;SELECTED UNIT SEEK INCOMPLETE
1164      020000      SURP03=20000 ;SELECTED UNIT IS AN RP03
1165      040000      SUOL=40000  ;SELECTED UNIT IS ONLINE
1166      100000      SURDY=100000 ;SELECTED UNIT IS READY
1167
1168      ;RPCS REGISTER BIT DEFINITIONS
1169
1170      000020      MEX0=20     ;EXTENDED MEMORY BIT #1
1171      000040      MEX1=40     ;EXTENDED MEMORY BIT #2
1172      000100      IDE=100     ;INTERRUPT ON DONE ENABLE BIT
1173      000200      RDY=200    ;RP11 CONTROLLER READY BIT
1174      004000      HDR=4000   ;HEADER OPERATION BIT
1175      010000      MODE=10000  ;MODE BIT
1176      020000      AIE=20000  ;ATTENTION INTERRUPT ENABLE BIT
1177      040000      HE=40000   ;HARD ERROR BIT
1178      100000      ERR=100000 ;ERROR BIT
1179
1180
1181

```

COMMON TAGS

.SBTTL COMMON TAGS

*THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
*USED IN THE PROGRAM.

1182									
1183									
1184									
1185									
1186									
1187									
1188		001100							
1189	001100		SCMTAG:	.=1100					:: START OF COMMON TAGS
1190	001100	000000	SPASS:	.WORD	0				:: CONTAINS PASS COUNT
1191	001102	000	\$STNM:	.BYTE	00				:: CONTAINS THE TEST NUMBER
1192	001103	000	SERFLG:	.BYTE	00				:: CONTAINS ERROR FLAG
1193	001104	000000	\$ICNT:	.WORD	00				:: CONTAINS SUBTEST ITERATION COUNT
1194	001106	000000	\$LPADR:	.WORD	00				:: CONTAINS SCOPE LOOP ADDRESS
1195	001110	000000	\$LPERR:	.WORD	00				:: CONTAINS SCOPE RETURN FOR ERRORS
1196	001112	000000	\$ERTTL:	.WORD	00				:: CONTAINS TOTAL ERRORS DETECTED
1197	001114	000	\$ITEMB:	.BYTE	00				:: CONTAINS ITEM CONTROL BYTE
1198	001115	001	\$ERMAX:	.BYTE	01				:: CONTAINS MAX. ERRORS PER TEST
1199	001116	000000	\$ERRPC:	.WORD	00				:: CONTAINS PC OF LAST ERROR INSTRUCTION
1200	001120	000000	\$GDADR:	.WORD	00				:: CONTAINS ADDRESS OF 'GOOD' DATA
1201	001122	000000	\$BDADR:	.WORD	00				:: CONTAINS ADDRESS OF 'BAD' DATA
1202	001124	000000	\$GDAT:	.WORD	00				:: CONTAINS 'GOOD' DATA
1203	001126	000000	\$BDAT:	.WORD	00				:: CONTAINS 'BAD' DATA
1204	001130	000000		.WORD	00				:: RESERVED--NOT TO BE USED
1205	001132	000000		.WORD	00				
1206	001134	000	\$AUTOB:	.BYTE	00				:: AUTOMATIC MODE INDICATOR
1207	001135	000	\$INTAG:	.BYTE	00				:: INTERRUPT MODE INDICATOR
1208	001136	000000		.WORD	00				
1209	001140	177570	\$SWR:	.WORD	DSWR				:: ADDRESS OF SWITCH REGISTER
1210	001142	177570	\$DISPLAY:	.WORD	DDISP				:: ADDRESS OF DISPLAY REGISTER
1211	001144	177560	\$TKS:	177560					:: TTY KBD STATUS
1212	001146	177562	\$TKB:	177562					:: TTY KBD BUFFER
1213	001150	177564	\$TPS:	177564					:: TTY PRINTER STATUS REG. ADDRESS
1214	001152	177566	\$TPB:	177566					:: TTY PRINTER BUFFER REG. ADDRESS
1215	001154	000	\$NULL:	.BYTE	0				:: CONTAINS NULL CHARACTER FOR FILLS
1216	001155	002	\$FILLS:	.BYTE	2				:: CONTAINS # OF FILLER CHARACTERS REQUIRED
1217	001156	012	\$FILLC:	.BYTE	12				:: INSERT FILL CHARS. AFTER A "LINE FEED"
1218	001157	000	\$TPFLG:	.BYTE	0				:: "TERMINAL AVAILABLE" FLAG (BIT<07>=0=YES)
1219	001160	000000	\$TMPO:	.WORD	0				:: USER DEFINED
1220	001162	000000	\$TIMES:	0					:: MAX. NUMBER OF ITERATIONS
1221	001164	000000	\$ESCAPE:	0					:: ESCAPE ON ERROR ADDRESS
1222	001166	177607	\$BELL:	.ASCIZ	<207><377><377>				:: CODE FOR BELL
1223	001172	077	\$QUES:	.ASCII	/?/				:: QUESTION MARK
1224	001173	015	\$CRLF:	.ASCII	<15>				:: CARRIAGE RETURN
1225	001174	000012	\$LF:	.ASCIZ	<12>				:: LINE FEED
1226									*****
1227	001176	000000	\$CHKDRV:	.WORD	0				:: ADDRESS OF DRIVE BEING USED
1228	001200	000000	\$BYPASS:	.WORD	00				:: TEST EXIT ADDRESS
1229	001202	000000	\$FLAG:	.WORD	00				:: INTERNAL PROGRAM FLAG WORD
1230	001204	000000	\$PATNUM:	.WORD	00				:: CURRENTLY SELECTED PATTERN
1231	001206	000000	\$CNTRLC:	.WORD	00				:: CONVERSATION MODE FLAG
1232	001210	000000	\$MAXCYL:	.WORD	0				:: MAXIMUM CYLINDER ADDRESS FOR THE DRIVE IN USE
1233	001212	001750	\$TIMOUT:	.WORD	1000.				:: TIME ALLOWED FOR AN OPERATION
1234	001214	000000	\$ICNT:	.WORD	0				:: TEST ITERATION COUNT
1235	001216	000000	\$BUSADR:	.WORD	0				:: CHANGE RP11 ADDRESS FLAG
1236	001220	000000	\$STALLT:	.WORD	0				:: STALL VALUE
1237	001222	000000	\$MMACTV:	.WORD	0				:: MEMORY MANAGEMENT IS ACTIVE FLAG

000377

DZRPYC.P11 09-SEP-77 16:03

COMMON TAGS

1238 001224 000000 000000
 1239 001230 000000 000000
 1240 001234 000000 000000
 1241 001240 000000 000000
 1242 001244 000000 000000
 1243 001250 000000 000000
 1244 001252 000000 000000
 1245 001254 000000 000000
 1246 001256 000000 000000
 1247 001260 000000 000000
 1248 001262 020000 000000
 1249 001264 000000 000000
 1250 001266 000000 000000
 1251 001270 000000 000000
 1252 001272 000000 000000
 1253 001274 000000 000000
 1254 001276 000000 000000
 1255 001300 000000 000000
 1256 001302 000000 000000
 1257
 1258 001304 000000 000000
 1259
 1260 001306 000000 000000
 1261 001310 000000 000000
 1262
 1263
 1264 001312 000000 000000
 1265 001314 000000 000000
 1266 001316 000003 000003
 1267 001320 000003 000003
 1268 001322 000000 000000
 1269
 1270
 1271
 1272 001324 176710 000256
 1273 001326 000254 000256
 1274 001332 000240 000256
 1275
 1276
 1277
 1278 001334 000000 000000
 1279 001336 000000 000000
 1280 001340 000000 000000
 1281 001342 000000 000000
 1282 001344 000000 000000
 1283 001346 000000 000000
 1284 001350 000000 000000
 1285 001352 000000 000000
 1286 001354 000000 000000
 1287 001356 000000 000000
 1288
 1289
 1290
 1291 001360 001 001
 1292 001361 002 002
 1293 001362 004 004

ACTMEM: .WORD 0,0
 MAXMEM: .WORD 0,0
 HIMEM: .WORD 0,0
 LOMEM: .WORD 0,0
 PAKSIZ: .WORD 0,0
 INCSEC: .WORD 0,0
 INCTRK: .WORD 0,0
 BLKS14: .WORD 0,0
 WC14: .WORD 0,0
 OPRSEL: .WORD 0,0
 LMTBK: .WORD 8192.
 MAXWC: .WORD 0,0
 SCYL: .WORD 0,0
 STRK: .WORD 0,0
 SSEC: .WORD 0,0
 RDERR: .WORD 0,0
 TSTNMS: .WORD 0,0
 PATRN: .WORD 0,0
 MEMSIZ: .WORD 0,0
 DRVTYP: .WORD 0
 DRVMSK: .WORD 0
 DRVBAD: .WORD 0
 DRVSEL: .WORD 0
 RETRY: .WORD 0
 LRETRY: .WORD 3
 CMP CT: .WORD 3
 LDRFLG: .WORD 0

;RP11 ADDRESS VALUES

RPADR: .WORD 176710
 RPVEC: .WORD 254,256
 RPPRIO: .WORD (<5*32.>)

;SAVE THE RP11E REGISTERS HERE

\$RPDS: .WORD 0
 \$RPER: .WORD 0
 \$RPCS: .WORD 0
 \$RPWC: .WORD 0
 \$RPBA: .WORD 0
 \$RPCA: .WORD 0
 \$RPDA: .WORD 0
 \$RPM1: .WORD 0
 \$SUCA: .WORD 0
 \$SILO: .WORD 0

;ATTENTION BITS

ATABIT: .BYTE 1
 .BYTE 2
 .BYTE 4

;PHYSICAL ADDRESS OF NEXT READ BUFFER
 ;ADDRESS OF LAST MEMORY BANK (FILLED BY \$SIZE)
 ;PHYSICAL ADDRESS OF LAST MEMORY BANK (FOR TEST 14)
 ;PHYSICAL ADDRESS OF FIRST BUFFER LOCATION
 ;SECTORS ON THE PACK ON THE DRIVE BEING TESTED
 ;SECTOR INCREMENT VALUE
 ;TRACK INCREMENT VALUE
 ;NUMBER SECTORS IN TRANSFER IN TEST 14
 ;WORD COUNT FOR TEST 14
 ;OPERATOR SELECTED ADDRESS FLAG
 ;LARGEST TRANSFER PERMITTED
 ;MAXIMUM WORD COUNT ALLOWED ON THIS SYSTEM
 ;OPERATOR SELECTED CYLINDER
 ;OPERATOR SELECTED TRACK
 ;OPERATOR SELECTED SECTOR
 ;READ RETRY COUNTER
 ;TEST SELECTION WORD. BIT 0 = TEST 0, ETC.
 ;PATTERN SELECTION WORD FOR TEST 14
 ;ADDRESS OF HIGHEST NON-MEMORY MANAGEMENT
 ;MEMORY LOCATION
 ;CONTAINS A BIT FOR EACH RP03 DRIVE
 ;ON THE SYSTEM. BIT00 = DRIVE 0, ETC.
 ;DRIVE TEST SELECTION MASK
 ;CONTAINS A BIT FOR THE DRIVE IF THE DRIVE
 ;GOES OFFLINE OR BECOMES UNSAFE DURING TESTING.
 ;BIT00 = DRIVE 0, ETC.
 ;CONTAINS A BIT FOR EACH DRIVE TO BE TESTED
 ;RETRY COUNTER FOR TEST 14
 ;RETRY LIMIT FOR TEST 14
 ;NUMBER OF COMPARE ERROR TYPEOUTS
 ;WHEN =0 LOADERS ARE AT TOP OF MEMORY

;RP11 BUS ADDRESS
 ;RP11 VECTOR ADDRESS
 ;RP11 PRIORITY

;DRIVE STATUS REGISTER
 ;ERROR REGISTER
 ;COMMAND & STATUS REGISTER
 ;WORD COUNT REGISTER
 ;BUFFER ADDRESS REGISTER
 ;CURRENT CYLINDER ADDRESS REGISTER
 ;TRACK-SECTOR ADDRESS REGISTER
 ;MAINTENANCE REGISTER #1
 ;SELECTED UNIT CYLINDER ADDRESS REGISTER
 ;SILO REGISTER

;DRIVE 0
 ;DRIVE 1
 ;DRIVE 2

13363	001363	010
13364	001364	020
13365	001365	040
13366	001366	100
13367	001367	200

.BYTE	10	DRIVE
.BYTE	20	DRIVE
.BYTE	40	DRIVE
.BYTE	100	DRIVE
.BYTE	200	DRIVE

:DRIVE STATUS INDICATORS (DRVSTA=B BYTES)
 :DRVSTA=0 DRIVE NONEXISTENT OR OFFLINE
 :DRVSTA>0 DRIVE IS ONLINE
 :DRVSTA<0 DRIVE IS UNSAFE

13370	001370	000
13371	001371	000
13372	001372	000
13373	001373	000
1310	001374	000
1311	001375	000
1312	001376	000
1313	001377	000

DRVSTA:	.BYTE	0	DRIVE
	.BYTE	0000	DRIVE
	.BYTE	0000	DRIVE
	.BYTE	0000	DRIVE
	.BYTE	0000	DRIVE
	.BYTE	0000	DRIVE
	.BYTE	0000	DRIVE
	.BYTE	0000	DRIVE
	.BYTE	0000	DRIVE

:BIT TABLE

1314	001400	000001
1315	001401	000002
1316	001402	000004
1317	001403	000008
1318	001404	000010
1319	001405	000020
1320	001406	000040
1321	001407	000080
1322	001408	000100
1323	001409	000200
1324	001410	000400
1325	001411	000800
1326	001412	001000
1327	001413	002000
1328	001414	004000
1329	001415	010000
1330	001416	020000
1331	001417	040000
1332	001418	100000

BITS:	.WORD	BIT00
	.WORD	BIT01
	.WORD	BIT02
	.WORD	BIT03
	.WORD	BIT04
	.WORD	BIT05
	.WORD	BIT06
	.WORD	BIT07
	.WORD	BIT08
	.WORD	BIT09
	.WORD	BIT10
	.WORD	BIT11
	.WORD	BIT12
	.WORD	BIT13
	.WORD	BIT14
	.WORD	BIT15

1333	001440	001500
1334	001441	001540
1335	001442	001600
1336	001443	001640
1337	001444	001700
1338	001445	001740
1339	001446	001780
1340	001447	002000
1341	001448	002040
1342	001449	002100
1343	001450	002140
1344	001451	002200
1345	001452	002240
1346	001453	002300
1347	001454	002340
1348	001455	002400
1349	001456	002440

PAT.PT:	.WORD	PAT0
	.WORD	PAT1
	.WORD	PAT2
	.WORD	PAT3
	.WORD	PAT4
	.WORD	PAT5
	.WORD	PAT6
	.WORD	PAT7
	.WORD	PAT8
	.WORD	PAT9
	.WORD	PAT10
	.WORD	PAT11
	.WORD	PAT12
	.WORD	PAT13
	.WORD	PAT14
	.WORD	PAT15

:TABLE OF POINTERS WHICH POINT TO THE
 :PATTERNS USED BY THE DATA TEST

```

1369 001540 000000
1370 001540 000001
1371 001542 000003
1372 001544 000007
1373 001546 000017
1374 001550 000037
1375 001552 000077
1376 001554 000177
1377 001556 000377
1378 001560 000777
1379 001562 001777
1380 001564 003777
1381 001566 007777
1382 001570 017777
1383 001572 037777
1384 001574 077777
1385 001576 177777
1386
1387 001600 177776
1388 001602 177774
1389 001604 177770
1390 001606 177760
1391 001610 177740
1392 001612 177700
1393 001614 177600
1394 001616 177400
1395 001620 177000
1396 001622 176000
1397 001624 174000
1398 001626 170000
1399 001630 160000
1400 001632 140000
1401 001634 100000
1402 001636 000000
1403
1404 001640 000000
1405 001642 000000

```

: PATTERNS 0 THRU 15

```

PAT0: .WORD 000000
       .WORD 000000
       .WORD 000000
       .WORD 000000
       .WORD 000000
       .WORD 000000
       .WORD 000000
       .WORD 000000
       .WORD 000000
       .WORD 000000
       .WORD 000000
       .WORD 000000
       .WORD 000000

```

; PATTERN 0

```

PAT1: .WORD 000001
       .WORD 000003
       .WORD 000007
       .WORD 000017
       .WORD 000037
       .WORD 000077
       .WORD 000177
       .WORD 000377
       .WORD 000777
       .WORD 001777
       .WORD 003777
       .WORD 007777
       .WORD 017777
       .WORD 037777
       .WORD 077777
       .WORD 177777

```

; PATTERN 1

```

PAT2: .WORD 177776
       .WORD 177774
       .WORD 177770
       .WORD 177760
       .WORD 177740
       .WORD 177700
       .WORD 177600
       .WORD 177400
       .WORD 177000
       .WORD 176000
       .WORD 174000
       .WORD 170000
       .WORD 160000
       .WORD 140000
       .WORD 100000
       .WORD 000000

```

; PATTERN 2

```

PAT3: .WORD 000000
       .WORD 000000

```

; PATTERN 3

1406	001644	000000	.WORD	000000
1407	001646	177777	.WORD	177777
1408	001650	177777	.WORD	177777
1409	001652	177777	.WORD	177777
1410	001654	000000	.WORD	000000
1411	001656	000000	.WORD	000000
1412	001660	177777	.WORD	177777
1413	001662	177777	.WORD	177777
1414	001664	000000	.WORD	000000
1415	001666	177777	.WORD	177777
1416	001670	000000	.WORD	000000
1417	001672	177777	.WORD	177777
1418	001674	177777	.WORD	177777
1419	001676	000000	.WORD	000000
1420				
1421	001700	000000	PAT4: .WORD	000000 ;PATTERN 4
1422	001702	010421	.WORD	010421
1423	001704	021042	.WORD	021042
1424	001706	031463	.WORD	031463
1425	001710	042104	.WORD	042104
1426	001712	052525	.WORD	052525
1427	001714	063146	.WORD	063146
1428	001716	073567	.WORD	073567
1429	001720	104210	.WORD	104210
1430	001722	114631	.WORD	114631
1431	001724	125252	.WORD	125252
1432	001726	135673	.WORD	135673
1433	001730	146314	.WORD	146314
1434	001732	156735	.WORD	156735
1435	001734	167356	.WORD	167356
1436	001736	177777	.WORD	177777
1437				
1438	001740	052525	PAT5: .WORD	052525 ;PATTERN 5
1439	001742	063146	.WORD	063146
1440	001744	073567	.WORD	073567
1441	001746	084188	.WORD	084188
1442	001750	094809	.WORD	094809
1443	001752	105430	.WORD	105430
1444	001754	116051	.WORD	116051
1445	001756	126672	.WORD	126672
1446	001760	137293	.WORD	137293
1447	001762	147914	.WORD	147914
1448	001764	158535	.WORD	158535
1449	001766	169156	.WORD	169156
1450	001770	179777	.WORD	179777
1451	001772	125252	.WORD	125252
1452	001774	125252	.WORD	125252
1453	001776	052525	.WORD	052525
1454				
1455	002000	007417	PAT6: .WORD	007417 ;PATTERN 6
1456	002002	007417	.WORD	007417
1457	002004	007417	.WORD	007417
1458	002006	170360	.WORD	170360
1459	002010	170360	.WORD	170360
1460	002012	170360	.WORD	170360
1461	002014	007417	.WORD	007417

1462	002016	007417	.WORD	007417
1463	002020	170360	.WORD	170360
1464	002022	170360	.WORD	170360
1465	002024	007417	.WORD	007417
1466	002026	170360	.WORD	170360
1467	002030	007417	.WORD	007417
1468	002032	170360	.WORD	170360
1469	002034	170360	.WORD	170360
1470	002036	007417	.WORD	007417
1471				
1472	002040	026455	PAT7: .WORD	026455 ;PATTERN 7
1473	002042	026455	.WORD	026455
1474	002044	026455	.WORD	026455
1475	002046	151322	.WORD	151322
1476	002050	151322	.WORD	151322
1477	002054	151322	.WORD	151322
1478	002056	026455	.WORD	026455
1479	002058	026455	.WORD	026455
1480	002060	151322	.WORD	151322
1481	002062	151322	.WORD	151322
1482	002064	026455	.WORD	026455
1483	002066	151322	.WORD	151322
1484	002070	026455	.WORD	026455
1485	002072	151322	.WORD	151322
1486	002074	151322	.WORD	151322
1487	002076	026455	.WORD	026455
1488				
1489	002100	077577	PAT8: .WORD	077577 ;PATTERN 8 (WORST CASE PATTERN)
1490	002102	077577	.WORD	077577
1491	002104	077577	.WORD	077577
1492	002106	077577	.WORD	077577
1493	002110	077577	.WORD	077577
1494	002112	077577	.WORD	077577
1495	002114	077577	.WORD	077577
1496	002116	077577	.WORD	077577
1497	002120	077577	.WORD	077577
1498	002122	077577	.WORD	077577
1499	002124	077577	.WORD	077577
1500	002126	077577	.WORD	077577
1501	002130	077577	.WORD	077577
1502	002132	077577	.WORD	077577
1503	002134	077577	.WORD	077577
1504	002136	077577	.WORD	077577
1505				
1506	002140	000001	PAT9: .WORD	000001 ;PATTERN 9
1507	002142	000002	.WORD	000002
1508	002144	000004	.WORD	000004
1509	002146	000010	.WORD	000010
1510	002150	000020	.WORD	000020
1511	002152	000040	.WORD	000040
1512	002154	000100	.WORD	000100
1513	002156	000200	.WORD	000200
1514	002160	000400	.WORD	000400
1515	002162	001000	.WORD	001000
1516	002164	002000	.WORD	002000
1517	002166	004000	.WORD	004000

1518	002170	010000	.WORD	010000	
1519	002172	020000	.WORD	020000	
1520	002174	040000	.WORD	040000	
1521	002176	100000	.WORD	100000	
1522					
1523	002200	177776	PAT10: .WORD	177776	:PATTERN 10
1524	002202	177775	.WORD	177775	
1525	002204	177773	.WORD	177773	
1526	002206	177767	.WORD	177767	
1527	002210	177757	.WORD	177757	
1528	002212	177737	.WORD	177737	
1529	002214	177677	.WORD	177677	
1530	002216	177577	.WORD	177577	
1531	002220	177377	.WORD	177377	
1532	002222	176777	.WORD	176777	
1533	002224	175777	.WORD	175777	
1534	002226	173777	.WORD	173777	
1535	002230	167777	.WORD	167777	
1536	002232	157777	.WORD	157777	
1537	002234	137777	.WORD	137777	
1538	002236	077777	.WORD	077777	
1539					
1540	002240	172666	PAT11: .WORD	172666	:PATTERN 11
1541	002242	155555	.WORD	155555	
1542	002244	172666	.WORD	172666	
1543	002246	155555	.WORD	155555	
1544	002250	172666	.WORD	172666	
1545	002252	155555	.WORD	155555	
1546	002254	172666	.WORD	172666	
1547	002256	155555	.WORD	155555	
1548	002260	172666	.WORD	172666	
1549	002262	155555	.WORD	155555	
1550	002264	172666	.WORD	172666	
1551	002266	155555	.WORD	155555	
1552	002270	172666	.WORD	172666	
1553	002272	155555	.WORD	155555	
1554	002274	172666	.WORD	172666	
1555	002276	155555	.WORD	155555	
1556					
1557	002300	077777	PAT12: .WORD	077777	:PATTERN 12
1558	002302	137777	.WORD	137777	
1559	002304	157777	.WORD	157777	
1560	002306	167777	.WORD	167777	
1561	002310	173777	.WORD	173777	
1562	002312	175777	.WORD	175777	
1563	002314	176777	.WORD	176777	
1564	002316	177377	.WORD	177377	
1565	002320	177577	.WORD	177577	
1566	002322	177677	.WORD	177677	
1567	002324	177737	.WORD	177737	
1568	002326	177757	.WORD	177757	
1569	002330	177767	.WORD	177767	
1570	002332	177773	.WORD	177773	
1571	002334	177775	.WORD	177775	
1572	002336	177776	.WORD	177776	

1574	002340	177777	PAT13:	.WORD	177777	:PATTERN 13
1575	002342	177777		.WORD	177777	
1576	002344	177777		.WORD	177777	
1577	002346	177777		.WORD	177777	
1578	002350	177777		.WORD	177777	
1579	002352	177777		.WORD	177777	
1580	002354	177777		.WORD	177777	
1581	002356	177777		.WORD	177777	
1582	002360	177777		.WORD	177777	
1583	002362	177777		.WORD	177777	
1584	002364	177777		.WORD	177777	
1585	002366	177777		.WORD	177777	
1586	002370	177777		.WORD	177777	
1587	002372	177777		.WORD	177777	
1588	002374	177777		.WORD	177777	
1589	002376	177777		.WORD	177777	
1590						
1591	002400	000000	PAT14:	.WORD	000000	:PATTERN 14
1592	002402	000000		.WORD	000000	
1593	002404	177777		.WORD	177777	
1594	002406	177777		.WORD	177777	
1595	002410	177777		.WORD	177777	
1596	002412	177777		.WORD	177777	
1597	002414	177777		.WORD	177777	
1598	002416	177777		.WORD	177777	
1599	002420	177777		.WORD	177777	
1600	002422	177777		.WORD	177777	
1601	002424	177777		.WORD	177777	
1602	002426	177777		.WORD	177777	
1603	002430	177777		.WORD	177777	
1604	002432	177777		.WORD	177777	
1605	002434	177777		.WORD	177777	
1606	002436	177777		.WORD	177777	
1607						
1608	002440	177777	PAT15:	.WORD	177777	:PATTERN 15
1609	002442	177777		.WORD	177777	
1610	002444	000000		.WORD	000000	
1611	002446	000000		.WORD	000000	
1612	002450	000000		.WORD	000000	
1613	002452	000000		.WORD	000000	
1614	002454	000000		.WORD	000000	
1615	002456	000000		.WORD	000000	
1616	002460	000000		.WORD	000000	
1617	002462	000000		.WORD	000000	
1618	002464	000000		.WORD	000000	
1619	002466	000000		.WORD	000000	
1620	002470	000000		.WORD	000000	
1621	002472	000000		.WORD	000000	
1622	002474	000000		.WORD	000000	
1623	002476	000000		.WORD	000000	
1624						
1625						
1626						
1627	002500	000	DPB:	.BYTE	0	:DRIVE NUMBER
1628	002501	000		.BYTE	0	: 'MEX' BITS
1629	002502	000		.BYTE	0	: OPERATION CODE

:DRIVE OPERATION CONTROL BLOCK

H03

MD-11-DZRPY-C, RP11-E FUNCTIONAL LOGIC & R/W TEST
DZRPYC.P11 09-SEP-77 16:03 COMMON TAGS

MACY11 30(1046) 09-SEP-77 16:14 PAGE 34

SEQ 0033

1630 002503 000
1631 002504 000000
1632 002506 000000
1633 002510 000000
1634 002512 000
1635 002513 000
1636

SWC: .BYTE 0
SBUF: .WORD 0000
SCYL: .WORD 0000
SSEC: .BYTE 0
STRK: .BYTE 0

:MODE AND HDR BITS
:WORD COUNT
:BUFFER ADDRESS
:CYLINDER ADDRESS
:SECTOR ADDRESS
:TRACK ADDRESS

ERROR POINTER TABLE

.SBTTL ERROR POINTER TABLE

:*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
:*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
:*LOCATION SITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
:*NOTE1: IF SITEMB IS 0 THE ONLY PERTINENT DATA IS (\$ERRPC).
:*NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

:* EM ::POINTS TO THE ERROR MESSAGE
:* OH ::POINTS TO THE DATA HEADER
:* DT ::POINTS TO THE DATA
:* DF ::POINTS TO THE DATA FORMAT

\$ERRTB:
:ERROR 1

:RP11 DIDN'T RESPOND TO ADDRESSING

:ERROR 2

:CAN'T START READ COMMAND

:ERROR 3

: 'CLEAR' COMMAND DIDN'T TERMINATE DATA TRANSFER OPERATION

:ERROR 4

:ERROR WRITING TEST SECTOR(S)

:ERROR 5

:WRITE CHECK ERROR CHECKING TEST SECTOR(S)

:ERROR 6

:EXPECTED 'WCE' DIDN'T OCCUR

1637
1638
1639
1640
1641
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1643
1644
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1648
1649
1650
1651 002514
1652
1653
1654 002514 032173
1655 002516 040315
1656 002520 043036
1657 002522 043516
1658
1659
1660
1661 002524 032235
1662 002526 040323
1663 002530 043040
1664 002532 043522
1665
1666
1667
1668 002534 032266
1669 002536 040323
1670 002540 043040
1671 002542 043522
1672
1673
1674
1675 002544 032357
1676 002546 040323
1677 002550 043040
1678 002552 043522
1679
1680
1681
1682 002554 032414
1683 002556 040465
1684 002560 043072
1685 002562 043532
1686
1687
1688
1689 002564 032466
1690 002566 040465
1691 002570 043072
1692 002572 043532

EM1
OH1
DT1
DF1

EM2
OH2
DT2
DF2

EM3
OH3
DT3
DF3

EM4
OH4
DT4
DF4

EM5
OH5
DT5
DF5

EM6
OH6
DT6
DF6

1693					
1694					
1695					
1696	002574	032522	EM7		:ERROR WRITING LESS THAN A FULL SECTOR
1697	002576	040323	DH2		
1698	002600	043040	DT2		
1699	002602	043522	DF2		
1700					
1701					
1702					
1703	002604	000000	0		:UNUSED
1704	002606	000000	0		
1705	002610	000000	0		
1706	002612	000000	0		
1707					
1708					
1709					
1710	002614	032570	EM11		:ONE OF 2 WORDS WRITTEN IN THE PARTIAL WRITE DIDN'T COMPARE
1711	002616	040637	DH11		
1712	002620	043126	DT11		
1713	002622	043542	DF11		
1714					
1715					
1716					
1717	002624	032663	EM12		:NON-ZERO DATA IN PART OF SECTOR WHICH SHOULD HAVE :BEEN ZERO FILLED DURING PARTIAL WRITE
1718					
1719	002626	040720	DH12		
1720	002630	043142	DT12		
1721	002632	043542	DF11		
1722					
1723					
1724					
1725	002634	033014	EM13		: 'EOP' DIDN'T SET DURING A 2 SECTOR WRITE BEGINNING :AT THE LAST SECTOR OF THE PACK
1726					
1727	002636	040323	DH2		
1728	002640	043040	DT2		
1729	002642	043522	DF2		
1730					
1731					
1732					
1733	002644	033137	EM14		: 'ERR' DIDN'T SET WITH 'EOP'
1734	002646	040775	DH14		
1735	002650	043156	DT14		
1736	002652	043542	DF11		
1737					
1738					
1739					
1740	002654	033173	EM15		: 'EOP' DIDN'T CLEAR RPOA
1741	002656	040323	DH2		
1742	002660	043040	DT2		
1743	002662	043522	DF2		
1744					
1745					
1746					
1747	002664	033223	EM16		: SUCA NOT CORRECT AFTER 'EOP'
1748	002666	041052	DH16		

1749	002670	043172	DT16	
1750	002672	043616	DF53	
1751				
1752				:ERROR 17
1753				
1754	002674	033260	EM17	:ERROR READING TEST SECTOR
1755	002676	040323	DH2	
1756	002700	043040	DT2	
1757	002702	043522	DF2	
1758				
1759				:ERROR 20
1760				
1761	002704	033312	EM20	:CONTENTS OF FIRST SECTOR OF PACK CHANGED AFTER
1762				:FORCING 'EOP' DURING WRITE
1763	002706	040637	DH11	
1764	002710	043126	DT11	
1765	002712	043542	DF11	
1766				
1767				:ERROR 21
1768				
1769	002714	033425	EM21	: 'PROG' ERROR DIDN'T SET WHEN READ COMMAND ISSUED
1770				:WHILE CONTROLLER BUSY
1771	002716	040775	DH14	
1772	002720	043156	DT14	
1773	002722	043542	DF11	
1774				
1775				:ERROR 22
1776				
1777	002724	033535	EM22	: 'ERR' DIDN'T SET WITH 'PROG'
1778	002726	040775	DH14	
1779	002730	043156	DT14	
1780	002732	043542	DF11	
1781				
1782				:ERROR 23
1783				
1784	002734	033572	EM23	:ERROR ATTEMPTING TO MISFORMAT SECTOR 0
1785	002736	040323	DH2	
1786	002740	043040	DT2	
1787	002742	043522	DF2	
1788				
1789				:ERROR 24
1790				
1791	002744	033641	EM24	:ERROR VERIFYING THE MISFORMATTED TEST HEADER
1792	002746	040775	DH14	
1793	002750	043156	DT14	
1794	002752	043542	DF11	
1795				
1796				:ERROR 25
1797				
1798	002754	033716	EM25	:MISFORMATTED TEST HEADER IS NOT CORRECT
1799	002756	041167	DH25	
1800	002760	043204	DT25	
1801	002762	043546	DF25	
1802				
1803				:ERROR 26
1804				

ERROR POINTER TABLE

1805	002764	033766	EM26	;'HNF' DIDN'T SET WHEN SEARCHING FOR MISFORMATTED SECTOR
1806	002766	040323	DH2	
1807	002770	043040	DT2	
1808	002772	043522	DF2	
1809				
1810				; ERROR 27
1811				
1812	002774	034056	EM27	;'DSK' NOT SET WITH 'HNF'
1813	002776	040775	DH14	
1814	003000	043156	DT14	
1815	003002	043542	DF11	
1816				
1817				; ERROR 30
1818				
1819	003004	034107	EM30	;'HE' DIDN'T SET WITH 'HNF'
1820	003006	040775	DH14	
1821	003010	043156	DT14	
1822	003012	043542	DF11	
1823				
1824				; ERROR 31
1825				
1826	003014	034142	EM31	;'ERR' NOT SET WITH 'HNF'
1827	003016	040775	DH14	
1828	003020	043156	DT14	
1829	003022	043542	DF11	
1830				
1831				; ERROR 32
1832				
1833	003024	034173	EM32	; ERROR WHILE RESTORING MISFORMATTED TEST HEADER
1834	003026	040323	DH2	
1835	003030	043040	DT2	
1836	003032	043522	DF2	
1837				
1838				; ERROR 33
1839				
1840	003034	034252	EM33	; CONTROLLER DIDN'T BECOME BUSY WHEN READ COMMAND ; ISSUED TO SEEKING DRIVE
1841				
1842	003036	040323	DH2	
1843	003040	043040	DT2	
1844	003042	043522	DF2	
1845				
1846				; ERROR 34
1847				
1848	003044	034363	EM34	; ERROR DURING READ COMMAND WHICH WAS ISSUED TO SEEKING DRIVE
1849	003046	040323	DH2	
1850	003050	043040	DT2	
1851	003052	043522	DF2	
1852				
1853				; ERROR 35
1854				
1855	003054	034457	EM35	; DATA INCORRECT FROM HEADER READ COMMAND ISSUED ; TO SEEKING DRIVE
1856				
1857	003056	041167	DH25	
1858	003060	043204	DT25	
1859	003062	043546	DF25	
1860				

ERROR POINTER TABLE

1861				;ERROR 36	
1862					
1863	003064	034561	EM36		; 'NXME' DIDN'T SET WHEN LOCATION 760000 ADDRESSED
1864	003066	040323	DH2		
1865	003070	043040	DT2		
1866	003072	043522	DF2		
1867					
1868				;ERROR 37	
1869					
1870	003074	034642	EM37		; 'HE' DIDN'T SET WITH 'NXME'
1871	003076	040775	DH14		
1872	003100	043156	DT14		
1873	003102	043542	DF11		
1874					
1875				;ERROR 40	
1876					
1877	003104	034676	EM40		; 'ERR' DIDN'T SET WITH 'NXME'
1878	003106	040775	DH14		
1879	003110	043156	DT14		
1880	003112	043542	DF11		
1881					
1882				;ERROR 41	
1883					
1884	003114	034733	EM41		;ERROR FORMATTING TRACK 0
1885	003116	040323	DH2		
1886	003120	043040	DT2		
1887	003122	043522	DF2		
1888					
1889				;ERROR 42	
1890					
1891	003124	034764	EM42		;ERROR READING THE HEADER FROM THE TEST SECTOR
1892	003126	040323	DH2		
1893	003130	043040	DT2		
1894	003132	043522	DF2		
1895					
1896				;ERROR 43	
1897					
1898	003134	035042	EM43		;SECTOR FIELD FROM HEADER IS NOT CORRECT
1899	003136	041256	DH43		
1900	003140	043224	DT43		
1901	003142	043556	DF43		
1902					
1903				;ERROR 44	
1904					
1905	003144	035112	EM44		;ERROR WRITING SECTOR ADDRESS IN DATA FIELD
1906	003146	040323	DH2		
1907	003150	043040	DT2		
1908	003152	043522	DF2		
1909					
1910				;ERROR 45	
1911					
1912	003154	035165	EM45		; 'SOT' OR SECTOR ADDRESS REGISTER IS NOT CORRECT
1913	003156	041502	DH45		
1914	003160	043262	DT45		
1915	003162	043572	DF45		
1916					

1917				; ERROR 46	
1918					
1919	003164	035245	EM46		; DATA NOT CORRECT FOR SECTOR READ
1920	003166	041674	DH46		
1921	003170	043306	DT46		
1922	003172	043542	DF11		
1923				; ERROR 47	
1924					
1925					
1926	003174	035316	EM47		; SECTOR CONTENTS WRONG, DATA SHOULD BE THE
1927					; TRACK NUMBER OF THE CURRENTLY SELECTED HEAD
1928	003176	042031	DH47		
1929	003200	043322	DT47		
1930	003202	043602	DF47		
1931				; ERROR 50	
1932					
1933					
1934	003204	035445	EM50		; ERROR AFTER 2 WORD READ STARTING AT LOC 177776
1935	003206	040323	DH2		
1936	003210	043040	DT2		
1937	003212	043522	DF2		
1938				; ERROR 51	
1939					
1940					
1941	003214	035524	EM51		; 'MEX0' DIDN'T SET AFTER 2 WORD READ STARTING
1942					; AT LOC 177776
1943	003216	042145	DH51		
1944	003220	043346	DT51		
1945	003222	043612	DF51		
1946				; ERROR 52	
1947					
1948					
1949	003224	035620	EM52		; 'MEX1' SET AFTER 2 WORD READ STARTING AT LOC 177776
1950	003226	042145	DH51		
1951	003230	043346	DT51		
1952	003232	043612	DF51		
1953				; ERROR 53	
1954					
1955					
1956	003234	035704	EM53		; CONTENTS OF LOC 200000 WRONG AFTER 2 WORD READ
1957					; STARTING AT LOC 177776
1958	003236	041167	DH25		
1959	003240	043204	DT25		
1960	003242	043616	DF53		
1961				; ERROR 54	
1962					
1963					
1964	003244	036013	EM54		; ERROR AFTER 2 WORD READ STARTING AT LOC 377776
1965	003246	040323	DH2		
1966	003250	043040	DT2		
1967	003252	043522	DF2		
1968				; ERROR 55	
1969					
1970					
1971					
1972	003254	036072	EM55		; 'MEX0' DIDN'T CLEAR AFTER 2 WORD STARTING AT LOC 377776

ERROR POINTER TABLE

1973	003256	042145	DH51	
1974	003250	043346	DT51	
1975	003262	043612	DF51	
1976				
1977				:ERROR 56
1978				
1979	003264	036162	EM56	: 'MEX1' DIDN'T SET AFTER 2 WORD READ STARTING AT LOC 377776
1980	003266	042145	DH51	
1981	003270	043346	DT51	
1982	003272	043612	DF51	
1983				:ERROR 57
1984				
1985	003274	036255	EM57	: CONTENTS OF LOC 400000 WRONG AFTER 2 WORD READ
1986				: STARTING AT LOCATION 377776
1987				
1988	003276	041167	DH25	
1989	003300	043204	DT25	
1990	003302	043616	DF53	
1991				:ERROR 60
1992				
1993				
1994	003304	036371	EM60	: ERROR RETURNING SECTOR TO PDP-11 MODE USING A
1995				: 2 WORD WRITE
1996				
1997	003306	040323	DH2	
1998	003310	043040	DT2	
1999	003312	043522	DF2	
2000				:ERROR 61
2001				
2002	003314	036465	EM61	: ERROR WRITING TEST SECTOR(S)
2003	003316	040465	DH2	
2004	003320	043072	DT5	
2005	003322	043532	DF5	
2006				:ERROR 62
2007				
2008				
2009	003324	036522	EM62	: ERROR READING TEST SECTOR(S)
2010	003326	040465	DH5	
2011	003330	043072	DT5	
2012	003332	043532	DF5	
2013				:ERROR 63
2014				
2015				
2016	003334	036557	EM63	: DRIVE DIDN'T RETURN TO CYL 0 FROM THE MAXIMUM
2017				: CYL ON CONTROLLER POWER FAIL
2018				
2019	003336	042242	DH63	
2020	003340	043366	DT63	
2021	003342	043622	DF63	
2022				:ERROR 64
2023				
2024	003344	036673	EM64	: CONTENTS OF MEMORY CHANGED DURING POWER
2025				: FAIL WHILE READING THE DISK
2026				
2027	003346	040720	DH12	
2028	003350	043142	DT12	
2029	003352	043542	DF11	

ERROR POINTER TABLE

2029					
2030				:ERROR 65	
2031					
2032	003354	037000	EM65		: 'SUOL' SET WITH DRIVE DISABLED
2033	003356	040775	DH14		
2034	003360	043156	DT14		
2035	003362	043542	DF11		
2036					
2037				:ERROR 66	
2038					
2039	003364	037037	EM66		: 'SUOL' NOT SET WITH DRIVE ENABLED
2040	003366	040775	DH14		
2041	003370	043156	DT14		
2042	003372	043542	DF11		
2043					
2044				:ERROR 67	
2045					
2046	003374	037101	EM67		: 'SUWP' NOT SET WITH DRIVE SET TO 'READ ONLY'
2047	003376	040775	DH14		
2048	003400	043156	DT14		
2049	003402	043542	DF11		
2050					
2051				:ERROR 70	
2052					
2053	003404	037156	EM70		: 'SUWP' SET WITH DRIVE SET TO 'READ/WRITE'
2054	003406	040775	DH14		
2055	003410	043156	DT14		
2056	003412	043542	DF11		
2057					
2058				:ERROR 71	
2059					
2060	003414	037230	EM71		: 'SUWP' SET WITH RP11 'WRITE LOCKOUT' SET, CYLINDER
2061					: LOA'S = 0, AND RPCA = 0
2062	003416	042327	DH71		
2063	003420	043404	DT71		
2064	003422	043622	DF63		
2065					
2066				:ERROR 72	
2067					
2068	003424	037344	EM72		: 'SUWP' SET WITH RPCA = 2 & CYL LOA'S = 0
2069	003426	042327	DH71		
2070	003430	043404	DT71		
2071	003432	043622	DF63		
2072					
2073				:ERROR 73	
2074					
2075	003434	037415	EM73		: 'SUWP' NOT SET; RPCA = VALUE IN CYL LOA'S
2076	003436	042327	DH71		
2077	003440	043404	DT71		
2078	003442	043622	DF63		
2079					
2080				:ERROR 74	
2081					
2082	003444	037467	EM74		: 'SUWP' SET WITH RPCA ONE GREATER THAN CYL LOA VALUE
2083	003446	042327	DH71		
2084	003450	043404	DT71		

ERROR POINTER TABLE

0085	003452	043622	DF63	
0086			:ERROR 75	
0087	003454	037553	EM75	: 'SUMP' SET WITH DRIVE LOA SWITCHES EQUAL SELECTED DRIVE
0088	003456	040775	DH14	
0089	003460	043156	DT14	
0090	003462	043542	DF11	
0091			:ERROR 76	
0092	003464	037646	EM76	:DRIVE HAS GONE OFFLINE
0093	003466	040323	DH2	
0094	003470	043040	DT2	
0095	003472	043522	DF2	
0096			:ERROR 77	
0097	003474	037675	EM77	:DRIVE IS UNSAFE
0098	003476	040323	DH2	
0099	003500	043040	DT2	
0100	003502	043522	DF2	
0101			:ERROR 100	
0102	003504	037715	EM100	:DRIVE TIMED OUT
0103	003506	040323	DH2	
0104	003510	043040	DT2	
0105	003512	043522	DF2	
0106			:ERROR 101	
0107	003514	037735	EM101	:CONTROLLER TIMED OUT
0108	003516	040323	DH2	
0109	003520	043040	DT2	
0110	003522	043522	DF2	
0111			:ERROR 102	
0112	003524	037762	EM102	:DATA COMPARE ERROR
0113	003526	042414	DH102	
0114	003530	043422	DT102	
0115	003532	043626	DF102	
0116			:ERROR 103	
0117	003534	040005	EM103	:DATA COMPARE ERROR (MEMORY MANAGEMENT ENABLED)
0118	003536	042414	DH102	
0119	003540	043450	DT103	
0120	003542	043636	DF103	
0121			:ERROR 104	
0122	003544	000000	0	:DATA COMPARE ERROR DETAIL LINE
0123	003546	000000	0	
0124	003550	043504	DT104	

E04

ERROR POINTER TABLE

003552	043652	DF104	
:ERROR 105			
003554	000000	0	:DATA COMPARE ERROR SUMMARY LINE
003556	01332717	DH105	
003560	0133512	DT105	
003562	013516	DF1	
:ERROR 106			
003564	010064	EM106	: 'ERR' DIDN'T SET WITH 'WCE'
003566	010075	DH14	
003570	0133156	DT14	
003572	013542	DF11	
:ERROR 107			

ERROR POINTER TABLE

159	003574	040120	EM107	: 'HE' DIDN'T SET WITH 'PROG'
160	003576	040775	DH14	
161	003600	043156	DT14	
162	003602	043542	DF11	
163				
164				: ERROR 110
165				
166	003604	000000	0	: UNSUCCESSFUL AFTER 3 RETRIES
167	003606	042745	DH110	
168	003610	000000	0	
169	003612	000000	0	
170				: ERROR 111
171				
172				
173	003614	000000	0	: SUCCESSFUL AFTER 'N' RETRIES
174	003616	043000	DH111	
175	003620	043514	DT111	
176	003622	043516	DF1	
177				: ERROR 112
178				
179				
180	003624	040154	EM112	: DRIVE UNSAFE AFTER POWER FAIL
181	003626	040323	DH2	
182	003630	043040	DT2	
183	003632	043522	DF2	
184				: ERROR 113
185				
186				
187	003634	040212	EM113	: 'SUWP' NOT SET WITH DRIVE LOA SWITCHES GREATER THAN SELECTED DR
188	003636	040775	DH14	
189	003640	043156	DT14	
190	003642	043542	DF11	
191				

```

2192
2193
2194
2195
2196
2197
2198
2199
2200
2201 003644 012737 177777 001216 START3: MOV    #-1,BUSADR    ;SET BUSADR FLAG
2202 003652 000402          BR          STRT1A
2203 003654 005037 001216 START1: CLR    @BUSADR    ;CLR BUSADR FLAG
2204 003660 005037 001206 STRT1A: CLR    @CNTRLC    ;NO CONVERSATION MODE
2205 003664 000411          BR          START
2206 003666 012737 177777 001216 START4: MOV    #-1,BUSADR    ;SET BUSADR FLAG
2207 003674 000402          BR          STRT2A
2208 003676 005037 001216 START2: CLR    @BUSADR    ;CLR BUSADR FLAG
2209 003702 012737 177777 001206 STRT2A: MOV    #-1,CNTRLC ;SET CONVERSATION MODE FLAG
2210 003710 000005          START: RESET    ;CLEAR THE BUS
2211
2212
2213 003712 012706 001100
2214 003716 005026          ;:CLEAR THE COMMON TAGS (SCMTAG) AREA
2215 003720 022706 001140          MOV    @SCMTAG,R6    ;:FIRST LOCATION TO BE CLEARED
2216 003724 001374          CLR    (R6)+        ;:CLEAR MEMORY LOCATION
2217 003726 012706 001100          CMP    @SWR,R6    ;:DONE?
2218
2219 003732 012737 021646 000020
2220 003740 012737 000340 000022
2221 003746 012737 017070 000030
2222 003754 012737 000340 000032
2223 003762 012737 022502 000034
2224 003770 012737 000340 000036
2225 003776 013737 017004 016776
2226 004004 005037 001162          MOV    @STACK,SP    ;:SETUP THE STACK POINTER
2227 004010 005037 001164          ;:INITIALIZE A FEW VECTORS
2228 004014 112737 000001 001115
2229 004022 012737 004022 001106
2230 004030 012737 004030 001110
2231
2232
2233 004036 013746 000004
2234 004042 012737 004076 000004
2235 004050 012737 177570 001140
2236 004056 012737 177570 001142
2237 004064 022777 177777 175046
2238 004072 001012
2239
2240 004074 000403
2241 004076 012716 004104 64$: BR    65$
2242 004102 000002
2243 004104 012737 000176 001140 65$: MOV    @ERRVEC, -(SP) ;:SAVE ERROR VECTOR
2244 004112 012737 000174 001142 65$: MOV    @64$, @ERRVEC ;:SET UP ERROR VECTOR
2245 004120 012637 000004 66$: MOV    @DSWR, SWR ;:SETUP FOR A HARDWARE SWICH REGISTER
2246
2247 004124 023737 000042 000046 CMP    @DDISP, DISPLAY ;:AND A HARDWARE DISPLAY REGISTER

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2248	004132	001405			BEQ	STARTS		:YES, SKIP TITLE PRINTOUT
2249	004134	005227	177777		INC	#-1		:FIRST START ?
2250	004140	001002			BNE	STARTS		:BR IF NOT
2251	004142	104401	043656		TYPE	TITLE		:TYPE THE PROGRAM'S MAINDEC NUMBER AND NAME
2252	004146	004737	020416		JSR	PC,STKINT		:SETUP THE TTY KEYBOARD
2253				STARTS:	GET VALUE	FOR SOFTWARE SWITCH REGISTER		
2254	004152	005737	000042	.SBTTL	TST	#42		:ARE WE RUNNING UNDER XXDP/ACT?
2255	004156	001006			BNE	64\$:BRANCH IF YES
2256	004160	023727	001140	000176	CMP	SWR,#SWREG		:SOFTWARE SWITCH REG SELECTED?
2257	004166	001005			BNE	65\$:BRANCH IF NO
2258	004170	104406			GTSWR			:GET SOFT-SWR SETTINGS
2259	004172	000403			BR	65\$		
2260	004174	112737	000001	001134	MOVB	#1,SAUTOB		:;SET AUTO-MODE INDICATOR
2261	004202			64\$:				
2262	004202	004737	023452	65\$:	JSR	PC,RESLDR		:RESTORE THE LOADER
2263	004206	004737	025700		JSR	PC,SIZMEM		:SET MEMORY SIZE VARIABLES
2264	004212	005037	001112		CLR	SEITL		:CLEAR TOTAL ERROR COUNT
2265	004216	005037	001260		CLR	OPRSEL		:CLEAR OPERATOR SELECTED ADDRESS FLAG
2266	004222	005037	001266		CLR	SCYL		:OPERATOR SPECIFIED CYLINDER
2267	004226	005037	001270		CLR	STRK		:OPERATOR SPECIFIED TRACK
2268	004232	005037	001272		CLR	SSEC		:OPERATOR SPECIFIED SECTOR
2269	004236	013737	001264	001256	MOV	MAXWC,WC14		:ASSUME THAT MAXIMUM WORD COUNT LESS THAN BK
2270	004244	023737	001264	001262	CMP	MAXWC,LMTBK		:MAXIMUM WORD COUNT GREATER THAN LIMIT
2271	004252	101403			IS	IS		:BR IF NOT
2272	004254	013737	001262	001256	MOV	LMTBK,WC14		:USE AN BK MAXIMUM
2273	004262	012737	177777	001300	MOV	#177777,PATRN		:ENABLE ALL PATTERNS
2274	004270	012737	017777	001276	MOV	#17777,†STMMS		:SELECT TESTS 0 - 14
2275	004276	005037	177776		CLR	PSW		:ENSURE THE PRIORITY = 0
2276	004302	005037	001310		CLR	DRVBD		:CLEAR OFFLINE/UNSAFE DRIVE BITS
2277	004306	005037	001162		CLR	STIMES		:INITIALIZE NUMBER OF ITERATIONS
2278	004312	005037	001164		CLR	SESCAPE		:CLEAR THE ESCAPE ON ERROR ADDRESS
2279	004316	012737	016616	001200	MOV	#SEOP,BYPASS		:INITIAL BYPASS ADDRESS
2280	004324	012737	000001	001104	MOV	#1,†ICNT		:PRESET ITERATION COUNT TO 1
2281	004332	112737	000001	001115	MOVB	#1,†SERMAX		:ALLOW ONE ERROR PER TEST
2282	004340	012737	004340	001106	MOV	#.,†SLPADR		:INITIALIZE THE LOOP ADDRESS FOR SCOPE
2283	004346	012737	004346	001110	MOV	#.,†SLPERR		:SETUP THE ERROR LOOP ADDRESS
2284	004354	005037	001222		CLR	MMACTV		:CLEAR MEMORY MANAGEMENT FLAG
2285	004360	005037	177776		START6:	CLR	PS	:CLEAR THE PROCESSOR PRIORITY
2286	004364	004737	026150		JSR	PC,GETADR		:CHECK THE RP11 ADDRESS
2287	004370	004737	022566		JSR	PC,RPINIT		:FIND OUT WHICH DRIVES ARE ON SYSTEM
2288	004374	012737	000312	001210	MOV	#202,MAXCYL		:ASSUME RPO2'S
2289	004402	005737	001304		TST	DRVTYP		:WHICH DRIVES ?
2290	004406	001403			BEQ	2\$:BR IF THEY REALLY WERE RPO2'S
2291	004410	012737	000625	001210	MOV	#405.,MAXCYL		:SET MAX CYLINDER FOR RPO3'S
2292	004416	005227	177777		2\$:	INC	#-1	:FIRST START ?
2293	004422	001404			BEQ	9\$:BR IF IT IS
2294	004424	032777	000200	174506	BIT	#SW07,†SWR		:BYPASS DRIVE STATUS TYPEOUT ?
2295	004432	001044			BNE	START?		:BR IF YES
2296	004434	104401	027236		9\$:	TYPE	DRSTAT	: 'DRIVE STATUS'
2297	004440	005001			CLR	R1		:CLEAR TABLE POINTER
2298	004442	012702	000010		MOV	#8.,R2		:COUNTER
2299	004446			3\$:				
2300	004446	010146			MOV	R1,-(SP)		:;SAVE R1 FOR TYPEOUT
2301								:;TYPE DRIVE NUMBER
2302	004450	104403			TYPOS			:;GO TYPE--OCTAL ASCII
2303	004452	001			.BYTE	1		:;TYPE 1 DIGIT(S)

2304	004453	000			.BYTE	0	:: SUPPRESS LEADING ZEROS
2305	004454	105761	001370		TSTB	DRVSTA(R1)	: CHECK DRIVE'S STATUS
2306	004460	001420			BEQ	7\$: DRIVE IS OFFLINE OR NON-EXISTENT
2307	004462	100403			BMI	4\$: DRIVE IS UNSAFE
2308	004464	104401	027262		TYPE	ONLINE	: 'ONLINE'
2309	004470	000402			BR	5\$	
2310	004472	104401	027276		TYPE	UNSAFE	: 'UNSAFE'
2311	004476	136137	001360	001304	4\$: BITB	ATABIT(R1),DRVTYP	: ;SEE WHICH DRIVE TYPE
2312	004504	001003			5\$: BNE	6\$: BR IF RPO3
2313	004506	104401	027327		TYPE	RPO2	: 'RPO2'
2314	004512	000405			BR	8\$	
2315	004514	104401	027336		6\$: TYPE	RPO3	: 'RPO3'
2316	004520	000402			BR	8\$	
2317	004522	104401	027312		7\$: TYPE	,OFFLIN	: 'OFFLINE'
2318	004526	104401	001173		8\$: TYPE	\$SCLF	: CR-LF
2319	004532	005201			INC	R1	: INCREMENT TABLE POINTER
2320	004534	005302			DEC	R2	: DECREMENT THE COUNTER
2321	004536	001343			BNE	3\$: CONTINUE
2322	004540	104401	001173		TYPE	\$SCLF	: CR-LF
2323	004544	005737	001206		START7: TST	CNTRLC	: CONVERSATION MODE ?
2324	004550	001403			BEQ	1\$: NO--BRANCH
2325	004552	004737	026314		JSR	PC,ENTPRM	: YES--GET PARAMETERS
2326	004556	000416			BR	5\$: GO TYPE DRIVES TO BE TESTED
2327	004560	005037	001312		1\$: CLR	DRVSEL	: NO DRIVES SELECTED
2328	004564	005000			CLR	R0	: DETERMINE THE DRIVES THAT
2329	004566	012701	000001		MOV	#1,R1	: ARE AVAILABLE FOR TESTING
2330	004572	105760	001370		2\$: TSTB	DRVSTA(R0)	: DRIVE ONLINE ?
2331	004576	003403			BLE	3\$: BR IF NOT
2332	004600	156037	001360	001312	3\$: BISB	ATABIT(R0),DRVSEL	: ;SET SELECTION BIT FOR DRIVE
2333	004606	005200			INC	R0	: INCREMENT DRIVE ADDRESS
2334	004610	106301			ASLB	R1	: COUNT
2335	004612	001367			BNE	2\$: BR IF NOT ALL DRIVES CHECKED
2336	004614	104401	027345		5\$: TYPE	DRVTST	: 'DRIVES TO BE TESTED'
2337	004620	005037	017004		CLR	\$ENDCT	: DETERMINE PASSES TO MAKE AND
2338	004624	005000			CLR	R0	: THE DRIVES TO BE TESTED
2339	004626	013701	001312		MOV	DRVSEL,R1	: ANY DRIVES SELECTED?
2340	004632	001004			BNE	6\$: YES--BRANCH
2341	004634	104401	027372		TYPE	NONE	: 'NONE'
2342	004640	000137	016616		JMP	#SEOP	: GO TO END OF PROGRAM
2343	004644	005737	001206		6\$: TST	CNTRLC	: CONVERSATION MODE START ?
2344	004650	001011			BNE	7\$: BR IF NOT
2345	004652	005737	000042		TST	42	: UNDER MONITOR CONTROL ?
2346	004656	001406			BEQ	7\$: BR IF NOT
2347	004660	122737	000003	000041	CMPB	#3,41	: LOADED BY RPO2/RPO3 ?
2348	004666	001002			BNE	7\$: BR IF NOT
2349	004670	006201			ASR	R1	: EXCLUDE DRIVE 0 FROM TESTING
2350	004672	000413			BR	8\$: CHECK ON THE OTHER DRIVES
2351	004674	006201			7\$: ASR	R1	: REPORT THE DRIVES TO BE TESTED
2352	004676	103011			BCC	8\$: BR IF DRIVE IS NOT SELECTED
2353	004700	005237	017004		INC	\$ENDCT	: GIVE THIS DRIVE A PASS
2354	004704	010046			MOV	R0,-(SP)	: ;SAVE R0 FOR TYPEOUT
2355							: ;TYPE THE DRIVE NUMBER
2356	004706	104403			TYPOS		: ;GO TYPE--OCTAL ASCII
2357	004710	001			.BYTE	1	: ;TYPE 1 DIGIT(S)
2358	004711	000			.BYTE	0	: ;SUPPRESS LEADING ZEROS
2359	004712	005701			TST	R1	: ;MORE DRIVES?


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2360 004714 001404          BEQ      9$          ;NO--BRANCH
2361 004716 104401 027377  TYPE     COMMA      ;
2362 004722 005200          INC      RO          ;INCREMENT DRIVE NUMBER
2363 004724 000763          BR       7$          ;CONTINUE
2364 004726 013737 017004 016776 9$:      MOV      SENDCT,SEOPCT ;SETUP TEST COUNT
2365 004734 005037 001206          CLR     CNTRLC      ;CLEAR CONVERSATION MODE FLAG
2366 004740 005037 001176          RSTRT1: CLR    CHKDRV ;INITIALIZE THE CHECK DRIVE KEY
2367 004744 012737 000001 001306 MOV     #1,DRVMSK   ;START TO CHECK DESIRED DRIVES
2368 004752 033737 001306 001312 RSTRT2: BIT    DRVMSK,DRVSEL ;IS THIS DRIVE SELECTED?
2369 004760 001011          RSTRT3: BNE   DRVOK  ;YES--GO CHECK IF DRIVE IS READY FOR TESTING
2370 004762 005237 001176          RESTART: INC   CHKDRV ;MOVE TO NEXT DRIVE NUMBER
2371 004766 106337 001306          ASLB   DRVMSK      ;POSITION THE MASK
2372 004772 103367          BCC    RSTRT2      ;BR IF MORE DRIVES
2373 004774 043737 001310 001312 BIC    DRVBAD,DRVSEL ;CLEAR SELECTION BITS FOR ANY OFFLINE/UNSAFE
2374                                DRIVES
2375 005002 000756          BR      RSTRT1     ;CONTINUE WITH CYCLE
2376 005004 113737 001176 002500 DRVOK:  MOVB   CHKDRV,DPB  ;PICKUP THE DRIVE NUMBER
2377 005012 104401 027401          TYPE   TSTING     ;'TESTING WITH DRIVE'
2378 005016 013746 001176          MOV    CHKDRV,-(SP) ;SAVE CHKDRV FOR TYPEOUT
2379                                ;TYPE THE DRIVE NUMBER
2380 005022 104403          TYPOS   ;GO TYPE--OCTAL ASCII
2381 005024 001          .BYTE  1          ;TYPE 1 DIGIT(S)
2382 005025 000          .BYTE  0          ;SUPPRESS LEADING ZEROS
2383 005026 104401 001173          TYPE   $CRLF     ;CR-LF
2384 005032 013704 001324          MOV    RPADR,R4   ;RP11 ADDRESS
2385 005036 004737 025654          JSR    PC,CLAP    ;CLEAR THE RP11
2386 005042 112737 000015 002502 MOVB   #HOMSEK,DPB+2 ;HOME SEEK COMMAND
2387 005050 004737 022770          JSR    PC,RP11    ;START THE COMMAND
2388 005054 004737 023546          JSR    PC,DRVRODY ;WAIT FOR THE DRIVE TO FINISH
2389 005060 000137 005064          JMP    TST0       ;START THE PROGRAM
2390
2391          .SBTTL  ##### TESTS #####
2392
2393          ;*****
2394          ;*TEST 0          TEST 'CLEAR' TERMINATION
2395          ;*****
2396          ;*VERIFY THAT 'CLEAR' TERMINATES AN OPERATION AND CAUSES CONTROLLER
2397          ;*READY TO SET.
2398          ;*****
2399          ;*****
2400          †TST0:
2401 005064 033737 001400 001276 BIT     BITS+(<STN*2),TSTNMS ;IS THIS TEST SELECTED
2402 005064 001002          BNE    .+6         ;BR IF YES
2403 005072 000137 005310          JMP    TST1        ;GO TO THE NEXT TEST
2404 005074 012737 005160 001110 MOV     #TST0,$LPERR ;SETUP THE ERROR LOOP ADDRESS
2405 005100 012737 005064 001106 MOV     #TST0,$LPADR ;SETUP THE SCOPE LOOP ADDRESS
2406 005106 012737 000000 001102 MOV     #0,$TSTNM   ;SETUP TEST NUMBER AND
2407 005114 012737          ;CLEAR THE ERROR FLAG ($ERFLG)
2408          MOV     $TSTNM,$DISPLAY ;LOAD THE TEST NUMBER INTO THE DISPLAY REGISTER
2409 005122 013777 001102 174012 MOV     #EXIT0,BYPASS ;SETUP BYPASS ADDRESS
2410 005130 012737 005306 001200 MOV     RPADR,R4   ;RP11E BUS ADDRESS
2411 005136 013704 001324          CLRB   DPB+1      ;CLEAR ANY EXTENDED MEMORY BITS
2412 005142 105037 002501          CLRB   DPB+3      ;CLEAR 'MODE' & 'HDR' BITS
2413 005146 105037 002503          MOV     #100,$TIMES ;DO 100. ITERATIONS
2414 005152 012737 000144 001162 TEST0:  MOV     #STACK,SP ;SETUP THE STACK POINTER
2415 005160 012706 001100

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TO TEST 'CLEAR' TERMINATION

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2416 005164 004737 025654 JSR PC,CLRP ;CLEAR THE RP11
2417 005170 012737 043656 002506 MOV #BUFFER,$BUF ;LOAD BUFFER ADDRESS
2418 005176 012737 000001 002504 MOV #1,$WC ;SETUP WORD COUNT FOR 1 WORD
2419 005204 005037 002510 CLR $CYL ;CYLINDER 0
2420 005210 005037 002512 CLR $SEC ;TRACK & SECTOR 0
2421 005214 112737 000005 002502 MOVB #RDSEK,DPB+2 ;READ COMMAND
2422 005222 004737 022770 JSR PC,RP11 ;ISSUE THE COMMAND AND RETURN
2423 005226 032764 000200 000004 BIT #RDY,$RPCS(R4) ;IS THE CONTROLLER READY ?
2424 005234 001404 BEQ $S ;BR IF NOT
2425 005236 004737 023240 JSR PC,SAVRP ;STORE THE REGISTERS
2426 005242 104002 ERROR 2 ;CAN'T START READ COMMAND
2427 005244 000420 BR EXITO ;EXIT FROM TEST
2428 005246 012764 000001 000004 1S: MOV #CLEAR,$RPCS(R4) ;ISSUE THE CONTROLLER CLEAR
2429 005254 012764 000001 000004 MOV #CLEAR,$RPCS(R4) ;CLEAR THE CONTROLLER AGAIN
2430 005262 004737 023240 JSR PC,SAVRP ;STORE THE REGISTERS
2431 005266 032737 000200 001340 BIT #RDY,$RPCS ;IS THE CONTROLLER READY ?
2432 005274 001004 BNE EXITO ;BR IF IT IS
2433 005276 104003 ERROR 3 ;'READY' DIDN'T SET AFTER ISSUING 'CLEAR'
2434 ;DURING READ COMMAND
2435 005300 000005 RESET ;FORCE THE CONTROLLER READY
2436 005302 004737 020416 JSR PC,$TKINT ;SETUP THE TTY KEYBOARD
2437 005306 000004 EXITO: SCOPE ;LOOP ?

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; *TEST 1 WRITE CHECK TEST

; *THIS TEST VERIFIES THE WRITE CHECK LOGIC AND TO VERIFY THAT A WRITE
; *CHECK ERROR CAN BE DETECTED. 'FLOATING ONES' AND 'FLOATING ZEROS'
; *PATTERNS ARE USED TO TEST THE WRITE CHECK COMPARE LOGIC.

; *ST1:

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2448 005310 033737 001402 001276 BIT BITS+(<$TN#2>),TSTNMS ;IS THIS TEST SELECTED
2449 005310 001002 BNE +6 ;BR IF YES
2450 005316 000137 005724 JMP TST2 ;GO TO THE NEXT TEST
2451 005320 012737 005404 001110 MOV #TST1,$LPERR ;SETUP THE ERROR LOOP ADDRESS
2452 005324 012737 005310 001106 MOV #TST1,$LPADR ;SETUP THE SCOPE LOOP ADDRESS
2453 005332 012737 000001 001102 MOV #1,$TSTNM ;SETUP TEST NUMBER AND
2454 ;CLEAR THE ERROR FLAG ($ERFLG)
2455 005340 013777 001102 173566 MOV $TSTNM,$DISPLAY ;LOAD THE TEST NUMBER INTO THE DISPLAY REGISTER
2456 005354 012737 005722 001200 MOV #EXIT1,BYPASS ;SETUP BYPASS ADDRESS
2457 005362 013704 001324 MOV RPADR,$R4 ;RP11E BUS ADDRESS
2458 005366 105037 002501 CLRB DPB+1 ;CLEAR ANY EXTENDED MEMORY BITS
2459 005372 105037 002503 CLRB DPB+3 ;CLEAR 'MODE' & 'HDR' BITS
2460 005376 012737 000144 001162 MOV #100,$TIMES ;DO 100. ITERATIONS
2461 005404 012706 001100 TEST1: MOV #STACK,$SP ;SETUP THE STACK POINTER
2462 005410 005037 002512 CLR $SEC ;CLEAR TRACK & SECTOR ADDRESS
2463 005414 005037 002510 CLR $CYL ;CLEAR CYLINDER ADDRESS
2464 005420 012737 000400 002504 MOV #256,$WC ;SET WORD COUNT TO 1 SECTOR
2465 005426 012737 000011 001204 MOV #9,$PATNUM ;STARTING PATTERN NUMBER
2466 005434 004737 024432 JSR PC,$SETBUF ;FILL THE BUFFER WITH THE PATTERN
2467 005440 012737 043656 002506 MOV #BUFFER,$BUF ;SETUP OUTPUT BUFFER
2468 005446 012737 005446 001110 1S: MOV #IS,$LPERR ;SETUP LOOP ON ERROR ADDRESS
2469 005454 004737 025654 JSR PC,CLRP ;CLEAR THE RP11
2470 005460 012737 000003 002502 MOV #WRTSEK,DPB+2 ;LOAD COMMAND

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2472	005466	004737	022770			JSR	PC,RP11	:WRITE THE SECTOR WITH THE TEST PATTERN
2473	005472	004737	023664			JSR	PC,CONRDY	:WAIT FOR THE OPERATION TO COMPLETE
2474	005476	004737	023240			JSR	PC,SAVRP	:SAVE THE REGISTERS
2475	005502	032737	100000	001340		BIT	#ERR,\$RPCS	:DID AN ERROR OCCUR ?
2476	005510	001401				BEQ	3\$:BR IF NOT
2477	005512	104004				ERROR	4	:ERROR OCCURED WRITING WRITE CHECK TEST PATTERN
2478	005514	012737	005514	001110	3\$:	MOV	#3\$,SLPERR	:CHANGE LOOP ON ERROR ADDRESS
2479	005522	004737	025654			JSR	PC,CLRP	:CLEAR THE RP11
2480	005526	012737	000007	002502		MOV	#WRTCHK,DPB+2	:LOAD WRITE CHECK COMMAND
2481	005534	004737	022770			JSR	PC,RP11	:CHECK THE SECTOR JUST WRITTEN
2482	005540	004737	023664			JSR	PC,CONRDY	:WAIT FOR WRITE CHECK TO COMPLETE
2483	005544	004737	023240			JSR	PC,SAVRP	:STORE THE REGISTERS
2484	005550	032737	100000	001340		BIT	#ERR,\$RPCS	:CHECK OK ?
2485	005556	001403				BEQ	4\$:BR IF OK
2486	005560	104005				ERROR	5	:ERROR ATTEMPTING TO CHECK TEST PATTERN
2487	005562	004737	025654			JSR	PC,CLRP	:CLEAR THE RP11
2488	005566	005001			4\$:	CLR	R1	:SETUP TO CLEAR BUFFER
2489	005570	005002				CLR	R2	:BUFFER INDEX
2490	005572	023727	001204	000011		CMP	PATNUM,#9.	: 'FLOATING 1' PATTERN ?
2491	005600	001002				BNE	5\$:BRANCH IF NOT
2492	005602	012701	177777			MOV	#-1,R1	:FILL BUFFER WITH ONES
2493	005606	010162	043656		5\$:	MOV	R1,BUFFER(R2).	:FILL BUFFER WITH PATTERN IN R1
2494	005612	022702	000400			CMP	#256.,R2	:SEE IF FINISHED
2495	005616	001403				BEQ	6\$:BR IF FINISHED
2496	005620	062702	000002			ADD	#2,R2	:INCREMENT THE INDEX
2497	005624	000770				BR	5\$:CONTINUE
2498	005626	012737	005626	001110	6\$:	MOV	#6\$,SLPERR	:CHANGE THE LOOP ON ERROR ADDRESS
2499	005634	004737	025654			JSR	PC,CLRP	:CLEAR THE RP11
2500	005640	004737	022770			JSR	PC,RP11	:CHECK THE SECTOR AGAIN, ERROR SHOULD OCCUR
2501	005644	004737	023664			JSR	PC,CONRDY	:WAIT FOR THE WRITE CHECK TO COMPLETE
2502	005650	004737	023240			JSR	PC,SAVRP	:STORE THE REGISTERS
2503	005654	032737	000010	001336		BIT	#BIT03,\$RPER	:DID WRITE CHECK ERROR SET?
2504	005662	001002				BNE	7\$:BRANCH IF YES
2505	005664	104006				ERROR	6	:WRITE CHECK ERROR DID NOT SET
2506	005666	000405				BR	8\$:BYPASS REST OF TEST
2507	005670	032737	100000	001340	7\$:	BIT	#ERR,\$RPCS	:DID 'ERR' SET ?
2508	005676	001001				BNE	8\$:BR IF IT SET
2509	005700	104106				ERROR	106	: 'ERR' DIDN'T SET WITH 'WCE'
2510	005702	023727	001204	000012	8\$:	CMP	PATNUM,#10.	:FLOATING A ZERO ?
2511	005710	001404				BEQ	EXIT1	:BR IF ZERO
2512	005712	012737	000012	001204		MOV	#10.,PATNUM	: 'FLOATING ZERO' PATTERN
2513	005720	000652				BR	1\$:DO THE TEST AGAIN
2514	005722	000004				EXIT1:	SCOPE	:LOOP ?

 ;*TEST 2 PARTIAL SECTOR WRITE TEST

;*CHECK THE ABILITY OF THE RP11E TO CLEAR THE REMAINDER OF A SECTOR
 ;*ON A PARTIAL WRITE OPERATION. A SECTOR OF ALL ONES IS WRITTEN AND
 ;*THEN A TWO WORD WRITE OPERATION IS PERFORMED. THE SECTOR IS THEN
 ;*READ BACK AND VERIFIED. THE FIRST TWO WORDS SHOULD BE ONES AND
 ;*THE REST SHOULD BE ZEROS.

 ;*ST2:

005724

2528	005724	033737	001404	001276	BIT	#STN*2, TSTNMS	: IS THIS TEST SELECTED
2529	005732	001002			BNE	+6	: BR IF YES
2530	005734	000137	006402		JMP	TST3	: GO TO THE NEXT TEST
2531	005740	012737	006020	001110	MOV	#TEST2, \$LPERR	: SETUP THE ERROR LOOP ADDRESS
2532	005746	012737	005724	001106	MOV	#TST2, \$LPADR	: SETUP THE SCOPE LOOP ADDRESS
2533	005754	012737	000002	001102	MOV	#2, \$TSTNM	: SETUP TEST NUMBER AND
2534							: CLEAR THE ERROR FLAG (\$ERFLG)
2535	005762	013777	001102	173152	MOV	\$TSTNM, \$DISPLAY	: LOAD THE TEST NUMBER INTO THE DISPLAY REGISTER
2536	005770	012737	005400	001200	MOV	#EXIT2, BYPASS	: SETUP BYPASS ADDRESS
2537	005776	013704	001324		MOV	RPADR, R4	: RP11E BUS ADDRESS
2538	006002	105037	002501		CLRB	DPB+1	: CLEAR ANY EXTENDED MEMORY BITS
2539	006006	105037	002503		CLRB	DPB+3	: CLEAR 'MODE' & 'HDR' BITS
2540	006012	012737	000144	001162	MOV	#100, \$TIMES	: DO 100. ITERATIONS
2541	006020	012706	001100		TEST2: MOV	#STACK, SP	: SETUP THE STACK POINTER
2542	006024	012737	006020	001110	MOV	#TEST2, \$LPERR	: SETUP INITIAL LOOP ON ERROR ADDRESS
2543	006032	004737	025654		JSR	PC, CLRP	: CLEAR THE RP11
2544	006036	012737	043656	002506	MOV	#BUFFER, \$BUF	: LOAD BUFFER ADDRESS
2545	006044	012737	000400	002504	MOV	#256, \$WC	: WORD COUNT - 1 SECTOR
2546	006052	005037	002512		CLR	\$SEC	: CLEAR SECTOR/TRACK ADDRESS
2547	006056	005037	002510		CLR	\$CYL	: CLEAR CYLINDER ADDRESS
2548	006062	012737	000015	001204	MOV	#13, \$PATNUM	: ONE'S PATTERN NUMBER
2549	006070	004737	024432		JSR	PC, SETBUF	: LOAD THE PATTERN INTO THE BUFFER
2550	006074	012737	000003	002502	MOV	#WRTSEK, DPB+2	: WRITE COMMAND
2551	006102	004737	022770		JSR	PC, RP11	: WRITE THE SECTOR
2552	006106	004737	023664		JSR	PC, CONRDY	: WAIT FOR THE WRITE TO COMPLETE
2553	006112	004737	023240		JSR	PC, SAVRP	: STORE THE REGISTERS
2554	006116	032737	100000	001340	BIT	#ERR, \$RPCS	: DID AN ERROR OCCUR ?
2555	006124	001402			BEQ	1\$: BR IF NOT
2556	006126	104004			ERROR	4	: ERROR OCCURED DURING WRITE
2557	006130	000523			BR	EXIT2	: BYPASS REST OF TEST
2558	006132	012737	006132	001110	1\$: MOV	#1\$, \$LPERR	: CHANGE LOOP ON ERROR ADDRESS
2559	006140	004737	025654		JSR	PC, CLRP	: CLEAR THE RP11
2560	006144	004737	024432		JSR	PC, SETBUF	: LOAD ONE'S PATTERN INTO THE BUFFER
2561	006150	012737	000002	002504	MOV	#2, \$WC	: 2 WORD TRANSFER
2562	006156	112737	000003	002502	MOVB	#WRTSEK, DPB+2	: CHANGE COMMAND TO WRITE
2563	006164	004737	022770		JSR	PC, RP11	: WRITE
2564	006170	004737	023664		JSR	PC, CONRDY	: WAIT FOR CONTROLLER READY
2565	006174	004737	023240		JSR	PC, SAVRP	: STORE THE REGISTERS
2566	006200	032737	100000	001340	BIT	#ERR, \$RPCS	: ERROR ?
2567	006206	001402			BEQ	2\$: BR IF NOT
2568	006210	104007			ERROR	7	: ERROR WRITING THE PARTIAL SECTOR
2569	006212	000472			BR	EXIT2	: BYPASS THE REST OF THE TEST
2570	006214	012737	006214	001110	2\$: MOV	#2\$, \$LPERR	: CHANGE THE ERROR LOOP ADDRESS
2571	006222	004737	025654		JSR	PC, CLRP	: CLEAR THE RP11
2572	006226	012737	000400	002504	MOV	#256, \$WC	: CHANGE THE WORD COUNT
2573	006234	012737	000005	002502	MOV	#RDSEK, DPB+2	: READ COMMAND
2574	006242	004737	022770		JSR	PC, RP11	: READ THE SECTOR
2575	006246	004737	023664		JSR	PC, CONRDY	: WAIT FOR CONTROLLER READY
2576	006252	004737	023240		JSR	PC, SAVRP	: STORE THE REGISTERS
2577	006256	032737	100000	001340	BIT	#ERR, \$RPCS	: DID AN ERROR OCCUR ?
2578	006264	001402			BEQ	3\$: BR IF NOT
2579	006266	104017			ERROR	17	: ERROR OCCURED READING SECTOR
2580	006270	000443			BR	EXIT2	: BYPASS REST OF TEST
2581	006272	022737	177777	043656	3\$: CMP	#177777, BUFFER	: COMPARE FIRST WORD SHOULD BE ONES
2582	006300	001004			BNE	4\$: BR IF NOT
2583	006302	022737	177777	043660	CMP	#177777, BUFFER+2	: CHECK THE SECOND WORD

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2584 006310 001407 BEQ 5$ ;BR IF OK
2585 006312 012737 177777 001124 4$: MOV #177777,$GDDAT ;EXPECTED DATA
2586 006320 013737 043656 001126 MOV BUFFER,$BDDAT ;ACTUAL DATA
2587 006326 104011 ERROR 11 ;DATA COMPARE ERROR IN FIRST 2 WORDS
2588 006330 012700 043662 5$: MOV #BUFFER+4,RO ;STARTING ADDRESS OF LAST PART OF THE BUFFER
2589 006334 012701 000376 MOV #254,R1 ;WORD COUNT
2590 006340 005720 6$: TST (RO)+ ;REMAINDER OF SECTOR SHOULD BE ZEROS
2591 006342 001003 BNE 7$ ;BRANCH IF NOT
2592 006344 005301 DEC R1 ;DECREMENT THE CUNT
2593 006346 001374 BNE 6$ ;BR IF NOT FINISHED
2594 006350 000413 BR EXIT2 ;EXIT
2595 006352 016037 177776 001126 7$: MOV -2(RO),$BDDAT ;INCORRECT DATA
2596 006360 005037 001124 CLR $GDDAT ;EXPECTED DATA (ZEROS)
2597 006364 010037 001122 MOV RO,$BDADR ;ADDRESS OF INCORRECT CHARACTER
2598 006370 162737 000002 001122 SUB #2,$BDADR ;DECREMENT THE ADDRESS
2599 006376 104012 ERROR 12 ;DATA FOUND IN AREA OF SECTOR
;WHICH SHOULD HAVE BEEN CLEARED
;BY A ONE WORD WRITE

```

2601 2602 006400 000004 EXIT2: SCOPE

2605 *****
2606 ;*TEST 3 'EOP' TEST

2608 ;*TEST THAT 'EOP' SETS WHEN THE CONTROLLER TRIES TO WRITE BEYOND THE
2609 ;*LIMITS OF THE DRIVE. THE FIRST SECTOR OF THE PACK IS WRITTEN WITH
2610 ;*ZEROS. THEN A TWO SECTOR WRITE OF ALL ONE'S IS ISSUED FOR THE MAXIMUM
2611 ;*CYLINDER, HEAD 19, SECTOR 9. 'EOP' AND THE ERROR BITS IN 'RPCS' SHOULD
2612 ;*BE SET. THE FIRST SECTOR OF THE PACK IS CHECKED TO MAKE SURE THAT IS
2613 ;*STILL CONTAINS ZEROS.

2615 *****
2616 ;*TEST3:

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2617 006402 033737 001406 001276 BIT BITS+(<STN*2>),TSTNMS ;IS THIS TEST SELECTED
2618 006410 001002 BNE +6 ;BR IF YES
2619 006412 000137 007060 JMP TST4 ;GO TO THE NEXT TEST
2620 006416 012737 006476 001110 MOV #TEST3,$LPERR ;SETUP THE ERROR LOOP ADDRESS
2621 006424 012737 006402 001106 MOV #TST3,$LPADR ;SETUP THE SCOPE LOOP ADDRESS
2622 006432 012737 000003 001102 MOV #3,$TSTNM ;SETUP TEST NUMBER AND
2623 ;CLEAR THE ERROR FLAG ($ERFLG)
2624 006440 013777 001102 172474 MOV $TSTNM,$DISPLAY ;LOAD THE TEST NUMBER INTO THE DISPLAY REGISTER
2625 006446 012737 007056 001200 MOV #EXIT3,BYPASS ;SETUP BYPASS ADDRESS
2626 006454 013704 001324 MOV RPADR,R4 ;RP11E BUS ADDRESS
2627 006460 105037 002501 CLRB DPB+1 ;CLEAR ANY EXTENDED MEMORY BITS
2628 006464 105037 002503 CLRB DPB+3 ;CLEAR 'MODE' & 'HDR' BITS
2629 006470 012737 000031 001162 MOV #25,$TIMES ;DO 25 ITERATIONS
2630 006476 012706 001100 TEST3: MOV #STACK,SP ;SETUP THE STACK POINTER
2631 006502 012737 006476 001110 MOV #TEST3,$LPERR ;INITIAL LOOP ON ERROR ADDRESS
2632 006510 004737 025654 JSR PC,CLRP ;CLEAR THE RP11
2633 006514 012737 000400 002504 MOV #256,$WC ;SET WORD COUNT TO 1 SECTOR
2634 006522 012737 043656 002506 MOV #BUFFER,$BUF ;SETUP OUTPUT BUFFER
2635 006530 005037 001204 CLR PATNUM ;SETUP FOR PATTERN ZERO
2636 006534 004737 024432 JSR PC,SETBUF ;LOAD ZEROS INTO THE BUFFER
2637 006540 005037 002512 CLR $SEC ;SET SECTOR & TRACK TO ZERO
2638 006544 005037 002510 CLR $CYL ;SET CYLINDER TO ZERO
2639 006550 112737 000003 002502 MOVB #WRTSEK,DPB+2 ;WRITE COMMAND

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2680 006556 004737 022770 JSR PC,RP11 WRITE ZEROS IN CO. TO .50
2681 006557 004737 023664 JSR PC,CONADY WAIT FOR THE ORDER TO COMPLETE
2682 006558 004737 023240 JSR PC,SAVRP STORE THE REGISTERS
2683 006559 004737 032737 BIT BERR,SRPCS DID AN ERROR OCCUR?
2684 006560 001402 BEO ERROR BRANCH IF NOT
2685 006561 104004 ERROR ERROR OCCURED DURING SETUP
2686 006562 000524 BR EXIT3 BYPASS REST OF TEST
2687 006563 012737 006606 001110 18: MOV B18,SLPERR CHANGE LOOP ON ERROR ADDRESS
2688 006564 004737 025654 JSR PC,CLRP CLEAR THE RP11
2689 006565 012737 000015 001204 MOV B13,PATNUM ONE'S PATTERN INDEX
2690 006566 004737 024432 JSR PC,SETBLF LOAD THE PATTERN INTO THE BUFFER
2691 006567 012737 001000 002504 MOV B512,SWC CHANGE WORD COUNT TO 2 SECTORS
2692 006568 013737 001210 002510 MOV MAXCYL,SCYL SELECT THE DRIVE'S MAXIMUM CYLINDER
2693 006569 012737 000011 002512 MOV B9,SSEC SELECT SECTOR 9
2694 006570 112737 000023 002513 MOVB B9,STRK SELECT HEAD 19
2695 006571 004737 022770 JSR PC,RP11 START THE READ
2696 006572 004737 023664 JSR PC,CONADY WAIT FOR CONTROLLER READY
2697 006573 004737 023240 JSR PC,SAVRP STORE THE RP11 REGISTERS
2698 006574 032737 000002 001336 BIT BBIT01,SRPER DID EOP ERROR SET?
2699 006575 001002 BNE BRANCH IF SET
2700 006576 104013 ERROR 'EOP' DIDN'T SET
2701 006577 000405 BR 35 CHECK RPCA
2702 006578 032737 100000 001340 28: BIT BERR,SRPCS DID THE ERROR FLAG SET?
2703 006579 001001 BNE BRANCH IF SET
2704 006580 104014 ERROR ERROR DID NOT SET AFTER GENERATING 'EOP'
2705 006581 005737 001346 38: TST SRPCA SEE IF THE CYLINDER ADDRESS REGISTER WAS CLEARED
2706 006582 001401 BEO BR IF CLEARED
2707 006583 104015 ERROR 'RPCA' NOT CLEARED BY 'EOP'
2708 006584 023737 001354 001210 48: CMP SUCA,MAXCYL SUCA STILL EQUAL TO THE MAXIMUM CLINDER
2709 006585 001401 BEO BR IF IT IS
2710 006586 104016 ERROR SUCA NOT CORRECT AFTER EOP ERROR
2711 006587 012737 006746 001110 58: MOV B55,SLPERR CHANGE LOOP ON ERROR ADDRESS
2712 006588 004737 025654 JSR PC,CLRP CLEAR THE RP11
2713 006589 012737 000002 002504 MOV B2,SWC WORD COUNT
2714 006590 005037 002512 CLR SSEC CLEAR SECTOR/TRACK ADDRESS
2715 006591 005037 002510 CLR SCYL CLEAR THE CYLINDER ADDRESS
2716 006592 012737 000005 002502 MOV B0DSEK,DPB+2 CHANGE COMMAND TO READ
2717 006593 004737 022770 JSR PC,RP11 EXECUTE THE COMMAND
2718 006594 004737 023664 JSR PC,CONADY WAIT FOR CONTROLLER READY
2719 006595 004737 023240 JSR PC,SAVRP STORE THE REGISTERS
2720 006596 032737 100000 001340 BIT BERR,SRPCS WERE THERE ANY ERRORS?
2721 006597 001402 BEO BRANCH IF NOT
2722 006598 104017 ERROR ERROR ENCOUNTERED ON 2 WORD READ
2723 006599 000411 BR EXIT3 OF FIRST SECTOR ON THE PACK
2724 006600 013737 043656 001126 68: MOV BUFFER,SBDDAT GET FIRST WORD OF BUFFER
2725 006601 005737 001126 TST SBDDAT DOES 1ST SECTOR STILL CONTAIN ZEROS?
2726 006602 001403 BEO BRANCH IF YES
2727 006603 005037 001124 CLR SGGDAT ZEROS WERE EXPECTED
2728 006604 104020 ERROR CONTENTS OF THE FIRST SECTOR OF THE
2729 006605 000004 EXIT3: SCOPE PACK CHANGED AFTER FORCING EOP
2730 *****
2731 ;*TEST 4 TEST 'PROG' ERROR

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;*VERIFY THAT THE RP11E GENERATES A 'PROG' ERROR IF A COMMAND IS ISSUED
;*WHILE THE CONTROLLER IS BUSY.

†ST4:

2700	007060	033737	001410	001276	BIT	BITS+(<STN#2>),TSTNMS	: IS THIS TEST SELECTED
2701	007060	001002			BNE	+6	: BR IF YES
2702	007066	000137	007310		JMP	†ST5	: GO TO THE NEXT TEST
2703	007070	012737	007154	001110	MOV	†TEST4,\$LPERR	: SETUP THE ERROR LOOP ADDRESS
2704	007074	012737	007060	001106	MOV	†ST4,\$LPADR	: SETUP THE SCOPE LOOP ADDRESS
2705	007102	012737	000004	001102	MOV	#4,†STNM	: SETUP TEST NUMBER AND
2706	007110	012737					: CLEAR THE ERROR FLAG (SERFLG)
2707							: LOAD THE TEST NUMBER INTO THE DISPLAY REGISTER
2708	007116	013777	001102	172016	MOV	†STNM,‡DISPLAY	: SETUP BYPASS ADDRESS
2709	007124	012737	007306	001200	MOV	†EXIT4,BYPASS	: RP11E BUS ADDRESS
2710	007132	013704	001324		MOV	RPADR,R4	: CLEAR ANY EXTENDED MEMORY BITS
2711	007136	105037	002501		CLRB	DPB+1	: CLEAR 'MODE' & 'HDR' BITS
2712	007142	105037	002503		CLRB	DPB+3	: DO 100 ITERATIONS
2713	007146	012737	000144	001162	MOV	#100,‡TIMES	: SETUP THE STACK POINTER
2714	007154	012706	001100		MOV	†STACK,SP	: CLEAR THE RP11
2715	007160	004737	025654		JSR	PC,CLRP	: CLEAR THE SECTOR - TRACK ADDRESSES
2716	007164	005037	002512		CLR	SSC	: CLEAR THE CYLINDER ADDRESS
2717	007170	005037	002510		CLR	SCYL	: SETUP BUFFER ADDRESS
2718	007174	012737	043656	002506	MOV	†BUFFER,\$BUF	: SETUP WORD COUNT
2719	007202	012737	002000	002504	MOV	#1024,‡WC	: READ COMMAND
2720	007210	112737	000005	002502	MOVB	†RDSEK,DPB+2	: ISSUE THE COMMAND
2721	007216	004737	022770		JSR	PC,RP11	: LOAD STALL VALUE
2722	007222	012737	000001	001220	MOV	#1,‡STALL	: STALL FOR 1 MILLISECOND
2723	007230	004737	023202		JSR	PC,‡STALL	: ISSUE READ COMMAND WHILE BUSY
2724	007234	112764	000005	000004	MOVB	†RDSEK,RPCS(R4)	: STORE THE REGISTERS
2725	007242	004737	023240		JSR	PC,SAVRP	: DID PROGRAM ERROR SET?
2726	007246	032737	002000	001336	BIT	†BIT10,\$RPER	: BRANCH IF SET
2727	007254	001002			BNE	15	: PROGRAM ERROR DID NOT SET WHEN A
2728	007256	104021			ERROR	21	: READ COMMAND WAS ISSUED WHILE
2729							: THE DEVICE WAS BUSY
2730							: BYPASS REST OF TEST
2731	007260	000412			BR	EXIT4	: 'HE' SET?
2732	007262	032737	040000	001340	BIT	†HE,\$RPCS	: BR IF NOT
2733	007270	001001			BNE	25	: 'HE' DIDN'T SET WITH 'PROG'
2734	007272	104107			ERROR	107	: DID 'ERR' SET?
2735	007274	032737	100000	001340	BIT	†ERR,\$RPCS	: BRANCH IF SET
2736	007302	001001			BNE	EXIT4	: 'ERR' DID NOT SET WITH PROGRAM ERROR
2737	007304	104022			ERROR	22	
2738	007306	000004			EXIT4:	SCOPE	

;†TEST 5 'HEADER NOT FOUND' TEST

;*UNFORMAT THE FIRST SECTOR ON THE PACK AND THEN READ IT BACK. VERIFY
;*THAT READ AND WRITE HEADER OPERATIONS WILL TRANSFER DATA CORRECTLY.
;*ISSUE A WRITE COMMAND TO SECTOR ZERO; THIS SHOULD CAUSE 'HEADER NOT
;*FOUND' TO SET. REFORMAT THE SECTOR.

†ST5:

2750	007310				BIT	BITS+(<STN#2>),TSTNMS	: IS THIS TEST SELECTED
2751	007310	033737	001412	001276			

2752	007316	001002			BNE	+6		: BR IF YES
2753	007320	000137	010034		JMP	TST6		: GO TO THE NEXT TEST
2754	007324	012737	007404	001110	MOV	#TEST5,SLPERR		: SETUP THE ERROR LOOP ADDRESS
2755	007332	012737	007310	001106	MOV	#TEST5,SLPADR		: SETUP THE SCOPE LOOP ADDRESS
2756	007340	012737	000005	001102	MOV	#5,STSTNM		: SETUP TEST NUMBER AND
2757								: CLEAR THE ERROR FLAG (SERFLG)
2758	007346	013777	001102	171566	MOV	STSTNM,DISP		: LOAD THE TEST NUMBER INTO THE DISPLAY REGISTER
2759	007354	012737	010032	001200	MOV	#EXIT5,BYPASS		: SETUP BYPASS ADDRESS
2760	007362	013704	001324		MOV	RPADR,R4		: RP11 BUS ADDRESS
2761	007366	105037	002501		CLRB	DPB+1		: CLEAR ANY EXTENDED MEMORY BITS
2762	007372	105037	002503		CLRB	DPB+3		: CLEAR 'MODE' & 'HDR' BITS
2763	007376	012737	000144	001162	MOV	#100,STIMES		: DO 100 ITERATIONS
2764	007404	012706	001100		MOV	#STACK,SP		: SETUP THE STACK POINTER
2765	007410	012737	007404	001110	MOV	#TEST5,SLPERR		: INITIAL LOOP ON ERROR ADDRESS
2766	007416	004737	025654		JSR	PC,CLRP		: CLEAR THE RP11
2767	007422	012737	000001	043656	MOV	#1,BUFFER		: SETUP INVALID HEADER IMAGE
2768	007430	012737	000001	043660	MOV	#1,BUFFER+2		: CYLINDER/TRACK
2769	007436	012737	000001	043662	MOV	#1,BUFFER+4		: SECTOR
2770	007444	012737	000003	002504	MOV	#3,SWC		: LOAD WORD COUNT
2771	007452	005037	002512		CLR	\$SEC		: CLEAR SECTOR/TRACK ADDRESS
2772	007456	005037	002510		CLR	\$CYL		: CLEAR THE CYLINDER ADDRESS
2773	007462	012737	014000	002502	MOV	#MODE,HDR,DPB+2		: 'MODE' AND 'HEADER' BITS
2774	007470	112737	000003	002502	MOVB	#WRTSEK,DPB+2		: WRITE ORDER
2775	007476	004737	022770		JSR	PC,RP11		: MISFORMAT SECTOR 0
2776	007502	004737	023664		JSR	PC,CONRDY		: WAIT FOR THE OPERATION TO COMPLETE
2777	007506	004737	023240		JSR	PC,SAVRP		: STORE THE REGISTERS
2778	007512	032737	100000	001340	BIT	#ERR,\$RPCS		: DID AN ERROR OCCUR ?
2779	007520	001402			BEG	1\$: BR IF NOT
2780	007522	104023			ERROR	23		: ERROR OCCURED ATTEMPTING TO MISFORMAT SECTOR 0
2781	007524	000506			BR	TST5B		: BYPASS TEST & REFORMAT SECTOR 0
2782	007526	012737	007526	001110	MOV	#1\$,SLPERR		: CHANGE LOOP ON ERROR ADDRESS
2783	007534	004737	025654		JSR	PC,CLRP		: CLEAR THE RP11
2784	007540	112737	000005	002502	MOVB	#RSEK,DPB+2		: CHANGE COMMAND
2785	007546	004737	022770		JSR	PC,RP11		: EXECUTE THE COMMAND
2786	007552	004737	023664		JSR	PC,CONRDY		: WAIT FOR CONTROLLER READY
2787	007556	032737	100000	001340	BIT	#ERR,\$RPCS		: ANY ERRORS?
2788	007564	001401			BEG	2\$: BRANCH IF NOT
2789	007566	104024			ERROR	24		: ERROR WHILE READING THE HEADER
2790								: ON SECTOR ZERO
2791	007570	012737	000001	001124	MOV	#1,\$GDDAT		: EXPECTED RESULT
2792	007576	005000			CLR	RO		: CLEAR THE INDEX
2793	007600	023760	001124	043656	CMP	\$GDDAT,BUFFER(RO)		: CHECK DATA READ BACK
2794	007606	001006			BNE	4\$: BRANCH ON NON COMPARE
2795	007610	062700	000002		ADD	#2,RO		: UPDATE MODIFIER
2796	007614	022700	000006		CMP	#6,RO		: END OF BUFFER?
2797	007620	001367			BNE	3\$: BR IF NOT
2798	007622	000404			BR	TST5A		: CONTINUE WITH TEST
2799	007624	016037	043656	001126	MOV	BUFFER(RO),\$BDDAT		: GET BAD DATA
2800	007632	104025			ERROR	25		: DATA DID NOT VERIFY AFTER READING
2801								: THE HEADER OF SECTOR ZERO
2802	007634	012737	007634	001110	MOV	#TST5A,SLPERR		: CHANGE LOOP ON ERROR ADDRESS
2803	007642	004737	025654		JSR	PC,CLRP		: CLEAR THE RP11
2804	007646	012737	000003	002502	MOV	#WRTSEK,DPB+2		: CHANGE COMMAND (AND CLEAR 'MODE' & 'HDR')
2805	007654	004737	022770		JSR	PC,RP11		: EXECUTE THE COMMAND
2806	007660	004737	023664		JSR	PC,CONRDY		: WAIT FOR CONTROLLER READY
2807	007664	004737	023240		JSR	PC,SAVRP		: STORE THE REGISTERS


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2808 007670 032737 010000 001334 1S: BIT #BIT12,SRPDS ;DID HEADER NOT FOUND SET?
2809 007676 001002 BNE #25 ;BRANCH IF YES
2810 007700 104026 ERROR #26 ;'HNF' DID NOT SET
2811 007702 000417 BR TST58 ;BYPASS REST OF TEST
2812 007704 032737 000001 001336 2S: BIT #BIT00,SRPER ;DID DISK ERROR SET?
2813 007712 001001 BNE #27 ;BRANCH IF YES
2814 007714 104027 ERROR #27 ;'DSK' DID NOT SET WITH 'HNF'
2815 007716 032737 040000 001340 3S: BIT #HE,SRPCS ;DID HARD ERROR SET?
2816 007724 001001 BNE #48 ;BRANCH IF YES
2817 007726 104030 ERROR #30 ;HARD ERROR NOT SET AFTER 'HNF'
2818 007730 032737 100000 001340 4S: BIT #ERR,SRPCS ;DID 'ERR' SET?
2819 007736 001001 BNE TST58 ;BRANCH IF YES
2820 007740 104031 ERROR #31 ;'ERR' DID NOT SET WITH 'HNF'
2821 007742 012737 007742 001110 TST58: MOV #TST58,SLPERR ;CHANGE LOOP ON ERROR ADDRESS
2822 007750 004737 025654 JSR PC,CLRP ;CLEAR THE RP11
2823 007754 005037 043656 CLR BUFFER ;SETUP HEADER IMAGE
2824 007760 005037 043660 CLR BUFFER+2 ;CYLINDER/TRACK = 0
2825 007764 005037 043662 CLR BUFFER+4 ;SECTOR = 0
2826 007770 012737 014000 002502 MOV #MODE!HDR,DPB+2 ;'MODE' AND 'HDR' BITS
2827 007776 112737 000003 002502 MOVB #WRTSEK,DPB+2 ;WRITE COMMAND
2828 010004 004737 022770 JSR PC,RP11 ;RESTORE THE HEADER
2829 010010 004737 023664 JSR PC,CONRDY ;WAIT FOR THE OPERATION TO COMPLETE
2830 010014 004737 023240 JSR PC,SAVRP ;STORE THE REGISTERS
2831 010020 032737 100000 001340 BIT #ERR,SRPCS ;DID AN ERROR OCCUR?
2832 010026 001557 BEQ EXIT6 ;BR IF NOT
2833 010030 104032 ERROR #32 ;ERROR ATTEMPTING TO RESTORE HEADER
2834 010032 000004
EXIT6: SCOPE

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*****
; *TEST 6 TEST COMMAND ISSUED TO A SEEKING DRIVE
*****
; *ISSUE A SEEK COMMAND AND WAIT FOR DONE TO SET. THEN ISSUE A READ
; *COMMAND WHILE THE HEADS ARE STILL MOVING. THE RP11E SHOULD
; *HOLD THE WRITE COMMAND TILL THE SEEK IS COMPLETE.

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*****
†TST6:
2845 010034
2846 010034 033737 001414 001276 BIT BITS+(STN*2),TSTNMS ;IS THIS TEST SELECTED
2847 010042 001002 BNE #+6 ;BR IF YES
2848 010044 000137 010370 JMP TST7 ;GO TO THE NEXT TEST
2849 010050 012737 010130 001110 MOV #TEST6,SLPERR ;SETUP THE ERROR LOOP ADDRESS
2850 010056 012737 010034 001106 MOV #TST6,SLPADR ;SETUP THE SCOPE LOOP ADDRESS
2851 010064 012737 000006 001102 MOV #6,STSTNM ;SETUP TEST NUMBER AND
2852 ;CLEAR THE ERROR FLAG ($ERFLG)
2853 010072 013777 001102 171042 MOV $STNM,$DISPLAY ;LOAD THE TEST NUMBER INTO THE DISPLAY REGISTER
2854 010100 012737 010366 001200 MOV #EXIT6,BYPASS ;SETUP BYPASS ADDRESS
2855 010106 013704 001324 MOV RPADR,R4 ;RP11E BUS ADDRESS
2856 010112 105037 002501 CLRB DPB+1 ;CLEAR ANY EXTENDED MEMORY BITS
2857 010116 105037 002503 CLRB DPB+3 ;CLEAR 'MODE' & 'HDR' BITS
2858 010122 012737 000031 001162 MOV #25,STIMES ;DO 25 ITERATIONS
2859 010130 012706 001100 TEST6: MOV #STACK,SP ;SETUP THE STACK POINTER
2860 010134 004737 025654 JSR PC,CLRP ;CLEAR THE RP11
2861 010140 005037 002512 CLR $SEC ;SECTOR/TRACK 0
2862 010144 005037 002510 CLR $CYL ;CYLINDER 0
2863 010150 112737 000015 002502 MOVB #HOMSEK,DPB+2 ;HOME SEEK COMMAND

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F05

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2864 010156 004737 022770 JSR PC,RP11 ;DO THE COMMAND
2865 010162 004737 023546 JSR PC,DRVRDY ;WAIT FOR DRIVE READY
2866 010166 012737 000300 002510 MOV #192,SCYL ;CHANGE CYLINDER TO CYLINDER 192
2867 010174 112737 000011 002502 MOVB #SEEK,DPB+2 ;SEEK COMMAND
2868 010202 004737 022770 JSR PC,RP11 ;INITIATE THE SEEK
2869 010206 012737 000001 001220 MOV #1,STALL ;LOAD STALL VALUE
2870 010214 004737 023202 JSR PC,STALL ;STALL FOR 1 MILLISECOND
2871 010220 012764 177775 000006 MOV #-3,RPWC(R4) ;SET WORD COUNT TO THREE WORDS
2872 010226 012764 043656 000010 MOV #BUFFER,RPBA(R4) ;BUFFER ADDRESS
2873 010234 052737 014000 002502 BIS #MODE!HOR,DPB+2 ;SETUP TO READ THE HEADER
2874 010242 112764 000017 000004 MOVB #READ,RPCS(R4) ;TRY TO DO A READ
2875 010250 004737 023546 JSR PC,DRVRDY ;WAIT FOR DRIVE READY
2876 010254 004737 023240 JSR PC,SAVRP ;STORE THE REGISTERS
2877 010260 032737 000200 001340 BIT #RDY,SRPCS ;DID CONTROLLER BECOME READY ALSO ?
2878 010266 001402 BEQ #6 ;BR IF NOT
2879 010270 104033 ERROR #33 ;READ DIDN'T START DURING SEEK
2880 010272 000435 BR ;EXIT
2881 010274 004737 023664 15: JSR PC,CONRDY ;WAIT FOR CONTROLLER READY
2882 010300 004737 023240 JSR PC,SAVRP ;STORE THE REGISTERS
2883 010304 032737 100000 001340 BIT #ERR,SRPCS ;ANY ERRORS?
2884 010312 001401 BEQ #25 ;BRANCH IF NO
2885 010314 104034 ERROR #34 ;ERROR OCCURED DURING A READ ISSUED
2886 ;WHILE THE DRIVE IS SEEKING
2887 010316 012737 014000 001124 25: MOV #<192.*32>,SGDDAT ;EXPECTED CYLINDER VALUE
2888 010324 013737 043660 001126 MOV BUFFER+2,$BDDAT ;RECEIVED DATA
2889 010332 023737 001124 001126 CMP $GDDAT,$BDDAT ;CORRECT ?
2890 010340 001366 BNE #25 ;BR IF NOT
2891 010342 005037 001124 CLR $GDDAT ;EXPECTED VALUE FOR SECTOR
2892 010346 013737 043662 001126 MOV BUFFER+4,$BDDAT ;RECEIVED SECTOR
2893 010354 023737 001124 001126 CMP $GDDAT,$BDDAT ;CORRECT ?
2894 010362 001401 BEQ #EXIT6 ;BR IF OK
2895 010364 104035 ERROR #35 ;HEADER DATA INCORRECT
2896 010366 000004 EXIT6: SCOPE
2897
2898
2899
2900 ;*****
2901 ;*TEST 7 TEST 'NXME' BIT
2902 ;*THE NON-EXISTENT MEMORY ERROR BIT IS TESTED BY ATTEMPTING TO READ 1
2903 ;*WORD INTO LOCATION 760000. 'NXME', 'HE', & 'ERR' SHOULD ALL BE SET.
2904 ;*****
2905
2906 †ST7:
2907 010370 033737 001416 001276 BIT BITS+(<STN#2>),TSTNMS ;IS THIS TEST SELECTED
2908 010376 001002 BNE #+6 ;BR IF YES
2909 010400 000137 010612 JMP #TST10 ;GO TO THE NEXT TEST
2910 010404 012737 010464 001110 MOV #TEST7,$LPERR ;SETUP THE ERROR LOOP ADDRESS
2911 010412 012737 010370 001106 MOV #TST7,$LPADR ;SETUP THE SCOPE LOOP ADDRESS
2912 010420 012737 000007 001102 MOV #7,$TSTNM ;SETUP TEST NUMBER AND
2913 ;CLEAR THE ERROR FLAG ($ERFLG)
2914 010426 013777 001102 170506 MOV $TSTNM,$DISPLAY ;LOAD THE TEST NUMBER INTO THE DISPLAY REGISTER
2915 010434 012737 010610 001200 MOV #EXIT7,BYPASS ;SETUP BYPASS ADDRESS
2916 010442 013704 001324 MOV RPADR,R4 ;RP11E BUS ADDRESS
2917 010446 105037 002501 CLRB DPB+1 ;CLEAR ANY EXTENDED MEMORY BITS
2918 010452 105037 002503 CLRB DPB+3 ;CLEAR 'MODE' & 'HOR' BITS
2919 010456 012737 000144 001162 MOV #100,STIMES ;DO 100. ITERATIONS

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2920 010464 012706 001100 TEST7: MOV      $STACK, SP      ; SETUP THE STACK POINTER
2921 010470 004737 025654      JSR      PC, CLR          ; CLEAR THE RP11
2922 010474 012737 000001 002504  MOV      $1, $WC         ; WORD COUNT
2923 010502 005037 002512      CLR      $SEC           ; SECTOR/TRACK ZERO
2924 010506 005037 002510      CLR      $CYL          ; CYLINDER ZERO
2925 010510 112737 000060 002501  MOVVB   $MEX1, MEX0, DPB+1 ; LOAD BOTH EXTENDED MEMORY BITS
2926 010514 012737 160000 002506  MOV      $160000, $BUF   ; LOAD ADDR TO FORCE NON EX MEMORY
2927 010518 112737 000005 002502  MOVVB   $RDSEK, DPB+2   ; TRY TO READ INTO THE LOCATION
2928 010522 004737 022770      JSR      PC, RP11       ; INITIATE THE OPERATION
2929 010526 004737 023664      JSR      PC, CONRDY     ; WAIT FOR CONTROLLER READY
2930 010530 004737 023240      JSR      PC, SAVRP      ; STORE THE REGISTERS
2931 010534 032737 000004 001336  BIT      $BIT02, $RPER   ; DID NON EX MEMORY SET ?
2932 010538 001002      BNE     2936           ; BRANCH IF SET
2933 010542 104036      ERROR   36           ; 'NXME' ERROR DIDN'T SET
2934 010546 000412      BR      EXIT7        ; DON'T CHECK THE RPCS BITS
2935 010550 032737 040000 001340 2$: BIT      $HE, $RPCS     ; DID 'HE' SET ON NON EX MEMORY
2936 010554 001001      BNE     2938         ; BRANCH IF SET
2937 010558 104037      ERROR   37           ; 'HE' DIDN'T SET
2938 010576 032737 100000 001340 3$: BIT      $ERR, $RPCS    ; DID 'ERR' SET WITH NON EX MEMORY
2939 010604 001001      BNE     EXIT7        ; BRANCH IF SET
2940 010606 104040      ERROR   40           ; 'ERR' DIDN'T SET
2941 010610 000004      EXIT7: SCOPE

```

 *TEST 10 SECTOR ADDRESSING TEST

*THE SECTOR ADDRESSING TEST TESTS THE CAPABILITY OF THE RP11E TO LOCATE
 *A SECTOR BY USING THE 'SOT' COUNTER (FOR 'HDR' OPERATIONS) AND TO LOCATE
 *A SECTOR BY COMPARING THE HEADER CONTENTS AGAINST THE CYLINDER AND TRACK/
 *SECTOR ADDRESS REGISTERS (NORMAL MODE). THE TEST IS PERFORMED IN 3 PARTS.

1. WRITE ALL HEADERS ON TRACK 0, CYLINDER 0 IN ASCENDING SEQUENCE FROM INDEX (0, 1, 2 ... 9).
2. READ EACH HEADER, BEGINNING WITH SECTOR 0, AND VERIFY THAT THE SECTOR FIELD IN THE HEADER IS CORRECT FOR THE HEADER EXPECTED.

 VERIFY THAT THE 'SOT' COUNTER AND THE SECTOR ADDRESS IN 'RPDA' ARE EQUAL AND ARE THE EXPECTED VALUE. THE EXPECTED VALUE IS 1 GREATER THAN THE SECTOR READ. THE SECTOR ADDRESS REGISTER AND THE 'SOT' WILL BE INCREMENTED BY THE TIME THE PROGRAM IS ABLE TO READ THE REGISTER.

 WRITE THE SECTOR'S ADDRESS IN THE DATA FIELD. (FOR SECTOR 0, AN OCTAL 12 IS WRITTEN.)

 CONTINUE THIS SEQUENCE FOR THE REMAINING SECTORS ON THE TRACK.
3. READ THE DATA FROM EACH SECTOR USING A NORMAL (I.E. NON-HEADER) READ AND VERIFY THAT THE DATA IS CORRECT FOR THE EXPECTED SECTOR.

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2973 010612 033737 001420 001276 *ST10: BIT      BITS+($TN*2), TSTNMS ; IS THIS TEST SELECTED
2974 010612 001002      BNE     .+6          ; BR IF YES
2975 010620 001002

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2976	010622	000137	011532		JMP	TST11	::GO TO THE NEXT TEST
2977	010626	012737	010706	001110	MOV	#TEST10,\$LPERR	::SETUP THE ERROR LOOP ADDRESS
2978	010634	012737	010612	001106	MOV	#TST10,\$LPADR	::SETUP THE SCOPE LOOP ADDRESS
2979	010642	012737	000010	001102	MOV	#10,\$STSTNM	::SETUP TEST NUMBER AND
2980							::CLEAR THE ERROR FLAG (\$ERFLG)
2981	010650	013777	001102	170264	MOV	\$STSTNM,\$DISPLAY	::LOAD THE TEST NUMBER INTO THE DISPLAY REGISTER
2982	010656	012737	011530	001200	MOV	#EXIT10,\$BYPASS	::SETUP BYPASS ADDRESS
2983	010664	013704	001324		MOV	RPADR,R4	::RP11E BUS ADDRESS
2984	010670	105037	002501		CLRB	DPB+1	::CLEAR ANY EXTENDED MEMORY BITS
2985	010674	105037	002503		CLRB	DPB+3	::CLEAR 'MODE' & 'HDR' BITS
2986	010700	012737	000031	001162	MOV	#25,\$STIMES	::DO 25 ITERATIONS
2987	010706	012706	001100		MOV	#STACK,\$SP	::RESET THE STACK POINTER
2988	010712	012737	010706	001110	MOV	#TEST10,\$LPERR	::LOOP ON ERROR ADDRESS
2989	010720	004737	025654		JSR	PC,\$CLAP	::CLEAR THE RP11
2990	010724	012737	043656	002506	MOV	#BUFFER,\$BUF	::SETUP BUFFER ADDRESS
2991	010732	012737	000036	002504	MOV	#30,\$SWC	::SETUP WORD COUNT FOR 10 SECTORS
2992	010740	005037	002510		CLR	\$CYL	::SETUP FOR CYLINDER 0
2993	010744	005037	002512		CLR	\$SEC	::TRACK/SECTOR 0
2994	010750	004737	024304		JSR	PC,\$LDDSEC	::SETUP FORMAT BUFFER
2995	010754	012737	014000	002502	MOV	#MODE!HDR,\$DPB+2	::SETUP THE HEADER CONTROL BITS
2996	010762	112737	000003	002502	MOVB	#WRTSEK,\$DPB+2	::THE COMMAND CODE
2997	010770	004737	022770		JSR	PC,\$RP11	::FORMAT THE TRACK
2998	010774	004737	023664		JSR	PC,\$CONRDY	::WAIT FOR THE ORDER TO COMPLETE
2999	011000	004737	023240		JSR	PC,\$SAVRP	::STORE THE REGISTERS
3000	011004	032737	100000	001340	BIT	#ERR,\$SRPCS	::ERROR ?
3001	011012	001403			BEQ	1\$::BR IF NOT
3002	011014	104041			ERROR	41	::ERROR TRYING TO FORMAT THE TRACK
3003	011016	000137	011530		JMP	EXIT10	::BYPASS REST OF THE TEST
3004	011022	105037	002512		CLRB	\$SEC	::START CHECK WITH SECTOR 0
3005	011026	012737	011026	001110	MOV	#2\$,\$LPERR	::CHANGE THE LOOP ON ERROR ADDRESS
3006	011034	004737	025654		JSR	PC,\$CLAP	::CLEAR THE RP11
3007	011040	012737	014000	002502	MOV	#MODE!HDR,\$DPB+2	::SET 'MODE' AND 'HDR' BITS
3008	011046	112737	000005	002502	MOVB	#RDSEK,\$DPB+2	::CHANGE THE OPERATION TO A READ
3009	011054	012737	000003	002504	MOV	#3,\$SWC	::CHANGE THE WORD COUNT TO 3
3010	011062	004737	022770		JSR	PC,\$RP11	::READ THE SECTOR'S HEADER
3011	011066	004737	023664		JSR	PC,\$CONRDY	::WAIT FOR THE ORDER TO COMPLETE
3012	011072	004737	023240		JSR	PC,\$SAVRP	::STORE THE REGISTERS
3013	011076	012737	000000	001220	MOV	#0,\$STALLT	::LOAD STALL VALUE
3014	011104	004737	023202		JSR	PC,\$STALL	::STALL FOR 0 MILLISECONDS
3015	011110	016437	000014	001350	MOV	RPDA(R4),\$RPDA	::GET THE CONTENTS OF THE ADDRESS REGISTER
3016	011116	032737	100000	001340	BIT	#ERR,\$SRPCS	::ERROR ?
3017	011124	001402			BEQ	3\$::BR IF NOT
3018	011126	104042			ERROR	42	::ERROR READING THE HEADER
3019	011130	000457			BR	6\$::BYPASS DATA CHECK
3020	011132	005037	001124		CLR	\$GDDAT	::CLEAR FOR EXPECTED DATA
3021	011136	113737	002512	001124	MOVB	\$SEC,\$GDDAT	::MOVE SECTOR ADDRESS
3022	011144	023737	001124	043662	CMP	\$GDDAT,\$BUFFER+4	::CHECK THE SECTOR FIELD
3023	011152	001401			BEQ	4\$::BR IF CORRECT
3024	011154	104043			ERROR	43	::SECTOR FIELD FROM HEADER NOT CORRECT
3025	011156	005237	001124		INC	\$GDDAT	::INCREMENT SECTOR ADDRESS FOR 'SOT' CHECK
3026	011162	023727	001124	000012	CMP	\$GDDAT,\$#10.	::EXCEED THE MAXIMUM ?
3027	011170	103402			BLO	+\$6	::BR IF NOT
3028	011172	005037	001124		CLR	\$GDDAT	::RESET COMPARISON VALUE
3029	011176	013737	001350	001126	MOV	\$RPDA,\$BDDAT	::CHECK THE 'SOT'
3030	011204	042737	177417	001126	BIC	#1C360,\$BDDAT	::CLEAR THE OTHER BITS
3031	011212	006237	001126		ASR	\$BDDAT	::SHIFT THE 'SOT'

3032	011216	006237	001126	ASR	\$BDDAT	:	SHIFT THE 'SOT'
3033	011222	006237	001126	ASR	\$BDDAT	:	SHIFT THE 'SOT'
3034	011226	006237	001126	ASR	\$BDDAT	:	SHIFT THE 'SOT'
3035	011232	013737	001350	MOV	\$APDA,\$BDADR	:	CHECK THE SECTOR ADDRESS
3036	011240	042737	177760	BIC	#17,\$BDADR	:	LEAVE THE SECTOR ADDRESS
3037	011246	023737	001124	CMP	\$GDDAT,\$BDDAT	:	IS THE 'SOT' CORRECT ?
3038	011254	001004		BNE	\$S	:	BR IF NOT
3039	011256	023737	001126	CMP-	\$BDDAT,\$BDADR	:	DOES THE SECTOR ADDRESS EQUAT THE 'SOT' ?
3040	011264	001401		BEQ	\$S	:	BR IF IT DOES
3041	011266	104045		ERROR	45	:	'SOT' OR SECTOR ADDRESS IS NOT CORRECT
3042	011270	012737	011270	MOV	\$S,\$LPERR	:	CHANGE THE LOOP ON ERROR ADDRESS
3043	011276	004737	025654	JSR	PC,CLRP	:	CLEAR THE RP11
3044	011302	105037	002503	CLRB	DPB+3	:	CLEAR 'MODE' & 'HDR' BITS
3045	011306	004737	024336	JSR	PC,FILSEC	:	SETUP TO WRITE SECTOR ADDRESS AS DATA
3046	011312	112737	000003	MOVB	\$WATSEK,DPB+2	:	CHANGE OPERATION
3047	011320	004737	022770	JSR	PC,RP11	:	WRITE THE SECTOR
3048	011324	004737	023664	JSR	PC,CONRDY	:	WAIT FOR THE ORDER TO COMPLETE
3049	011330	004737	023240	JSR	PC,SAVRP	:	STORE THE REGISTERS
3050	011334	032737	100000	BIT	\$ERR,\$SRPCS	:	ERROR ?
3051	011342	001401		BEQ	\$S	:	BR IF NOT
3052	011344	104044		ERROR	44	:	ERROR TRYING TO WRITE SECTOR ADDRESS AS DATA
3053	011346	112737	000005	MOVB	\$RDSEK,DPB+2	:	CHANGE THE OPERATION
3054	011354	105237	002512	INCB	\$SEC	:	INCREMENT THE SECTOR
3055	011360	123727	002512	CMPB	\$SEC,#10.	:	AT MAXIMUM SECTOR ADDRESS ?
3056	011366	001217		BNE	\$S	:	BR IF NOT
3057	011370	012737	011424	MOV	\$S,\$LPERR	:	CHANGE THE LOOP ON ERROR ADDRESS
3058	011376	012737	000400	MOV	\$256,\$WC	:	CHANGE WORD COUNT
3059	011404	105037	002512	CLRB	\$SEC	:	START WITH SECTOR ZERO
3060	011410	012737	000012	MOV	#10,\$GDDAT	:	SECTOR ZERO PATTERN
3061	011416	112737	000005	MOVB	\$RDSEK,DPB+2	:	CHANGE OPERATION CODE
3062	011424	004737	025654	JSR	PC,CLRP	:	CLEAR THE RP11
3063	011430	004737	022770	JSR	PC,RP11	:	READ THE SECTOR
3064	011434	004737	023664	JSR	PC,CONRDY	:	WAIT FOR THE ORDER TO COMPLETE
3065	011440	004737	023240	JSR	PC,SAVRP	:	STORE THE REGISTERS
3066	011444	032737	100000	BIT	\$ERR,\$SRPCS	:	ERROR ?
3067	011452	001402		BEQ	\$S	:	BR IF NOT
3068	011454	104017		ERROR	17	:	ERROR TRYING TO READ THE SECTOR
3069	011456	000412		BR	10\$:	BYPASS DATA CHECK
3070	011460	005037	001120	CLR	\$GDADR	:	CLEAR FOR EXPECTED DATA
3071	011464	113737	002512	MOVB	\$SEC,\$GDADR	:	EXPECTED DATA
3072	011472	023737	001124	CMP	\$GDDAT,BUFFER	:	CORRECT CONTENTS ?
3073	011500	001401		BEQ	10\$:	BR IF CORRECT
3074	011502	104046		ERROR	46	:	SECTOR PATTERN NOT CORRECT FOR SECTOR ADDRESS
3075	011504	105237	002512	INCB	\$SEC	:	INCREMENT SECTOR ADDRESS
3076	011510	123727	002512	CMPB	\$SEC,#10.	:	FINISHED SECTOR NINE ?
3077	011516	001404		BEQ	EXIT10	:	BR IF COMPLETED
3078	011520	113737	002512	MOVB	\$SEC,\$GDDAT	:	NEXT SECTOR
3079	011526	000736		BR	\$S	:	CONTINUE CHECKING
3080	011530	000004		EXIT10:	SCOPE	:	LOOP ?

*TEST 11 TRACK ADDRESSING TEST

*TRACK ADDRESSING IS TESTED BY WRITING THE TRACK'S ADDRESS AS DATA IN
*SECTOR 0 OF EACH TRACK IN CYLINDER 0. SECTOR 0 IS THEN READ BACK AND

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;*THE DATA IS CHECKED TO VERIFY THAT THE PROPER HEAD WAS SELECTED.

```

TST11:
BIT          BITS+(<STN*2>),TSTNMS      : IS THIS TEST SELECTED
BNE          +6                          : BR IF YES
JMP          TST12                       : GO TO THE NEXT TEST
MOV          #TST11,$LPERR              : SETUP THE ERROR LOOP ADDRESS
MOV          #TST11,$LPADR              : SETUP THE SCOPE LOOP ADDRESS
MOV          #11,$TSTNM                 : SETUP TEST NUMBER AND
                                          : CLEAR THE ERROR FLAG (SERFLG)
MOV          $TSTNM,$DISPLAY            : LOAD THE TEST NUMBER INTO THE DISPLAY REGISTER
MOV          #EXIT11,BYPASS             : SETUP BYPASS ADDRESS
MOV          RPADR,R4                   : RP11E BUS ADDRESS
CLRB        DPB+1                       : CLEAR ANY EXTENDED MEMORY BITS
CLRB        DPB+3                       : CLEAR 'MODE' & 'HDR' BITS
MOV          #25,$TIMES                 : DO 25 ITERATIONS
CLR         $SEC                        : CLEAR SECTOR/TRACK ADDRESS
CLR         $CYL                        : CLEAR CYLINDER ADDRESS
MOV          #256,$WC                   : SET WORD COUNT TO ONE SECTOR
MOV          #BUFFER,$BUF               : SETUP THE BUFFER ADDRESS
TEST11:   MOV          #STACK,$P        : SETUP THE STACK POINTER
1$:       MOV          #1,$SLPERR        : CHANGE LOOP ON ERROR ADDRESS
          JSR         PC,$CLRP          : CLEAR THE RP11
          MOVVB      #WRTSEK,DPB+2      : CHANGE COMMAND TO WRITE
          JSR         PC,$FILTRK        : FILL BUFFER WITH THE TRACK ADDRESS
          JSR         PC,$RP11          : WRITE THE TRACK'S ADDRESS
          JSR         PC,$CONRDY        : WAIT FOR CONTROLLER TO FINISH
          JSR         PC,$SAVRP         : STORE THE REGISTERS
          BIT        #ERR,$SRPCS        : ERROR ?
          BEQ        2$                : BR IF NOT
          ERROR     4                   : ERROR WRITTING TEST PATTERN
          INCB       $TRK               : INCREMENT THE TRACK ADDRESS
          CMPB      $TRK,#20           : FINISHED ?
          BLO       1$                 : BR IF NOT FINISHED
          CLR        $GDDAT            : SETUP EXTPECTED VALUE
          CLR        $SEC              : START WITH ZERO
          MOV        #4,$SLPERR        : CHANGE THE LOOP ON ERROR ADDRESS
          JSR         PC,$CLRP          : CLEAR THE RP11
          MOVVB      #RDSEK,DPB+2      : CHANGE COMMAND
          JSR         PC,$RP11          : READ THE SECTOR
          JSR         PC,$CONRDY        : WAIT FOR THE CONTROLLER TO BE READY
          JSR         PC,$SAVRP         : STORE THE REGISTERS
          BIT        #ERR,$SRPCS        : ERROR ?
          BEQ        5$                : BR IF NOT
          ERROR     17                  : ERROR READING THE SECTOR
          BR         6$                : BYPASS THE DATA CHECK
          CMPB      $GDDAT+1,BUFFER+1 : ;CORRECT VALUE ?
          BEQ        6$                : BR IF OK
          ERROR     47                  : CONTENTS WRONG
          CMPB      $TRK,#19           : FINISHED ?
          BEQ        EXIT11            : BR IF FINISHED
          INCB       $TRK               : INCREMENT THE TRACK ADDRESS
          INCB       $GDDAT+1          : COMPARISON WORD
          BR         4$                : CONTINUE
EXIT11:   SCOPE                        : LOOP ?

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3091	011532		001422	001276		
3092	011532	033737				
3093	011540	001002				
3094	011542	000137	012062			
3095	011546	012737	011652	001110		
3096	011554	012737	011532	001106		
3097	011562	012737	000011	001102		
3098						
3099	011570	013777	001102	167344		
3100	011576	012737	012060	001200		
3101	011604	013704	001324			
3102	011610	105037	002501			
3103	011614	105037	002503			
3104	011620	012737	000031	001162		
3105	011626	005037	002512			
3106	011632	005037	002510			
3107	011636	012737	000400	002504		
3108	011644	012737	043656	002506		
3109	011652	012706	001100			
3110	011656	012737	011656	001110		
3111	011664	004737	025654			
3112	011670	112737	000003	002502		
3113	011676	004737	024376			
3114	011702	004737	022770			
3115	011706	004737	023664			
3116	011712	004737	023240			
3117	011716	032737	100000	001340		
3118	011724	001401				
3119	011726	104004				
3120	011730	105237	002513		2\$:	
3121	011734	123727	002513	000024		
3122	011742	103745				
3123	011744	005037	001124		3\$:	
3124	011750	005037	002512			
3125	011754	012737	011754	001110	4\$:	
3126	011762	004737	025654			
3127	011766	112737	000005	002502		
3128	011774	004737	022770			
3129	012000	004737	023664			
3130	012004	004737	023240			
3131	012010	032737	100000	001340		
3132	012016	001402				
3133	012020	104017				
3134	012022	000405				
3135	012024	123737	001125	043657	5\$:	
3136	012032	001401				
3137	012034	104047				
3138	012036	123727	002513	000023	6\$:	
3139	012044	001405				
3140	012046	105237	002513			
3141	012052	105237	001125			
3142	012056	000736				
3143	012060	000004				

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012062			
012062	033737	001424	001276
012070	001002		
012072	000137	012720	
012076	012737	012202	001110
012104	012737	012062	001106
012112	012737	000012	001102
012120	013777	001102	167014
012126	012737	012712	001200
012134	013704	001324	
012140	105037	002501	
012144	105037	002503	
012150	012737	000144	001162
012156	005737	022232	
012162	100402		
012164	000137	012720	
012170	005737	001232	
012174	001002		
012176	000137	012720	
012202	012706	001100	
012206	012737	012202	001110
012214	004737	025654	
012220	005037	043656	
012224	012737	177777	043660
012232	012737	043656	002506
012240	012737	000002	002504
012246	005037	002512	
012252	005037	002510	
012256	012737	000003	002502
012264	004737	022770	
012270	004737	023664	
012274	004737	023240	

```

*****
*TEST 12      EXTENDED MEMORY ADDRESS TEST
*****
*THIS TEST TESTS THE OPERATION OF THE EXTENDED MEMORY ADDRESS BITS.  IF THE
*SYSTEM DOES NOT HAVE MEMORY MANAGEMENT OR HAS MEMORY MANAGEMENT AND ONLY
*32K, THE TEST WILL NOT BE PERFORMED.
*
*   1.  THE PROGRAM WRITES A 2 WORD TEST SECTOR OF ALL ONES.
*
*   2.  EXTENDED ADDRESS BIT 'MEX00' IS TESTED BY CLEARING LOCATION
*       200000 AND READING THE TEST SECTOR INTO LOCATION 177776.  LOCATION
*       200000 IS CHECKED TO VERIFY THAT THE DATA IS CORRECT (ONES).  THE
*       PROGRAM VERIFIES THAT 'MEX00' HAS SET AND THAT 'MEX01' IS NOT SET.
*
*   3.  IF THE SYSTEM HAS AT LEAST 64K, 'MEX01' IS TESTED.  LOCATION
*       400000 IS CLEARED AND THE TEST SECTOR IS READ INTO LOCATION 377776
*       ('MEX00' IS SET FOR THE READ).  LOCATION 400000 IS CHECKED FOR THE
*       PROPER CONTENTS (ONES).  'MEX00' SHOULD HAVE RESET AND 'MEX01' SHOULD
*       BE SET.
*****

```

```

*****
†ST12:
BIT      BITS+(<STN*2>),TSTNMS  IS THIS TEST SELECTED
BNE      +6                      BR IF YES
JMP      TST13                   GO TO THE NEXT TEST
MOV      #TEST12,$LPERR          SETUP THE ERROR LOOP ADDRESS
MOV      #TST12,$LPADR          SETUP THE SCOPE LOOP ADDRESS
MOV      #12,$TSTNM             SETUP TEST NUMBER AND
                                CLEAR THE ERROR FLAG ($ERFLG)
MOV      $TSTNM,$DISPLAY        LOAD THE TEST NUMBER INTO THE DISPLAY REGISTER
MOV      #EXIT12,BYPASS         SETUP BYPASS ADDRESS
MOV      RPADR,R4               RP11E BUS ADDRESS
CLRB     DPB+1                  CLEAR ANY EXTENDED MEMORY BITS
CLRB     DPB+3                  CLEAR 'MODE' & 'HDR' BITS
MOV      #100,$TIMES            DO 100 ITERATIONS
TST      $KT11                  MEMORY MANAGEMENT ON THE SYSTEM ?
BMI      IS                      BR IF ON SYSTEM
JMP      TST13                   GO TO THE NEXT TEST
IS:      TST      MAXMEM+2        ENOUGH MEMORY FOR THIS TEST?
BNE      TEST12                 BR IF YES
JMP      TST13                   GO TO THE NEXT TEST
TEST12: MOV      #STACK,SP        SETUP THE STACK POINTER
MOV      #TEST12,$LPERR          INITIAL ERROR LOOP ADDRESS
JSR      PC,CLRP                CLEAR THE RP11
CLR      BUFFER                 TEST PATTERN
MOV      #-1,BUFFER+2           SECOND WORD OF PATTERN
MOV      #BUFFER,$BUF           SETUP THE BUFFER ADDRESS
MOV      #2,$WC                 WORD COUNT OF 2
CLR      $SEC                   SECTOR/TRACK ZERO
CLR      $CYL                   CYLINDER ZERO
MOV      #WRTSEK,DPB+2          WRITE COMMAND
JSR      PC,RP11                INITIATE THE COMMAND
JSR      PC,CONRDY              WAIT FOR CONTROLLER READY
JSR      PC,SAVRP               STORE THE REGISTERS

```

3200	012300	032737	100000	001340		BIT	#ERR,SRPCS	:DID OPERATION TERMINATE WITH AN ERROR ?
3201	012306	001403				BEQ	1\$:BR IF NOT
3202	012310	104004				ERROR	4	:ERROR AFTER 2 WORD WRITE
3203	012312	000137	012712			JMP	EXIT12	:BYPASS REST OF TEST
3204	012316	012737	012316	001110	1\$:	MOV	#1\$,SLPERR	:CHANGE LOOP ON ERROR ADDRESS
3205	012324	004737	025654			JSR	PC,CLAP	:CLEAR THE RP11
3206	012330	012737	177600	172356		MOV	#177600,KIPAR7	:OPEN I/O REGISTERS
3207	012336	005037	172340			CLR	KIPAR0	:ENABLE FIRST 4K
3208	012342	012737	000200	172342		MOV	#200,KIPAR1	:ENABLE SECOND 4K
3209	012350	012737	002000	172344		MOV	#2000,KIPAR2	:ENABLE FIRST TEST LOCATION
3210	012356	012737	077406	172300		MOV	#77406,KIPDR0	:LOAD DESCRIPTOR REGISTER 0
3211	012364	012737	077406	172302		MOV	#77406,KIPDR1	:LOAD DESCRIPTOR REGISTER 1
3212	012372	012737	077406	172304		MOV	#77406,KIPDR2	:LOAD DESCRIPTOR REGISTER 2
3213	012400	012737	077406	172316		MOV	#77406,KIPDR7	:LOAD DESCRIPTOR REGISTER 7
3214	012406	012737	000001	177572		MOV	#1,MSAO	:TURN ON MEMORY MANAGEMENT
3215	012414	005037	040000			CLR	#40000	:CLEAR LOCATION 200000
3216	012420	012737	177776	002506		MOV	#177776,\$BUF	:SETUP BUS ADDR
3217	012426	112737	000005	002502		MOVB	#RDSEK,DPB+2	:CHANGE COMMAND TO READ
3218	012434	004737	022770			JSR	PC,RP11	:START THE COMMAND
3219	012440	004737	023664			JSR	PC,CONRDY	:WAIT FOR CONTROLLER READY
3220	012444	004737	023240			JSR	PC,SAVRP	:STORE THE REGISTERS
3221	012450	032737	100000	001340		BIT	#ERR,SRPCS	:ANY ERRORS?
3222	012456	001401				BEQ	2\$:BRANCH IF NOT
3223	012460	104050				ERROR	50	:ERROR AFTER READING 2 WORDS
3224	012462	032737	000020	001340	2\$:	BIT	#MEX0,SRPCS	:DID 'MEX0' SET ?
3225	012470	001001				BNE	3\$:BRANCH IF IT DID
3226	012472	104051				ERROR	51	: 'MEX0' DID NOT SET AFTER 2 WORD READ
3227								:STARTING AT LOCATION 177776
3228	012474	032737	000040	001340	3\$:	BIT	#MEX1,SRPCS	: 'MEX1' SHOULD NOT BE SET
3229	012502	001401				BEQ	4\$:BRANCH IF NOT SET
3230	012504	104052				ERROR	52	: 'MEX1' IS SET - SHOULD NOT BE
3231	012506	022737	177777	040000	4\$:	CMP	#177777,#40000	:WAS DATA READ INTO LOCATION
3232	012514	001407				BEQ	TST12B	:200000 CORRECTLY? - BRANCH IF YES
3233	012516	012737	177777	001124		MOV	#177777,\$GDDAT	:EXPECTED CONTENTS
3234	012524	013737	040000	001126		MOV	#40000,\$BDDAT	:CONTENTS OF LOCATION 200000
3235	012532	104053				ERROR	53	:DATA COMPARE ERROR AT LOC 200000
3236	012534	023727	001232	000002	TST12B:	CMP	MAXMEM+2,#2	:ENOUGH MEMORY FOR THE NEXT PART OF THE TEST ?
3237	012542	103463				BLO	EXIT12	:BR IF NOT
3238	012544	012737	004000	172344		MOV	#4000,KIPAR2	:CHANGE TEST LOCATION
3239	012552	012737	012552	001110	1\$:	MOV	#1\$,SLPERR	:CHANGE THE LOOP ON ERROR ADDRESS
3240	012560	004737	025654			JSR	PC,CLAP	:CLEAR THE RP11
3241	012564	005037	040000			CLR	#40000	:CLEAR LOCATION 400000
3242	012570	012737	177776	002506		MOV	#177776,\$BUF	:BUFFER ADDRESS
3243	012576	112737	000020	002501		MOVB	#MEX0,DPB+1	:SETUP EXTENDED MEMORY ADDRESS BIT
3244	012604	112737	000005	002502		MOVB	#RDSEK,DPB+2	:READ COMMAND
3245	012612	004737	022770			JSR	PC,RP11	:START THE COMMAND
3246	012616	004737	023664			JSR	PC,CONRDY	:WAIT FOR CONTROLLER READY
3247	012622	004737	023240			JSR	PC,SAVRP	:STORE THE REGISTERS
3248	012626	032737	100000	001340		BIT	#ERR,SRPCS	:ANY ERRORS?
3249	012634	001401				BEQ	2\$:BRANCH IF NOT
3250	012636	104054				ERROR	54	:ERROR WHILE READING TWO WORDS
3251	012640	032737	000020	001340	2\$:	BIT	#MEX0,SRPCS	:IS 'MEX0' SET ?
3252	012646	001401				BEQ	3\$:BRANCH IF NOT
3253	012650	104055				ERROR	55	: 'MEX0' DID NOT CLEAR AFTER 2 WORD
3254								:READ STARTING AT LOCATION 377776
3255	012652	032737	000040	001340	3\$:	BIT	#MEX1,SRPCS	:DID 'MEX1' SET ?

M05

```

3256 012660 001001 BNE 45 ;BR IF SET
3257 012662 104056 ERROR 56 ;'MEX1' DID NOT SET AFTER 2 WORD TRANSFER
3258 ;STARTING AT LOCATION 377776
3259 012664 022737 177777 040000 45: CMP #177777, @#40000 ;WAS DATA READ INTO LOCATION 400000
3260 012672 001407 BEQ EXIT12 ;CORRECTLY? - BRANCH IF YES
3261 012674 012737 177777 001124 MOV #177777, $GDDAT ;EXPECTED DATA
3262 012702 013737 040000 001126 MOV @#40000, $BDDAT ;DATA FROM LOCATION 400000
3263 012710 104057 ERROR 57 ;DATA COMPARE ERROR AT LOC 400000
3264 012712 005037 177572 EXIT12: CLR @#SR0 ;TURN OFF MEMORY MANAGEMENT
3265 012716 000004 SCOPE ;LOOP ?

```

```

*****
*TEST 13 DATA BUFFER REGISTER BIT TEST

```

```

*THE DATA BUFFER REGISTER TEST VERIFIES THAT ALL 36 BITS OF THE DATA
*BUFFER REGISTER CAN BE SET AND CLEARED.

```

```

1. THE TEST FIRST WRITES THE FOLLOWING TEST PATTERN IN CYLINDER 0,
TRACK 0, SECTOR 0 USING 10/15 MODE ('MODE' BIT SET).

```

```

052525
052525
000005
125252
125252
000012
052525
052525
000005
125252

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ETC

```

2. THE TEST SECTOR IS THEN READ AND THE DATA IS COMPARED. AT THE
COMPLETION OF THE TEST, THE PROGRAM RESTORES THE TEST SECTOR TO
PDP-11 MODE.

```

```

*****
*TEST 13:

```

```

3297 012720 033737 001426 001276 BIT BITS+(<STN*2>, TSTNMS ;IS THIS TEST SELECTED
3298 012720 001002 BNE +6 ;BR IF YES
3299 012726 000137 013574 JMP TST14 ;GO TO THE NEXT TEST
3300 012730 012737 013014 001110 MOV #TST13, $LPERR ;SETUP THE ERROR LOOP ADDRESS
3301 012734 012737 012720 001106 MOV #TST13, $LPADR ;SETUP THE SCOPE LOOP ADDRESS
3302 012742 012737 000013 001102 MOV #13, $TSTNM ;SETUP TEST NUMBER AND
3303 ;CLEAR THE ERROR FLAG ($ERFLG)
3304 ;LOAD THE TEST NUMBER INTO THE DISPLAY REGISTER
3305 012756 013777 001102 166156 MOV $TSTNM, @DISPLAY
3306 012764 012737 013572 001200 MOV #EXIT13, BYPASS ;SETUP BYPASS ADDRESS
3307 012772 013704 001324 MOV RPADR, R4 ;RP11E BUS ADDRESS
3308 012776 105037 002501 CLRB DPB+1 ;CLEAR ANY EXTENDED MEMORY BITS
3309 013002 105037 002503 CLRB DPB+3 ;CLEAR 'MODE' & 'HDR' BITS
3310 013006 012737 000144 001162 MOV #100, $TIMES ;DO 100. ITERATIONS
3311 013014 012706 001100 TEST13: MOV #STACK, SP ;SETUP THE STACK POINTER

```

3312	013020	012701	043656		MOV	#BUFFER, R1	:: BUFFER ADDRESS
3313	013024	012702	000200		MOV	#128, R2	:: SIZE
3314	013030	012721	052525	1\$:	MOV	#52525, (R1)+	:: PATTERN FOR BITS 20 - 35
3315	013034	012721	052525		MOV	#52525, (R1)+	:: PATTERN FOR BITS 4 - 19
3316	013040	012721	000005		MOV	#5, (R1)+	:: PATTERN FOR BITS 0 - 3
3317	013044	005302			DEC	R2	:: AT THE END ?
3318	013046	001410			BEQ	R2	:: BR IF END
3319	013050	012721	125252		MOV	#125252, (R1)+	:: PATTERN FOR BITS 20 - 35
3320	013054	012721	125252		MOV	#125252, (R1)+	:: PATTERN FOR BITS 4 - 19
3321	013060	012721	000012		MOV	#12, (R1)+	:: PATTERN FOR BITS 0 - 3
3322	013064	005302			DEC	R2	:: AT THE END ?
3323	013066	001360			BNE	1\$:: BR IF NOT
3324	013070	012737	013070	001110	MOV	#2\$, SLPERR	:: LOOP ON ERROR ADDRESS
3325	013076	004737	025654	2\$:	JSR	PC, CLR	:: CLEAR THE RP11


```

0137454 032777 000020 165456 BIT BSW04 JSWR ;CONTINUE COMPARING ANYWAY ?
0137454 001404 BFO TST13A ;BR IF NOT
0137454 005302 9S: DEC TST13A ;FINISHED COMPARING ?
0137454 001323 BNE BSW ;BR IF NOT
0137454 000401 BR TST13A ;CHANGE MODE OF THE SECTOR
0137454 000000 .WORD 0 ;COMPARISON ERROR COUNTER
0137454 012737 001110 TST13A: MOV @TST13A,SLPERR ;CHANGE LOOP ON ERROR ADDRESS
0137454 004737 JSR PC CLAP ;CLEAR THE RP11
0137454 105037 002504 CLR DPB+3 ;CLEAR THE 'MODE' & 'HDR' BITS
0137454 012737 000003 MOV #2,SWC ;CHANGE WORD COUNT
0137454 000003 002502 MOV BSWTSEK,DPB+2 ;CHANGE COMMAND
0137454 005037 043656 CLR BUFFER ;SET DATA TO ZERO
0137454 005037 043660 CLR BUFFER+2 ;OTHER DATA WORD
0137454 012737 002506 MOV @BUFFER,SBUF ;BUFFER ADDRESS
0137454 004737 JSR PC,RP11 ;RE-WRITE THE SECTOR
0137454 004737 JSR PC,CONADY ;WAIT FOR THE OPERATION TO COMPLETE
0137454 004737 JSR PC,SAVRP ;STORE THE REGISTERS
0137454 032737 100000 BIT @SWR SRPCS ;DID THE WRITE FINISH WITHOUT ERROR ?
0137454 001401 BFO EXIT13 ;BR IF IT DID
0137454 104060 ERROR ;ERROR RE-WRITING THE SECTOR
013572 000004 EXIT13: SCOPE ;LOOP ?

```

```

*****
: #TEST 14 DATA STORAGE/RETRIEVAL TEST

```

```

*THE DATA TEST PERFORMS DATA STORAGE AND RETRIEVAL TESTING USING THE CONTROLLER
*AND THE CURRENTLY SELECTED DRIVE. UNLESS ALTERED BY THE OPERATOR, THE
*TEST WILL WRITE AND WRITE CHECK THE ENTIRE DISK PACK USING ALL 16 DATA
*PATTERNS IN A ROTATING MANNER. THE BUFFER SIZE USED FOR THE WRITE AND
*WRITE CHECK ORDERS IS DETERMINED BY THE AVAILABLE MEMORY ON THE SYSTEM
*(MINUS THE SPACE REQUIRED BY THE PROGRAM LOADER). THE PROGRAM WILL USE
*THE AVAILABLE MEMORY SIZE OR 8K, WHICHEVER IS LESS, AS THE BUFFER
*SIZE.
*
*AFTER THE PACK HAS BEEN WRITTEN, THE TEST WILL READ AND COMPARE THE
*DATA. THE BUFFER SIZE USED FOR THE READ IS THE SAME SIZE AS THAT USED
*FOR THE WRITE/WRITE CHECK PART OF THE TEST. FOR THE READ, THE TEST WILL
*ROTATE THE READ BUFFER THROUGH MEMORY SO THAT ALL OF THE AVAILABLE MEMORY
*IS USED BY THE READ AND COMPARE OPERATIONS. THE TEST USES MEMORY MANAGEMENT
*ON SYSTEMS WITH MORE THAN 28K. (MEMORY MANAGEMENT WILL NOT BE USED IF
*ON MEMORY MANAGEMENT SYSTEMS WITH 28K OR LESS.)

```

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*THE DATA PATTERNS USED BY THE DATA TEST ARE GIVEN BELOW:

```

PAT 0	PAT 1	PAT 2	PAT 3	PAT 4	PAT 5	PAT 6	PAT 7
000000	000001	177776	000000	000000	052525	007417	026455
000000	000003	177774	000000	010421	052525	007417	026455
000000	000007	177770	000000	021042	052525	007417	026455
000000	000017	177760	177777	031463	125252	170360	151322
000000	000037	177740	177777	042104	125252	170360	151322
000000	000077	177700	177777	052525	125252	170360	151322
000000	000177	177600	000000	063146	052525	007417	026455
000000	000377	177400	000000	073567	052525	007417	026455
000000	000777	177000	177777	104210	125252	170360	151322
000000	001777	176000	177777	114631	125252	170360	151322

E06

3549	013764	005037	001242		35:	CLR	LOMEM+2	: CLEAR UPPER STORAGE WORD
3550	013770	013737	001240	001224		MOV	LOMEM,ACTMEM	: STARTING ADDRESS OF FIRST BUFFER
3551	013776	013737	001240	001226		MOV	LOMEM+2,ACTMEM+2	: UPPER ADDRESS BITS
3552	014004	013737	001256	001254		MOV	WC14,BLKS14	: CONVERT THE WORD COUNT TO A SECTOR COUNT
3553	014012	000337	001254			SWAB	BLKS14	: RIGHT JUSTIFY THE SECTOR COUNT
3554	014016	105037	001254			CLRB	BLKS14+1	: CLEAR THE RESIDUE
3555	014022	005237	001254			INC	BLKS14	: CHANGE TO ABSOLUTE COUNT
3556	014026	013746	001254			MOV	BLKS14,-(SP)	: CONVERT BLOCK COUNT TO SECTOR/TRACK INCREMENTS
3557	014030	005037	001250			CLR	INCTRK	: CLEAR TRACK STORAGE
3558	014036	005037	001250			CLR	INCSEC	: CLEAR SECTOR STORAGE
3559	014042	162716	000012		45:	SUB	#10.,(SP)	: SUBTRACT A FULL SECTOR
3560	014046	100403				BMI	55	: BR IF MINUS
3561	014050	005237	001252			INC	INCTRK	: COUNT THE TRACK
3562	014054	000772				BR	45	: CONTINUE
3563	014056	062716	000012		55:	ADD	#10.,(SP)	: CORRECT THE SECTOR RESIDUE
3564	014062	012637	001250			MOV	(SP)+,INCSEC	: SECTOR INCREMENT VALUE
3565	014066	012706	001100		TEST14:	MOV	#STACK,SP	: LOAD THE STACK POINTER
3566	014072	004737	024002			JSR	PC,SECNBR	: GET THE NUMBER OF SECTORS ON THE PACK
3567								: BEING USED
3568	014076	113737	001272	002512	15:	MOV	SSEC,SSEC	: STARTING SECTOR ADDRESS
3569	014104	113737	001270	002513		MOV	STAK,STAK	: STARTING TRACK ADDRESS
3570	014112	013737	001266	002510		MOV	SCYL,SCYL	: STARTING CYLINDER ADDRESS
3571	014120	013737	001240	002506		MOV	LOMEM,\$BUF	: BUFFER ADDRESS
3572	014126	013737	001256	002504		MOV	WC14,\$WC	: STARTING WORD COUNT
3573	014134	012737	000017	001204		MOV	#15,PATNUM	: PRESET THE PATTERN INDEX
3574	014142	005737	001260			TST	OPRSEL	: USING AN OPERATOR SPECIFIED ADDRESS ?
3575	014146	001005				BNE	25	: BR IF YES
3576	014150	163737	001254	001244		SUB	BLKS14,PAKSIZ	: DECREMENT THE SECTOR COUNT FOR THE
3577	014156	005637	001246			SBC	PAKSIZ+2	: FIRST OPERATION
3578	014162	004737	024050		25:	JSR	PC,PATSEL	: SELECT THE PATTERN
3579	014166	012737	014166	001110	35:	MOV	#3,\$LPERR	: LOOP ON ERROR ADDRESS
3580	014174	004737	025654			JSR	PC,CLRP	: CLEAR THE RP11
3581	014200	017746	164734			MOV	\$SWR,-(SP)	: INHIBIT WRITE AND
3582	014204	005116				COM	(SP)	: WRITE CHECK ?
3583	014206	032726	000003			BIT	#SW00,\$SW01,(SP)+	
3584	014212	001453				BEG	TST14A	: YES--BRANCH
3585	014214	004737	024432			JSR	PC,SETBUF	: MOVE DATA PATTERN INTO THE BUFFER
3586	014220	032777	000001	164712		BIT	#SW00,\$SWR	: INHIBIT WRITE ?
3587	014226	001016				BNE	45	: YES--BRANCH
3588	014230	112737	000003	002502		MOV	\$WRTSEK,DPB+2	: COMMAND=WRITE
3589	014236	004737	022770			JSR	PC,RP11	: DO THE COMMAND
3590	014242	004737	023664			JSR	PC,CONRDY	: WAIT FOR THE COMMAND TO COMPLETE
3591	014246	004737	023240			JSR	PC,SAVRP	: STORE THE REGISTERS
3592	014252	032737	100000	001340		BIT	#ERR,\$RPCS	: ERROR OCCURED ?
3593	014260	001401				BEG	45	: BR IF NOT
3594	014264	104061				ERROR	61	: REPORT THE ERROR
3595	014264	032777	000002	164646	45:	BIT	#SW01,\$SWR	: INHIBIT WRITE CHECK ?
3596	014272	001020				BNE	55	: YES--BRANCH
3597	014274	004737	025654			JSR	PC,CLRP	: CLEAR THE RP11
3598	014300	112737	000007	002502		MOV	\$WATCHK,DPB+2	: COMMAND=WRITE CHECK
3599	014306	004737	022770			JSR	PC,RP11	: DO THE WRITE CHECK
3600	014312	004737	023664			JSR	PC,CONRDY	: WAIT FOR THE COMMAND TO FINISH
3601	014316	004737	023240			JSR	PC,SAVRP	: STORE THE REGISTERS
3602	014322	032737	100000	001340		BIT	#ERR,\$RPCS	: ERROR ?
3603	014330	001401				BEG	55	: BR IF NOT
3604	014332	104005				ERROR	5	: REPORT THE WRITE CHECK ERROR

T14 DATA STORAGE/RETRIEVAL TEST

```

3550 014334 004737 024114 55: JSR PC,INCADR ; INCREMENT THE ADDRESS
3551 014340 000710 BR 25 ; CONTINUE
3552 014342 004737 024002 TST14A: JSR PC,SECNR ; GET NUMBER OF SECTORS ON THE DRIVE
3553 ; BEING TESTED
3554 014346 113737 001272 002512 MOVB SSEC,SSEC ; STARTING SECTOR
3555 014354 113737 001270 002513 MOVB STAK,STAK ; STARTING TRACK
3556 014362 013737 001266 002510 MOV SCYL,SCYL ; STARTING CYLINDER
3557 014370 013737 001256 002504 MOV WC14,SWC ; INITIAL WORD COUNT
3558 014376 012737 000017 001204 MOV B15,PATNUM ; PRESET PATTERN NUMBER
3559 014404 005737 001260 TST OPARSEL ; USING AN OPERATOR SPECIFIED PATTERN
3560 014410 001005 BNE 15 ; BR IF YES
3561 014412 163737 001254 001244 SUB BLKS14,PAKSIZ ; DECREMENT THE SECTOR COUNT FOR
3562 014420 005637 001246 SBC PAKSIZ+2 ; THE FIRST OPERATION
3563 014424 005037 001314 15: CLR RETRY ; CLEAR THE RETRY COUNTER
3564 014430 004737 024050 JSR PC,PATSEL ; SELECT A PATTERN
3565 014434 004737 024502 JSR PC,GETBUF ; GENERATE A BUFFER ADDRESS
3566 014440 012737 014440 001110 25: MOV B25,$LPERR ; LOOP ON ERROR ADDRESS
3567 014446 004737 025654 JSR PC,CLRP ; CLEAR THE RP11
3568 014452 032777 000004 164460 BIT #SW02,$SWR ; INHIBIT READ DATA AND SOFTWARE COMPARE ?
3569 014460 001056 BNE EXIT14 ; YES--BRANCH
3570 014462 112737 000005 002502 MOVB #RDSEK,DPB+2 ; LOAD READ COMMAND
3571 014470 004737 022770 JSR PC,RP11 ; START THE READ
3572 014474 004737 023664 JSR PC,CONRDY ; WAIT FOR THE READ TO COMPLETE
3573 014500 004737 023240 JSR PC,SAVRP ; STORE THE REGISTERS
3574 014504 032737 100000 001340 BIT #ERR,$RPCS ; ERROR ?
3575 014512 001420 BEQ 55 ; BR IF NOT
3576 014514 005737 001314 TST RETRY ; RETRY ATTEMPT ?
3577 014520 001404 BEQ 35 ; BR IF NOT
3578 014522 032777 000040 164410 BIT #SW05,$SWR ; DISPLAY ALL RETRY ATTEMPTS
3579 014530 001401 BEQ 45 ; BR IF NOT
3580 014532 104062 35: ERROR 62 ; REPORT THE ERROR
3581 014534 005237 001314 45: INC RETRY ; INCREMENT THE RETRY COUNT
3582 014540 023737 001314 001316 CMP RETRY,LRETRY ; RETRY LIMIT ?
3583 014546 101734 BLOS 25 ; BR IF NOT
3584 014550 104110 ERROR 110 ; REPORT UNSUCCESSFUL RETRY
3585 014552 000410 BR 65 ; CONTINUE
3586 014554 005737 001314 55: TST RETRY ; COMING FROM A SUCCESSFUL RETRY ?
3587 014560 001405 BEQ 65 ; BR IF NOT
3588 014562 112737 000111 001114 MOVB #111,$ITEMB ; RETRY COUNT MESSAGE NUMBER
3589 014570 004737 017254 JSR PC,TYPERR ; REPORT THE RETRY COUNT
3590 014574 032777 000010 164336 65: BIT #SW03,$SWR ; COMPARE THE DATA ?
3591 014602 001002 BNE 75 ; NO--BRANCH
3592 014604 004737 024614 JSR PC,DATCMP ; YES--DO IT
3593 014610 004737 024114 75: JSR PC,INCADR ; INCREMENT THE DISK ADDRESS
3594 014614 000703 BR 15 ; CONTINUE
3595 014616 000004 EXIT14: SCOPE ; LOOP ?
3596 014620 004737 023452 JSR PC,RESLDR ; RESTORE THE LOADER IF IT WAS MOVED
3597 014624 000137 016616 JMP SEOP ; GO TO END OF TEST

```

TEST 15 POWER FAIL TEST

```

; *TEST THE ABILITY OF THE RP11E TO SENSE POWER FAILURE AND FOR THE DRIVES
; *TO RETRACT THEIR HEADS. WHEN POWER IS TURNED ON AGAIN
; *THE CYLINDER ADDRESS IS TESTED FOR ZERO. AFTER TYPING THE MESSAGE
; *REQUESTING POWER TO BE TURNED OFF, THE PROGRAM GOES INTO A LOOP

```

```

3598
3599
3600
3601
3602
3603
3604
3605

```

;*READING FROM THE SELECTED DISK DRIVE. AFTER POWER IS RESTORED,
;*MEMORY IS CHECKED TO SEE THAT THE DISK DID NOT TRANSFER ANY DATA
;*TO MEMORY WHILE POWER WAS GOING DOWN.

↑T15:

3606					BIT	BITS+(STN#2),TSTNMS	IS THIS TEST SELECTED	
3607					BNE	+6	BR IF YES	
3608					JMP	TST16	GO TO THE NEXT TEST	
3609					MOV	#TEST15,\$LPERR	SETUP THE ERROR LOOP ADDRESS	
3610					MOV	#TST15,\$LPADR	SETUP THE SCOPE LOOP ADDRESS	
3611	014630				MOV	#15,\$TSTNM	SETUP TEST NUMBER AND	
3612	014630	033737	001432	001276			CLEAR THE ERROR FLAG (\$ERFLG)	
3613	014636	001002			MOV	\$TSTNM,\$DISPLAY	LOAD THE TEST NUMBER INTO THE DISPLAY REGISTER	
3614	014640	000137	015372		MOV	#EXIT15,\$BYPASS	SETUP BYPASS ADDRESS	
3615	014644	012737	014736	001110	MOV	RPADR,R4	RP11E BUS ADDRESS	
3616	014652	012737	014630	001106	CLRB	DPB+1	CLEAR ANY EXTENDED MEMORY BITS	
3617	014660	012737	000015	001102	CLRB	DPB+3	CLEAR 'MODE' & 'HDR' BITS	
3618					TYPE	MSG15A	'POWER FAIL TEST'	
3619	014666	013777	001102	164246	JSR	PC,MOVLDR	MOVE THE LOADER	
3620	014674	012737	015302	001200	MOV	(SP)+,\$LOMEM	BUFFER STARTING ADDRESS	
3621	014702	013704	001324		CLR	\$LOMEM+2	CLEAR THE UPPER BITS JUST TO BE NICE	
3622	014706	105037	002501		TEST15:	MOV	\$STACK,\$SP	SETUP THE STACK POINTER
3623	014712	105037	002503		JSR	PC,CLRP	CLEAR THE RP11	
3624	014716	104401	027433		MOV	#TST15B,\$#24	SET UP POWER FAIL VECTOR	
3625	014722	004737	023346		MOV	#SP7,\$#26	LOCKOUT INTERRUPTS	
3626	014726	012637	001240		MOV	\$LOMEM,\$R0	START OF FREE MEMORY SPACE	
3627	014732	005037	001242		15:	MOV	#125252,\$R0)+	FILL MEMORY WITH CHECKERBOARD
3628	014736	012706	001100		CMP	\$R0,\$MEMSIZ	PATTERN	
3629	014742	004737	025654		BLOS	15	BR IF NOT FINISHED	
3630	014746	012737	015314	000024	MOV	MAXCYL,\$CYL	LOAD MAXIMUM CYLINDER	
3631	014754	012737	000340	000026	CLR	\$SEC	CLEAR SECTOR/TRACK	
3632	014762	013700	001240		MOV	#256,\$WC	WORD COUNT = 1 SECTOR	
3633	014766	012720	125252		MOV	\$LOMEM,\$BUF	BUFFER ADDRESS	
3634	014772	020037	001302		MOVB	#WRTSEK,\$DPB+2	WRITE OPERATION	
3635	014776	101773			JSR	PC,RP11	START THE COMMAND	
3636	015000	013737	001210	002510	JSR	PC,CONRDY	WAIT FOR THE CONTROLLER TO BE READY	
3637	015006	005037	002512		JSR	PC,\$AVRP	STORE THE REGISTERS	
3638	015012	012737	000256	002504	BIT	#ERR,\$RPCS	ERROR DURING OPERATION ?	
3639	015020	013737	001240	002506	BEQ	25	BR IF NOT	
3640	015026	112737	000003	002502	ERROR	4	REPORT THE ERROR	
3641	015034	004737	022770		25:	TYPE	MSG15B	'TURN CPU POWER OFF AFTER MESSAGE'
3642	015040	004737	023664		35:	JSR	PC,CLRP	CLEAR THE RP11
3643	015044	004737	023240		MOVB	#RDSEK,\$DPB+2	READ COMMAND	
3644	015050	032737	100000	001340	JSR	PC,RP11	INITIATE THE READ	
3645	015056	001401			JSR	PC,CONRDY	WAIT FOR CONTROLLER READY	
3646	015060	104004			BR	35	LOOP, READING THE DISK	
3647	015062	104401	027456					
3648	015066	004737	025654					
3649	015072	112737	000005	002502				
3650	015100	004737	022770					
3651	015104	004737	023664					
3652	015110	000766						
3653								
3654								
3655								

;AFTER MACHINE IS POWERED DOWN AND UP, CONTROL IS TRANSFERRED HERE.

3656	015112	004737	022566		TST15A:	JSR	PC,RPINIT	INITIALIZE THE RP11E
3657	015116	104401	027652		TYPE	MSG15D	'PROGRAM WILL LOOP, WAITING FOR DRIVE TO COME ONLINE'	
3658	015122	012737	015122	001110	15:	MOV	#15,\$LPERR	SETUP LOOP ON ERROR ADDRESS
3659	015130	012737	002500	001220	MOV	#2500,\$STALL	LOAD STALL VALUE	
3660	015136	004737	023202		JSR	PC,\$STALL	STALL FOR 2500 MILLISECONDS	
3661	015142	013701	001176		MOV	CHKDRV,\$R1	DRIVE ADDRESS	

T15 POWER FAIL TEST

```

3662 015146 004737 022666 JSR PC,DRVINT ;CHECK THE DRIVE'S STATUS
3663 015152 105761 001370 TSTB DRVSTA(R1) ;IS THE DRIVE ONLINE ?
3664 015156 001761 BEQ 15 ;CONTINUE WAITING FOR DRIVE TO COME ONLINE
3665 015160 003002 BGT 25 ;BR IF IT IS ONLINE
3666 015162 104112 ERROR 112 ;DRIVE UNSAFE AFTER POWER FAIL
3667 015164 000446 BR EXIT15 ;DRIVE UNSAFE, EXIT
3668 015166 113764 001176 000005 25: MOVB CHKDRV,RPCS+1(R4) ;SELECT THE DRIVE
3669 015174 004737 023240 JSR PC,SAVRP ;STORE THE REGISTERS
3670 015200 005737 001354 TST $SUCA ;SELECTED CYLINDER SHOULD BE ZERO
3671 015204 001404 BEQ 35 ;BR IF IT DID
3672 015206 012737 015216 001164 MOV #35,$ESCAPE ;ESCAPE TO 35 ON ERROR
3673 015214 104063 ERROR 63 ;DRIVE DID NOT RETURN TO CYLINDER 0
3674 ;ON POWER FAILURE
3675 015216 35:
3676 015216 012737 015276 001164 MOV #65,$ESCAPE ;ESCAPE TO 65 ON ERROR
3677 015224 013700 001240 MOV LOMEM,R0 ;BUFFER ADDRESS
3678 015230 022720 125252 45: CMP #125252,(R0)+ ;DID MEMORY RETAIN PATTERN ON POWER FAIL ?
3679 015234 001004 BNE 55 ;BRANCH IF NOT
3680 015236 020037 001302 CMP R0,MEMSIZ ;END OF MEMORY ?
3681 015242 001372 BNE 45 ;BR IF NOT
3682 015244 000414 BR 65 ;EXIT
3683 015246 012737 125252 001124 55: MOV #125252,$GDDAT ;EXPECTED MEMORY CONTENTS
3684 015254 016037 177776 001126 MOV -2(R0),$BDDAT ;ACTUAL MEMORY CONTENTS
3685 015262 010037 001122 MOV R0,$BDADR ;ADDRESS
3686 015266 162737 000002 001122 SUB #2,$BDADR ;CORRECT IT
3687 015274 104064 ERROR 64 ;CONTENTS OF MEMORY CHANGED AFTER POWER FAIL
3688 015276 104401 027617 65: TYPE .MSG15C ;'END OF POWER FAIL TEST'
3689 015302 000004 EXIT15: SCOPE ;LOOP ?
3690 015304 004737 023452 JSR PC,RESLDR ;RESTORE THE LOADER
3691 015310 000137 003676 JMP START2 ;RETURN TO 'CONVERSATION MODE' ENTRANCE
3692
3693 ;POWER FAIL TRAP HANDLER
3694
3695 015314 012737 015324 000024 TST15B: MOV #TST15C,@#24 ;SET UP POWER UP VECTOR
3696 015322 000774 BR TST15B ;LOOP
3697
3698 ;POWER UP TRAP HANDLER
3699
3700 015324 012737 000026 000024 TST15C: MOV #26,@#24 ;RESTORE TRAP CATCHER
3701 015332 005037 000026 CLR @#26 ;CATCHER HALT
3702 015336 012706 001100 MOV #STACK,SP ;SETUP STACK POINTER
3703 015342 005037 177776 CLR PS ;RETURN TO PRIORITY ZERO
3704 015346 012737 011610 001220 MOV #5000,STALLT ;LOAD STALL VALUE
3705 015354 004737 023202 JSR PC,STALL ;STALL FOR 5000. MILLISECONDS
3706 015360 004737 020416 JSR PC,STKINT ;INITIALIZE THE TTY KEYBOARD
3707 015364 013704 001324 MOV RPADR,R4 ;RPIE ADDRESS
3708 015370 000650 BR TST15A ;VERIFY POWER DOWN SEQUENCE

```

```

;*****
;*TEST 16 HEAD ALIGNMENT ROUTINE
;*****
;*THIS ROUTINE PERFORMS HEAD SELECTION AS DIRECTED FROM THE KEYBOARD.
;*THE ALIGNMENT OF THE SELECTED HEAD MAY BE CHECKED OR ADJUSTED.
;*****

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3709
3710
3711
3712
3713
3714
3715
3716
3717

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3718 015372 033737 001434 001276 TST16: BIT BITS+(<STN*2>),TSTNMS :IS THIS TEST SELECTED
3719 015372 001002 001434 001276 BNE +6 :BR IF YES
3720 015400 001002 001434 001276 JMP TST17 :GO TO THE NEXT TEST
3721 015402 000137 015624 001110 MOV #TST16,$LPERR :SETUP THE ERROR LOOP ADDRESS
3722 015406 012737 015460 001110 MOV #TST16,$LPADR :SETUP THE SCOPE LOOP ADDRESS
3723 015414 012737 015372 001106 MOV #16,$TSTNM :SETUP TEST NUMBER AND
3724 015422 012737 000016 001102 :CLEAR THE ERROR FLAG (SERFLG)
3725 :
3726 015430 013777 001102 163504 MOV $TSTNM,$DISPLAY :LOAD THE TEST NUMBER INTO THE DISPLAY REGISTER
3727 015436 012737 015620 001200 MOV #EXIT16,BYPASS :SETUP BYPASS ADDRESS
3728 015444 013704 001324 001200 MOV RPADR,R4 :RP11E BUS ADDRESS
3729 015450 105037 002501 001200 CLRB DPB+1 :CLEAR ANY EXTENDED MEMORY BITS
3730 015454 105037 002503 001200 CLRB DPB+3 :CLEAR 'MODE' & 'HDR' BITS
3731 015460 012706 001100 001200 TEST16: MOV #STACK,SP :SETUP THE STACK POINTER
3732 015464 104401 027743 001200 TYPE MSG16A :'HEAD ALIGNMENT ROUTINE'
3733 015470 000410 001200 BR 3$ :GET THE HEAD ADDRESS WITHOUT THE SW07 NONSENSE
3734 015472 032777 000200 163440 1$: BIT #SW07,$SWR :SWITCH 7 SET ?
3735 015500 001774 000200 163430 2$: BEQ 1$ :BR IF NOT
3736 015502 032777 000200 163430 2$: BIT #SW07,$SWR :SWITCH 7 RESET ?
3737 015510 001374 000200 163430 2$: BNE 2$ :BR IF NOT
3738 015512 104401 030255 3$: TYPE ,MSG16B :'ENTER HEAD ADDRESS (IN OCTAL)'
3739 015516 104411 030255 3$: RDLIN :GET THE ENTRY
3740 015520 012601 030255 3$: MOV (SP)+,R1 :STARTING ADDRESS OF INPUT ASCII STRING
3741 015522 004037 027136 3$: JSR RD,CK.NUM :CHECK THE NUMBER
3742 015526 015512 027136 3$: 3$ :ILLEGAL INPUT
3743 015530 015534 027136 3$: 4$ :TERMINATED WITH A 'CR'
3744 015532 015512 027136 3$: 3$ :NO ENTRY - 'CR' ONLY
3745 015534 122702 000024 4$: CMPB #20.,R2 :VALID ?
3746 015540 101764 000024 4$: BLOS 3$ :BR IF NOT
3747 015542 110237 002513 4$: MOVB R2,$TRK :LOAD NEW HEAD ADDRESS
3748 015546 136237 001360 001304 BITB ATABIT(R2),DRVTYPE :RPO2 OR RPO3 ?
3749 015554 001004 001360 001304 BNE 5$ :BR IF RPO3
3750 015556 012737 000111 002510 MOV #73.,$CYL :RPO2 ALIGNMENT CYLINDER
3751 015564 000403 000111 002510 BR 6$ :CONTINUE
3752 015566 012737 000222 002510 5$: MOV #146.,$CYL :RPO3 ALIGNMENT CYLINDER
3753 015574 112737 000011 002502 6$: MOVB #SEEK,DPB+2 :SEEK COMMAND
3754 015602 004737 025654 6$: JSR PC,CLAP :CLEAR THE RP11
3755 015606 004737 022770 6$: JSR PC,RP11 :SEEK AND LOAD THE HEAD
3756 015612 004737 023546 6$: JSR PC,DRVRDY :WAIT FOR THE DRIVE TO BECOME READY
3757 015616 000725 023546 6$: BR 1$ :WAIT FOR SWITCH 7 TO BE TOGGLED
3758 015620 000000 023546 6$: EXIT16: HALT :'EXIT' FOR COMPATABILITY WITH OTHER TESTS
3759 015622 000776 023546 6$: BR .-2 :LOCK THE HALT

```

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*****
;TEST 17 RP11E/DRIVE CONTROL PANEL SWITCH TEST

```

```

;THIS TEST TEST THE 'ENABLE/DISABLE' AND 'READ ONLY' SWITCHES ON
;THE DRIVE CONTROL PANEL AND TESTS THE 'WRITE LOCKOUT' AND 'LOA'
;SWITCHES ON THE RP11E/DRIVE CONTROL PANEL.

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*****
TST17:

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```

3770 015624 033737 001436 001276 BIT BITS+(<STN*2>),TSTNMS :IS THIS TEST SELECTED
3771 015624 001002 001436 001276 BNE +6 :BR IF YES
3772 015632 000137 016616 001276 JMP #SEOP :GO TO END OF TEST
3773 015634 000137 016616 001276

```

```

3774 015640 012737 015716 001110      MOV      #TEST17,$LPERR      ;SETUP THE ERROR LOOP ADDRESS
3775 015646 012737 015624 001106      MOV      #TST17,$LPADR      ;SETUP THE SCOPE LOOP ADDRESS
3776 015654 012737 000017 001102      MOV      #17,$STSTNM        ;SETUP TEST NUMBER AND
3777                                     ;CLEAR THE ERROR FLAG ($ERFLG)
3778 015662 013777 001102 163252      MOV      $STSTNM,$DISPLAY    ;LOAD THE TEST NUMBER INTO THE DISPLAY REGISTER
3779 015670 012737 016604 001200      MOV      #EXIT17,$BYPASS     ;SETUP BYPASS ADDRESS
3780 015676 013704 001324                MOV      RPADR,R4            ;RP11E BUS ADDRESS
3781 015702 105037 002501                CLRB     DPB+1              ;CLEAR ANY EXTENDED MEMORY BITS
3782 015706 105037 002503                CLRB     DPB+3              ;CLEAR 'MODE' & 'HDR' BITS
3783 015712 104401 030317                TYPE     MSG17A             ;'RP11E CONTROL PANEL SWITCH TEST'
3784 015716 012706 001100      TEST17: MOV      #STACK,SP    ;SETUP THE STACK POINTER
3785
3786                                     ;CHECK THE ENABLE/DISABLE SWITCH ON THE DRIVE
3787
3788 015722 104401 030362                TYPE     ,MSG17B ;'TYPE A CR TO START TEST AFTER EACH SETUP'
3789 015726 104401 030441      2$:      TYPE     MSG17C      ;'DISABLE DRIVE #'
3790 015732 013746 001176      MOV      CHKDRV,-(SP)        ;SAVE CHKDRV FOR TYPEOUT
3791                                     ;TYPE DRIVE NUMBER
3792 015736 104403                TYPOS                    ;GO TYPE--OCTAL ASCII
3793 015740                .BYTE 1                    ;TYPE 1 DIGIT(S)
3794 015741                .BYTE 1                    ;TYPE LEADING ZEROS
3795 015742 104411                RDLIN                    ;WAIT FOR THE 'CR'
3796 015744 012737 015744 001110      3$:      MOV      #3$, $LPERR      ;CHANGE LOOP ON ERROR ADDRESS
3797 015752 004737 025654                JSR      PC,$CLRP          ;CLEAR THE RP11
3798 015756 113764 001176 000005      MOV      CHKDRV,RPCS+1(R4)  ;SELECT DRIVE
3799 015764 004737 023240                JSR      PC,$SAVAP        ;SAVE THE RP11 REGISTERS
3800 015770 032737 040000 001334      BIT      #SUOL,$RPDS       ;IS THE UNIT ON LINE?
3801 015776 001401                BEQ      4$                ;BRANCH IF NOT
3802 016000 104065                ERROR   65                 ;SELECTED UNIT ON LINE WITH THE
3803                                     ;DRIVE DISABLED
3804 016002 104401 030464      4$:      TYPE     MSG17D          ;'ENABLE DRIVE #'
3805 016006 013746 001176      MOV      CHKDRV,-(SP)        ;SAVE CHKDRV FOR TYPEOUT
3806                                     ;TYPE DRIVE NUMBER
3807 016012 104403                TYPOS                    ;GO TYPE--OCTAL ASCII
3808 016014                .BYTE 1                    ;TYPE 1 DIGIT(S)
3809 016015                .BYTE 1                    ;TYPE LEADING ZEROS
3810 016016 104411                RDLIN                    ;WAIT FOR THE 'CR'
3811 016020 012737 016020 001110      5$:      MOV      #5$, $LPERR      ;CHANGE THE LOOP ON ERROR ADDRESS
3812 016026 004737 025654                JSR      PC,$CLRP          ;CLEAR THE RP11
3813 016032 113764 001176 000005      MOV      CHKDRV,RPCS+1(R4)  ;RESELECT THE UNIT
3814 016040 004737 023240                JSR      PC,$SAVAP        ;SAVE THE RP11 REGISTERS
3815 016044 032737 040000 001334      BIT      #SUOL,$RPDS       ;IS THE UNIT ON LINE?
3816 016052 001001                BNE     RDSW               ;BR IF ONLINE
3817 016054 104066                ERROR   66                 ;SELECTED UNIT NOT ON LINE WITH
3818                                     ;THE DRIVE ENABLED
3819
3820                                     ;CHECK THE DRIVE'S WRITE LOCKOUT SWITCH
3821
3822 016056 104401 030506      RDSW:   TYPE     MSG17E          ;'SET READ ONLY ON DRIVE '
3823 016062 013746 001176      MOV      CHKDRV,-(SP)        ;SAVE CHKDRV FOR TYPEOUT
3824                                     ;TYPE DRIVE NUMBER
3825 016066 104403                TYPOS                    ;GO TYPE--OCTAL ASCII
3826 016070                .BYTE 1                    ;TYPE 1 DIGIT(S)
3827 016071                .BYTE 1                    ;TYPE LEADING ZEROS
3828 016072 104411                RDLIN                    ;WAIT FOR 'CR'
3829 016074 012737 016074 001110      2$:      MOV      #2$, $LPERR      ;LOAD LOOP ON ERROR ADDRESS

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3830 016102 004737 025654      JSR   PC,CLRP      ;CLEAR THE RP11
3831 016106 113764 001176 000005  MOVB  CHKDRV,RPCS+1(R4) ;SELECT THE DRIVE
3832 016114 004737 023240      JSR   PC,SAVRP     ;SAVE THE RP11 REGISTERS
3833 016120 032737 000400 001334  BIT   #SUMP,SRPDS  ;IS SELECTED UNIT WRITE PROTECTED ?
3834 016126 001001      BNE   3$          ;BR IF NOT
3835 016130 104067      ERROR  67        ;SELECTED UNIT WRITE PROTECT DID
3836                                ;NOT SET WITH READ ONLY SET
3837 016132 104401 030542      3$:  TYPE  MSG17F    ;'SET READ/WRITE ON DRIVE'
3838 016136 013746 001176      MOV   CHKDRV,-(SP) ;SAVE CHKDRV FOR TYPEOUT
3839                                ;TYPE DRIVE NUMBER
3840 016142 104403      TYPOS ;GO TYPE--OCTAL ASCII
3841 016144      .BYTE  1      ;TYPE 1 DIGIT(S)
3842 016145      .BYTE  1      ;TYPE LEADING ZEROS
3843 016146 104411      RDLIN ;WAIT FOR 'CR'
3844 016150 012737 016150 001110  4$:  MOV   #4$,SLPERR  ;CHANGE THE LOOP ON ERROR ADDRESS
3845 016156 004737 025654      JSR   PC,CLRP     ;CLEAR THE RP11
3846 016162 113764 001176 000005  MOVB  CHKDRV,RPCS+1(R4) ;RESELECT THE DRIVE
3847 016170 004737 023240      JSR   PC,SAVRP     ;SAVE THE RP11 REGISTERS
3848 016174 032737 000400 001334  BIT   #SUMP,SRPDS  ;IS SELECTED UNIT WRITE PROTECTED ?
3849 016202 001401      BEQ   WRSW       ;BR IF NOT
3850 016204 104070      ERROR  70        ;SELECTED UNIT WRITE PROTECT SET
3851                                ;WITH DRIVE WRITE ENABLED
3852
3853                                ;TEST THE SETTING OF WRITE LOCKOUT AND THE 'LOA' SWITCHES ON THE
3854                                ;RP11E CONTROL PANEL.
3855
3856 016206 104401 030577      WRSW: TYPE  ,MSG17G  ;'SET WRITE LOCKOUT' ,ETC
3857 016212 104411      RDLIN ;WAIT FOR 'CR' FROM THE OPERATOR
3858 016214 012737 016214 001110  1$:  MOV   #1$,SLPERR  ;CHANGE LOOP ON ERROR ADDRESS
3859 016222 005064 000012      CLR   RPCA(R4)    ;CLEAR THE CYLINDER REGISTER
3860 016226 004737 023240      JSR   PC,SAVRP     ;SAVE THE RP11 REGISTERS
3861 016232 032737 000400 001334  BIT   #SUMP,SRPDS  ;IS SELECTED UNIT WRITE PROTECT SET ?
3862 016240 001001      BNE   2$          ;BR IF SET
3863 016242 104071      ERROR  71        ;SELECTED UNIT WRITE PROTECT SHOULD
3864                                ;BE SET - RPCA EQUAL TO ZERO
3865 016244 012764 000002 000012  2$:  MOV   #2,RPCA(R4) ;LOAD A 2 INTO RPCA
3866 016252 004737 023240      JSR   PC,SAVRP     ;SAVE THE RP11 REGISTERS
3867 016256 032737 000400 001334  BIT   #SUMP,SRPDS  ;IS SELECTED UNIT WRITE PROTECTED ?
3868 016264 001401      BEQ   WRSW1      ;BR IF NOT PROTECTED
3869 016266 104072      ERROR  72        ;SELECTED UNIT WRITE PROTECT IS SET
3870                                ;WITH EQUAL TO 2.
3871
3872                                ;CHECK THE CYLINDER 'LOA' SWITCHES
3873
3874 016270 012700 000002      WRSW1: MOV   #2,RO    ;INITIALIZE SWITCH PATTERN
3875 016274 104401 030714      TYPE  ,MSG17H    ;'SET FOLLOWING VALUES IN THE
3876                                ;CYLINDER LOA SWITCHES'
3877 016300 104401 031014      1$:  TYPE  MSG17I  ;'SET'
3878 016304 006200      ASR   RO        ;SETUP PATTERN FOR TYP0UT
3879 016306 010046      MOV   RO,-(SP)  ;SAVE RO FOR TYPEOUT
3880                                ;TYPE THE SWITCH VALUE
3881 016310 104403      TYPOS ;GO TYPE--OCTAL ASCII
3882 016312      .BYTE  3      ;TYPE 3 DIGIT(S)
3883 016313      .BYTE  1      ;TYPE LEADING ZEROS
3884 016314 006300      ASL   RO        ;RESTORE THE PATTERN
3885 016316 104411      RDLIN ;WAIT FOR 'CR' FROM THE OPERATOR

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3886 016320 012737 016320 001110 2$: MOV #2$, $LPERR ;CHANGE LOOP ON ERROR ADDRESS
3887 016326 010064 000012 000012 2$: MOV R0, RPCA(R4) ;LOAD PATTERN INTO RPCA
3888 016332 004737 023240 000000 2$: JSR PC, SAVRP ;SAVE THE RP11 REGISTERS
3889 016336 032737 000400 001334 2$: BIT #SUMP, SRPDS ;IS UNIT WRITE PROTECT SET ?
3890 016344 001001 000000 000000 2$: BNE 3$ ;BR IF SET
3891 016346 104073 000000 000000 2$: ERROR 73 ;UNIT WRITE PROTECT IS NOT SET
3892 016346 104073 000000 000000 2$: ;RPCA EQUALS CONTENTS OF BOUNDARY
3893 016346 104073 000000 000000 2$: ;SWITCHES
3894 016350 062764 000002 000012 3$: ADD #2, RPCA(R4) ;INCREMENT CYLINDER ADDRESS
3895 016356 012737 016356 001110 4$: MOV #4$, $LPERR ;CHANGE THE LOOP ON ERROR ADDRESS
3896 016364 004737 023240 000000 4$: JSR PC, SAVRP ;SAVE THE RP11 REGISTERS
3897 016370 032737 000400 001334 4$: BIT #SUMP, SRPDS ;IS UNIT WRITE PROTECT SET ?
3898 016376 001401 000000 000000 4$: BEQ 5$ ;BR IF NOT
3899 016400 104074 000000 000000 4$: ERROR 74 ;UNIT WRITE PROTECT IS SET WITH
3900 016400 104074 000000 000000 4$: ;RPCA ONE GREATER THAN BOUNDARY
3901 016400 104074 000000 000000 4$: ;SWITCH SETTINGS
3902 016402 006300 000000 000000 5$: ASL R0 ;UPDATE TEST PATTERN
3903 016404 032700 001000 001000 5$: BIT #BIT09, R0 ;IS PATTERN EXCEEDED?
3904 016410 001733 000000 000000 5$: BEQ 1$ ;BRANCH IF NOT
3905 016410 001733 000000 000000 5$:
3906 016410 001733 000000 000000 5$: ;CHECK DRIVE 'LOA' SWITCHES
3907 016410 001733 000000 000000 5$:
3908 016412 005037 001176 001176 WRSW2: CLR CHKDRV ;START WITH DRIVE 0
3909 016416 104401 031037 031037 WRSW2: TYPE ,MSG17J ;RESET THE CYLINDER LOA SWITCHES
3910 016416 104401 031037 031037 WRSW2: ;SET THE FOLLOWING VALUES IN THE DRIVE
3911 016416 104401 031037 031037 WRSW2: ;'LOA SWITCHES'
3912 016422 012764 000002 000012 1$: MOV #2, RPCA(R4) ;LOAD CYLINDER ADDRESS
3913 016430 113764 001176 000005 1$: MOV#B CHKDRV, RPCS+1(R4) ;SELECT THE FIRST DRIVE NUMBER
3914 016436 104401 031014 001176 1$: TYPE MSG17I ;SET SWITCHES TO '
3915 016442 013746 001176 001176 1$: MOV CHKDRV, -(SP) ;SAVE CHKDRV FOR TYPEOUT
3916 016442 013746 001176 001176 1$: ;TYPE SWITCH SETTING
3917 016446 104403 000000 000000 1$: TYPOS ;GO TYPE--OCTAL ASCII
3918 016450 001 000000 000000 1$: .BYTE 1 ;TYPE 1 DIGIT(S)
3919 016451 001 000000 000000 1$: .BYTE 1 ;TYPE LEADING ZEROS
3920 016452 104411 000000 000000 1$: RDLIN ;WAIT FOR THE OPERATOR TO ENTER A 'CR'
3921 016454 012737 016454 001110 2$: MOV #2$, $LPERR ;CHANGE LOOP ON ERROR ADDRESS
3922 016462 004737 023240 000000 2$: JSR PC, SAVRP ;STORE THE RP11 REGISTERS
3923 016466 032737 000400 001334 2$: BIT #SUMP, SRPDS ;IS THE SELECTED DRIVE WRITE PROTECTED ?
3924 016474 001401 000000 000000 2$: BEQ 3$ ;BR IF NOT
3925 016476 104075 000000 000000 2$: ERROR 75 ;'SUMP' SET WHEN DRIVE LOA SWITCHES
3926 016476 104075 000000 000000 2$: ;EQUAL THE SELECTED DRIVE'
3927 016500 005737 001176 001176 3$: TST CHKDRV ;THROUGH HERE THE FIRST TIME ?
3928 016504 001004 000000 000000 3$: BNE 4$ ;BR IF NOT
3929 016506 012737 000001 001176 3$: MOV #1, CHKDRV ;SET FOR DRIVE 1
3930 016514 000406 000000 000000 3$: BR 5$ ;CONTINUE
3931 016516 006337 001176 001176 4$: ASL CHKDRV ;SHIFT TEST ADDRESS
3932 016522 032737 000010 001176 4$: BIT #BIT03, CHKDRV ;FINISHED WITH ADDRESSES ?
3933 016530 001025 000000 000000 4$: BNE EXIT17 ;BR IF FINISHED
3934 016532 104401 031014 001176 5$: TYPE MSG17I ;SET SWITCHES TO '
3935 016536 013746 001176 001176 5$: MOV CHKDRV, -(SP) ;SAVE CHKDRV FOR TYPEOUT
3936 016536 013746 001176 001176 5$: ;TYPE SWITCH SETTING
3937 016542 104403 000000 000000 5$: TYPOS ;GO TYPE--OCTAL ASCII
3938 016544 001 000000 000000 5$: .BYTE 1 ;TYPE 1 DIGIT(S)
3939 016545 001 000000 000000 5$: .BYTE 1 ;TYPE LEADING ZEROS
3940 016546 104411 000000 000000 5$: RDLIN ;WAIT FOR THE OPERATOR TO ENTER A 'CR'
3941 016550 012737 016550 001110 6$: MOV #6$, $LPERR ;CHANGE ERROR LOOP ADDRESS

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3942 016556 004737 023240 JSR PC,SAVRP ;STORE THE REGISTERS
3943 016562 032737 000400 001334 BIT #SUMP,SRPDS ;IS THE DRIVE WRITE PROTECTED ?
3944 016570 001001 BNE 7$ ;BR IF IT IS
3945 016572 104113 ERROR 113 ;'SUMP' NOT SET WHEN DRIVE LOA SWITCHES
3946 ;GREATER THAN SELECTED DRIVE NUMBER
3947 016574 113764 001176 000005 7$: MOVB CHKDRV,RPCS+1(R4) ;SELECT THE NEXT DRIVE
3948 016602 000724 BR 2$ ;CONTINUE WITH THE TEST
3949 016604 000004 EXIT17: SCOPE ;LOOP ?
3950 016606 104401 031176 TYPE MSG17K ;END OF CONTROL PANEL TEST - RETURN SWITCHES TO NORMAL
3951 016612 000137 003676 JMP START2 ;RETURN TO THE 'CONVERSATION MODE' ENTRANCE
3952
3953 .SBTTL END OF PASS ROUTINE
3954
3955 ;*****
3956 ;*INCREMENT THE PASS NUMBER ($PASS)
3957 ;*INDICATE END-OF-PROGRAM AFTER 1 PASSES THRU THE PROGRAM
3958 ;*IF THERES A MONITOR GO TO IT
3959 ;*IF THERE ISN'T JUMP TO RESTART
3960
3961 016616 SEOP: TYPE 65$ ;TYPE ASCIZ STRING
3962 016616 104401 016624 BR 64$ ;GET OVER THE ASCIZ
3963 016622 000410 ;:65$: .ASCIZ <15><12><12>/END OF PASS/
3964
3965 016644 ;:64$: TST 2#DRVSEL ;ANY DRIVES SELECTED
3966 016644 005737 001312 BEQ 1$ ;NO, BRANCH
3967 016650 001436 TYPE 67$ ;TYPE ASCIZ STRING
3968 016652 104401 016660 BR 66$ ;GET OVER THE ASCIZ
3969 016656 000407 ;:67$: .ASCIZ / USING DRIVE/
3970
3971 016676 ;:66$: MOV 2#CHKDRV,-(SP) ;SAVE 2#CHKDRV FOR TYPEOUT
3972 016676 013746 001176 TYPOS ;GO TYPE--OCTAL ASCII ..
3973 016702 104403 .BYTE 2 ;TYPE 2 DIGIT(S)
3974 016704 002 .BYTE 0 ;SUPPRESS LEADING ZEROS
3975 016705 000 TYPE 69$ ;TYPE ASCIZ STRING
3976 016706 104401 016714 BR 68$ ;GET OVER THE ASCIZ
3977 016712 000412 ;:69$: .ASCIZ / ERRORS DETECTED=/
3978
3979 016740 ;:68$: MOV 2#SERTTL,-(SP) ;SAVE 2#SERTTL FOR TYPEOUT
3980 016740 013746 001112 TYPOC ;GO TYPE--OCTAL ASCII(ALL DIGITS)
3981 016744 104402 1$: CLR 2#SERTTL ;ZERO ERROR TOTAL
3982 016746 005037 001112 CLR $STNM ;ZERO THE TEST NUMBER
3983 016752 005037 001102 CLR $TIMES ;ZERO THE NUMBER OF ITERATIONS
3984 016756 005037 001162 INC $PASS ;INCREMENT THE PASS NUMBER
3985 016762 005237 001100 BIC #10000,$PASS ;DON'T ALLOW A NEG. NUMBER
3986 016766 042737 100000 001100 DEC (PC)+ ;LOOP?
3987 016774 005327 SEOPCT: .WORD 1 ;YES
3988 016776 000001 BGT $DOAGN ;RESTORE COUNTER
3989 017000 003027 MOV (PC)+,2(PC)+ ;RESTORE COUNTER
3990 017002 012737 SENDCT: .WORD 1
3991 017004 000001 SEOPCT
3992 017006 016776 TYPE 65$ ;TYPE ASCIZ STRING
3993 017010 104401 017016 BR 64$ ;GET OVER THE ASCIZ
3994 017014 000407 ;:65$: .ASCIZ <15><12>/END OF TEST/
3995
3996 017034 ;:64$: TYPE ,SENULL ;TYPE NULL CHARACTER
3997 017034 104401 017064

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3998 017040 013700 000042          $GET42: MOV      @#42,RO          ;; GET MONITOR ADDRESS
3999 017044 001405                   BEQ      $DOAGN          ;; BRANCH IF NO MONITOR
4000 017046 000005                   RESET                      ;; CLEAR THE WORLD
4001 017050 004710          SENDAD: JSR      PC,(RO)      ;; GO TO MONITOR
4002 017052 000240                   NOP                          ;; SAVE ROOM
4003 017054 000240                   NOP                          ;; FOR
4004 017056 000240                   NOP                          ;; ACT11
4005 017060                   SDOAGN:                   ;;
4006 017060 000137                   JMP      @PC+            ;; RETURN
4007 017062 004762          SRTNAD: .WORD    RESTART
4008 017064 377 000          SENU1L: .BYTE   -1,-1,0    ;; NULL CHARACTER STRING
4009 017070
4010
4011
4012
4013          .SBTTL  *** SUBROUTINES ***
4014
4015          .SBTTL  ERROR HANDLER ROUTINE
4016
4017          ;*****
4018          ;THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT,
4019          ;SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
4020          ;AND GO TO TYPERR ON ERROR
4021          ;THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
4022          ;SW15=1      HALT ON ERROR
4023          ;SW13=1      INHIBIT ERROR TYPEOUTS
4024          ;SW10=1      BELL ON ERROR
4025          ;SW09=1      LOOP ON ERROR
4026          ;CALL
4027          ;*      ERROR      N      ;;ERROR=EMT AND N=ERROR ITEM NUMBER
4028
4029          SERROR:
4030 017070 104407                   7$:  CKSWR          ;; TEST FOR CHANGE IN SOFT-SWR
4031 017072 105237 001103          INCB          SERFLG          ;; SET THE ERROR FLAG
4032 017076 001775                   BEQ      7$              ;; DON'T LET THE FLAG GO TO ZERO
4033 017100 013777 001102 162034          MOV      $STNM,@DISPLAY  ;; DISPLAY TEST NUMBER AND ERROR FLAG
4034 017106 032777 002000 162024          BIT      @BIT10,@SWR    ;; BELL ON ERROR?
4035 017114 001402                   BEQ      1$              ;; NO - SKIP
4036 017116 104401 001166                   TYPE      $BELL          ;; RING BELL
4037 017122 005237 001112          INC      $ERTTL         ;; COUNT THE NUMBER OF ERRORS
4038 017126 011637 001116          MOV      (SP),SERRPC    ;; GET ADDRESS OF ERROR INSTRUCTION
4039 017132 162737 000002 001116          SUB      #2,SERRPC
4040 017140 117737 161752 001114          MOV     @SERRPC,$ITEMB  ;; STRIP AND SAVE THE ERROR ITEM CODE
4041 017146 032777 020000 161764          BIT      @BIT13,@SWR    ;; SKIP TYPEOUT IF SET
4042 017154 001004                   BNE      20$             ;; SKIP TYPEOUTS
4043 017156 004737 017254          JSR      PC,TYPERR      ;; GO TO USER ERROR ROUTINE
4044 017162 104401 001173          TYPE      ,SCLF
4045 017166
4046 017166 005777 161746          20$:  TST      @SWR          ;; HALT ON ERROR
4047 017172 100002                   BPL      3$              ;; SKIP IF CONTINUE
4048 017174 000000                   HALT                      ;; HALT ON ERROR!
4049 017176 104407                   CKSWR          ;; TEST FOR CHANGE IN SOFT-SWR
4050 017200 032777 001000 161732          3$:  BIT      @BIT09,@SWR  ;; LOOP ON ERROR SWITCH SET?
4051 017206 001402                   BEQ      4$              ;; BR IF NO
4052 017210 013716 001110          MOV      $LPERR,(SP)    ;; FUDGE RETURN FOR LOOPING
4053 017214 005737 001164          4$:  TST      $ESCAPE     ;; CHECK FOR AN ESCAPE ADDRESS

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4054 017220 001402          BEQ      55          ;; BR IF NONE
4055 017222 013716 001164    MOV      $ESCAPE, (SP) ;; FUDGE RETURN ADDRESS FOR ESCAPE
4056 017224 022737 017050 000042 55:     CMP      @SENDAD, @#42 ;; ACT-11 AUTO-ACCEPT?
4057 017226 001001          BNE     65          ;; BRANCH IF NO
4058 017228 000000          HALT                    ;; YES
4059 017240 023737 000042 000046 65:     CMP      @#42, @#46          :ACT11 AUTO MODE?
4060 017242 001001          BNE     125         :NO RETURN
4061 017244 000000          HALT                    :HALT ON ERROR
4062 017252 000002          RTI                     :RETURN
4063 017254 000000          ;; *****
4064 017256 000000          ;; THIS ROUTINE USES THE "ITEM CONTROL BYTE" ($ITEMB) TO DETERMINE
4065 017258 000000          ;; WHICH ERROR IS TO BE REPORTED. IT THEN OBTAINS FROM THE "ERROR
4066 017260 000000          ;; TABLE" ($ERRTB) AND REPORTS THE APPROPRIATE INFORMATION
4067 017262 000000          ;; CONCERNING THE ERROR.
4068 017264 000000          ;; CALL
4069 017266 000000          ;;
4070 017268 000000          ;;
4071 017270 000000          ;;
4072 017272 000000          ;;
4073 017274 000000          ;;
4074 017276 000000          ;;
4075 017278 000000          ;;
4076 017280 000000          ;;
4077 017282 000000          ;;
4078 017284 000000          ;;
4079 017286 000000          ;;
4080 017288 000000          ;;
4081 017290 000000          ;;
4082 017292 000000          ;;
4083 017294 000000          ;;
4084 017296 000000          ;;
4085 017298 000000          ;;
4086 017300 000000          ;;
4087 017302 000000          ;;
4088 017304 000000          ;;
4089 017306 000000          ;;
4090 017308 000000          ;;
4091 017310 000000          ;;
4092 017312 000000          ;;
4093 017314 000000          ;;
4094 017316 000000          ;;
4095 017318 000000          ;;
4096 017320 000000          ;;
4097 017322 000000          ;;
4098 017324 000000          ;;
4099 017326 000000          ;;
4100 017328 000000          ;;
4101 017330 000000          ;;
4102 017332 000000          ;;
4103 017334 000000          ;;
4104 017336 000000          ;;
4105 017338 000000          ;;
4106 017340 000000          ;;
4107 017342 000000          ;;
4108 017344 000000          ;;
4109 017346 000000          ;;

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TYPERR: SAVREG ; SAVE RD-R5
BIT @SW13, @SWR ; INHIBIT TYPEOUTS ?
BNE 20\$; BR IF YES
CLR \$TMPD ; CLEAR LOCATION FOR TEST NUMBER
MOVB \$STNM, \$TMPD ; LOAD TEST NUMBER FOR TYPEOUT
CLR @ ; CLEAR RD FOR ERROR NUMBER
MOVB \$ITEMB, @ ; ERROR NUMBER
DEC @ ; FORM INDEX FOR ERROR TABLE
ASL @ ;
ASL @ ;
ASL @ ;
15: ADD @ERRTB, @ ; FORM ADDRESS
MOV (@)+, 2\$; GET ERROR MESSAGE (EM) POINTER
BEQ 3\$; BRANCH IF THERE ISN'T ONE
TYPE , \$CRLF ; CARRIAGE RETURN - LINE FEED
25: .WORD 0 ; "EM" POINTER GOES HERE
35: MOV (@)+, 4\$; PICK UP DATA HEADER (DH) POINTER
BEQ 5\$; BRANCH IF NONE
TYPE , \$CRLF ; CARRIAGE RETURN-LINE FEED
45: .WORD 0 ; "DH" POINTER GOES HERE
55: MOV (@)+, R1 ; PICKUP DATA TABLE (DT) POINTER
BEQ 20\$; BRANCH IF NONE
CLR R5 ; SET INDENT SWITCH
MOV (@)+, @ ; DATA FORMAT (DF) POINTER
MOV (@)+, R2 ; NUMBER OF DH'S TO TYPE
BEQ 20\$; BRANCH IF DH NUMBER IS 0
COM R5 ; NO INDENT
105: MOVB (@)+, R3 ; NUMBER OF DATA WORDS TO TYPE
BEQ 20\$; BR IF NO WORDS TO TYPE
MOVB (@)+, R4 ; AND HOW TO TYPE THEM
TYPE , \$CRLF ; CR-LF
TST R5 ; INDENT ?


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4110 017410 001002      BNE      11$      ;BR IF NOT
4111 017412 104401 027430      TYPE     BLNKS2  ;BLANKS
4112 017414 006004      11$:     ROR      R4      ;OCTAL OR DECIMAL?
4113 017416 103403      BCS      12$      ;DECIMAL--BRANCH
4114 017418 013146      MOV      2(R1)+,-(SP) ;SAVE 2(R1)+ FOR TYPEOUT
4115 017420 104402      TYP0C   BR        13$ ;GO TYPE--OCTAL ASCII(ALL DIGITS)
4116 017422 000402      BR
4117 017424 013146      12$:     MOV      2(R1)+,-(SP) ;SAVE 2(R1)+ FOR TYPEOUT
4118 017426 104405      TYPDS   ;GO TYPE--DECIMAL ASCII WITH SIGN
4119 017428 005303      13$:     DEC      R3      ;MORE NUMBERS TO TYPE?
4120 017430 001403      BEQ     14$      ;NO--BRANCH
4121 017432 104401 027430      TYPE     BLNKS2  ;YES--TYPE SEPARATORS
4122 017434 000764      BR      11$      ;LOOP
4123 017436 005302      14$:     DEC      R2      ;MORE OH'S?
4124 017438 003413      BLE     20$      ;NO--BRANCH
4125 017440 104401 001173      TYPE     SCRLF   ;YES--START A NEW LINE
4126 017442 005105      COM     R5      ;INDENT?
4127 017444 001002      BNE     15$      ;NO--BRANCH
4128 017446 104401 027430      TYPE     BLNKS2  ;YES--TYPE SPACES
4129 017448 012037 017474      15$:     MOV      (R0)+,16$ ;GET NEXT OH
4130 017450 104401      TYPE     ;AND TYPE IT
4131 017452 000000      16$:     .WORD   0      ;OH POINTER GOES HERE
4132 017454 000736      BR      10$      ;LOOP
4133 017456 104413      20$:     RESREG  ;RESTORE R0-R5
4134 017500 000207      RTS     PC      ;RETURN

```

.SBTTL TYPE ROUTINE

```

*****
;ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
;THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
;NOTE1: $NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
;NOTE2: $FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
;NOTE3: $FILLC CONTAINS THE CHARACTER TO FILL AFTER.
;
;CALL:
;1) USING A TRAP INSTRUCTION
;   TYPE ,MESADR ;MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
;OR
;   TYPE
;   MESADR
;

```

```

4154 017504 105737 001157      $TYPE:  TSTB   $TFPLG  ;IS THERE A TERMINAL?
4155 017510 100002      BPL     1$      ;BR IF YES
4156 017512 000000      HALT   ;HALT HERE IF NO TERMINAL
4157 017514 000407      BR     ;LEAVE
4158 017516 010046      1$:     MOV     R0,-(SP) ;SAVE R0
4159 017520 017600 000002      MOV     2(SP),R0 ;GET ADDRESS OF ASCIZ STRING
4160 017524 112046      2$:     MOVB   (R0)+,-(SP) ;PUSH CHARACTER TO BE TYPED ONTO STACK
4161 017526 001005      BNE     4$      ;BR IF IT ISN'T THE TERMINATOR
4162 017530 005726      TST    (SP)+    ;IF TERMINATOR POP IT OFF THE STACK
4163 017532 012600      60$:    MOV     (SP)+,R0 ;RESTORE R0
4164 017534 062716 000002      3$:     ADD     2,(SP)  ;ADJUST RETURN PC
4165 017540 000002      RTI

```



```

4278 020140 012616
4279 020142 000002
4280 020144 000
4281 020145 000
4282 020146 000
4283 020147 000
4284 020150 000000
4285
4286
4287
4288
4289
4290
4291
4292
4293
4294
4295
4296
4297
4298 020152
4299 020152 010046
4300 020154 010146
4301 020156 010246
4302 020160 010346
4303 020162 010546
4304 020164 012746 020200
4305 020170 016605 000020
4306 020174 100004
4307 020176 005405
4308 020200 112766 000055 000001
4309 020206 005000 1$: CLR
4310 020210 012703 020366
4311 020214 112723 000040
4312 020220 005002 2$: CLR
4313 020222 016001 020356
4314 020226 160105 3$: MOV
4315 020230 002402
4316 020232 005202
4317 020234 000774
4318 020236 060105 4$: ADD
4319 020240 005702
4320 020242 001002
4321 020244 105716
4322 020246 100407
4323 020250 106316 5$: ASLB
4324 020252 103003
4325 020254 116663 000001 177777
4326 020262 052702 000060
4327 020266 052702 000040 7$: BIS
4328 020272 110223
4329 020274 005720
4330 020276 020027 000010
4331 020302 002746
4332 020304 003002
4333 020306 010502

```

```

MOV (SP)+,(SP)
RTI
;; RETURN
BS: .BYTE 00 ;; STORAGE FOR ASCII DIGIT
      .BYTE 00 ;; TERMINATOR FOR TYPE ROUTINE
SOCNT: .BYTE 00 ;; OCTAL DIGIT COUNTER
SOFILL: .BYTE 00 ;; ZERO FILL SWITCH
SOMODE: .WORD 0 ;; NUMBER OF DIGITS TO TYPE

.SBTTL CONVERT BINARY TO DECIMAL AND TYPE ROUTINE

*****
; THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 5-DIGIT
; SIGNED DECIMAL (ASCII) NUMBER AND TYPE IT. DEPENDING ON WHETHER THE
; NUMBER IS POSITIVE OR NEGATIVE A SPACE OR A MINUS SIGN WILL BE TYPED
; BEFORE THE FIRST DIGIT OF THE NUMBER. LEADING ZEROS WILL ALWAYS BE
; REPLACED WITH SPACES.
; CALL:
; * MOV NUM,-(SP) ;; PUT THE BINARY NUMBER ON THE STACK
; * TYPDS ;; GO TO THE ROUTINE

STYPDS: MOV R0,-(SP) ;; PUSH R0 ON STACK
        MOV R1,-(SP) ;; PUSH R1 ON STACK
        MOV R2,-(SP) ;; PUSH R2 ON STACK
        MOV R3,-(SP) ;; PUSH R3 ON STACK
        MOV R5,-(SP) ;; PUSH R5 ON STACK
        MOV #20200,-(SP) ;; SET BLANK SWITCH AND SIGN
        MOV 20(SP),R5 ;; GET THE INPUT NUMBER
        BPL 1$ ;; BR IF INPUT IS POS.
        NEG R5 ;; MAKE THE BINARY NUMBER POS.
        MOVB #'-',1(SP) ;; MAKE THE ASCII NUMBER NEG.
        CLR R0 ;; ZERO THE CONSTANTS INDEX
        MOV #SDBLK,R3 ;; SETUP THE OUTPUT POINTER
        MOVB #'',(R3)+ ;; SET THE FIRST CHARACTER TO A BLANK
        CLR R2 ;; CLEAR THE BCD NUMBER
        MOV #0TAB,(R0),R1 ;; GET THE CONSTANT
        SUB R1,R5 ;; FORM THIS BCD DIGIT
        BLT 4$ ;; BR IF DONE
        INC R2 ;; INCREASE THE BCD DIGIT BY 1
        BR 3$

4$: ADD R1,R5 ;; ADD BACK THE CONSTANT
   TST R2 ;; CHECK IF BCD DIGIT=0
   BNE 5$ ;; FALL THROUGH IF 0
   TSTB (SP) ;; STILL DOING LEADING 0'S?
   BMI 7$ ;; BR IF YES
   ASLB (SP) ;; MSD?
   BCC 6$ ;; BR IF NO
   MOVB 1(SP),-1(R3) ;; YES--SET THE SIGN
   BIS #'C,R2 ;; MAKE THE BCD DIGIT ASCII
   BIS #' ',R2 ;; MAKE IT A SPACE IF NOT ALREADY A DIGIT
   MOVB R2,(R3)+ ;; PUT THIS CHARACTER IN THE OUTPUT BUFFER
   TST (R0)+ ;; JUST INCREMENTING
   CMP R0,#10 ;; CHECK THE TABLE INDEX
   BLT 2$ ;; GO DO THE NEXT DIGIT
   BGT 8$ ;; GO TO EXIT
   MOV R5,R2 ;; GET THE LSD

```

```

4334 020310 000764
4335 020312 105726
4336 020314 100003
4337 020316 116663 177777 177776
4338 020324 105013
4339 020326 012605
4340 020330 012603
4341 020332 012602
4342 020334 012601
4343 020336 012600
4344 020340 104401 020366 000004
4345 020344 016666 000002 000004
4346 020352 012616
4347 020354 000002
4348 020356 023420
4349 020360 001750
4350 020362 000144
4351 020364 000012
4352 020366 000004
4353
4354
4355
4356
4357
4358 020376 000000
4359 020400 000000
4360 020402 000000
4361 020404 000012
4362 020416
4363
4364
4365
4366
4367
4368
4369
4370
4371
4372 020416 005037 020376
4373 020422 012737 020404 020400
4374 020430 013737 020400 020402
4375 020436 012737 020466 000060
4376 020444 012737 000200 000062
4377 020452 005777 160470
4378 020456 012777 000100 160460
4379 020464 000207
4380
4381
4382
4383
4384
4385
4386
4387
4388 020466 117746 160454
4389 020472 042716 177600

```

```

BR 6$ :: GO CHANGE TO ASCII
TSTB (SP)+ :: WAS THE LSD THE FIRST NON-ZERO?
BPL 9$ :: BR IF NO
MOVB -1(SP),-2(R3) :: YES--SET THE SIGN FOR TYPING
CLRB (R3) :: SET THE TERMINATOR
MOV (SP)+,R5 :: POP STACK INTO R5
MOV (SP)+,R3 :: POP STACK INTO R3
MOV (SP)+,R2 :: POP STACK INTO R2
MOV (SP)+,R1 :: POP STACK INTO R1
MOV (SP)+,R0 :: POP STACK INTO R0
TYPE $DBLK :: NOW TYPE THE NUMBER
MOV 2(SP),4(SP) :: ADJUST THE STACK
MOV (SP)+,(SP)
RTI :: RETURN TO USER

$DTBL: 1000.
1000.
100.
10.

$DBLK: .BLKW 4

.SBTTL TTY INPUT ROUTINE

:*****
.ENABL LSB
$TKCNT: .WORD 0 :: NUMBER OF ITEMS IN QUEUE
$TKQIN: .WORD 0 :: INPUT POINTER
$TKQOUT: .WORD 0 :: OUTPUT POINTER
$TKQSRV: .BLKB 10. :: TTY KEYBOARD QUEUE
$TKQEND=.

:*TK INITIALIZE ROUTINE
:*THIS ROUTINE WILL INITIALIZE THE TTY KEYBOARD INPUT QUEUE
:*SETUP THE INTERRUPT VECTOR AND TURN ON THE KEYBOARD INTERRUPT
:
:*CALL:
:* JSR PC,$TKINT
:* RETURN
$TKINT: CLR $TKCNT :: CLEAR COUNT OF ITEMS IN QUEUE
MOV $TKQSRV,$TKQIN :: MOVE THE STARTING ADDRESS OF THE
MOV $TKQIN,$TKQOUT :: QUEUE INTO THE INPUT & OUTPUT POINTERS.
MOV $TKSRV,$TKVEC :: INITIALIZE THE KEYBOARD VECTOR
MOV #200,$TKVEC+2 :: "BR" LEVEL 4
TST $TKB :: CLEAR DONE FLAG
MOV #100,$TKS :: ENABLE TTY KEYBOARD INTERRUPT
RTS PC :: RETURN TO CALLER

:*TK SERVICE ROUTINE
:*THIS ROUTINE WILL SERVICE THE TTY KEYBOARD INTERRUPT
:*BY READING THE CHARACTER FROM THE INPUT BUFFER AND PUTTING
:*IT IN THE QUEUE.
:*IF THE CHARACTER IS A "CONTROL-C" (^C) $TKINT IS CALLED AND
:*UPON RETURN EXIT IS MADE TO THE "CONTROL-C" RESTART ADDRESS (START2)
$TKSRV: MOVB $TKB,-(SP) :: PICKUP THE CHARACTER
BIC #^C177,(SP) :: STRIP THE JUNK

```

TTY INPUT ROUTINE

```

44390 020476 021627 000003      CMP      (SP),#3      :: IS IT A CONTROL C?
44391 020502 001007      BNE      1$          :: BRANCH IF NO
44392 020504 104401 021604      TYPE     $CNTLC      :: TYPE A CONTROL-C (1C)
44393 020510 004737 020416      JSR      PC,STKINT   :: INIT THE KEYBOARD
44394 020514 005726      TST      (SP)+       :: CLEAN UP STACK
44395 020516 000137 003676      JMP      START2      :: CONTROL C RESTART
44396 020520 021627 000007      CMP      (SP),#7     :: IS IT A CONTROL G?
44397 020526 001004      BNE      2$          :: BRANCH IF NO
44398 020530 022737 000176 001140      CMP      $SWREG,SWR  :: IS SOFT-SWR SELECTED?
44399 020536 001500      BEQ      6$          :: GO TO SWR CHANGE
44400
44401 020540      2$:
44402 020540 022737 000012 020376      CMP      #10, $STKCNT :: IS THE QUEUE FULL?
44403 020546 001004      BNE      3$          :: BRANCH IF NO
44404 020550 104401 001166      TYPE     $BELL       :: RING THE TTY BELL
44405 020554 005726      TST      (SP)+       :: CLEAN CHARACTER OFF OF STACK
44406 020556 000451      BR      5$          :: EXIT
44407 020560 021627 000023      CMP      (SP),#23    :: IS IT A CONTROL-S?
44408 020564 001021      BNE      32$         :: BRANCH IF NO
44409 020566 005077 160352      CLR      $STKS       :: DISABLE TTY KEYBOARD INTERRUPTS
44410 020572 005726      TST      (SP)+       :: CLEAN CHAR OFF STACK
44411 020574 105777 160344      TSTB    $STKS       :: WAIT FOR A CHAR
44412 020600 100375      BPL      31$        :: LOOP UNTIL ITS THERE
44413 020602 117746 160340      MOVB    $STKB, -(SP) :: GET THE CHARACTER
44414 020606 042716 177600      BIC     #177, (SP)   :: MAKE IT 7-BIT ASCII
44415 020612 022627 000021      CMP     (SP)+, #21   :: IS IT A CONTROL-Q?
44416 020616 001366      BNE     31$         :: BRANCH IF NO
44417 020620 012777 000100 160316      MOV     #100, $STKS  :: REENABLE TTY KEYBOARD INTERRUPTS
44418 020626 000002      RTI
44419 020630 005237 020376      32$:
44420 020634 021627 000140      INC     $STKCNT     :: COUNT THIS CHARACTER
44421 020640 002405      CMP     (SP), #140  :: IS IT UPPER CASE?
44422 020642 021627 000175      BLT    4$          :: BRANCH IF YES
44423 020646 003002      CMP     (SP), #175  :: IS IT A SPECIAL CHAR?
44424 020650 042716 000040      BGT    4$          :: BRANCH IF YES
44425 020654 112677 177520      BIC     #40, (SP)   :: MAKE IT UPPER CASE
44426 020660 005237 020400      MOVB   (SP)+, $STKQIN :: AND PUT IT IN QUEUE
44427 020664 023727 020400 020416      INC     $STKQIN     :: UPDATE THE POINTER
44428 020672 001003      CMP     $STKQIN, $STKQEND :: GO OFF THE END?
44429 020674 012737 020404 020400      BNE     5$          :: BRANCH IF NO
44430 020702 000002      MOV     $STKQSR, $STKQIN :: RESET THE POINTER
44431
44432
44433
44434
44435
44436
44437 020704 022737 000176 001140 $CKSWR:
44438 020712 001124      CMP     $SWREG, SWR  :: IS THE SOFT-SWR SELECTED
44439 020714 105777 160224      BNE     15$         :: EXIT IF NOT
44440 020720 100121      TSTB   $STKS       :: IS A CHAR WAITING?
44441 020722 117746 160220      BPL     15$         :: IF NOT, EXIT
44442 020726 042716 177600      MOVB   $STKB, -(SP) :: YES
44443 020732 021627 000007      BIC     #177, (SP)   :: MAKE IT 7-BIT ASCII
44444 020736 001300      CMP     (SP), #7     :: IS IT A CONTROL-G?
44445
44446
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```

```

*****
*SOFTWARE SWITCH REGISTER CHANGE ROUTINE.
*ROUTINE IS ENTERED FROM THE TRAP HANDLER, AND WILL
*SERVICE THE TEST FOR CHANGE IN SOFTWARE SWITCH REGISTER TRAP
*CALL WHEN OPERATING IN TTY INTERRUPT MODE.

```

```

*****
*CONTROL IS PASSED TO THIS POINT FROM EITHER THE TTY INTERRUPT SERVICE
*ROUTINE OR FROM THE SOFTWARE SWITCH REGISTER TRAP CALL AS A RESULT OF A
*CONTROL-G BEING TYPED, AND THE SOFTWARE SWITCH REGISTER BEING SELECTED.
*****
4446 020740 123727 001134 000001 6$: CMPB $AUTOB,#1 ;; ARE WE RUNNING IN AUTO-MODE?
4447 020746 001674 BEQ 2$ ;; BRANCH IF YES
4448 020750 005726 TST (SP)+ ;; CLEAR CONTROL-G OFF STACK
4449 020752 004737 020416 JSR PC,$TKINT ;; FLUSH THE TTY INPUT QUEUE
4450 020756 005077 160162 CLR $TKS ;; DISABLE TTY KEYBOARD INTERRUPTS
4451 020762 112737 000001 001135 MOVB #1,$INTAG ;; SET INTERRUPT MODE INDICATOR
4452 020770 104401 021616 SGTSWR: TYPE , $CNTLG ;; ECHO THE CONTROL-G (IG)
4453 020774 104401 021623 TYPE $MSWR ;; TYPE CURRENT CONTENTS
4454 021000 013746 000176 MOV $WREG,-(SP) ;; SAVE SWREG FOR TYPEOUT
4455 021004 104402 TYP0C ;; GO TYPE--OCTAL ASCII(ALL DIGITS)
4456 021006 104401 021634 TYPE , $MNEW ;; PROMPT FOR NEW SWR
4457 021012 005046 19$: CLR -(SP) ;; CLEAR COUNTER
4458 021014 005046 CLR -(SP) ;; THE NEW SWR
4459 021016 105777 160122 7$: TSTB $TKS ;; CHAR THERE?
4460 021022 100375 BPL 7$ ;; IF NOT TRY AGAIN
4461 021024 117746 160116 MOVB $TKB,-(SP) ;; PICK UP CHAR
4462 021030 042716 177600 BIC #1C177,(SP) ;; MAKE IT 7-BIT ASCII
4463 021034 021627 000003 CMP (SP),#3 ;; IS IT A CONTROL-C?
4464 021040 001015 BNE 9$ ;; BRANCH IF NOT
4465 021042 104401 021604 TYPE , $CNTLC ;; YES, ECHO CONTROL-C (IC)
4466 021046 062706 000006 ADD #6,SP ;; CLEAN UP STACK
4467 021052 123727 001135 000001 CMPB $INTAG,#1 ;; REENABLE TTY KEYBOARD INTERRUPTS?
4468 021050 001003 BNE 8$ ;; BRANCH IF NO
4469 021062 012777 000100 160054 MOV #100,$TKS ;; ALLOW TTY KEYBOARD INTERRUPTS
4470 021070 000137 003676 8$: JMP START2 ;; CONTROL-C RESTART
4471 021074 021627 000025 9$: CMP (SP),#25 ;; IS IT A CONTROL-U?
4472 021100 001005 BNE 10$ ;; BRANCH IF NOT
4473 021102 104401 021611 TYPE , $CNTLU ;; YES, ECHO CONTROL-U (IU)
4474 021106 062706 000006 20$: ADD #6,SP ;; IGNORE PREVIOUS INPUT
4475 021112 000737 BR 19$ ;; LET'S TRY IT AGAIN
4476 021114 021627 000015 10$: CMP (SP),#15 ;; IS IT A <CR>?
4477 021120 001022 BNE 16$ ;; BRANCH IF NO
4478 021122 005766 000004 TST 4(SP) ;; YES, IS IT THE FIRST CHAR?
4479 021126 001403 BEQ 11$ ;; BRANCH IF YES
4480 021130 016677 000002 160002 MOV 2(SP),$SWR ;; SAVE NEW SWR
4481 021136 062706 000006 11$: ADD #6,SP ;; CLEAN UP STACK
4482 021142 104401 001173 14$: TYPE , $CRLF ;; ECHO <CR> AND <LF>
4483 021146 123727 001135 000001 CMPB $INTAG,#1 ;; RE-ENABLE TTY KBD INTERRUPTS?
4484 021154 001003 BNE 15$ ;; BRANCH IF NOT
4485 021156 012777 000100 157760 MOV #100,$TKS ;; RE-ENABLE TTY KBD INTERRUPTS
4486 021164 000002 15$: RTI ;; RETURN
4487 021166 004737 017654 16$: JSR PC,$TYPEC ;; ECHO CHAR
4488 021172 021627 000060 CMP (SP),#60 ;; CHAR < 0?
4489 021176 002420 BLT 18$ ;; BRANCH IF YES

```

TTY INPUT ROUTINE

4502 021200 021627 000067
 4503 021204 003015
 4504 021206 042726 000060
 4505 021212 005766 000002
 4506 021216 001403
 4507 021220 006316
 4508 021222 006316
 4509 021224 006316
 4510 021226 005266 000002
 4511 021232 056616 177776
 4512 021236 000667
 4513 021240 104401 001172
 4514 021244 000720

CMP (SP), #67
 BGT 18\$
 BIC #60, (SP)+
 TST 2(SP)
 BEQ 17\$
 ASL (SP)
 ASL (SP)
 ASL (SP)
 17\$: INC 2(SP)
 BR -2(SP), (SP)
 18\$: TYPE \$QUES
 BR 20\$
 .DSABL LSB

:: CHAR > ??
 :: BRANCH IF YES
 :: STRIP-OFF ASCII
 :: IS THIS THE FIRST CHAR
 :: BRANCH IF YES
 :: NO, SHIFT PRESENT
 :: CHAR OVER TO MAKE
 :: ROOM FOR NEW ONE.
 :: KEEP COUNT OF CHAR
 :: SET IN NEW CHAR
 :: GET THE NEXT ONE
 :: TYPE ?<CR><LF>
 :: SIMULATE CONTROL-U

 *THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY
 *CALL:
 * RDCHR :: GET A CHARACTER FROM THE QUEUE
 * RETURN HERE :: CHARACTER IS ON THE STACK
 * :: WITH PARITY BIT STRIPPED OFF

4526 021246 011646
 4527 021250 016666 000004 000002
 4528 021256 005066 000004
 4529 021262 005046
 4530 021264 012746 021272
 4531 021270 000002
 4532 021272
 4533 021272 005737 020376
 4534 021276 001775
 4535 021300 005337 020376
 4536 021304 117766 177072 000004
 4537 021312 005237 020402
 4538 021316 023727 020402 020416
 4539 021324 001003
 4540 021326 012737 020404 020402
 4541 021334 000002

\$RDCHR: MOV (SP), -(SP)
 MOV 4(SP), 2(SP)
 CLR 4(SP)
 CLR -(SP)
 MOV #64\$, -(SP)
 RTI
 64\$:
 1\$: TST \$TKCNT
 BEQ 1\$
 DEC \$TKCNT
 MOV \$TKQOUT, 4(SP)
 INC \$TKQOUT
 CMP \$TKQOUT, #STKQEND
 BNE 2\$
 MOV #STKQRT, \$TKQOUT
 RTI
 2\$:

:: PUSH DOWN THE PC AND
 :: THE PS
 :: GET READY FOR A CHARACTER
 :: PUT NEW PS ON STACK
 :: PUT NEW PC ON STACK
 :: POP NEW PC AND PS
 :: WAIT ON A CHARACTER
 :: DECREMENT THE COUNTER
 :: GET ONE CHARACTER
 :: UPDATE THE POINTER
 :: DID IT GO OFF OF THE END?
 :: BRANCH IF NO
 :: RESET THE POINTER
 :: RETURN

 *THIS ROUTINE WILL INPUT A STRING FROM THE TTY
 *CALL:
 * RDLIN :: INPUT A STRING FROM THE TTY
 * RETURN HERE :: ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK
 * :: TERMINATOR WILL BE A BYTE OF ALL 0'S

4549 021336 010346
 4550 021340 005046
 4551 021342 012703 021572
 4552 021346 022703 021604
 4553 021352 101456
 4554 021354 104410
 4555 021356 112613
 4556 021360 122713 000177
 4557 021364 001022

\$RDLIN: MOV R3, -(SP)
 CLR -(SP)
 1\$: MOV #STTYIN, R3
 2\$: CMP #STTYIN+10., R3
 BLOS 4\$
 RDCHR
 MOV (SP)+, (R3)
 10\$: CMPB #177, (R3)
 BNE 5\$

:: SAVE R3
 :: CLEAR THE RUBOUT KEY
 :: GET ADDRESS
 :: BUFFER FULL?
 :: BR IF YES
 :: GO READ ONE CHARACTER FROM THE TTY
 :: GET CHARACTER
 :: IS IT A RUBOUT
 :: BR IF NO

TTY INPUT ROUTINE

```

4558 021366 005716          TST      (SP)          ;; IS THIS THE FIRST RUBOUT?
4559 021370 001007          BNE     6$           ;; BR IF NO
4560 021372 112737 000134 021570  MOVB    #' \, 9$     ;; TYPE A BACK SLASH
4561 021400 104401 021570     TYPE    9$
4562 021404 012716 177777     MOV     4-1, (SP)    ;; SET THE RUBOUT KEY
4563 021410 005303          DEC     R3           ;; BACKUP BY ONE
4564 021412 020327 021572     CMP     R3, #STTYIN ;; STACK EMPTY?
4565 021416 103434          BLO    4$           ;; BR IF YES
4566 021420 111337 021570     MOVB   (R3), 9$     ;; SETUP TO TYPEOUT THE DELETED CHAR.
4567 021424 104401 021570     TYPE    9$         ;; GO TYPE
4568 021430 000746          BR     2$           ;; GO READ ANOTHER CHAR.
4569 021432 005716          TST    (SP)         ;; RUBOUT KEY SET?
4570 021434 001406          BEQ    7$           ;; BR IF NO
4571 021436 112737 000134 021570  MOVB    #' \, 9$     ;; TYPE A BACK SLASH
4572 021444 104401 021570     TYPE    9$
4573 021450 005016          CLR    (SP)        ;; CLEAR THE RUBOUT KEY
4574 021452 122713 000025 7$:    CMPB   #25, (R3)    ;; IS CHARACTER A CTRL U?
4575 021456 001003          BNE    8$           ;; BR IF NO
4576 021460 104401 021611     TYPE    $CNTLU     ;; TYPE A CONTROL "U"
4577 021464 000726          BR     1$           ;; GO START OVER
4578 021466 122713 000022 8$:    CMPB   #22, (R3)    ;; IS CHARACTER A "↑R"?
4579 021472 001011          BNE    3$           ;; BRANCH IF NO
4580 021474 105013          CLRB   (R3)        ;; CLEAR THE CHARACTER
4581 021476 104401 001173     TYPE    $SCRLF     ;; TYPE A "CR" & "LF"
4582 021502 104401 021572     TYPE    $STTYIN    ;; TYPE THE INPUT STRING
4583 021506 000717          BR     2$           ;; GO PICKUP ANOTHER CHARACTER
4584 021510 104401 001172 4$:    TYPE    $QUES     ;; TYPE A '?'
4585 021514 000712          BR     1$           ;; CLEAR THE BUFFER AND LOOP
4586 021516 111337 021570 3$:    MOVB   (R3), 9$     ;; ECHO THE CHARACTER
4587 021522 104401 021570     TYPE    9$
4588 021526 122723 000015     CMPB   #15, (R3)+  ;; CHECK FOR RETURN
4589 021532 001305          BNE    2$           ;; LOOP IF NOT RETURN
4590 021534 105063 177777     CLRB   -1(R3)      ;; CLEAR RETURN (THE 15)
4591 021540 104401 001174     TYPE    $LF        ;; TYPE A LINE FEED
4592 021544 005726          TST    (SP)+       ;; CLEAN RUBOUT KEY FROM THE STACK
4593 021546 012603          MOV    (SP)+, R3   ;; RESTORE R3
4594 021550 011646          MOV    (SP)-, (SP) ;; ADJUST THE STACK AND PUT ADDRESS OF THE
4595 021552 016666 000004 000002  MOV    4(SP), 2(SP) ;; FIRST ASCII CHARACTER ON IT
4596 021560 012766 021572 000004  MOV    #STTYIN, 4(SP)
4597 021566 000002          RTI
4598 021570 000          9$:    .BYTE  0           ;; RETURN
4599 021571 000          .BYTE  0           ;; STORAGE FOR ASCII CHAR. TO TYPE
4600 021572 000012          $TTYIN: .BLKB 10.  ;; TERMINATOR
4601 021604 041536 005015 000     $CNTLC: .ASCIZ /↑C/<15><12> ;; RESERVE 10. BYTES FOR TTY INPUT
4602 021611 136 006525 000012  $CNTLU: .ASCIZ /↑U/<15><12> ;; CONTROL "C"
4603 021616 043536 005015 000     $CNTLG: .ASCIZ /↑G/<15><12> ;; CONTROL "U"
4604 021623 015 051412 051127  $MSWR:  .ASCIZ <15><12>/SWR = / ;; CONTROL "G"
4605 021630 036440 000040          $MNEW:  .ASCIZ / NEW = /
4606 021634 020040 042516 020127
4607 021642 020075 000          .EVEN
4608 021646
4609
4610          .SBTTL SCOPE HANDLER ROUTINE
4611
4612          ;; *****
4613          ;; *THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT

```

```

4614      ;*AND LOAD THE TEST NUMBER($TSTNM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
4615      ;*AND LOAD THE ERROR FLAG ($ERFLG) INTO DISPLAY<15:08>
4616      ;*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
4617      ;*SW14=1      LOOP ON TEST
4618      ;*SW11=1      INHIBIT ITERATIONS
4619      ;*SW09=1      LOOP ON ERROR
4620      ;*CALL
4621      ;*      SCOPE      ;;SCOPE=IOT
4622
4623      $SCOPE:
4624      021646      104407      CKSWR      ;: TEST FOR CHANGE IN SOFT-SWR
4625      021650      032777      040000      157262      1$:      BIT      @BIT14,@SWR      ;: LOOP ON PRESENT TEST?
4626      021656      001101      BNE      $OVER      ;: YES IF SW14=1
4627      ;*****START OF CODE FOR THE XOR TESTER*****
4628      021660      000416      $XTSTR: BR      6$      ;: IF RUNNING ON THE "XOR" TESTER CHANGE
4629      ;: THIS INSTRUCTION TO A "NOP" (NOP=240)
4630      021662      013746      000004      MOV      @ERRVEC, -(SP)      ;: SAVE THE CONTENTS OF THE ERROR VECTOR
4631      021666      012737      021706      000004      MOV      @SS,@ERRVEC      ;: SET FOR TIMEOUT
4632      021674      005737      177060      TST      @177060      ;: TIME OUT ON XOR?
4633      021700      012637      000004      MOV      (SP)+,@ERRVEC      ;: RESTORE THE ERROR VECTOR
4634      021704      000453      BR      $SVLAD      ;: GO TO THE NEXT TEST
4635      021706      022626      5$:      CMP      (SP)+,(SP)+      ;: CLEAR THE STACK AFTER A TIME OUT
4636      021710      012637      000004      MOV      (SP)+,@ERRVEC      ;: RESTORE THE ERROR VECTOR
4637      021714      000413      BR      7$      ;: LOOP ON THE PRESENT TEST
4638      6$: ;*****END OF CODE FOR THE XOR TESTER*****
4639      021716      105737      001103      2$:      TSTB      $ERFLG      ;: HAS AN ERROR OCCURRED?
4640      021722      001421      BEQ      3$      ;: BR IF NO
4641      021724      123737      001115      001103      CMPB      $ERMAX,$ERFLG      ;: MAX. ERRORS FOR THIS TEST OCCURRED?
4642      021732      101015      BHI      3$      ;: BR IF NO
4643      021734      032777      001000      157176      BIT      @BIT09,@SWR      ;: LOOP ON ERROR?
4644      021742      001404      BEQ      4$      ;: BR IF NO
4645      021744      013737      001110      001106      7$:      MOV      $LPERR,$LPADR      ;: SET LOOP ADDRESS TO LAST SCOPE
4646      021752      000443      BR      $OVER
4647      021754      105037      001103      4$:      CLRB      $ERFLG      ;: ZERO THE ERROR FLAG
4648      021760      005037      001162      CLR      $TIMES      ;: CLEAR THE NUMBER OF ITERATIONS TO MAKE
4649      021764      000415      BR      1$      ;: ESCAPE TO THE NEXT TEST
4650      021766      032777      004000      157144      3$:      BIT      @BIT11,@SWR      ;: INHIBIT ITERATIONS?
4651      021774      001011      BNE      1$      ;: BR IF YES
4652      021776      005737      001100      TST      $PASS      ;: IF FIRST PASS OF PROGRAM
4653      022002      001406      BEQ      1$      ;: INHIBIT ITERATIONS
4654      022004      005237      001104      INC      $ICNT      ;: INCREMENT ITERATION COUNT
4655      022010      023737      001162      001104      CMP      $TIMES,$ICNT      ;: CHECK THE NUMBER OF ITERATIONS MADE
4656      022016      002021      BGE      $OVER      ;: BR IF MORE ITERATION REQUIRED
4657      022020      012737      000001      001104      1$:      MOV      #1,$ICNT      ;: REINITIALIZE THE ITERATION COUNTER
4658      022026      013737      022076      001162      MOV      $MXCNT,$TIMES      ;: SET NUMBER OF ITERATIONS TO DO
4659      022034      105237      001102      $SVLAD: INCB      $TSTNM      ;: COUNT TEST NUMBERS
4660      022040      011637      001106      MOV      (SP),$LPADR      ;: SAVE SCOPE LOOP ADDRESS
4661      022044      011637      001110      MOV      (SP),$LPERR      ;: SAVE ERROR LOOP ADDRESS
4662      022050      005037      001164      CLR      $ESCAPE      ;: CLEAR THE ESCAPE FROM ERROR ADDRESS
4663      022054      112737      000001      001115      MOVB      #1,$ERMAX      ;: ONLY ALLOW ONE(1) ERROR ON NEXT TEST
4664      022062      013777      001102      157052      $OVER:  MOV      $TSTNM,@DISPLAY      ;: DISPLAY TEST NUMBER
4665      022070      013716      001106      MOV      $LPADR,(SP)      ;: FUDGE RETURN ADDRESS
4666      022074      000002      RTI      ;: FIXES PS
4667      022076      000001      $MXCNT: 1      ;: MAX. NUMBER OF ITERATIONS
4668
4669      .SBTTL SAVE AND RESTORE R0-R5 ROUTINES

```

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4682
4683
4684
4685
4686 022100
4687 022100 010046
4688 022102 010146
4689 022104 010246
4690 022106 010346
4691 022110 010446
4692 022112 010546
4693 022114 016646 000022
4694 022120 016646 000022
4695 022124 016646 000022
4696 022130 016646 000022
4697 022134 000002
4698
4699
4700
4701
4702 022136
4703 022136 012666 000022
4704 022142 012666 000022
4705 022146 012666 000022
4706 022152 012666 000022
4707 022156 012605
4708 022160 012604
4709 022162 012603
4710 022164 012602
4711 022166 012601
4712 022170 012600
4713 022172 000002
4714
4715
4716
4717
4718
4719
4720
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4722
4723
4724
4725

```
*****  
:SAVE RO-R5  
:CALL:  
: SAVREG  
:UPON RETURN FROM $$SAVREG THE STACK WILL LOOK LIKE:  
:  
:TOP---(+16)  
: +2---(+18)  
: +4---R5  
: +6---R4  
: +8---R3  
: +10---R2  
: +12---R1  
: +14---R0
```

```
$$SAVREG:  
MOV RO,-(SP) ;: PUSH RO ON STACK  
MOV R1,-(SP) ;: PUSH R1 ON STACK  
MOV R2,-(SP) ;: PUSH R2 ON STACK  
MOV R3,-(SP) ;: PUSH R3 ON STACK  
MOV R4,-(SP) ;: PUSH R4 ON STACK  
MOV R5,-(SP) ;: PUSH R5 ON STACK  
MOV 22(SP),-(SP) ;: SAVE PS OF MAIN FLOW  
MOV 22(SP),-(SP) ;: SAVE PC OF MAIN FLOW  
MOV 22(SP),-(SP) ;: SAVE PS OF CALL  
MOV 22(SP),-(SP) ;: SAVE PC OF CALL  
RTI
```

```
:RESTORE RO-R5  
:CALL:  
: RESREG  
$RESREG:  
MOV (SP)+,22(SP) ;: RESTORE PC OF CALL  
MOV (SP)+,22(SP) ;: RESTORE PS OF CALL  
MOV (SP)+,22(SP) ;: RESTORE PC OF MAIN FLOW  
MOV (SP)+,22(SP) ;: RESTORE PS OF MAIN FLOW  
MOV (SP)+,R5 ;: POP STACK INTO R5  
MOV (SP)+,R4 ;: POP STACK INTO R4  
MOV (SP)+,R3 ;: POP STACK INTO R3  
MOV (SP)+,R2 ;: POP STACK INTO R2  
MOV (SP)+,R1 ;: POP STACK INTO R1  
MOV (SP)+,R0 ;: POP STACK INTO R0  
RTI
```

.SBTTL ROUTINE TO SIZE MEMORY

```
*****  
:CALL:  
: JSR PC,$SIZE  
: RETURN  
: $LSTAD WILL CONTAIN:  
: WITH KT11 OPTION -- LAST VIRTUAL ADDRESS OF THE LAST BANK  
: WITHOUT KT11 OPTION -- LAST ABSOLUTE ADDRESS OF AVAILABLE MEMORY  
: $LSTBK WILL CONTAIN THE LAST BANK AS A SAF  
: $KT11 IS THE MEMORY MANAGEMENT KEY
```

NO7

```

4726 ;#BIT07 = 0 DON'T USE MEMORY MANAGEMENT
4727 ;# MUST BE SETUP BEFORE THE CALL
4728 ;#BIT15 = 0 DON'T HAVE MEMORY MANAGEMENT OPTION
4729 ;# DETERMINED BY ROUTINE
4730
4731 022174 010046 $SIZE: MOV R0,-(SP) ;;SAVE R0 ON THE STACK
4732 022176 010146 MOV R1,-(SP) ;;SAVE R1 ON THE STACK
4733 022200 010246 MOV R2,-(SP) ;;SAVE R2 ON THE STACK
4734 022202 010346 MOV R3,-(SP) ;;SAVE R3 ON THE STACK
4735 022204 013746 000004 MOV @#ERRVEC,-(SP) ;;SAVE PRESENT ERROR VECTOR PS & PC
4736 022210 013746 000006 MOV @#ERRVEC+2,-(SP)
4737 022214 010600 MOV SP,R0 ;;SAVE THE STACK POINTER
4738 ;;SET THE ERRVEC PS TO THE PRESENT PS
4739 022216 104400 TRAP ;;PUSH OLD PSW AND PC ON STACK
4740 022220 012637 000006 MOV (SP)+,@#ERRVEC+2 ;;SAVE THE PSW IN @#ERRVEC+2
4741 022224 012701 003776 MOV @3776,R1 ;;SETUP ADDRESS
4742 022230 105727 TSTB (PC)+ ;;USE MEMORY MANAGEMENT?
4743 022232 000200 SKT11: WORD 200 ;;SET TO USE MEMORY MANAGEMENT
4744 022234 100062 BPL $SCORE ;;BR IF NO
4745 022236 012737 022374 000004 MOV @SKTNEX,@#ERRVEC ;;SET FOR TIMEOUT
4746 022244 005737 177572 TST @#SRO ;;KT11 ARE YOU THERE?
4747 022250 052737 100000 022232 BIS @100000,SKT11 ;;YES--SET KT11 KEY
4748 022256 005046 CLR -(SP) ;;INITIALIZE FOR "PAR" LOADING
4749 022260 012702 172340 MOV @KIPAR0,R2 ;;ADDRESS OF FIRST "PAR"
4750 022264 012703 000010 MOV @108,R3 ;;LOAD EIGHT "PAR.'S" AND EIGHT "PDR.'S"
4751 022270 012762 077406 177740 1$: MOV @77406,-40(R2) ;;PDR = 4K UP, READ/WRITE
4752 022276 011622 MOV (SP),(R2)+ ;;LOAD "PAR"
4753 022300 062716 000200 ADD @200,(SP) ;;UPDATE FOR NEXT "PAR"
4754 022304 077307 SOB R3,1$ ;;LOOP UNTIL ALL EIGHT ARE LOADED.
4755 022306 012742 177600 MOV @177600,-(R2) ;;SETUP KIPAR7 FOR I/O
4756 022312 005042 CLR -(R2) ;;SETUP KIPAR6 FOR TESTING
4757 022314 012737 022332 000004 MOV @2$,@#ERRVEC ;;CATCH TIMEOUT IF NO SR3
4758 022322 012737 000020 172516 MOV @20,@#SR3 ;;ENABLE 22 BIT MODE
4759 022330 000401 BR 3$ ;;THIS PDP-11 HAS A SR3 REGISTER
4760 022332 022626 2$: CMP (SP)+,(SP)+ ;;CLEAN OFF THE STACK--NO SR3
4761 022334 005237 177572 3$: INC @#SRO ;;TURN ON MEMORY MANAGEMENT
4762 022340 012737 022364 000004 MOV @SKTOUT,@#ERRVEC ;;SET FOR TIME OUT
4763 022346 005737 143776 4$: TST @143776 ;;TRAP ON NON-EX-MEM
4764 022352 062712 000040 ADD @40,(R2) ;;MAKE A 1K STEP
4765 022356 023712 172356 CMP @#KIPAR7,(R2) ;;LAST ONE?
4766 022362 101371 BHI 4$ ;;NO--TRY IT
4767 022364 011202 SKTOUT: MOV (R2),R2 ;;GET LAST BANK+1
4768 022366 005037 177572 CLR @#SRO ;;TURN OFF MEMORY MANAGEMENT
4769 022372 000421 BR $SIZEX
4770 022374 042737 100000 022232 SKTNEX: BIC @100000,SKT11 ;;KT11 NON-EXISTENT
4771 022402 012737 022432 000004 SCORE: MOV @SCROUT,@#ERRVEC ;;SET FOR TIMEOUT
4772 022410 005002 CLR R2 ;;SET UP BANK
4773 022412 062701 004000 1$: ADD @4000,R1 ;;INCREMENT BY 1K
4774 022416 062702 000040 ADD @40,R2 ;;1K STEP
4775 022422 005711 TST (R1) ;;TRAP ON TIME OUT
4776 022424 022701 177776 CMP @177776,R1 ;;LAST ONE
4777 022430 001370 BNE 1$ ;;NO--TRY AGAIN
4778 022432 162701 004000 SKROUT: SUB @4000,R1 ;;DROP BACK
4779 022436 162702 000040 $SIZEX: SUB @40,R2 ;;RESTORE THE STACK
4780 022442 010006 MOV R0,SP ;;RESTORE THE STACK
4781 022444 012637 000006 MOV (SP)+,@#ERRVEC+2 ;;RESTORE ERROR VECTOR

```

4782 022450 012637 000004
 4783 022454 010137 022476
 4784 022460 010237 022500
 4785 022464 012603
 4786 022470 012602
 4787 022470 012601
 4788 022472 012600
 4789 022474 000207
 4790 022476 000000
 4791 022500 000000

```

MOV (SP)+,R0,BERRVEC
MOV R1,SLSTAD      ;; LAST ADDRESS
MOV R2,SLSTBK     ;; LAST BANK
MOV (SP)+,R3      ;; RESTORE R3
MOV (SP)+,R2      ;; RESTORE R2
MOV (SP)+,R1      ;; RESTORE R1
MOV (SP)+,R0      ;; RESTORE R0
RTS PC
SLSTAD: .WORD 0    ;; CONTAINS THE LAST ADDRESS
SLSTBK: .WORD 0    ;; CONTAINS THE LAST BANK

```

.SBTTL TRAP DECODER

```

*****
*THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
*AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
*OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL
*GO TO THAT ROUTINE.

```

4801 022502 010046
 4802 022504 016600 000002
 4803 022510 005740
 4804 022512 111000
 4805 022514 006300
 4806 022516 016000 022536
 4807 022522 000200

```

STRAP: MOV R0,-(SP)    ;; SAVE R0
MOV 2(SP),R0          ;; GET TRAP ADDRESS
TST -(R0)             ;; BACKUP BY 2
MOVB (R0),R0         ;; GET RIGHT BYTE OF TRAP
ASL R0                ;; POSITION FOR INDEXING
MOV STRPAD(R0),R0    ;; INDEX TO TABLE
RTS R0                ;; GO TO ROUTINE

```

;; THIS IS USE TO HANDLE THE "GETPRI" MACRO

4811 022524 011646
 4812 022526 016666 000004 000002
 4813 022526 016666
 4814 022534 000002

```

STRAP2: MOV (SP)-,(SP)  ;; MOVE THE PC DOWN
MOV 4(SP),2(SP)       ;; MOVE THE PSW DOWN
RTI                   ;; RESTORE THE PSW

```

.SBTTL TRAP TABLE

```

*THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
*BY THE "TRAP" INSTRUCTION.

```

4821
 4822
 4823 022536 022524
 4824 022540 017504
 4825 022542 017750
 4826 022544 017724
 4827 022546 017764
 4828 022550 020152
 4829
 4830 022552 020774
 4831
 4832 022554 020704
 4833 022556 021246
 4834 022560 021336
 4835 022562 022100
 4836 022564 022136
 4837

```

: ROUTINE
: -----
STRPAD: .WORD STRAP2
$TYPE    ;; CALL=TYPE TRAP+1(104401) TTY TYPEOUT ROUTINE
$TYPOC   ;; CALL=TYPOC TRAP+2(104402) TYPE OCTAL NUMBER (WITH LEADING ZEROS)
$TYPOS   ;; CALL=TYPOS TRAP+3(104403) TYPE OCTAL NUMBER (NO LEADING ZEROS)
$TYPON   ;; CALL=TYPON TRAP+4(104404) TYPE OCTAL NUMBER (AS PER LAST CALL)
$TYPDS   ;; CALL=TYPDS TRAP+5(104405) TYPE DECIMAL NUMBER (WITH SIGN)

$GTSWR   ;; CALL=GTSWR TRAP+6(104406) GET SOFT-SWR SETTING

$CKSWR   ;; CALL=CKSWR TRAP+7(104407) TEST FOR CHANGE IN SOFT-SWR
$RDCHR   ;; CALL=RDCHR TRAP+10(104410) TTY TYPEIN CHARACTER ROUTINE
$RDLIN   ;; CALL=RDLIN TRAP+11(104411) TTY TYPEIN STRING ROUTINE
$SAVREG  ;; CALL=SAVREG TRAP+12(104412) SAVE R0-R5 ROUTINE
$RESREG  ;; CALL=RESREG TRAP+13(104413) RESTORE R0-R5 ROUTINE

```

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4854
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4888
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4892
4893

: THIS ROUTINE CLEARS THE RP11 AND DETERMINES WHICH DRIVES ARE AVAILABLE.
: THE TABLE 'DRVSTA' IS LOADED TO REFLECT THE SYSTEM STATUS.

```

RPINIT: SAVREG          ;SAVE R0-R5
        MOV             RPVEC,R1 ;VECTOR ADDRESS
        CLR             (R1)+    ;SET INTERRUPT ADDRESS TO ZERO
        MOV             RPPRIO,(R1) ;RP11 PRIORITY
        CLR             DRVSTYP ;CLEAR DRIVE TYPE STORAGE
        CLR             DRVSTA  ;SET DRIVE STATUS TO OFFLINE
        CLR             DRVSTA+2 ;SET DRIVE STATUS TO OFFLINE
        CLR             DRVSTA+4 ;SET DRIVE STATUS TO OFFLINE
        CLR             DRVSTA+6 ;SET DRIVE STATUS TO OFFLINE
        MOV             RPAOR,R4 ;PUT RP11 ADDRESS INTO R4
        MOV             @CLEAR,RPCS(R4) ;CLEAR THE RP11
        MOV             @CLEAR,RPCS(R4) ;CLEAR THE RP11 AGAIN
        MOV             @7,R1   ;'DRVSTA' TABLE INDEX
1$:     JSR             PC,DRVINT ;CHECK THE DRIVE'S STATUS
        DEC             R1      ;DECREMENT THE TABLE INDEX
        BPL            1$      ;BR IF NOT FINISHED
        RESREG          ;RESTORE R0-R5
        RTS             PC     ;RETURN
  
```

: ROUTINE TO CHECK THE DRIVE'S STATUS. DRIVE NUMBER MUST BE IN R1

```

DRVINT: MOVB           R1,RPCS+1(R4) ;SELECT DRIVE
        BIT             @SUFU,RPDS(R4) ;SEE IF DRIVE UNSAFE
        BEQ            1$         ;BR IF NOT UNSAFE
        MOVB           @-1,DRVSTA(R1) ;SET INDICATOR TO 'UNSAFE'
        BR             3$         ;EXIT
1$:     BIT             @SUOL,RPDS(R4) ;IS DRIVE ONLINE ?
        BEQ            4$         ;BR IF NOT
        BIT             @SUSI,RPDS(R4) ;SEEK INCOMPLETE SET ?
        BNE            2$         ;BR IF SET
        BIT             @SURDY,RPDS(R4) ;IS DRIVE READY ?
        BEQ            4$         ;BR IF NOT
2$:     MOVB           @1,DRVSTA(R1) ;SET DRIVE INDICATOR TO 'ONLINE'
3$:     BIT             @SURPO3,RPDS(R4) ;IS THE DRIVE AN RPO3 ?
        BEQ            4$         ;BR IF NOT
4$:     BISB           @TABIT(R1),DRVSTYP ;SET RPO3 INDICATOR
        RTS             PC     ;RETURN
  
```

: DO THE REQUESTED COMMAND. THE RP11 ADDRESS MUST BE IN R4

```

RP11:  SAVREG          ;SAVE R0 - R5
        MOVB           DPB,RPCS+1(R4) ;SELECT THE DRIVE
        CLR             R1          ;CLEAR R1
        MOVB           DPB,R1      ;DRIVE ADDRESS
        JSR             PC,CIR     ;SEE IF DRIVE IS READY
        MOV             SWC,-(SP)  ;COMPLEMENT THE WORD COUNT
        NEG             (SP)      ;COMPLEMENT
        MOV             (SP)+,RPWC(R4) ;LOAD THE WORD COUNT REGISTER
        MOV             @BUF,RPBA(R4) ;BUFFER ADDRESS
        MOV             @CYL,RPCA(R4) ;CYLINDER ADDRESS
        MOV             @SEC,RPDA(R4) ;SECTOR/TRACK ADDRESS
        MOV             @24000,2$ ;SET UP FOR...
  
```

```

4894 023054 005337 023060 1S: DEC 2S :...A DELAY OF
4895 023060 000000 2S: 0 :...AT LEAST
4896 023062 001374 :...25 MILLISECONDS.
4897 023064 113764 002500 000005 MOVB DPB,RPCS+1(R4) ;SELECT THE DRIVE
4898 023072 153764 002503 000005 BISB DPB+3,RPCS+1(R4) ;SET 'MODE' & 'HDR' BITS
4899 023100 013746 002502 MOV DPB+2,-(SP) ;COMMAND CODE
4900 023104 153716 002501 BISB DPB+1,(SP) ;'MEX' BITS
4901 023110 112664 000004 MOVB (SP)+,RPCS(R4) ;START THE COMMAND
4902 023114 104413 RESREG ;RESTORE R0 - R5
4903 023116 000207 RTS PC ;RETURN
4904
4905 ;SEE IF DRIVE IS STILL ONLINE
4906
4907
4908 023120 CIR:
4909 023120 012737 023152 001164 MOV #2S,SESCAPE ;:ESCAPE TO 2S ON ERROR
4910 023122 004737 022666 JSR PC,DRVINT ;:CHECK ON THE DRIVE'S STATUS
4911 023124 004737 023240 JSR PC,SAVRP ;:STORE RP11 REGISTERS IN CASE OF ERROR
4912 023126 105761 001370 TSTB DRVSTA(R1) ;:IS THE DRIVE STILL ONLINE ?
4913 023128 003014 BGT 3S ;:BR IF IT IS
4914 023130 002401 BLT 1S ;:BR IF DRIVE IS UNSAFE
4915 023132 104076 ERROR 76 ;:DRIVE HAS GONE OFFLINE
4916 023134 104077 ERROR 77 ;:DRIVE HAS BECOME UNSAFE
4917 023136 032777 001000 155760 2S: BIT #SW09,JSWR ;:LOOP ON THE ERROR ?
4918 023138 001357 CIR ;:BR IF LOOP
4919 023140 156137 001360 001310 BISB ATABIT(R1),DRVBAD ;:SETUP TO DEASSIGN THE DRIVE
4920 023142 000137 016616 JMP #SEOP ;:GO TO THE END OF PASS ROUTINE
4921 023144 005037 001164 3S: CLR $ESCAPE ;:CLEAR THE 'ESCAPE' FLAG
4922 023200 000207 RTS PC ;:RETURN
4923
4924 ;THIS ROUTINE WILL PROVIDE A STALL IN MILLISECONDS FOR A SPECIFIC
4925 ;AMOUNT OF TIME. STALL VALUE IS STORED IN 'STALL'.
4926 ;
4927 ; JSR PC,STALL
4928
4929 STALL: MOV STALL,-(SP) ;:PICKUP THE STALL TIME
4930 023202 013746 001220 CLR -(SP) ;:CLEAR TEMP. LOCATION
4931 023204 005046 SUB #1,2(SP) ;:MORE STALL REQUIRED?
4932 023206 162766 000001 000002 3S: BLO 3S ;:NO--BRANCH
4933 023208 103406 MOV #300.,(SP) ;:STALL FOR ABOUT 1 MILLISECOND
4934 023210 012716 000454 2S: TST R0 ;:NOP TO KILL TIME
4935 023212 005700 DEC (SP) ;:COUNT
4936 023214 005316 BNE 2S ;:LOOP IF MORE COUNTS NEEDED
4937 023216 001375 BR 1S
4938 023218 000766 3S: CMP (SP)+,(SP)+ ;:CLEAN OFF THE STACK
4939 023220 022626 RTS PC ;:EXIT
4940
4941 ;THIS ROUTINE STORES THE RP11E REGISTERS FOR USE BY THE PROGRAM.
4942 ;THE PROGRAM DOES NOT USE THE 'LIVE' REGISTERS FOR ERROR DETERMINATION.
4943 ;CALL
4944 ; JSR PC,SAVRP
4945 ; RETURN
4946
4947 SAVRP: MOV R4,-(SP) ;:SAVE R4
4948 023240 010446 MOV RPADR,R4 ;:RP11 ADDRESS
4949 023242 013704 001324 MOV RPD5(R4),$RPD5 ;:STORE RPD5
4950 023244 016437 000000 001334 MOV RPER(R4),$RPER ;:STORE RPER
4951 023246 016437 000002 001336 MOV RPCS(R4),$RPCS ;:STORE RPCS
4952 023248 016437 000004 001340

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4950 023270 016437 000006 001342
4951 023276 016437 000010 001344
4952 023304 016437 000012 001346
4953 023312 016437 000014 001350
4954 023320 016437 000016 001352
4955 023326 016437 000024 001354
4956 023334 016437 000026 001356
4957 023342 012604
4958 023344 000207
4959
4960
4961
4962 023346 105737 000041
4963 023352 001005
4964 023354 012702 000144
4965 023360 012701 044166
4966 023364 000404
4967 023366 012702 002734
4968 023372 012701 051546
4969 023376 012703 157776
4970 023402 005737 022232
4971 023406 100402
4972 023410 013703 022476
4973 023414 005046
4974 023416 016616 000002
4975 023422 010166 000002
4976 023426 005737 001322
4977 023432 001006
4978 023434 014341
4979 023436 005302
4980 023440 001375
4981 023442 012737 177777 001322
4982 023450 000207
4983
4984
4985
4986 023452 104412
4987 023454 005737 001322
4988 023460 001430
4989 023462 105737 000041
4990 023466 001005
4991 023470 012702 000144
4992 023474 012701 044166
4993 023500 000404
4994 023502 012702 002734
4995 023506 012701 051546
4996 023512 012703 157776
4997 023516 005737 022232
4998 023522 100402
4999 023524 013703 022476
5000 023530 014143
5001 023532 005302
5002 023534 001375
5003 023536 005037 001322
5004 023542 104413
5005 023544 000207

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MOV RPWC(R4),SRPWC :STORE RPWC
MOV RPBA(R4),SRPBA :STORE RPBA
MOV RPCA(R4),SRPCA :STORE RPCA
MOV RPDA(R4),SRPDA :STORE RPDA
MOV RPM1(R4),SRPM1 :STORE RPM1
MOV SUCA(R4),SSUCA :STORE SUCA
MOV SILO(R4),SSILO :STORE SILO
MOV (SP)+,R4 :RESTORE R4
RTS PC :RETURN

:ROUTINE TO MOVE THE LOADER FROM HIGH MEMORY TO JUST ABOVE THE PROGRAM
MOVLDR: TSTB 41 :WHO LOADED THE PROGRAM
BNE 1$ :BR IF NOT 'ABS'
MOV #100.,R2 :'ABS' LOADER SIZE
MOV #BUFFER+200.,R1 :RELOCATE TO HERE
BR 2$ :FINISH
1$: MOV #1500.,R2 :'XXDP' SIZE
MOV #BUFFER+3000.,R1 :RELOCATE TO HERE (UPPER BOUNDARY)
2$: MOV #157776,R3 :TRY THIS UPPER MEMORY ADDRESS
TST $KT11 :MEMORY MANAGEMENT ?
BMI 3$ :BR IF YES
MOV $LSTAD,R3 :USE ACTUAL HIGH ADDRESS
3$: CLR -(SP) :CLEAR THE STACK
MOV 2(SP), (SP) :MOVE THE RETURN ADDRESS DOWN THE STACK
MOV R1, 2(SP) :BEGINNING ADDRESS OF BUFFER
TST LDRFLG :LOADER ALREADY RELOCATED ?
BNE 5$ :BR IF YES
4$: MOV -(R3), -(R1) :MOVE A WORD
DEC R2 :COUNT IT
BNE 4$ :BR IF MORE TO GO
MOV #-1,LDRFLG :SET THE LOADER MOVED FLAG
5$: RTS PC :RETURN

:ROUTINE TO RELOCATE THE LOADER TO UPPER MEMORY
RESLDR: SAVREG :SAVE R0 - R5
TST LDRFLG :LOADER MOVED ?
BEQ 5$ :BR IF NOT
TSTB 41 :'ABS' LOADER
BNE 1$ :BR IF NOT
MOV #100.,R2 :'ABS' LOADER SIZE
MOV #BUFFER+200.,R1 :RELOCATE FROM HERE
BR 3$ :FINISH
1$: MOV #1500.,R2 :'XXDP' SIZE
MOV #BUFFER+3000.,R1 :RELOCATE FROM HERE (UPPER BOUNDARY)
3$: MOV #157776,R3 :TRY THIS UPPER MEMORY ADDRESS
TST $KT11 :MEMORY MANAGEMENT ?
BMI 4$ :BR IF YES
MOV $LSTAD,R3 :USE ACTUAL HIGH ADDRESS
4$: MOV -(R1), -(R3) :MOVE A WORD
DEC R2 :COUNT IT
BNE 4$ :BR IF MORE TO GO
CLR LDRFLG :CLEAR THE LOADER MOVED FLAG
5$: RESREG :RESTORE R0 - R5
RTS PC :RETURN

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5006
5007 ;ROUTINE TO WAIT FOR DRIVE READY
5008
5009 023546 032764 100000 000000 DRVRDY: BIT #SURDY,RPDS(R4) ;DRIVE READY ?
5010 023554 001042 BNE 6$ ;BR IF READY
5011 023556 013746 001212 MOV TIMEOUT,-(SP) ;WAIT TIME: APPROX 1 SEC
5012 023562 005046 CLR -(SP) ;CLEAR WORKING LOCATION
5013 023564 162766 000001 000002 1$: SUB #1,2(SP) ;ANY TIME LEFT IN WAIT LOOP ?
5014 023572 001411 BEQ 3$ ;BR IF NOT
5015 023574 012716 000144 MOV #100,(SP) ;VALUE FOR APPROX 1 MS
5016 023580 032764 100000 000000 2$: BIT #SURDY,RPDS(R4) ;DRIVE READY ?
5017 023586 001023 BNE 5$ ;BR IF READY
5018 023590 005316 DEC (SP) ;COUNT SOME TIME
5019 023594 001372 BNE 2$ ;BR IF NOT FINISHED
5020 023598 000763 BR 1$ ;COUNT MAJOR TIME AGAIN
5021 023602 004737 023240 JSR PC,SAVRP ;STORE RP11 REGISTERS FOR ERROR MESSAGE
5022 023606 012737 023632 001164 3$: MOV #4$,SESCAPE ;ESCAPE TO 4$ ON ERROR
5023 023610 104100 ERROR 100 ;DRIVE TIMED OUT
5024 023614 005037 001164 4$: CLR $ESCAPE ;CLEAR THE 'ESCAPE' FLAG
5025 023618 062706 000004 ADD #4,SP ;CORRECT THE STACK POINTER
5026 023622 032777 001000 155270 BIT #SW09,$SWR ;LOOP ON THE ERROR ?
5027 023626 001336 BNE DRVRDY ;LOOP IF SET
5028 023630 000177 155322 JMP $BYPASS ;GO THE 'BYPASS' ADDRESS
5029 023634 062706 000004 5$: ADD #4,SP ;CORRECT THE STACK POINTER
5030 023638 000207 6$: RTS PC ;RETURN
5031
5032 ;ROUTINE TO WAIT FOR CONTROLLER READY
5033
5034
5035 023664 032764 000200 000004 CONRDY: BIT #RDY,RPCS(R4) ;CONTROLLER READY ?
5036 023672 001042 BNE 6$ ;BR IF READY
5037 023674 013746 001212 MOV TIMEOUT,-(SP) ;WAIT TIME: APPROX 1 SEC
5038 023700 005046 CLR -(SP) ;CLEAR WORKING LOCATION
5039 023702 162766 000001 000002 1$: SUB #1,2(SP) ;ANY TIME LEFT IN WAIT LOOP ?
5040 023710 001411 BEQ 3$ ;BR IF NOT
5041 023712 012716 000144 MOV #100,(SP) ;VALUE FOR APPROX 1 MS
5042 023716 032764 000200 000004 2$: BIT #RDY,RPCS(R4) ;CONTROLLER READY ?
5043 023724 001023 BNE 5$ ;BR IF READY
5044 023726 005316 DEC (SP) ;COUNT SOME TIME
5045 023730 001372 BNE 2$ ;BR IF NOT FINISHED
5046 023732 000763 BR 1$ ;COUNT MAJOR TIME AGAIN
5047 023734 004737 023240 JSR PC,SAVRP ;STORE RP11 REGISTERS FOR ERROR MESSAGE
5048 023740 012737 023750 001164 3$: MOV #4$,SESCAPE ;ESCAPE TO 4$ ON ERROR
5049 023746 104101 ERROR 101 ;CONTROLLER TIMED OUT
5050 023750 005037 001164 4$: CLR $ESCAPE ;CLEAR THE 'ESCAPE' FLAG
5051 023754 062706 000004 ADD #4,SP ;CORRECT THE STACK POINTER
5052 023760 032777 001000 155152 BIT #SW09,$SWR ;LOOP ON THE ERROR ?
5053 023766 001336 BNE CONRDY ;BR IF LOOPING
5054 023770 000177 155204 JMP $BYPASS ;'BYPASS' THE TEST
5055 023774 062706 000004 5$: ADD #4,SP ;CORRECT THE STACK POINTER
5056 024000 000207 6$: RTS PC ;RETURN
5057
5058 ;ROUTINE TO DETERMINE THE NUMBER OF SECTORS ON THE DRIVE BEING TESTED
5059
5060 024002 010046 SECNBR: MOV RO,-(SP) ;SAVE RC
5061 024004 012737 117230 001244 MOV #40600.,PAKSIZ ;START WITH THE NUMBER OF SECTORS ON

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5063 024012 005037 001246          CLR      PAKSIZ+2      ;AN RPO2
5064 024016 005000          CLR      RO          ;USE RO AS AN INDEX
5065 024020 113700 002500          MOVB    DPB,RO      ;GET THE DRIVE NUMBER
5066 024024 136037 001360 001304    BITB    ATABIT(RO),DRVTYP ; IS THE DRIVE AN RPO3 ?
5067 024032 001404          BEQ     1$          ;BR IF NOT
5068 024034 006337 001244          ASL     PAKSIZ      ;DOUBLE THE SECTOR COUNT
5069 024040 006137 001246          ROL     PAKSIZ+2    ;GET THE CARRY
5070 024044 012600          1$:    MOV     (SP)+,RO ;RESTORE RO
5071 024046 000207          RTS     PC          ;RETURN

;ROUTINE TO SELECT A PATTERN
5072
5073
5074 024050 010046          PATSEL: MOV    RO,-(SP) ;SAVE RO
5075 024052 005237 001204          1$:    INC    PATNUM    ;INCREMENT THE PATTERN NUMBER
5076 024056 022737 000020 001204    CMP     #16.,PATNUM ;NUMBER AT MAXIMUM
5077 024064 101002          BHI     2$          ;BR IF NOT
5078 024066 005037 001204          CLR     PATNUM      ;START AT ZERO AGAIN
5079 024072 013700 001204          2$:    MOV     PATNUM,RO ;SEE IF THE PATTERN IS IN USE
5080 024076 006300          ASL     RO          ;CONVERT INTO A TABLE INDEX
5081 024100 036037 001400 001300    BIT     BITS(RO),PATRN ;INUSE INDICATOR SET FOR THIS PATTERN ?
5082 024106 001761          BEQ     1$          ;BR IF NOT
5083 024110 012600          MOV     (SP)+,RO   ;RESTORE RO
5084 024112 000207          RTS     PC          ;RETURN

;ROUTINE TO INCREMENT THE DISK ADDRESS
; RETURN + 2 WHEN THE END OF THE PACK IS REACHED
5086
5087
5088
5089 024114 005737 001260          INCADR: TST    OPRSEL ;USING AN OPERATOR SPECIFIED ADDRESS ?
5090 024120 001066          BNE     5$          ;BR IF YES
5091 024122 005737 001246          TST    PAKSIZ+2    ;SECTORS LEFT ?
5092 024126 003017          BGT     1$          ;BR IF AN UPPER COUNT
5093 024130 005737 001244          TST    PAKSIZ      ;LOWER COUNT AT ZERO ?
5094 024134 001460          BEQ     5$          ;BR IF IT IS
5095 024136 023737 001244 001254    CMP     PAKSIZ,BLKS14 ;ENOUGH SECTORS LEFT FOR A FULL TRANSFER ?
5096 024144 103010          BHS     1$          ;BR IF THERE ARE
5097 024146 013737 001244 002504    MOV     PAKSIZ,$WC  ;USE THE RESIDUE COUNT FOR THIS TRANSFER
5098 024154 000337 002504          SWAB   $WC          ;CONVERT TO A WORD COUNT
5099 024160 005037 001244          CLR     PAKSIZ      ;FORCE COUNT TO ZERO
5100 024164 000410          BR     2$          ;GO AND INCREMENT THE ADDRESS
5101 024166 013737 001256 002504          1$:    MOV     WC14,$WC  ;USE THE STANDARD WORD COUNT
5102 024174 163737 001254 001244    SUB     BLKS14,PAKSIZ ;DECREMENT THE TOTAL SECTORS AVAILABLE
5103 024202 005637 001246          SBC     PAKSIZ+2    ;SUBTRACT ANY CARRY
5104 024206 005046          2$:    CLR     -(SP)      ;CLEAR THE STACK
5105 024210 113716 002512          MOVB   $SEC,(SP)   ;GET CURRENT SECTOR
5106 024214 063716 001250          ADD     INCSEC,(SP) ;ADD THE SECTOR INCREMENT
5107 024220 121627 000012          CMPB   (SP),#10.   ;EXCEED THE MAXIMUM ?
5108 024224 002404          BLT     3$          ;BR IF NOT
5109 024226 162716 000012          SUB     #10.,(SP)  ;CORRECT THE RESIDUE
5110 024232 105237 002513          INCB   $TRK        ;INCREMENT THE TRACK
5111 024236 111637 002512          3$:    MOVB   (SP),$SEC   ;NEW SECTOR ADDRESS
5112 024242 113716 002513          MOVB   $TRK,(SP)  ;GET CURRENT TRACK VALUE
5113 024246 063716 001252          ADD     INCTRK,(SP) ;ADD THE TRACK INCREMENT
5114 024252 121627 000024          CMPB   (SP),#20.   ;EXCEED THE MAXIMUM ?
5115 024256 002404          BLT     4$          ;BR IF NOT
5116 024260 162716 000024          SUB     #20.,(SP)  ;CORRECT THE SIZE
5117 024264 005237 002510          INC     $CYL        ;INCREMENT THE CYLINDER ADDRESS

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TRAP TABLE

118 024270 112637 002513
 119 024274 000402
 120 024276 062716 000002
 121 024302 000207
 122
 123
 124
 125
 126 024304 104412
 127 024306 013701 002506
 128 024312 005003
 129 024314 005021
 130 024316 005021
 131 024320 010321
 132 024322 005203
 133 024324 022703 000012
 134 024330 001371
 135 024332 104413
 136 024334 000207
 137
 138
 139
 140 024336 104412
 141 024340 005003
 142 024342 113703 002512
 143 024346 001002
 144 024350 012703 000012
 145 024354 013702 002504
 146 024360 013701 002506
 147 024364 010321
 148 024366 005302
 149 024370 001375
 150 024372 104413
 151 024374 000207
 152
 153
 154
 155 024376 104412
 156 024400 012701 043657
 157 024404 013702 002504
 158 024410 005003
 159 024412 113711 002513
 160 024416 062701 000002
 161 024422 005302
 162 024424 001372
 163 024426 104413
 164 024430 000207
 165
 166
 167
 168
 169 024432 104412
 170 024434 013700 001204
 171 024440 006300
 172 024442 013701 002506
 173 024446 013702 002504

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4$:  MOV  (SP)+, $TRK      ; NEW TRACK ADDRESS
      BR   6$              ; EXIT
5$:  ADD  #2, (SP)         ; INCREMENT RETURN - AT END
6$:  RTS  PC               ; RETURN

; ROUTINE TO SETUP A BUFFER WITH THE SEQUENTIAL HEADERS FOR CYLINDER 0
; TRACK 0

LODSEC: SAVREG              ; SAVE R0 - R5
        MOV  $BUF, R1      ; BUFFER ADDRESS
        CLR  R3            ; SECTOR COUNTER
1$:    CLR  (R1)+          ; UPPER WORD OF HEADER
        CLR  (R1)+          ; CYLINDER/TRACK WORD OF HEADER
        MOV  R3, (R1)+     ; SECTOR ADDRESS
        INC  R3            ; INCREMENT THE SECTOR ADDRESS
        CMP  #10, R3       ; FINISHED ?
        BNE  1$           ; BR IF NOT
        RESREG             ; RESTORE R0 - R5
        RTS  PC           ; RETURN

; FILL THE BUFFER WITH THE SECTOR ADDRESS (CONTAINED IN '$SEC')

FILSEC: SAVREG              ; STORE R0-R5
        CLR  R3            ; CLEAR THE INDEX
        MOV  $SEC, R3      ; SECTOR ADDRESS
        BNE  1$           ; BR IF NOT SECTOR ZERO
        MOV  #10, R3       ; USE 10, AS VALUE FOR SECTOR ZERO
1$:    MOV  $WC, R2        ; WORD COUNT
        MOV  $BUF, R1      ; BUFFER START ADDRESS
2$:    MOV  R3, (R1)+     ; MOVE DATA INTO THE BUFFER
        DEC  R2            ; DECREMENT THE COUNTER
        BNE  2$           ; BR IF MORE TO DO
        RESREG             ; RESTORE R0-R5
        RTS  PC           ; RETURN

; ROUTINE TO FILL THE BUFFER WITH THE TRACK ADDRESS (CONTAINED IN '$TRK')

FILTRK: SAVREG              ; STORE R0-R5
        MOV  #BUFFER+1, R1 ; BUFFER ADDRESS
        MOV  $WC, R2       ; WORD COUNT
        CLR  R3            ; CLEAR THE DATA STORAGE
1$:    MOV  $TRK, (R1)     ; MOVE TRACK ADDRESS INTO THE BUFFER
        ADD  #2, R1        ; INCREMENT THE BUFFER POINTER
        DEC  R2            ; DECREMENT THE COUNTER
        BNE  1$           ; BR IF MORE TO DO
        RESREG             ; RESTORE R0-R5
        RTS  PC           ; RETURN

; THIS ROUTINE WILL MOVE THE 16 WORDS OF THE
; DESIRED PATTERN INTO THE DATA BUFFER.

SETBUF: SAVREG              ; SAVE R0 - R5
        MOV  PATNUM, R0    ; GET THE PATTERN NUMBER
        ASL  R0            ; CONVERT NUMBER TO INDEX
        MOV  $BUF, R1      ; FIRST ADDRESS
        MOV  $WC, R2       ; WORD COUNT
  
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S174 024452 016003 001440 1S:  MOV  PAT.PT(R0),R3  ; PICKUP PATTERN POINTER
S175 024456 012704 000020      MOV  #16,R4          ; PATTERN COUNTER
S176 024462 012321      2S:  MOV  (R3)+,(R1)+    ; MOVE WORD 'N' INTO DATA BUFFER
S177 024464 005302      DEC  R2             ; DECREMENT THE COUNTER
S178 024466 001403      BEQ  J$            ; BR IF DONE
S179 024470 005304      DEC  R4             ; DECREMENT THE PATTERN COUNTER
S180 024472 001767      BEQ  I$            ; START PATTERN AGAIN
S181 024474 000772      BR   J$            ; CONTINUE
S182 024476 104413      3S:  RESREG          ; RESTORE R0 - R5
S183 024500 000207      RTS  PC            ; RETURN

; THIS ROUTINE LOADS THE PHYSICAL TRANSFER ADDRESS INTO 'SBUF'
S187 024502 013737 001224 002506 GETBUF: MOV  ACTMEM,SBUF  ; LOWER 16 BITS OF PHYSICAL ADDRESS
S188 024510 013746 001226      MOV  ACTMEM+2,-(SP) ; UPPER ADDRESS BITS ON THE STACK
S189 024514 042716 177774      BIC  #1C3,(SP)     ; LEAVE ONLY THE LOWER 2 BITS
S190 024520 006316      ASL  (SP)          ; ALIGN UPPER ADDRESS BITS
S191 024522 006316      ASL  (SP)          ; ALIGN UPPER ADDRESS BITS
S192 024524 006316      ASL  (SP)          ; ALIGN UPPER ADDRESS BITS
S193 024526 006316      ASL  (SP)          ; ALIGN UPPER ADDRESS BITS
S194 024530 112637 002501      MOVB (SP)+,DPB+1   ; LOAD 'MEX' BITS
S195 024534 013746 002504      MOV  $WC,-(SP)     ; PUT WORD COUNT ON THE STACK
S196 024540 006316      ASL  (SP)          ; DOUBLE THE WORD COUNT
S197 024542 062637 001224      ADD  (SP)+,ACTMEM  ; FORM NEXT STARTING ADDRESS
S198 024546 005537 001226      ADC  ACTMEM+2      ; ADD ANY CARRY
S199 024552 023737 001226 001236      CMP  ACTMEM+2,HIMEM+2 ; EXCEEDING HIGHEST BANK ADDRESS
S200 024560 101005      BHI  I$            ; BR IF HIGHER
S201 024562 103413      BLO  J$            ; BR IF LOWER
S202 024564 023737 001224 001234      CMP  ACTMEM,HIMEM  ; CHECK LOWER 16 BITS
S203 024572 103407      BLO  J$            ; BR IF LOWER
S204 024574 013737 001240 001224 1S:  MOV  LOMEM,ACTMEM  ; RESET ADDRESS
S205 024602 013737 001242 001226      MOV  LOMEM+2,ACTMEM+2 ; UPPER ADDRESS BITS
S206 024610 000734      BR   GETBUF       ; TRY AGAIN
S207 024612 000207      2S:  RTS  PC            ; RETURN

; THIS ROUTINE COMPARES A 16 WORD DATA PATTERN AGAINST THE
; DATA BUFFER. THE PATTERN NUMBER IS IN 'PATNUM'
S212 024614 104412 001222 DATCMP: SAVREG      ; SAVE R0-R5
S213 024616 005737      TST  MMACTV        ; MEMORY MANAGEMENT ACTIVE ?
S214 024622 001510      BEQ  J$            ; BR IF NOT
    
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5215	024624	005037	172340		CLR	KIPAR0	: LOAD ADDRESS REGISTER 0
5216	024630	012737	000200	172342	MOV	#200, KIPAR1	: LOAD ADDRESS REGISTER 1
5217	024636	012737	000400	172344	MOV	#400, KIPAR2	: LOAD ADDRESS REGISTER 2
5218	024644	012737	177600	172356	MOV	#177600, KIPAR7	: ACCESS TO I/O PAGE
5219	024652	005046			CLR	-(SP)	: MAKE ROOM ON THE STACK
5220	024654	005046			CLR	-(SP)	: MAKE ROOM ON THE STACK
5221	024656	013766	002506	000002	MOV	\$BUF, 2(SP)	: LOWER 16 BITS OF STARTING ADDRESS
5222	024664	113716	002501		MOVB	DPB+1, (SP)	: UPPER ADDRESS BITS
5223	024670	006216			ASR	(SP)	: RIGHT JUSTIFY THE ADDRESS BITS
5224	024672	006216			ASR	(SP)	: RIGHT JUSTIFY THE ADDRESS BITS
5225	024674	006216			ASR	(SP)	: RIGHT JUSTIFY THE ADDRESS BITS
5226	024676	006216			ASR	(SP)	: RIGHT JUSTIFY THE ADDRESS BITS
5227	024700	012703	000006		MOV	#6, R3	: LOAD COUNTER
5228	024704	006216			ASR	(SP)	: CONVERT ADDRESS TO BLOCK ADDRESS
5229	024706	006066	000002		ROR	2(SP)	: LOWER 16 BITS
5230	024712	005303			DEC	R3	: DECREMENT THE COUNTER
5231	024714	001373			BNE	1\$: CONTINUE UNTIL FINISHED
5232	024716	042766	000177	000002	BIC	#177, 2(SP)	: CLEAR LOWER BLOCK ADDRESS BITS
5233	024724	016637	000002	172346	MOV	2(SP), KIPAR3	: LOAD ADDRESS REGISTER 3
5234	024732	062766	000200	000002	ADD	#200, 2(SP)	: INCREMENT BLOCK ADDRESS BY 4K
5235	024740	016637	000002	172350	MOV	2(SP), KIPAR4	: LOAD ADDRESS REGISTER # 4
5236	024746	012737	077406	172300	MOV	#77406, KIPDR0	: LOAD DESCRIPTOR REGISTER # 0
5237	024754	012737	077406	172302	MOV	#77406, KIPDR1	: LOAD DESCRIPTOR REGISTER # 1
5238	024762	012737	077406	172304	MOV	#77406, KIPDR2	: LOAD DESCRIPTOR REGISTER # 2
5239	024770	012737	077406	172306	MOV	#77406, KIPDR3	: LOAD DESCRIPTOR REGISTER # 3
5240	024776	012737	077406	172310	MOV	#77406, KIPDR4	: LOAD DESCRIPTOR REGISTER # 4
5241	025004	012737	077406	172316	MOV	#77406, KIPDR7	: LOAD DESCRIPTOR REGISTER # 7
5242	025012	023727	002504	010000	CMP	\$WC, #4096.	: WAS TRANSFER GREATER THAN 4K ?
5243	025020	101411			BLOS	2\$: BR IF NOT
5244	025022	062766	000200	000002	ADD	#200, 2(SP)	: INCREMENT THE ADDRESS BY 4K
5245	025030	016637	000002	172352	MOV	2(SP), KIPAR5	: LOAD ADDRESS REGISTER # 5
5246	025036	012737	077406	172312	MOV	#77406, KIPDR5	: LOAD DESCRIPTOR REGISTER # 5
5247	025044	013701	002506		MOV	\$BUF, R1	: LOAD STARTING ADDRESS
5248	025050	005737	001222		TST	MMACTV	: MEMORY MANAGEMENT ACTIVE ?
5249	025054	001425			BEQ	CMPAR	: BR IF NOT
5250	025056	042701	140000		BIC	#140000, R1	: CHANGE PHYSICAL BASE TO VIRTUAL BASE
5251	025062	052701	060000		BIS	#60000, R1	: START AT VIRTUAL 12K
5252	025066	062706	000004		ADD	#4, SP	: CORRECT THE STACK POINTER
5253	025072	012737	025110	000004	MOV	#3\$, ERRVEC	: CHANGE THE ERROR VECTOR
5254	025100	012737	000020	172516	MOV	#20, #SR3	: ENABLE 22 BIT ADDRESSING MODE
5255	025106	000402			BR	4\$: BR IF AN 11/70
5256	025110	062706	000004		ADD	#4, SP	: CORRECT THE STACK POINTER
5257	025114	012737	000006	000004	MOV	#ERRVEC+2, ERRVEC	: RESTORE THE ERROR VECTOR
5258	025122	012737	000001	177572	MOV	#1, #SR0	: ENABLE MEMORY MANAGEMENT
5259	025130	013737	001342	025420	CMPAR:	\$RPWC, CMCNT	: WORD COUNT TO WORKING LOCATION
5260	025136	063737	002504	025420	ADD	\$WC, CMCNT	: CALCULATE ACTUAL WORDS TRANSFERED
5261	025144	013737	002510	025422	MOV	\$CYL, CMCYL	: CYLINDER ADDRESS
5262	025152	113737	002513	025426	MOVB	\$TRK, CMTRK	: TRACK ADDRESS
5263	025160	113737	002512	025424	MOVB	\$SEC, CMSEC	: SECTOR ADDRESS
5264	025166	013737	001320	025416	CMSTR:	CMP. CT, LIMIT	: DISPLAY LIMIT
5265	025174	005237	025416		INC	LIMIT	: CONVERT PARAMETER INTO LIMIT VALUE
5266	025200	010137	025430		MOV	R1, CMBUF	: STARTING ADDRESS OF SECTOR BUFFER
5267	025204	005037	025412		CLR	FASTER	: CLEAR 'FIRST ERROR' INDICATOR
5268	025210	005037	025414		CLR	ERCTR	: CLEAR ERROR COUNTER
5269	025214	023727	025420	000400	CMP	CMCNT, #256.	: IS BUFFER SIZE GREATER THAN ONE SECTOR ?
5270	025222	101003			BHI	1\$: BR IF IT IS

5271	025224	013702	025420		MOV	CMCNT,R2		: LESS THAN, USE REMAINING BUFFER
5272	025230	000402			BR	2\$		
5273	025233	012703	000400	1\$:	MOV	#256,R2		: COMPARE SECTOR
5274	025236	160237	025420	2\$:	SUB	R2,CMCNT		: DECREMENT WORD COUNT
5275	025242	013704	001204		MOV	PATNUM,R4		: PATTERN NUMBER
5276	025246	006304			ASL	R4		: GENERATE INTO AN INDEX
5277	025250	012703	000020	CMDAT:	MOV	#16,R3		: R3 IS PATTERN POSITION COUNTER
5278	025254	016405	001440		MOV	PAT.PT(R4),R5		: PATTERN ADDRESS
5279	025260	022125		1\$:	CMP	(R1)+,(R5)+		: COMPARE BUFFER WITH PATTERN
5280	025262	001406			BEQ	2\$: BR IF EQUAL
5281	025264	032777	000010	153646	BIT	#5W03,2SWR		: SWITCH 3 SET ?
5282	025272	001002			BNE	2\$: BR IF NOT
5283	025274	004737	025432		JSR	PC,CMPRT		: TYPE THE ERROR
5284	025300	005302		2\$:	DEC	R2		: DECREMENT SIZE COUNT
5285	025302	001403			BEQ	3\$: BR WHEN AT END
5286	025304	005303			DEC	R3		: DECREMENT PATT POS COUNT
5287	025306	001364			BNE	1\$: BR IF NOT AT END OF PATT
5288	025310	000757			BR	CMDAT		: RESTART THE PATTERN
5289	025312	004737	025630	3\$:	JSR	PC,ENDCMP		: PRINT LAST LINE (IF ERRORS)
5290	025316	005737	025420		TST	CMCNT		: AT END OF BUFFER
5291	025322	003424			BLE	CMPRX		: BR IF AT END
5292	025324	005237	025424		INC	CMSEC		: INCREMENT SECTOR
5293	025330	023727	025424	000012	CMP	CMSEC,#10.		: SECTOR GREATER THAN MAX ?
5294	025336	103713			BLO	CMSTR		: BR IF NOT GREATER THAN MAX
5295	025340	005037	025424		CLR	CMSEC		: CLEAR SECTOR ADDRESS
5296	025344	005237	025426		INC	CMTRK		: INCREMENT TRACK
5297	025350	023727	025426	000024	CMP	CMTRK,#20.		: TRACK GREATER THAN MAX ?
5298	025356	103703			BLO	CMSTR		: BR IF NOT GREATER
5299	025360	005037	025426		CLR	CMTRK		: RESET TRACK ADDRESS
5300	025364	005237	025422		INC	CMCYL		: INCREMENT CYLINDER ADDRESS
5301	025370	000137	025166		JMP	CMSTR		: CONTINUE WITH COMPARE
5302	025374	104413		CMPRX:	RESREG			: RESTORE R0 - R5
5303	025376	005737	001222		TST	MMACTV		: MEMORY MANAGEMENT ACTIVE ?
5304	025402	001402			BEQ	1\$: BR IF NOT
5305	025404	005037	177572		CLR	2\$SRD		: TURN OFF MEMORY MANAGEMENT
5306	025410	000207		1\$:	RTS	PC		
5307								
5308	025412	000000		FRSTER:	.WORD	0		: FIRST ERROR INDICATOR
5309	025414	000000		ERCTR:	.WORD	0		: NUMBER OF ERRORS
5310	025416	000000		LIMIT:	.WORD	0		: DISPLAY LIMIT
5311	025420	000000		CMCNT:	.WORD	0		: WORD COUNT
5312	025422	000000		CMCYL:	.WORD	0		: CYLINDER ADDRESS
5313	025424	000000		CMSEC:	.WORD	0		: SECTOR ADDRESS
5314	025426	000000		CMTRK:	.WORD	0		: TRACK ADDRESS
5315	025430	000000		CMBUF:	.WORD	0		: BEGINNING ADDRESS OF SECTOR
5316								
5317								
5318								
5319	025432	005737	025412	CMPRT:	TST	FRSTER		: FIRST ERROR?
5320	025436	001037			BNE	CMPRT1		: BR IF NOT
5321	025440	005737	001222		TST	MMACTV		: MEMORY MANAGEMENT ACTIVE ?
5322	025444	001011			BNE	1\$: BR IF ACTIVE
5323	025446	012737	025446	001116	MOV	#. \$ERRPC		: PC FOR ERROR MESSAGE
5324	025454	112737	000102	001114	MOVB	#102,\$ITEMB		: COMPARISON ERROR MESSAGE NUMBER
5325	025462	004737	017254		JSR	PC,TYPERR		: REPORT THE ERROR
5326	025466	000412			BR	2\$: CONTINUE

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5327 025470 005037 177572 1S: CLR 2#SRO ;TURN OFF MEMORY MANAGEMENT
5328 025474 012737 025474 001116 MOV #,SERRPC ;PC FOR ERROR MESSAGE
5329 025502 112737 000103 001114 MOV #103,$ITEMB ;COMPARISON ERROR MESSAGE WITH MM REGISTERS
5330 025510 004737 017254 JSR PC,TYPERR ;REPORT THE COMPARISON ERROR
5331 025514 012737 177777 025412 2S: MOV #-1,FRSTER ;SET FIRST ERROR INDICATOR
5332 025522 005737 001222 TST MMACTV ;MEMORY MANAGEMENT ACTIVE ?
5333 025526 001403 BEQ CMPRT1 ;BR IF 0
5334 025530 012737 000001 177572 MOV #1,2#SRO ;ENABLE MEMORY MANAGEMENT AGAIN
5335 025536 005737 025416 CMPRT1: TST LIMIT ;TIMEOUT LIMIT REACHED ?
5336 025542 001403 BEQ 1S ;BR IF IT HAS
5337 025544 005337 025416 DEC LIMIT ;DECREMENT LIMIT COUNTER
5338 025550 001004 BNE 2S ;BR IF NOT AT LIMIT
5339 025552 032777 000020 153360 1S: BIT #SW04,2SWR ;PRINT ALL DATA COMPARE ERRORS ?
5340 025560 001420 BEQ 3S ;BR IF NOT
5341 025562 010137 001122 2S: MOV R1,$BDADR ;ADDRESS OF BAD WORD
5342 025566 162737 000002 001122 SUB #2,$BDADR ;ADJUST ADDRESS
5343 025574 016537 177776 001124 MOV -2(R5),$GDDAT ;GOOD DATA
5344 025602 016137 177776 001126 MOV -2(R1),$BDDAT ;BAD DATA
5345 025610 112737 000104 001114 MOV #104,$ITEMB ;ERROR TABLE INDEX
5346 025616 004737 017254 JSR PC,TYPERR ;DISPLAY WORD THAT DIDN'T COMPARE
5347 025622 005237 025414 3S: INC ERCTR ;COUNT THE ERROR
5348 025626 000207 RTS PC ;RETURN
5349
5350 ;LAST LINE OF COMPARE ERROR REPORTING
5351
5352 025630 005737 025414 ENDCMP: TST ERCTR ;SEE IF ANY ERRORS
5353 025634 001406 BEQ 2S ;BR IF NONE
5354 025636 012737 025646 001164 MOV #1S,$ESCAPE ;ESCAPE TO 1S ON ERROR
5355 025644 104105 ERROR 105 ;NUMBER OF COMPARE ERRORS
5356 025646 005037 001164 1S: CLR $ESCAPE ;CLEAR THE ERROR ESCAPE ADDRESS
5357 025652 000207 2S: RTS PC ;RETURN
5358
5359 ;ROUTINE TO CLEAR THE RP11E
5360
5361 ;THE FIRST 'CLEAR' TERMINATES ANY ORDER WHICH IS IN PROGRESS. THE SECOND
5362 ;'CLEAR' CLEARS THE 'PROGRAM ERROR' WHICH MAY HAVE BEEN SET BY THE FIRST
5363 ;'CLEAR'.
5364
5365 ;NOTE: R4 MUST CONTAIN THE BASE ADDRESS OF THE RP11E
5366
5367 025654 012764 000001 000004 CLRP: MOV #CLEAR,RPCS(R4) ;CLEAR THE RP11
5368 025662 051616 BIS (SP),(SP) ;DELAY FOR...
5369 025664 051616 BIS (SP),(SP) ;...AT LEAST
5370 025666 051616 BIS (SP),(SP) ;...4 MICROSECONDS.
5371 025670 012764 000001 000004 MOV #CLEAR,RPCS(R4) ;CLEAR THE RP11
5372 025676 000207 RTS PC ;RETURN
5373
5374 ;ROUTINE TO SIZE MEMORY AND TO SETUP FOR TEST 14
5375
5376 025700 012737 000200 022232 SIZMEM: MOV #BIT07,$KT11 ;TELL THE 'SIZE' ROUTINE TO USE MM
5377 025706 004737 022174 6S: JSR PC,$SIZE ;FIND OUT HOW MUCH MEMORY AND IF THE
5378 ;SYSTEM HAS A KT11
5379 025712 005737 022232 TST $KT11 ;DOES THE SYSTEM HAVE A KT11 ?
5380 025716 100060 BPL 2S ;BR IF NOT
5381 025720 023727 022500 001540 CMP $LSTBK,#1540 ;MORE THAN 28K ON THE SYSTEM ?
5382 025726 101003 BHI 1S ;BR IF MORE THAN 28K

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5383	025730	005037	022232		CLR	\$KT11	: CLEAR MEMORY MANAGEMENT INDICATOR
5384	025734	000764			BR	6\$: DO IT AGAIN WITH MEM. MAN. DESELECTED
5385	025736	012737	157776	001302	MOV	#157776, MEMSIZ	: ADDRESS OF LAST NON MEMORY MANAGEMENT LOCATION
5386	025744	013737	022500	001230	MOV	\$LSTBK, MAXMEM	: HIGH MEMORY BANK ADDRESS
5387	025752	005037	001232		CLR	MAXMEM+2	: CLEAR UPPER MEMORY ADDRESS BITS
5388	025756	006337	001230		ASL	MAXMEM	: CONVERT THE BANK COUNT INTO AN-
5389	025762	006137	001232		ROL	MAXMEM+2	: ABSOLUTE MEMORY ADDRESS
5390	025766	006337	001230		ASL	MAXMEM	: CONVERT THE BANK COUNT INTO AN
5391	025772	006137	001232		ROL	MAXMEM+2	: ABSOLUTE MEMORY ADDRESS
5392	025776	006337	001230		ASL	MAXMEM	: CONVERT THE BANK COUNT INTO AN
5393	026002	006137	001232		ROL	MAXMEM+2	: ABSOLUTE MEMORY ADDRESS
5394	026006	006337	001230		ASL	MAXMEM	: CONVERT THE BANK COUNT INTO AN
5395	026012	006137	001232		ROL	MAXMEM+2	: ABSOLUTE MEMORY ADDRESS
5396	026016	006337	001230		ASL	MAXMEM	: CONVERT THE BANK COUNT INTO AN
5397	026022	006137	001232		ROL	MAXMEM+2	: ABSOLUTE MEMORY ADDRESS
5398	026026	006337	001230		ASL	MAXMEM	: CONVERT THE BANK COUNT INTO AN
5399	026032	006137	001232		ROL	MAXMEM+2	: ABSOLUTE MEMORY ADDRESS
5400	026036	063737	022476	001230	ADD	\$LSTAD, MAXMEM	: ADD THE SIZE OF THE LAST BANK
5401	026044	005537	001232		ADC	MAXMEM+2	: HANDLE ANY CARRY
5402	026050	012737	020000	001264	MOV	#8192., MAXWC	: SETUP FOR MAXIMUM TRANSFER SIZE
5403	026056	000433			BR	5\$: EXIT
5404	026060	013737	022476	001302	MOV	\$LSTAD, MEMSIZ	: HIGH NON MEMORY MANAGEMENT ADDRESS
5405	026066	013737	022476	001230	MOV	\$LSTAD, MAXMEM	: HIGH NON MEMORY MANAGEMENT ADDRESS
5406	026074	005037	001232		CLR	MAXMEM+2	: DON'T NEED THE UPPER WORD
5407	026100	013737	001230	001264	MOV	MAXMEM, MAXWC	: CONVERT MEMORY SIZE TO WORD COUNT
5408	026106	162737	043656	001264	SUB	#BUFFER, MAXWC	: SUBTRACT STARTING ADDRESS OF BUFFER
5409	026114	000241			CLC		: CLEAR THE 'C' BIT
5410	026116	006037	001264		ROR	MAXWC	: CHANGE TO WORD COUNT
5411	026122	105737	000041		TSTB	41	: SEE WHICH LOADER
5412	026126	001004			BNE	4\$: BR IF 'XXDP'
5413	026130	162737	000144	001264	SUB	#100., MAXWC	: 'ABS' LOADER SIZE (PLUS A LITTLE BIT)
5414	026136	000403			BR	5\$: EXIT
5415	026140	162737	002734	001264	SUB	#1500., MAXWC	: 'XXDP' SIZE
5416	026146	000207			S\$:	RTS	: RETURN

: THIS ROUTINE IS USED TO ENSURE THAT THE BUS ADDRESS
 : OF THE RP11 IS SETUP TO READ THE PROPER VALUE.
 : IT WILL ALSO READ THE ADDRESS FROM THE TTY IF
 : REQUIRED.
 : NOTE: THIS ROUTINE DESTROYS R0 - R4

5424	026150	005737	001216		GETADR: TST	BUSADR	: INPUT FROM TTY REQUESTED?
5425	026154	001425			BEQ	3\$: NO--BRANCH
5426	026156	005037	001216		CLR	BUSADR	: YES--CLEAR THE REQUEST FLAG
5427	026162	104401	001173	1\$:	TYPE	, \$CRLF	: CR-LF
5428	026166	104401	032162		TYPE	, #RPADR	: 'RPADR='
5429	026172	013746	001324		MOV	RPADR, -(SP)	: SAVE RPADR FOR TYPEOUT
5430							: RP11 ADDRESS
5431	026176	104403			TYPOS		: GO TYPE--OCTAL ASCII
5432	026200	006			.BYTE	6	: TYPE 6 DIGIT(S)
5433	026201	001			.BYTE	1	: TYPE LEADING ZEROS
5434	026202	104401	031322		TYPE	, SLASH	: /
5435	026206	104411			RDLIN		: GET THE ENTRY
5436	026210	012601			MOV	(SP)+, R1	: STARTING ADDRESS OF INPUT ASCII STRING
5437	026212	004037	027136		JSR	RO, CK.NUM	: CHECK THE NUMBER
5438	026216	026162			1\$: ILLEGAL INPUT

TRAP TABLE

Address	Instruction	OpCode	Target	Label	Comments	
5439	026220	026224		2\$: TERMINATED WITH A 'CR'	
5440	026222	026230		3\$: NO ENTRY - 'CR' ONLY	
5441	026224	010237	001324	2\$:	MOV R2, RPADR ; STORE THE ADDRESS	
5442	026230	013701	000004	3\$:	MOV ERVVEC, R1 ; SAVE THE ERROR VECTOR	
5443	026234	012737	026254	000004	MOV #45, ERVVEC ; SETUP FOR TRAP	
5444	026242	005777	153056		TST @RPADR ; CHECK FOR RP11	
5445	026244	010137	000004		MOV R1, ERVVEC ; RESTORE ERROR VECTOR	
5446	026250	000207			RTS PC ; RETURN	
5447	026254	010137	000004	4\$:	MOV R1, ERVVEC ; RESTORE ERROR VECTOR	
5448	026260	022626			CMP (SP)+, (SP)+ ; CLEAN OFF THE STACK	
5449	026262	012737	026272	001164	MOV #55, \$ESCAPE ; ESCAPE TO 5\$ ON ERROR	
5450	026270	104001			ERROR 1 ; REPORT THE ERROR	
5451	026272	005037	001164	5\$:	CLR \$ESCAPE ; CLEAR ERROR ESCAPE ADDRESS	
5452	026276	005737	000042		TST @#42 ; IS THERE A MONITOR?	
5453	026302	001727			BEQ 1\$; NO--GO ASK FOR ADDRESS	
5454	026304	005037	016776		CLR \$EOPCT ; NO PASSES	
5455	026310	000137	016616		JMP \$EOP ; GO TO END OF PROGRAM	
5456						
5457					: THIS ROUTINE ALLOWS THE OPERATOR TO CONTROL THE PROGRAM	
5458					: (CONVERSATION MODE)	
5459						
5460	026314	104401	031324		ENTPRM: TYPE , ENTNM ; 'ENTER TEST NUMBER'	
5461	026320	104411			RDLIN ; GET THE ENTRY	
5462	026322	012601			MOV (SP)+, R1 ; STARTING ADDRESS OF INPUT ASCII STRING	
5463	026324	004037	027136		JSR R0, CK.NUM ; CHECK THE NUMBER	
5464	026330	026314			ENTPRM ; ILLEGAL INPUT	
5465	026332	026336			1\$; TERMINATED WITH A 'CR'	
5466	026334	026362			2\$; NO ENTRY - 'CR' ONLY	
5467	026336	022702	000020	1\$:	CMP #STN, R2 ; VALID NUMBER ?	
5468	026342	101764			BLOS ENTPRM ; BR IF NOT	
5469	026344	006302			ASL R2 ; CONVERT INTO A BIT INDEX	
5470	026346	016237	001400	001276	MOV BITS(R2), TSTNMS ; TEST SELECTION BIT	
5471	026354	006202			ASL R2 ; CHANGE BACK INTO A WORD COUNT	
5472	026356	010246			MOV R2, -(SP) ; SAVE THE TEST NUMBER ENTRY	
5473	026360	000405			BR ENTPR1 ; GET THE DRIVE NUMBER	
5474	026362	012737	017777	001276	2\$:	MOV #17777, TSTNMS ; SELECT ALL TESTS
5475	026370	012746	177777		MOV #-1, -(SP) ; ALL TESTS SELECTED	
5476	026374	005037	001312		ENTPR1: CLR DRVSEL ; CLEAR DRIVE SELECTION WORD	
5477	026400	004737	022566		JSR PC, RPINIT ; CHECK THE DRIVE STATUS	
5478	026404	012737	000312	001210	MOV #202, MAXCYL ; ASSUME ONLY RPO2'S	
5479	026412	005737	001304		TST DRVTP ; SEE WHICH TYPE	
5480	026416	001403			BEQ 1\$; BR IF ONLY RPO2'S	
5481	026420	012737	000625	001210	MOV #405, MAXCYL ; RPO3'S	
5482	026426	104401	031352	1\$:	TYPE , DRVN ; ENTER DRIVE NUMBER	
5483	026432	104411			RDLIN ; GET THE ENTRY	
5484	026434	012601			MOV (SP)+, R1 ; STARTING ADDRESS OF INPUT ASCII STRING	
5485	026436	004037	027136		JSR R0, CK.NUM ; CHECK THE NUMBER	
5486	026442	026426			1\$; ILLEGAL INPUT	
5487	026444	026450			2\$; TERMINATED WITH A 'CR'	
5488	026446	026524			3\$; NO ENTRY - 'CR' ONLY	
5489	026450	022702	000010	2\$:	CMP #8, R2 ; VALID ENTRY ?	
5490	026454	101764			BLOS 1\$; BR IF NOT	
5491	026456	116237	001360	001312	MOVB ATABIT(R2), DRVSEL ; DRIVE SELECTION BIT	
5492	026464	105762	001370		TSTB DRVSTA(R2) ; IS DRIVE ONLINE	
5493	026470	003035			BGT 8\$; BR IF ONLINE	
5494	026472	104401	031377		TYPE , DRIVE ; 'DRIVE'	

Address	Code	Label	Hex	Op	Opnd	Comment
495	026476	105763	001370	TSTB	DRVSTA(R2)	CHECK DRIVE STATUS AGAIN
496	026478	100403		BMI	3S	BR IF UNSAFE
497	026479	104401	027312	TYPE	OFFLIN	'OFFLINE'
498	026480	000402		BR	4S	CONTINUE
499	026481	104401	027276	3S:	TYPE	'UNSAFE'
500	026482	104401	001173	4S:	TYPE	'SCALF'
501	026483	000724		BR	ENTPR1	TRY AGAIN
502	026484	005001		5S:	CLR	R1 - USE IT AS AN INDEX
503	026485	005716		TST	(SP)	ALL TESTS SELECTED ?
504	026486	100403		BMI	6S	BR IF THEY WERE
505	026487	022716	000015	CMPI	815,(SP)	TEST 15, 16, OR 17 SELECTED ?
506	026488	101733		BLOS	1S	BR IF YES, SPECIFIC DRIVE NUMBER REQUIRED
507	026489	105761	001370	6S:	TSTB	DRVSTA(R1)
508	026490	003403		BLE	7S	BR IF NOT
509	026491	151137	001360	001312	BITB	ATBIT(R1),DRVSEL
510	026492	005201		7S:	INC	R1
511	026493	020127	000010	CMPI	R1,88	REACHED MAXIMUM ?
512	026494	103766		BLO	8S	BR IF NOT
513	026495	022726	000014	8S:	CMPI	814,(SP)+
514	026496	001137		BNE	ENTPRX	BR IF NOT
515	026497	104401	031406	ENTPR2:	TYPE	'CHNGPM'
516	026498	104411		RDLIN		GET THE RESPONSE
517	026499	123627	000131	CMPI	9(SP)+,'Y'	'Y' ENTERED ?
518	026500	001131		BNE	ENTPRX	BR IF NOT
519	026501	104401	031447	TYPE	MAXWDS	MAXIMUM WORD COUNT FOR TEST 14:
520	026502	013746	001264	MOV	MAXWC,-(SP)	SAVE MAXWC FOR TYPEOUT
521	026503					MAXIMUM WORD COUNT FOR TEST 14
522	026504					GO TYPE--OCTAL ASCII
523	026505					TYPE 5 DIGIT(S)
524	026506					SUPPRESS LEADING ZEROS
525	026507					CR-LF
526	026508	104401	001173	1S:	TYPE	'SCALF'
527	026509	104401	031523	1S:	TYPE	'ENTHC'
528	026510	104411		RDLIN		GET THE ENTRY
529	026511	012601		MOV	(SP)+,R1	STARTING ADDRESS OF INPUT ASCII STRING
530	026512	004037	027136	JSR	RD,CK.NUM	CHECK THE NUMBER
531	026513	026626		IS		ILLEGAL INPUT
532	026514	026650		2S		TERMINATED WITH A 'CR'
533	026515	026670		ENTPR3		NO ENTRY - 'CR' ONLY
534	026516	020237	001264	2S:	CMPI	R2,MAXWC
535	026517	101364		BHI	1S	BR IF TOO LARGE
536	026518	020237	001262	CMPI	R2,LMTBK	NEW WORD COUNT LARGER THAN BK ?
537	026519	101361		BHI	1S	BR IF LARGER
538	026520	010237	001256	MOV	R2,WC14	LOAD NEW WORD COUNT
539	026521	104401	031565	ENTPR3:	TYPE	'CHNGPT'
540	026522	104411		RDLIN		ENTER PATTERN CODE
541	026523	012601		MOV	(SP)+,R1	READ THE ENTRY
542	026524	004037	027136	JSR	RD,CK.NUM	STARTING ADDRESS OF INPUT ASCII STRING
543	026525	026670		ENTPR3		CHECK THE NUMBER
544	026526	026712		IS		ILLEGAL INPUT
545	026527	026716		ENTPR4		TERMINATED WITH A 'CR'
546	026528	010237	001300	1S:	MOV	R2,PATRN
547	026529	104401	031640	ENTPR4:	TYPE	'CHNGAD'
548	026530	104411		RDLIN		USE A SPECIFIC DISK ADDRESS ?
549	026531	123627	000131	CMPI	9(SP)+,'Y'	READ THE ENTRY
550	026532	001057		BNE	ENTPRX	'Y' ENTERED ?
		104401	031713	TYPE	'ONEPAT'	BR IF NOT
						'A SINGLE DATA PATTERN MUST HAVE BEEN SPECIFIED:'

TRAP TABLE

5607				:	MOV	#ADR,R1	:	ADDRESS OF ASCII CHARACTER
5608				:	JSR	RO,CK.CHR	:	CHECK CHARACTER
5609				:	RETURN	ADR1	:	UNKNOWN CHARACTER
5610				:	RETURN	ADR2	:	CARRIAGE RETURN * (R1)=ADR+1
5611				:			:	
5612	027122	105711		CK.CHR:	TSTB	(R1)	:	"CARRIAGE RETURN"?
5613	027124	001002			BNE	IS	:	NO -- BRANCH
5614	027126	005720			TST	(RO)+	:	CARRIAGE RETURN
5615	027130	005201			INC	R1	:	MOVE POINTER TO NEXT CHARACTER
5616	027132	011000		IS:	MOV	(RO),RO	:	UNKNOWN CHARACTER
5617	027134	000200			RTS	RO	:	RETURN

: THIS ROUTINE CHECKS AN ASCII STRING FOR LEGAL CHARACTERS
: AND FORMS AN OCTAL NUMBER IN R2

5618				:	MOV	#ADR,R1	:	ADDRESS OF ASCII STRING
5619				:	JSR	RO,CK.NUM	:	GO FORM THE NUMBER
5620				:	RETURN	ADR1	:	ILLEGAL CHARACTER IN THE INPLT STRING
5621				:	RETURN	ADR2	:	"CR"--R2=NUMBER
5622				:	RETURN	ADR3	:	"CR" WAS FIRST ENTRY
5623				:			:	
5624				:			:	
5625				:			:	
5626				:			:	
5627				:			:	
5628				:			:	
5629				:			:	
5630	027136	010346		CK.NUM:	MOV	R3,-(SP)	:	SAVE R3
5631	027140	005003			CLR	R3	:	START NUMBER AT ZERO
5632	027142	004037	027072		JSR	RO,CK.OCT	:	OCTAL DIGIT?
5633	027146	000404			BR	IS	:	YES--BRANCH
5634	027150	004037	027122		JSR	RO,CK.CHR	:	CHECK ONE CHARACTER
5635	027154	027230			BNE	IS	:	ILLEGAL CHARACTER
5636	027156	027222			BNE	IS	:	CARRIAGE RETURN
5637	027160	005201		IS:	INC	R1	:	MOVE TO NEXT CHARACTER
5638	027162	006303			ASL	R3	:	FOR THE OCTAL NUMBER IN R3
5639	027164	103421			BCS	6S	:	DON'T LET IT GET TO BIG
5640	027166	006303			ASL	R3	:	
5641	027170	103417			BCS	6S	:	
5642	027172	006303			ASL	R3	:	
5643	027174	103415			BCS	6S	:	
5644	027176	060203			ADD	R2,R3	:	
5645	027200	004037	027072		JSR	RO,CK.OCT	:	IS THIS AN OCTAL DIGIT?
5646	027204	000765			BR	IS	:	YES--MAKE IT PART OF THE NUMBER
5647	027206	010302		2S:	MOV	R3,R2	:	SAVE THE OCTAL NUMBER
5648	027210	005003			CLR	R3	:	START WITH ZERO INDEX
5649	027212			3S:			:	
5650	027216	004037	027122		JSR	RO,CK.CHR	:	CHECK ONE CHARACTER
5651	027220	027230			BNE	IS	:	ILLEGAL CHARACTER
5652	027222	027224			BNE	IS	:	CARRIAGE RETURN
5653	027224	005723		8S:	TST	(R3)+	:	"CR" ONLY
5654	027226	005723		5S:	TST	(R3)+	:	"CR"
5655	027230	060300			ADD	R3,RO	:	YES--SAVE THE OCTAL NUMBER
5656	027230	012603		6S:	MOV	(SP)+,R3	:	RESTORE R3
5657	027232	011000			MOV	(RO),RO	:	PICKUP EXIT ADDRESS
5658	027234	000200			RTS	RO	:	RETURN

.SBTTL TELETYPE MESSAGES

56E1	027236	005015	042012	044522	DRSTAT:	.ASCIZ	<15><12><12>@DRIVE STATUS: @<15><12><12>
56E2	027244	042526	051440	040524			

TELETYPE MESSAGES

5663	027252	052524	035123	005015	
5664	027260	000012			
5665	027262	020040	020040	047440	ONLINE: .ASCIZ @ ONLINE@
5666	027270	046116	047111	000105	
5667	027276	020040	020040	052440	UNSAFE: .ASCIZ @ UNSAFE@
5668	027304	051516	043101	000105	
5669	027312	020040	020040	047440	OFFLIN: .ASCIZ @ OFFLINE@
5670	027320	043106	044514	042516	
5671	027326	000			
5672	027327	040	051040	030120	RP02: .ASCIZ @ RP02@
5673	027334	000062			
5674	027336	020040	050122	031460	RP03: .ASCIZ @ RP03@
5675	027344	000			
5676	027345	104	044522	042526	DRVTST: .ASCIZ @DRIVE(S) TO BE USED @
5677	027352	051450	020051	047524	
5678	027360	041040	020105	051525	
5679	027366	042105	000040		
5680	027372	047516	042516	000	NONE: .ASCIZ @NONE@
5681	027377	054	000		COMMA: .ASCIZ @,@
5682	027401	015	005012	042524	TSTING: .ASCIZ <15><12><12>@TESTING WITH DRIVE @
5683	027406	052123	047111	020107	
5684	027414	044527	044124	042040	
5685	027422	044522	042526	000040	
5686	027430	020040	000		BLNKS2: .ASCIZ @ @
5687					
5688	027433	015	005012	047520	MSG15A: .ASCIZ <15><12><12>@POWER FAIL TEST@
5689	027440	042527	020122	040506	
5690	027446	046111	052040	051505	
5691	027454	000124			
5692	027456	005015	052012	051125	MSG15B: .ASCII <15><12><12>@TURN CPU POWER O F F AFTER THIS MESSAGE. WAIT@<15><12>
5693	027464	020116	050103	020125	
5694	027472	047520	042527	020122	
5695	027500	047440	043040	043040	
5696	027506	020040	043101	042524	
5697	027514	020122	044124	051511	
5698	027522	046440	051505	040523	
5699	027530	042507	020054	040527	
5700	027536	052111	005015		
5701	027542	020065	042523	047503	.ASCIZ @5 SECONDS, THEN TURN CPU POWER O N AGAIN@<15><12>
5702	027550	042116	026123	052040	
5703	027556	042510	020116	052524	
5704	027564	047122	041440	052520	
5705	027572	050040	053517	051105	
5706	027600	020040	020117	020116	
5707	027606	040440	040507	047111	
5708	027614	005015	000		
5709	027617	015	042412	042116	MSG15C: .ASCIZ <15><12>@END OF POWER FAIL TEST@<15><12>
5710	027624	047440	020106	047520	
5711	027632	042527	020122	040506	
5712	027640	046111	052040	051505	
5713	027646	006524	000012		
5714	027652	005015	050012	047522	MSG15D: .ASCIZ <15><12><12>@PROGRAM WILL LOOP, WAITING FOR DRIVE TO COME ONLINE@<15><12>
5715	027660	051107	046501	053440	
5716	027666	046111	020114	047514	
5717	027674	050117	020054	040527	
5718	027702	052111	047111	020107	

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5719 027710 047506 020123 051104
5720 027716 053111 020105 047524
5721 027724 041440 046517 020105
5722 027732 047117 044514 042516
5723 027740 005015 000
5724 027743 015 005012 042510
5725 027750 042101 040440 044514
5726 027756 047107 042515 052116
5727 027764 051040 052517 044524
5728 027772 042516 047117
5729 027774 005015 041412 047117
5730 030002 042516 052102 040440
5731 030010 045040 046520 042520
5732 030016 020123 042524 053524
5733 030024 042516 020116 044520
5734 030032 051516 045040 031461
5735 030040 031115 023040 045040
5736 030046 031461 031103 047440
5737 030054 020116 041224 020105
5738 030062 050123 030461 020105
5739 030070 040502 045503 046120
5740 030076 047101 105
5741 030101 015 020012 051050
5742 030106 043105 051105 052040
5743 030114 020117 044124 020105
5744 030122 050101 046120 041511
5745 030130 041101 042514 046440
5746 030136 044501 052516 047105
5747 030144 047101 042503 046440
5748 030152 047101 040525 020114
5749 030160 047506 020123 044124
5750 030166 020105 051104 053111
5751 030174 020105 042523 052524
5752 030202 027120 051
5753 030205 015 005012 047524
5754 030212 043507 042514 051440
5755 030220 044527 041524 020110
5756 030226 020067 047506 020122
5757 030234 042510 042101 051440
5758 030242 046105 041505 044524
5759 030250 047117 005015 000
5760 030255 015 042412 052116
5761 030262 051105 044040 040505
5762 030270 020104 042101 051104
5763 030276 051505 020123 044450
5764 030304 020116 041517 040524
5765 030312 024514 020072 000
5766
5767
5768 030317 015 005012 050122
5769 030324 030461 020105 047503
5770 030332 052116 047522 020114
5771 030340 040520 042516 020114
5772 030346 053523 052111 044103
5773 030354 052040 051505 000124
5774 030362 005015 054524 042520

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MSG16A: .ASCII <15><12><12>@HEAD ALIGNMENT ROUTINE@

.ASCII <15><12><12>@CONNECT A JUMPER BETWEEN PINS J13M2 & J13C2 ON THE RP11E BA

.ASCII <15><12>@ (REFER TO THE APPLICABLE MAINTENANCE MANUAL FOR THE DRIVE SETU

.ASCIZ <15><12><12>@TOGGLE SWITCH 7 FOR HEAD SELECTION@<15><12>

MSG16B: .ASCIZ <15><12>@ENTER HEAD ADDRESS (IN OCTAL): @

MSG17A: .ASCIZ <15><12><12>@RP11E CONTROL PANEL SWITCH TEST@

MSG17B: .ASCIZ <15><12>@TYPE A 'CR' TO START TEST AFTER EACH SETUP@<15><12>

5775 030370 040440 023440 051103
5776 030376 020047 047524 051440
5777 030404 040524 052122 052040
5778 030412 051505 020124 043101
5779 030420 042524 020124 040505
5780 030426 044103 051440 052105
5781 030434 050125 005015 000
5782 030441 015 042012 051511
5783 030446 041101 042514 042040
5784 030454 044522 042526 021440
5785 030462 000040
5786 030464 005015 047105 041101
5787 030472 042514 042040 044522
5788 030500 042526 021440 000040
5789 030506 005015 042523 020124
5790 030514 051047 040505 020104
5791 030522 047117 054514 020047
5792 030530 047117 042040 044522
5793 030536 042526 000040
5794 030542 005015 042523 020124
5795 030550 051047 040505 026504
5796 030556 051127 052111 023505
5797 030564 047440 020116 051104
5798 030572 053111 020105 000
5799 030577 015 051412 052105
5800 030604 023440 051127 052111
5801 030612 020105 047514 045503
5802 030620 052517 023524 040440
5803 030626 042116 041440 042514
5804 030634 051101 052040 042510
5805 030642 046040 040517 051440
5806 030650 044527 041524 042510
5807 030656 123
5808 030657 015 047412 020116
5809 030664 044124 020105 050122
5810 030672 030461 020105 047503
5811 030700 052116 047522 020114
5812 030706 040520 042516 000114
5813 030714 005015 042523 020124
5814 030722 044124 020105 047506
5815 030730 046114 053517 047111
5816 030736 020107 041517 040524
5817 030744 020114 040526 052514
5818 030752 051505 044440 020116
5819 030760 044124 020105 054503
5820 030766 044514 042116 051105
5821 030774 046040 040517 051440
5822 031002 044527 041524 042510
5823 031010 035123 000040
5824 031014 005015 042523 020124
5825 031022 053523 052111 044103
5826 031030 051505 052040 020117
5827 031036 000
5828 031037 015 051012 051505
5829 031044 052105 052040 042510
5830 031052 041440 046131 047111

MSG17C: .ASCIZ <15><12>DISABLE DRIVE # 2

MSG17D: .ASCIZ <15><12>ENABLE DRIVE # 2

MSG17E: .ASCIZ <15><12>SET 'READ ONLY' ON DRIVE 2

MSG17F: .ASCIZ <15><12>SET 'READ-WRITE' ON DRIVE 2

MSG17G: .ASCII <15><12>SET 'WRITE LOCKOUT' AND CLEAR THE LOA SWITCHES

.ASCIZ <15><12>ON THE RP11E CONTROL PANEL

MSG17H: .ASCIZ <15><12>SET THE FOLLOWING OCTAL VALUES IN THE CYLINDER LOA SWITCHES: 2

MSG17I: .ASCIZ <15><12>SET SWITCHES TO 2

MSG17J: .ASCII <15><12>RESET THE CYLINDER LOA SWITCHES

5831	031060	042504	020123	047514
5832	031066	020101	053523	052111
5833	031074	044103	051505	
5834	031100	005015	051412	052105
5835	031106	052040	042510	043040
5836	031114	046117	047514	044527
5837	031122	043516	047440	052103
5838	031130	046101	053040	046101
5839	031136	042520	020123	047111
5840	031144	052040	042510	042040
5841	031152	044522	042526	046040
5842	031160	040517	051440	044527
5843	031166	041524	042510	035123
5844	031174	000040		
5845	031176	005015	042412	042116
5846	031204	047440	020106	047503
5847	031212	051116	046117	050040
5848	031220	047101	046105	051440
5849	031226	044527	041524	020110
5850	031234	042522	052123	
5851	031240	005015	042522	052524
5852	031246	047122	051040	030520
5853	031254	042461	041440	047117
5854	031262	051124	046117	050040
5855	031270	047101	046105	051440
5856	031276	044527	041524	042510
5857	031304	020123	047524	047040
5858	031312	051117	040515	006514
5859	031320	000012		
5860				
5861	031322	000057		SLASH: .ASCIZ @/@
5862	031324	005015	047105	042524
5863	031332	020122	042524	052123
5864	031340	047040	046525	042502
5865	031346	035122	000040	
5866	031352	047105	042524	020122
5867	031360	051104	053111	020105
5868	031366	052516	041115	051105
5869	031374	020072	000	
5870	031377	104	044522	042526
5871	031404	000040		
5872	031406	044103	047101	042507
5873	031414	050040	051101	046501
5874	031422	052105	051105	020123
5875	031430	047506	020122	042524
5876	031436	052123	030440	020064
5877	031444	020077	000	
5878	031447	115	054101	046511
5879	031454	046525	053440	051117
5880	031462	020104	047503	047125
5881	031470	020124	047506	020122
5882	031476	042524	052123	030440
5883	031504	020064	044450	020116
5884	031512	041517	040524	024514
5885	031520	020072	000	
5886	031523	105	052116	051105

.ASCIZ <15><12><12>@SET THE FOLLOWING OCTAL VALUES IN THE DRIVE LOA SWITCHES: @

MSG17K: .ASCII <15><12><12>@END OF CONROL PANEL SWITCH TEST@

.ASCIZ <15><12>@RETURN RP11E CONTROL PANEL SWITCHES TO NORMAL@<15><12>

ENTNM: .ASCIZ <15><12>@ENTER TEST NUMBER: @

DRVNM: .ASCIZ @ENTER DRIVE NUMBER: @

DRIVE: .ASCIZ @DRIVE @

CHNGPM: .ASCIZ @CHANGE PARAMETERS FOR TEST 14 ? @

MXWRDS: .ASCIZ @MAXIMUM WORD COUNT FOR TEST 14 (IN OCTAL): @

ENTWC: .ASCIZ @ENTER NEW WORD COUNT (IN OCTAL): @

5887	031530	047040	053505	053440
5888	031536	051117	020104	047503
5889	031544	047125	020124	044450
5890	031552	020116	041517	040524
5891	031560	024514	020072	000
5892	031565	051105	052116	051105
5893	031572	050040	052101	042524
5894	031600	047122	051440	046105
5895	031606	041505	044524	047117
5896	031614	041440	042117	020105
5897	031622	047506	020122	042524
5898	031630	052123	030440	035064
5899	031636	000040		
5900	031640	051525	020105	020101
5901	031646	050123	041505	043111
5902	031654	041511	042040	051511
5903	031662	020113	042101	051104
5904	031670	051505	020123	047506
5905	031676	020122	042524	052123
5906	031704	030440	020064	020077
5907	031712	000		
5908	031713	040	040450	051440
5909	031720	047111	046107	020105
5910	031726	040504	040524	050040
5911	031734	052101	042524	047122
5912	031742	046440	051525	020124
5913	031750	040510	042526	041040
5914	031756	042505	020116	050123
5915	031764	041505	043111	042511
5916	031772	006504	012	
5917	031775	040	043111	047040
5918	032002	052117	020054	040520
5919	032010	052124	051105	020116
5920	032016	020060	040450	046114
5921	032024	055040	051105	051517
5922	032032	020051	044527	046114
5923	032040	041040	020105	051525
5924	032046	042105	006451	000012
5925	032054	047105	042524	020122
5926	032062	054503	044514	042116
5927	032070	051105	040440	042104
5928	032076	042522	051523	020072
5929	032104	000		
5930	032105	105	052116	051105
5931	032112	052040	040522	045503
5932	032120	040440	042104	042522
5933	032126	051523	020072	000
5934	032133	105	052116	051105
5935	032140	051440	041505	047524
5936	032146	020122	042101	051104
5937	032154	051505	035123	000040
5938				
5939	032162	050122	042101	020122
5940	032170	020075	000	
5941				
5942				

CHNGPT: .ASCIZ ENTER PATTERN SELECTION CODE FOR TEST 14: 3

CHNGAD: .ASCIZ USE A SPECIFIC DISK ADDRESS FOR TEST 14 ? 3

ONEPAT: .ASCII 3 (A SINGLE DATA PATTERN MUST HAVE BEEN SPECIFIED)3<15><12>

.ASCIZ 3 IF NOT, PATTERN 0 (ALL ZEROS) WILL BE USED)3<15><12>

ENTCYL: .ASCIZ ENTER CYLINDER ADDRESS: 3

ENTRK: .ASCIZ ENTER TRACK ADDRESS: 3

ENTSEC: .ASCIZ ENTER SECTOR ADDRESS: 3

MRPADR: .ASCIZ RPADR = 3

.SBTTL ERROR MESSAGES

5943						
5944	032173	122	030520	020061	EM1:	.ASCIZ @RP11 DIDN'T RESPOND TO ADDRESSING@
5945	032200	044504	047104	052047		
5946	032206	051040	051505	047520		
5947	032214	042116	052040	020117		
5948	032222	042101	051104	051505		
5949	032230	044523	043516	000		
5950	032236	051440	047101	052047	EM2:	.ASCIZ @CAN'T START READ COMMAND@
5951	032242	051440	040524	052122		
5952	032250	051040	040505	020104		
5953	032256	047503	046515	047101		
5954	032264	000104				
5955	032266	041447	042514	051101	EM3:	.ASCIZ @'CLEAR' COMMAND DIDN'T TERMINATE DATA TRANSFER OPERATIONS@
5956	032274	020047	047503	046515		
5957	032302	047101	020104	044504		
5958	032310	047104	052047	052040		
5959	032316	051105	044515	040516		
5960	032324	042524	042040	052101		
5961	032332	020101	051124	047101		
5962	032340	043123	051105	047440		
5963	032346	042520	040522	044524		
5964	032354	047117	000			
5965	032357	105	051122	051117	EM4:	.ASCIZ @ERROR WRITING TEST SECTOR(S)@
5966	032364	053440	044522	044524		
5967	032372	043516	052040	051505		
5968	032400	020124	042523	052103		
5969	032406	051117	051450	000051	EM5:	.ASCIZ @WRITE CHECK ERROR CHECKING TEST SECTOR(S)@
5970	032414	051127	052111	020105		
5971	032422	044103	041505	020113		
5972	032430	051105	047522	020122		
5973	032436	044103	041505	044513		
5974	032444	043516	052040	051505		
5975	032452	020124	042523	052103		
5976	032460	051117	051450	000051	EM6:	.ASCIZ @EXPECTED 'WCE' DIDN'T OCCUR@
5977	032466	054105	042520	052103		
5978	032474	042105	023440	041527		
5979	032502	023505	042040	042111		
5980	032510	023516	020124	041517		
5981	032516	052503	000122			
5982	032522	051105	047522	020122	EM7:	.ASCIZ @ERROR WRITING LESS THAN A FULL SECTOR@
5983	032530	051127	052111	047111		
5984	032536	020107	042514	051523		
5985	032544	052040	040510	020116		
5986	032552	020101	052506	046114		
5987	032560	051440	041505	047524		
5988	032566	000122				
5989	032570	047117	020105	043117	EM11:	.ASCIZ @ONE OF 2 WORDS WRITTEN IN THE PARTIAL WRITE DIDN'T COMPARE@
5990	032576	031040	053440	051117		
5991	032604	051504	053440	044522		
5992	032612	052124	047105	044440		
5993	032620	020116	044124	020105		
5994	032626	040520	052122	040511		
5995	032634	020114	051127	052111		
5996	032642	020105	044504	047104		
5997	032650	052047	041440	046517		
5998	032656	040520	042522	000		

ERROR MESSAGES

5999	032663	116	047117	055055	EM12: .ASCII	NON-ZERO DATA IN PART OF SECTOR WHICH SHOULD HAVE	<15><12>
6000	032670	051105	020117	040504			
6001	032676	040524	044440	020116			
6002	032704	040520	052122	047440			
6003	032712	020106	042523	052103			
6004	032720	051117	053440	044510			
6005	032726	044103	051440	047510			
6006	032734	046123	020104	040510			
6007	032742	042526	005015				
6008	032746	042502	047105	055040	.ASCIZ	BEEN ZERO FILLED DURING PARTIAL WRITE	
6009	032754	051105	020117	044506			
6010	032762	046114	042105	042040			
6011	032770	051125	047111	020107			
6012	032776	040520	052122	040511			
6013	033004	020114	051127	052111			
6014	033012	000105					
6015	033014	042447	050117	020047	EM13: .ASCII	'EOP' DIDN'T SET DURING A 2 SECTOR WRITE BEGINNING	
6016	033022	044504	047104	052047			
6017	033030	051440	052105	042040			
6018	033036	051125	047111	020107			
6019	033044	020101	020062	042523			
6020	033052	052103	051117	053440			
6021	033060	044522	042524	041040			
6022	033066	043505	047111	044516			
6023	033074	043516					
6024	033076	005015	052101	052040	.ASCIZ	<15><12> AT THE LAST SECTOR OF THE PACK	
6025	033104	042510	046040	051501			
6026	033112	020124	042523	052103			
6027	033120	051117	047440	020106			
6028	033126	044124	020105	040520			
6029	033134	045503	000				
6030	033137	047	051105	023522	EM14: .ASCIZ	'ERR' DIDN'T SET WITH 'EOP'	
6031	033144	042040	042111	023516			
6032	033152	020124	042523	020124			
6033	033160	044527	044124	023440			
6034	033166	047505	023520	000			
6035	033173	047	047505	023520	EM15: .ASCIZ	'EOP' DIDN'T CLEAR RPCA	
6036	033200	042040	042111	023516			
6037	033206	020124	046103	040505			
6038	033214	020122	050122	040503			
6039	033222	000					
6040	033223	123	041525	020101	EM16: .ASCIZ	SUCA NOT CORRECT AFTER 'EOP'	
6041	033230	047516	020124	047503			
6042	033236	051122	041505	020124			
6043	033244	043101	042524	020122			
6044	033252	042447	050117	000047			
6045	033260	051105	047522	020122	EM17: .ASCIZ	ERROR READING TEST SECTOR	
6046	033266	042522	042101	047111			
6047	033274	020107	042524	052123			
6048	033302	051440	041505	047524			
6049	033310	000122					
6050	033312	047503	052116	047105	EM20: .ASCII	CONTENTS OF FIRST SECTOR ON PACK CHANGED AFTER	<15><12>
6051	033320	051524	047440	020106			
6052	033326	044506	051522	020124			
6053	033334	042523	052103	051117			
6054	033342	047440	020116	040520			

ERROR MESSAGES

6055	033350	045503	041440	040510	
6056	033356	043516	042105	040440	
6057	033364	052106	051105	005015	
6058	033372	047506	041522	047111	.ASCIZ @FORCING 'EOP' DURING WRIT@
6059	033400	020107	042447	050117	
6060	033406	020047	052504	044522	
6061	033414	043516	053440	044522	
6062	033422	042524	000		
6063	033425	047	051120	043517	EM21: .ASCII @'PROG' ERROR DIDN'T SET WHEN READ COMMAND ISSUED@
6064	033432	020047	051105	047522	
6065	033440	020122	044504	047104	
6066	033446	052047	051440	052105	
6067	033454	053440	042510	020116	
6068	033462	042522	042101	041440	
6069	033470	046517	040515	042116	
6070	033476	044440	051523	042525	
6071	033504	104			
6072	033505	015	053412	044510	.ASCIZ <15><12>@WHILE CONTROLLER BUSY@
6073	033512	042514	041440	047117	
6074	033520	051124	046117	042514	
6075	033526	020122	052502	054523	
6076	033534	000			
6077	033535	047	051105	023522	EM22: .ASCIZ @'ERR' DIDN'T SET WITH 'PROG'@
6078	033542	042040	042111	023516	
6079	033550	020124	042523	020124	
6080	033556	044527	044124	023440	
6081	033564	051120	043517	000047	
6082	033572	051105	047522	020122	EM23: .ASCIZ @ERROR ATTEMPTING TO MISFORMAT SECTOR @
6083	033600	052101	042524	050115	
6084	033606	044524	043516	052040	
6085	033614	020117	044515	043123	
6086	033622	051117	040515	020124	
6087	033630	042523	052103	051117	
6088	033636	030040	000		
6089	033641	105	051122	051117	EM24: .ASCIZ @ERROR VERIFYING THE MISFORMATTED TEST HEADER@
6090	033646	053040	051105	043111	
6091	033654	044531	043516	052040	
6092	033662	042510	046440	051511	
6093	033670	047506	046522	052101	
6094	033676	042524	020104	042524	
6095	033704	052123	044040	040505	
6096	033712	042504	000122		
6097	033716	044515	043123	051117	EM25: .ASCIZ @MISFORMATTED TEST HEADER IS NOT CORRECT@
6098	033724	040515	052124	042105	
6099	033732	052040	051505	020124	
6100	033740	042510	042101	051105	
6101	033746	044440	020123	047516	
6102	033754	020124	047503	051122	
6103	033762	041505	000124		
6104	033766	044047	043116	020047	EM26: .ASCIZ @'HNF' DIDN'T SET WHEN SEARCHING FOR MISFORMATTED SECTOR@
6105	033774	044504	047104	052047	
6106	034002	051440	052105	053440	
6107	034010	042510	020116	042523	
6108	034016	051101	044103	047111	
6109	034024	020107	047506	020122	
6110	034032	044515	043123	051117	

ERROR MESSAGES

6111	034040	040515	052124	042105		
6112	034046	051440	041505	047524		
6113	034054	000122				
6114	034056	042047	045523	020047	EM27:	.ASCIZ @'DSK' NOT SET WITH 'HNF'@
6115	034064	047516	020124	042523		
6116	034072	020124	044527	044124		
6117	034100	023440	047110	023506		
6118	034106	000				
6119	034107	047	042510	020047	EM30:	.ASCIZ @'HE' DIDN'T SET WITH 'HNF'@
6120	034114	044504	047104	052047		
6121	034122	051440	052105	053440		
6122	034130	052111	020110	044047		
6123	034136	043116	000047			
6124	034142	042447	051122	020047	EM31:	.ASCIZ @'ERR' NOT SET WITH 'HNF'@
6125	034150	047516	020124	042523		
6126	034156	020124	044527	044124		
6127	034164	023440	047110	023506		
6128	034172	000				
6129	034173	105	051122	051117	EM32:	.ASCIZ @ERROR WHILE RESTORING MISFORMATTED TEST HEADER@
6130	034200	053440	044510	042514		
6131	034206	051040	051505	047524		
6132	034214	044522	043516	046440		
6133	034222	051511	047506	046522		
6134	034230	052101	042524	020104		
6135	034236	042524	052123	044040		
6136	034244	040505	042504	000122		
6137	034252	747503	052116	047522	EM33:	.ASCII @CONTROLLER DIDN'T BECOME BUSY WHEN READ COMMAND@
6138	034260	146114	051105	042040		
6139	034266	2111	023516	020124		
6140	034274	42502	047503	042515		
6141	034302	41040	051525	020131		
6142	034310	044127	047105	051040		
6143	034316	040505	020104	047503		
6144	034324	046515	047101	104		
6145	034331	015	044412	051523		.ASCIZ <15><12>@ISSUED TO SEEKING DRIVE@
6146	034336	042525	020104	047524		
6147	034344	051440	042505	044513		
6148	034352	043516	042040	044522		
6149	034360	042526	000			
6150	034363	105	051122	051117	EM34:	.ASCIZ @ERROR DURING READ COMMAND WHICH WAS ISSUED TO SEEKING DRIVE@
6151	034370	042040	051125	047111		
6152	034376	020107	042522	042101		
6153	034404	041440	046517	040515		
6154	034412	042116	053440	044510		
6155	034420	044103	053440	051501		
6156	034426	044440	051523	042525		
6157	034434	020104	047524	051440		
6158	034442	042505	044513	043516		
6159	034450	042040	044522	042526		
6160	034456	000				
6161	034457	104	052101	020101	EM35:	.ASCIZ @DATA INCORRECT FROM HEADER READ COMMAND ISSUED TO A SEEKING DRIVE@
6162	034464	047111	047503	051122		
6163	034472	041505	020124	051106		
6164	034500	046517	044040	040505		
6165	034506	042504	020122	042522		
6166	034514	042101	041440	046517		

ERROR MESSAGES

6167	034522	040515	042116	044440	
6168	034530	051523	042525	020104	
6169	034536	047524	040440	051440	
6170	034544	042505	044513	043516	
6171	034552	042040	044522	042526	
6172	034560	000			
6173	034561	047	054116	042515	EM36: .ASCIZ @'NXME' DIDN'T SET WHEN LOCATION 760000 ADDRESSED@
6174	034566	020047	044504	047104	
6175	034574	052047	051440	052105	
6176	034602	053440	042510	020116	
6177	034610	047514	040503	044524	
6178	034616	047117	033440	030066	
6179	034624	030060	020060	042101	
6180	034632	051104	051505	042523	
6181	034640	000104			
6182	034642	044047	023505	042040	EM37: .ASCIZ @'HE' DIDN'T SET WITH 'NXME'@
6183	034650	042111	023516	020124	
6184	034656	042523	020124	044527	
6185	034664	044124	023440	054116	
6186	034672	042515	000047		
6187	034676	042447	051122	020047	EM40: .ASCIZ @'ERR' DIDN'T SET WITH 'NXME'@
6188	034704	044504	047104	052067	
6189	034712	051440	052105	053440	
6190	034720	052111	020110	047047	
6191	034726	046530	023505	000	
6192	034733	105	051122	051117	EM41: .ASCIZ @ERROR FORMATTING TRACK @
6193	034740	043040	051117	040515	
6194	034746	052124	047111	020107	
6195	034754	051124	041501	020113	
6196	034762	000060			
6197	034764	051105	047522	020122	EM42: .ASCIZ @ERROR READING THE HEADER FROM THE TEST SECTOR@
6198	034772	042522	042101	047111	
6199	035000	020107	044124	020105	
6200	035006	042510	042101	051105	
6201	035014	043040	047522	020115	
6202	035022	044124	020105	042524	
6203	035030	052123	051440	041505	
6204	035036	047524	000122		
6205	035042	042523	052103	051117	EM43: .ASCIZ @SECTOR FIELD FROM HEADER IS NOT CORRECT@
6206	035050	043040	042511	042114	
6207	035056	043040	047522	020115	
6208	035064	042510	042101	051105	
6209	035072	044440	020123	047516	
6210	035100	020124	047503	051122	
6211	035106	041505	000124		
6212	035112	051105	047522	020122	EM44: .ASCIZ @ERROR WRITING SECTOR ADDRESS IN DATA FIELD@
6213	035120	051127	052111	047111	
6214	035126	020107	042523	052103	
6215	035134	051117	040440	042104	
6216	035142	042522	051523	044440	
6217	035150	020116	040504	040524	
6218	035156	043040	042511	042114	
6219	035164	000			
6220	035165	047	047523	023524	EM45: .ASCIZ @'SOT' OR SECTOR ADDRESS REGISTER IS NOT CORRECT@
6221	035172	047440	020122	042523	
6222	035200	052103	051117	040440	

02768 035672 041517 030440
0277 035664 020107 052101
0276 035656 040522 052102
0275 035650 040522 052101
0274 035642 040522 051117
0273 035634 040522 052106
0272 035626 040522 052106
0271 035620 040522 051440
0270 035616 040522 051440
0269 035610 040522 051440
0268 035602 040522 052101
0267 035594 040522 051440
0266 035586 040522 051440
0265 035578 040522 051440
0264 035570 040522 051440
0263 035562 040522 051440
0262 035554 040522 051440
0261 035546 040522 051440
0260 035538 040522 051440
0259 035530 040522 051440
0258 035522 040522 051440
0257 035514 040522 051440
0256 035506 040522 051440
0255 035498 040522 051440
0254 035490 040522 051440
0253 035482 040522 051440
0252 035474 040522 051440
0251 035466 040522 051440
0250 035458 040522 051440
0249 035450 040522 051440
0248 035442 040522 051440
0247 035434 040522 051440
0246 035426 040522 051440
0245 035418 040522 051440
0244 035410 040522 051440
0243 035402 040522 051440
0242 035394 040522 051440
0241 035386 040522 051440
0240 035378 040522 051440
0239 035370 040522 051440
0238 035362 040522 051440
0237 035354 040522 051440
0236 035346 040522 051440
0235 035338 040522 051440
0234 035330 040522 051440
0233 035322 040522 051440
0232 035314 040522 051440
0231 035306 040522 051440
0230 035298 040522 051440
0229 035290 040522 051440
0228 035282 040522 051440
0227 035274 040522 051440
0226 035266 040522 051440
0225 035258 040522 051440
0224 035250 040522 051440
0223 035242 040522 051440
0222 035234 040522 051440
0221 035226 040522 051440
0220 035218 040522 051440
0219 035210 040522 051440
0218 035202 040522 051440
0217 035194 040522 051440
0216 035186 040522 051440
0215 035178 040522 051440
0214 035170 040522 051440
0213 035162 040522 051440
0212 035154 040522 051440
0211 035146 040522 051440
0210 035138 040522 051440
0209 035130 040522 051440
0208 035122 040522 051440
0207 035114 040522 051440
0206 035106 040522 051440
0205 035098 040522 051440
0204 035090 040522 051440
0203 035082 040522 051440
0202 035074 040522 051440
0201 035066 040522 051440
0200 035058 040522 051440
0199 035050 040522 051440
0198 035042 040522 051440
0197 035034 040522 051440
0196 035026 040522 051440
0195 035018 040522 051440
0194 035010 040522 051440
0193 035002 040522 051440
0192 034994 040522 051440
0191 034986 040522 051440
0190 034978 040522 051440
0189 034970 040522 051440
0188 034962 040522 051440
0187 034954 040522 051440
0186 034946 040522 051440
0185 034938 040522 051440
0184 034930 040522 051440
0183 034922 040522 051440
0182 034914 040522 051440
0181 034906 040522 051440
0180 034898 040522 051440
0179 034890 040522 051440
0178 034882 040522 051440
0177 034874 040522 051440
0176 034866 040522 051440
0175 034858 040522 051440
0174 034850 040522 051440
0173 034842 040522 051440
0172 034834 040522 051440
0171 034826 040522 051440
0170 034818 040522 051440
0169 034810 040522 051440
0168 034802 040522 051440
0167 034794 040522 051440
0166 034786 040522 051440
0165 034778 040522 051440
0164 034770 040522 051440
0163 034762 040522 051440
0162 034754 040522 051440
0161 034746 040522 051440
0160 034738 040522 051440
0159 034730 040522 051440
0158 034722 040522 051440
0157 034714 040522 051440
0156 034706 040522 051440
0155 034698 040522 051440
0154 034690 040522 051440
0153 034682 040522 051440
0152 034674 040522 051440
0151 034666 040522 051440
0150 034658 040522 051440
0149 034650 040522 051440
0148 034642 040522 051440
0147 034634 040522 051440
0146 034626 040522 051440
0145 034618 040522 051440
0144 034610 040522 051440
0143 034602 040522 051440
0142 034594 040522 051440
0141 034586 040522 051440
0140 034578 040522 051440
0139 034570 040522 051440
0138 034562 040522 051440
0137 034554 040522 051440
0136 034546 040522 051440
0135 034538 040522 051440
0134 034530 040522 051440
0133 034522 040522 051440
0132 034514 040522 051440
0131 034506 040522 051440
0130 034498 040522 051440
0129 034490 040522 051440
0128 034482 040522 051440
0127 034474 040522 051440
0126 034466 040522 051440
0125 034458 040522 051440
0124 034450 040522 051440
0123 034442 040522 051440
0122 034434 040522 051440
0121 034426 040522 051440
0120 034418 040522 051440
0119 034410 040522 051440
0118 034402 040522 051440
0117 034394 040522 051440
0116 034386 040522 051440
0115 034378 040522 051440
0114 034370 040522 051440
0113 034362 040522 051440
0112 034354 040522 051440
0111 034346 040522 051440
0110 034338 040522 051440
0109 034330 040522 051440
0108 034322 040522 051440
0107 034314 040522 051440
0106 034306 040522 051440
0105 034298 040522 051440
0104 034290 040522 051440
0103 034282 040522 051440
0102 034274 040522 051440
0101 034266 040522 051440
0100 034258 040522 051440
0099 034250 040522 051440
0098 034242 040522 051440
0097 034234 040522 051440
0096 034226 040522 051440
0095 034218 040522 051440
0094 034210 040522 051440
0093 034202 040522 051440
0092 034194 040522 051440
0091 034186 040522 051440
0090 034178 040522 051440
0089 034170 040522 051440
0088 034162 040522 051440
0087 034154 040522 051440
0086 034146 040522 051440
0085 034138 040522 051440
0084 034130 040522 051440
0083 034122 040522 051440
0082 034114 040522 051440
0081 034106 040522 051440
0080 034098 040522 051440
0079 034090 040522 051440
0078 034082 040522 051440
0077 034074 040522 051440
0076 034066 040522 051440
0075 034058 040522 051440
0074 034050 040522 051440
0073 034042 040522 051440
0072 034034 040522 051440
0071 034026 040522 051440
0070 034018 040522 051440
0069 034010 040522 051440
0068 034002 040522 051440
0067 033994 040522 051440
0066 033986 040522 051440
0065 033978 040522 051440
0064 033970 040522 051440
0063 033962 040522 051440
0062 033954 040522 051440
0061 033946 040522 051440
0060 033938 040522 051440
0059 033930 040522 051440
0058 033922 040522 051440
0057 033914 040522 051440
0056 033906 040522 051440
0055 033898 040522 051440
0054 033890 040522 051440
0053 033882 040522 051440
0052 033874 040522 051440
0051 033866 040522 051440
0050 033858 040522 051440
0049 033850 040522 051440
0048 033842 040522 051440
0047 033834 040522 051440
0046 033826 040522 051440
0045 033818 040522 051440
0044 033810 040522 051440
0043 033802 040522 051440
0042 033794 040522 051440
0041 033786 040522 051440
0040 033778 040522 051440
0039 033770 040522 051440
0038 033762 040522 051440
0037 033754 040522 051440
0036 033746 040522 051440
0035 033738 040522 051440
0034 033730 040522 051440
0033 033722 040522 051440
0032 033714 040522 051440
0031 033706 040522 051440
0030 033698 040522 051440
0029 033690 040522 051440
0028 033682 040522 051440
0027 033674 040522 051440
0026 033666 040522 051440
0025 033658 040522 051440
0024 033650 040522 051440
0023 033642 040522 051440
0022 033634 040522 051440
0021 033626 040522 051440
0020 033618 040522 051440
0019 033610 040522 051440
0018 033602 040522 051440
0017 033594 040522 051440
0016 033586 040522 051440
0015 033578 040522 051440
0014 033570 040522 051440
0013 033562 040522 051440
0012 033554 040522 051440
0011 033546 040522 051440
0010 033538 040522 051440
0009 033530 040522 051440
0008 033522 040522 051440
0007 033514 040522 051440
0006 033506 040522 051440
0005 033498 040522 051440
0004 033490 040522 051440
0003 033482 040522 051440
0002 033474 040522 051440
0001 033466 040522 051440
0000 033458 040522 051440

EM46: .ASCIZ 2DATA READ FROM THE SECTOR IS NOT CORRECT2

EM47: .ASCII 2SECTOR CONTENTS WRONG, DATA SHOULD BE THE2(15)(12)

.ASCIZ 2TRACK NUMBER OF THE CURRENTLY SELECTED HEAD2

EM50: .ASCIZ 2ERROR AFTER 2 WORD READ STARTING AT LOC 1777762

EM51: .ASCII 2'MEXD' DIDN'T SET AFTER 2 WORD READ STARTING2(15)(12)

.ASCIZ 2AT LOC 1777762

EM52: .ASCIZ 2'MEXI' SET AFTER 2 WORD READ STARTING AT LOC 1777762

63279	035700	033467	000066		
63280	035704	047503	052116	047105	EMS3: .ASCII 2CONTENTS OF LOC 20000 WRONG AFTER 2 WORD READ(15)<12>
63281	035712	047514	047440	020106	
63282	035714	047514	020103	030062	
63283	035714	047514	040505	044440	
63284	035714	047514	051101	031040	
63285	035714	047514	051101	050104	
63286	035714	047514	042101	005015	
63287	035714	047514	051101	044515	.ASCIZ 2STARTING AT LOC 1777762
63288	035714	047514	020440	020124	
63289	035714	047514	020103	033461	
63290	035714	047514	033067	000	
63291	036013	04105	051122	051117	EMS4: .ASCIZ 2ERROR AFTER 2 WORD READ STARTING AT LOC 3777762
63292	036013	04105	052106	051105	
63293	036013	04105	044440	051105	
63294	036013	04105	044440	051101	
63295	036013	04105	040505	042122	
63296	036013	04105	020107	040505	
63297	036013	04105	041517	031440	
63298	036013	04105	033467	000066	
63299	036013	04105	054105	023466	EMS5: .ASCIZ 2'MEXD' DIDN'T CLEAR AFTER 2 WORD STARTING AT LOC 3777762
63300	036013	04105	042105	023466	
63301	036107	042105	042105	023466	
63302	036107	042105	046103	040505	
63303	036107	042105	043101	042122	
63304	036114	042122	020062	047527	
63305	036114	042122	051440	040505	
63306	036114	042122	047111	020107	
63307	036114	042122	046040	041517	
63308	036152	031440	033467	033467	
63309	036160	000066			
63310	036160	046447	054105	023461	EMS6: .ASCIZ 2'MEXI' DIDN'T SET AFTER 2 WORD READ STARTING AT LOC 3777762
63311	036170	042105	042111	023516	
63312	036176	020124	042123	020124	
63313	036204	043101	047527	020122	
63314	036212	020062	047527	042122	
63315	036220	051040	040505	020104	
63316	036226	052123	051101	044524	
63317	036234	043516	040440	020124	
63318	036242	047514	020103	033463	
63319	036250	033467	033067	000	
63320	036255	103	047117	042524	EMS7: .ASCII 2CONTENTS OF LOC 40000 WRONG AFTER 2 WORD READ(15)<12>
63321	036265	052116	020123	043117	
63322	036270	046040	041517	032040	
63323	036276	030060	030060	020060	
63324	036304	051127	047117	020107	
63325	036312	043101	042524	020122	
63326	036320	020062	047527	042122	
63327	036326	051040	040505	006504	
63328	036334	012			
63329	036335	123	040524	052122	.ASCIZ 2STARTING AT LOCATION 3777762
63330	036342	047111	020107	052101	
63331	036350	046040	041517	052101	
63332	036356	047511	020116	033463	
63333	036364	033467	033067	000	
63334	036371	105	051122	051117	EM60: .ASCII 2ERROR RETURNING SECTOR TO PDP-11 MODE USING A2(15)<12>

6335	036376	051040	052105	051125
6336	036406	051516	052116	051140
6337	036419	051506	047524	020122
6338	036440	051750	050040	050104
6339	036444	051750	050040	050104
6340	036444	051750	050040	050104
6341	036444	051750	050040	050104
6342	036444	051750	050040	050104
6343	036444	051750	050040	050104
6344	036444	051750	050040	050104
6345	036444	051750	050040	050104
6346	036444	051750	050040	050104
6347	036444	051750	050040	050104
6348	036444	051750	050040	050104
6349	036444	051750	050040	050104
6350	036444	051750	050040	050104
6351	036444	051750	050040	050104
6352	036444	051750	050040	050104
6353	036444	051750	050040	050104
6354	036444	051750	050040	050104
6355	036444	051750	050040	050104
6356	036444	051750	050040	050104
6357	036444	051750	050040	050104
6358	036444	051750	050040	050104
6359	036444	051750	050040	050104
6360	036444	051750	050040	050104
6361	036444	051750	050040	050104
6362	036444	051750	050040	050104
6363	036444	051750	050040	050104
6364	036444	051750	050040	050104
6365	036444	051750	050040	050104
6366	036444	051750	050040	050104
6367	036444	051750	050040	050104
6368	036444	051750	050040	050104
6369	036444	051750	050040	050104
6370	036444	051750	050040	050104
6371	036444	051750	050040	050104
6372	036444	051750	050040	050104
6373	036444	051750	050040	050104
6374	036444	051750	050040	050104
6375	036444	051750	050040	050104
6376	036444	051750	050040	050104
6377	036444	051750	050040	050104
6378	036444	051750	050040	050104
6379	036444	051750	050040	050104
6380	036444	051750	050040	050104
6381	036444	051750	050040	050104
6382	036444	051750	050040	050104
6383	036444	051750	050040	050104
6384	036444	051750	050040	050104
6385	036444	051750	050040	050104
6386	036444	051750	050040	050104
6387	036444	051750	050040	050104
6388	036444	051750	050040	050104
6389	036444	051750	050040	050104
6390	036444	051750	050040	050104

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.ASCIZ @2 WORD WRITED
EM61: .ASCIZ @ERROR WRITING TEST SECTOR(S)@
EM62: .ASCIZ @ERROR READING TEST SECTOR(S)@
EM63: .ASCII @DRIVE DIDN'T RETURN TO CYL D FROM THE MAXIMUM@ (15) (12)
.ASCIZ @CYL ON CONTROLLER POWER FAIL@
EM64: .ASCII @CONTENTS OF MEMORY CHANGED DURING POWER@ (15) (12)
.ASCIZ @FAIL WHILE READING THE DISK@
EM65: .ASCIZ @'SUOL' SET WITH DRIVE DISABLED@
EM66: .ASCIZ @'SUOL' NOT SET WITH DRIVE ENABLED@

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ERROR MESSAGES

6391	037074	046102	042105	000	
6392	037101	047	052523	050127	EM67: .ASCIZ @'SUWP' NOT SET WITH DRIVE SET TO 'READ ONLY'@
6393	037106	047	047516	020124	
6394	037110	047	047516	020124	
6395	037112	047	047516	020124	
6396	037114	047	047516	020124	
6397	037116	047	047516	020124	
6398	037118	047	047516	020124	
6399	037120	047	047516	020124	
6400	037122	047	047516	020124	
6401	037124	047	047516	020124	
6402	037126	047	047516	020124	
6403	037128	047	047516	020124	
6404	037130	047	047516	020124	
6405	037132	047	047516	020124	
6406	037134	047	047516	020124	
6407	037136	047	047516	020124	
6408	037138	047	047516	020124	
6409	037140	047	047516	020124	
6410	037142	047	047516	020124	
6411	037144	047	047516	020124	
6412	037146	047	047516	020124	
6413	037148	047	047516	020124	
6414	037150	047	047516	020124	
6415	037152	047	047516	020124	
6416	037154	047	047516	020124	.ASCIZ @LOA'S = 0, AND RPCA = 0@
6417	037156	047	047516	020124	
6418	037158	047	047516	020124	
6419	037160	047	047516	020124	
6420	037162	047	047516	020124	
6421	037164	047	047516	020124	EM72: .ASCIZ @'SUWP' SET WITH RPCA = 2 & CYL LOA'S = 0@
6422	037166	047	047516	020124	
6423	037168	047	047516	020124	
6424	037170	047	047516	020124	
6425	037172	047	047516	020124	
6426	037174	047	047516	020124	
6427	037176	047	047516	020124	EM73: .ASCIZ @'SUWP' NOT SET; RPCA = VALUE IN CYL LOA'S@
6428	037178	047	047516	020124	
6429	037180	047	047516	020124	
6430	037182	047	047516	020124	
6431	037184	047	047516	020124	
6432	037186	047	047516	020124	
6433	037188	047	047516	020124	
6434	037190	047	047516	020124	
6435	037192	047	047516	020124	EM74: .ASCIZ @'SUWP' SET WITH RPCA ONE GREATER THAN CYL LOA VALUES
6436	037194	047	047516	020124	
6437	037196	047	047516	020124	
6438	037198	047	047516	020124	
6439	037200	047	047516	020124	
6440	037202	047	047516	020124	
6441	037204	047	047516	020124	
6442	037206	047	047516	020124	
6443	037208	047	047516	020124	
6444	037210	047	047516	020124	EM75: .ASCIZ @'SUWP' SET WITH DRIVE LOA SWITCHES EQUAL TO SELECTED DRIVES
6445	037212	047	047516	020124	
6446	037214	047	047516	020124	

ERROR MESSAGES

6447	037574	044522	042526	046040	
6448	037602	040517	051440	044524	
6449	037610	041524	042510	020123	
6450	037616	050505	040525	020114	
6451	037620	047522	045144	010104	
6452	037624	041522	045144	020104	
6453	037628	041522	045144	020104	
6454	037632	041522	045144	020104	
6455	037636	041522	045144	020104	
6456	037640	041522	045144	020104	
6457	037644	041522	045144	020104	
6458	037648	041522	045144	020104	
6459	037652	041522	045144	020104	
6460	037656	041522	045144	020104	
6461	037660	041522	045144	020104	
6462	037664	041522	045144	020104	
6463	037668	041522	045144	020104	
6464	037672	041522	045144	020104	
6465	037676	041522	045144	020104	
6466	037680	041522	045144	020104	
6467	037684	041522	045144	020104	
6468	037688	041522	045144	020104	
6469	037692	041522	045144	020104	
6470	037696	041522	045144	020104	
6471	037700	041522	045144	020104	
6472	037704	041522	045144	020104	
6473	037708	041522	045144	020104	
6474	037712	041522	045144	020104	
6475	037716	041522	045144	020104	
6476	037720	041522	045144	020104	
6477	037724	041522	045144	020104	
6478	037728	041522	045144	020104	
6479	037732	041522	045144	020104	
6480	037736	041522	045144	020104	
6481	037740	041522	045144	020104	
6482	037744	041522	045144	020104	
6483	037748	041522	045144	020104	
6484	037752	041522	045144	020104	
6485	037756	041522	045144	020104	
6486	037760	041522	045144	020104	
6487	037764	041522	045144	020104	
6488	037768	041522	045144	020104	
6489	037772	041522	045144	020104	
6490	037776	041522	045144	020104	
6491	037780	041522	045144	020104	
6492	037784	041522	045144	020104	
6493	037788	041522	045144	020104	
6494	037792	041522	045144	020104	
6495	037796	041522	045144	020104	
6496	037800	041522	045144	020104	
6497	037804	041522	045144	020104	
6498	037808	041522	045144	020104	
6499	037812	041522	045144	020104	
6500	037816	041522	045144	020104	
6501	037820	041522	045144	020104	
6502	037824	041522	045144	020104	
					EM76: .ASCIZ @DRIVE HAS GONE OFFLINE@
					EM77: .ASCIZ @DRIVE IS UNSAFE@
					EM100: .ASCIZ @DRIVE TIMED OUT@
					EM101: .ASCIZ @CONTROLLER TIMED OUT@
					EM102: .ASCIZ @DATA COMPARE ERROR@
					EM103: .ASCIZ @DATA COMPARE ERROR (MEMORY MANAGEMENT ENABLED)@
					EM106: .ASCIZ @'ERR' DIDN'T SET WITH 'WCE'@
					EM107: .ASCIZ @'HE' DIDN'T SET WITH 'PROG'@
					EM112: .ASCIZ @DRIVE UNSAFE AFTER POWER FAIL@
					EM113: .ASCIZ @'SUWP' NOT SET WITH DRIVE L0A SWITCHES GREATER THAN SELECTED DRIVES

6503	040272	040510	020116	042523
6504	040300	042514	052103	042105
6505	040306	042040	044522	042526
6506	040314	000		
6507				
6508				
6509				
6510	040315	122	040520	051104
6511	040322	000		
6512	040323	124		
6513	040330	020043	051505	020124
6514	040336	050040	042440	051122
6515	040344	020103	020103	042040
6516	040352	053122	021440	020040
6517	040360	051040	042120	020123
6518	040366	020040	051040	042520
6519	040374	020122	020040	051040
6520	040402	041520	020123	020040
6521	040410	051040	053520	000103
6522	040416	050122	040502	020040
6523	040424	020040	050122	040503
6524	040432	020040	020040	050122
6525	040440	040504	020040	020040
6526	040446	050122	030515	020040
6527	040454	020040	052523	040503
6528	040462	020040	020040	044523
6529	040465	047514	000	
6530	040472	124	051505	020124
6531	040500	020043	042440	051122
6532	040506	050040	020103	042040
6533	040514	053122	021440	020040
6534	040522	050040	052101	047122
6535	040530	021440	051040	042120
6536	040536	020123	020040	051040
6537	040544	042520	020122	020040
6538	040552	051040	041520	000123
6539	040560	050122	041527	020040
6540	040566	020040	050122	040502
6541	040574	020040	020040	050122
6542	040602	040503	020040	020040
6543	040610	050122	040504	020040
6544	040616	020040	050122	030515
6545	040624	020040	020040	052523
6546	040632	040503	020040	020040
6547	040637	044523	047514	000
6548	040644	124	051505	020124
6549	040652	020043	042440	051122
6550	040660	050040	020103	042040
6551	040666	053122	021440	020040
6552	040674	042440	050130	023524
6553	040702	020104	051461	020124
6554	040710	047527	042122	031040
6555	040716	042116	053440	051117
6556	040720	000104		
6557	040726	042524	052123	021440
6558	040734	020040	051105	020122
		041520	020040	051104

;'DH' MESSAGE LINES

DH1:	.ASCIZ	QRPADRQ						
DH2:	.ASCIZ	QTEST	ERR PC	DRV	RPDS	RPER	RPCS	RPWCQ
DH2A:	.ASCIZ	QRPBA	RPCA	RPDA	RPM1	SUCA	SILOS	
DH5:	.ASCIZ	QTEST	ERR PC	DRV	PATRN	RPDS	RPER	RPCSQ
DH5A:	.ASCIZ	QRPWC	RPBA	RPCA	RPDA	RPM1	SUCA	SILOS
DH11:	.ASCIZ	QTEST	ERR PC	DRV	EXPT'D	1ST WORD	2ND WORDQ	
DH12:	.ASCIZ	QTEST	ERR PC	DRV	EXPT'D	RECV'D	LOCNQ	

ERROR MESSAGES

6559	040742	020126	020043	020040
6560	040750	054105	052120	042047
6561	040756	020040	042522	053103
6562	040764	042047	020040	047514
6563	040772	047103	000	
6564	040775	000	051505	020124
6565	041002	020043	042440	051122
6566	041010	050040	020103	042040
6567	041016	053122	021440	020040
6568	041024	051040	042120	020123
6569	041032	020040	051040	042520
6570	041040	020122	020040	051040
6571	041046	041520	000123	
6572	041052	020040	020040	020040
6573	041060	020040	020040	020040
6574	041066	020040	020040	020040
6575	041074	020040	020040	020040
6576	041102	054105	052120	042047
6577	041110	020040	041501	052524
6578	041116	046101	005015	
6579	041122	042524	052123	
6580	041130	020040	051105	021440
6581	041136	041520	020040	020122
6582	041144	020126	020043	051104
6583	041152	052523	040503	020040
6584	041160	020040	052523	040503
6585	041166	000		
6586	041167	000		
6587	041174	020043	042440	020124
6588	041202	050040	020103	051122
6589	041210	053122	021440	042040
6590	041216	042440	050130	020040
6591	041224	020104	051040	023524
6592	041232	023526	000104	041505
6593	041236	042510	042101	051105
6594	041244	041440	047117	042524
6595	041252	052116	000123	
6596	041256	020040	020040	020040
6597	041264	020040	020040	020040
6598	041272	020040	020040	020040
6599	041300	020040	020040	020040
6600	041306	054105	052120	042047
6601	041314	020040	041501	052524
6602	041322	046101	005015	
6603	041326	042524	052123	021440
6604	041334	020040	051105	020122
6605	041342	041520	020040	051104
6606	041350	020126	020043	020040
6607	041356	042523	052103	051117
6608	041364	020040	042523	052103
6609	041372	051117	000	
6610	041375	000		
6611	041402	020040	042120	020123
6612	041410	020122	051040	042520
6613	041416	041520	020123	051040
6614	041424	051040	053520	020103

DH14: .ASCIZ @TEST # ERR PC DRV # RPDS RPER RPCS@

DH16: .ASCII @ EXPT'D ACTUAL@<15><12>

.ASCIZ @TEST # ERR PC DRV # SUCA SUCA@

DH25: .ASCIZ @TEST # ERR PC DRV # EXPT'D RECV'D@

DH25A: .ASCIZ @HEADER CONTENTS@

DH43: .ASCII @ EXPT'D ACTUAL@<15><12>

.ASCIZ @TEST # ERR PC DRV # SECTOR SECTOR@

DH43A: .ASCIZ @RPDS RPER RPCS RPWC RPBA RPCA RPDA@

6615	041432	020040	051040	041120
6616	041440	020101	020040	051040
6617	041446	041520	020101	020040
6618	041454	051040	042120	000101
6619	041462	042510	042101	051105
6620	041470	041440	047117	042524
6621	041476	052116	000123	
6622	041502	020040	020040	020040
6623	041510	020040	020040	020040
6624	041516	020040	020040	020040
6625	041524	020040	020040	020040
6626	041532	054105	052120	042047
6627	041540	020040	051447	052117
6628	041546	020047	020040	042523
6629	041554	052103	051117	005015
6630	041562	042524	052123	021440
6631	041570	020040	051105	020122
6632	041576	041520	020040	051104
6633	041604	020126	020043	020040
6634	041612	040526	052514	020105
6635	041620	041440	052517	052116
6636	041626	051105	020040	042101
6637	041634	051104	000	
6638	041637	122	042120	020123
6639	041644	020040	051040	042520
6640	041652	020122	020040	051040
6641	041660	041520	020123	020040
6642	041666	051040	042120	000101
6643	041674	020040	020040	020040
6644	041702	020040	020040	020040
6645	041710	020040	020040	020040
6646	041716	020040	020040	020040
6647	041724	020040	020040	020040
6648	041732	020040	054105	052120
6649	041740	042047	020040	042522
6650	041746	053103	042047	005015
6651	041754	042524	052123	021440
6652	041762	020040	051105	020122
6653	041770	041520	020040	051104
6654	041776	020126	020043	020040
6655	042004	042523	052103	051117
6656	042012	020040	040504	040524
6657	042020	020040	020040	040504
6658	042026	040524	000	
6659	042031	124	051505	020124
6660	042036	020043	042440	051122
6661	042044	050040	020103	042040
6662	042052	053122	021440	020040
6663	042060	042440	050130	023524
6664	042066	020104	051040	041505
6665	042074	023526	000104	
6666	042100	050122	051504	020040
6667	042106	020040	050122	051105
6668	042114	020040	020040	050122
6669	042122	051503	020040	020040
6670	042130	050122	040503	020040

DH43B: .ASCIZ @HEADER CONTENTS@

DH45: .ASCII @ EXPT'D 'SOT' SECTOR@ (15) (12)

.ASCIZ @TEST # ERR PC DRV # VALUE COUNTER ADDR@

DH45A: .ASCIZ @RPDS RPER RPCS RPDA@

DH46: .ASCII @ EXPT'D RECV'D@ (15) (12)

.ASCIZ @TEST # ERR PC DRV # SECTOR DATA DATA@

DH47: .ASCIZ @TEST # ERR PC DRV # EXPT'D RECV'D@

DH47A: .ASCIZ @RPDS RPER RPCS RPCA RPDA@

ERROR MESSAGES

6727	042632	032122	020040	044513					
6728	042640	042120	032522	000					
6729	042645	126	051111	052524	DH103A:	.ASCII	@VIRTUAL GOOD	BAD@<15><12>	
6730	042652	046101	043440	047517					
6731	042660	020104	020040	041040					
6732	042666	042101	005015						
6733	042672	042101	051104	020040		.ASCIZ	@ADDR DATA DATA@		
6734	042700	020040	040504	040524					
6735	042706	020040	020040	040504					
6736	042714	040524	000						
6737	042717	124	052117	046101	DH105:	.ASCIZ	@TOTAL COMPARE ERRORS:@		
6738	042724	041440	046517	040520					
6739	042732	042522	042440	051122					
6740	042740	051117	035123	000					
6741	042745	125	051516	042503	DH110:	.ASCIZ	@UNSUCCESSFUL AFTER 3 RETRIES@		
6742	042752	051523	052506	020114					
6743	042760	043101	042524	020122					
6744	042766	020063	042522	051124					
6745	042774	042511	000123						
6746	043000	042522	051124	020131	DH111:	.ASCII	@RETRY SUCESSFUL@<15><12>		
6747	043006	052523	042503	051523					
6748	043014	052506	006514	012					
6749	043021	122	052105	054522		.ASCIZ	@RETRY COUNT:@		
6750	043026	041440	052517	052116					
6751	043034	000072							
6752									
6753						.EVEN			
6754						;'DT' WORDS			
6755									
6756									
6757	043036	001324			DT1:	.WORD	RPADR		
6758	043040	001160	001116	001176	DT2:	.WORD	\$TMPD,\$ERRPC,\$CHKDRV,\$SRPDS,\$SRPER,\$SRPCS,\$SRPWC		
6759	043046	001334	001336	001340					
6760	043054	001342							
6761	043056	001344	001346	001350		.WORD	\$RPBA,\$RPCA,\$RPDA,\$RPM1,\$\$SUCA,\$\$SILO		
6762	043064	001352	001354	001356					
6763	043072	001160	001116	001176	DT5:	.WORD	\$TMPD,\$ERRPC,\$CHKDRV,\$PATNUM,\$SRPDS,\$SRPER,\$SRPCS		
6764	043100	001204	001334	001336					
6765	043106	001340							
6766	043110	001342	001344	001346		.WORD	\$RPWC,\$RPBA,\$RPCA,\$RPDA,\$RPM1,\$\$SUCA,\$\$SILO		
6767	043116	001350	001352	001354					
6768	043124	001356							
6769	043126	001160	001116	001176	DT11:	.WORD	\$TMPD,\$ERRPC,\$CHKDRV,\$GDDAT,\$BUFFER,\$BUFFER+2		
6770	043134	001124	043656	043660					
6771	043142	001160	001116	001176	DT12:	.WORD	\$TMPD,\$ERRPC,\$CHKDRV,\$GDDAT,\$BDDAT,\$BDADR		
6772	043150	001124	001126	001122					
6773	043156	001160	001116	001176	DT14:	.WORD	\$TMPD,\$ERRPC,\$CHKDRV,\$SRPDS,\$SRPER,\$SRPCS		
6774	043164	001334	001336	001340					
6775	043172	001160	001116	001176	DT16:	.WORD	\$TMPD,\$ERRPC,\$CHKDRV,\$MAXCYL,\$\$SUCA		
6776	043200	001210	001354						
6777	043204	001160	001116	001176	DT25:	.WORD	\$TMPD,\$ERRPC,\$CHKDRV,\$GDDAT,\$BDDAT		
6778	043212	001124	001126						
6779	043216	043656	043660	043662		.WORD	\$BUFFER,\$BUFFER+2,\$BUFFER+4		
6780	043224	001160	001116	001176	DT43:	.WORD	\$TMPD,\$ERRPC,\$CHKDRV,\$GDDAT,\$BUFFER+4		
6781	043232	001124	043662						
6782	043236	001334	001336	001340		.WORD	\$SRPDS,\$SRPER,\$SRPCS,\$SRPWC,\$RPBA,\$RPCA,\$RPDA		

6783	043244	001342	001344	001346			
6784	043252	001350					
6785	043254	043656	043660	043662	DT45:	.WORD	BUFFER, BUFFER+2, BUFFER+4
6786	043262	001160	001116	001176		.WORD	STMPD, \$ERRPC, CHKDRV, \$GDDAT, \$BDDAT, \$BDADR
6787	043270	001124	001126	001122			
6788	043276	001334	001336	001340		.WORD	\$RPDS, \$RPER, \$RPCS, \$RPDA
6789	043304	001350					
6790	043306	001160	001116	001176	DT46:	.WORD	STMPD, \$ERRPC, CHKDRV, \$GDADR, \$GDDAT, \$BDDAT
6791	043314	001120	001124	001126			
6792	043322	001160	001116	001176	DT47:	.WORD	STMPD, \$ERRPC, CHKDRV, \$GDDAT, BUFFER
6793	043330	001124	043656				
6794	043334	001334	001336	001340		.WORD	\$RPDS, \$RPER, \$RPCS, \$RPCA, \$RPDA
6795	043342	001346	001350				
6796	043346	001160	001116	001176	DT51:	.WORD	STMPD, \$ERRPC, CHKDRV, \$RPDS, \$RPER, \$RPCS, \$RPWC, \$RPBA
6797	043354	001334	001336	001340			
6798	043362	001342	001344				
6799	043366	001160	001116	001176	DT63:	.WORD	STMPD, \$ERRPC, CHKDRV, \$RPDS, \$RPER, \$RPCS, \$SUCA
6800	043374	001334	001336	001340			
6801	043402	001354					
6802	043404	001160	001116	001176	DT71:	.WORD	STMPD, \$ERRPC, CHKDRV, \$RPDS, \$RPER, \$RPCS, \$RPCA
6803	043412	001334	001336	001340			
6804	043420	001346					
6805	043422	001160	001116	001176	DT102:	.WORD	STMPD, \$ERRPC, CHKDRV, CMBUF, CMCYL, CMTRK, CMSEC, PATNUM
6806	043430	025430	025422	025426			
6807	043436	025424	001204				
6808	043442	001122	001124	001126		.WORD	\$BDADR, \$GDDAT, \$BDDAT
6809	043450	001160	001116	001176	DT103:	.WORD	STMPD, \$ERRPC, CHKDRV, CMBUF, CMCYL, CMTRK, CMSEC, PATNUM
6810	043456	025430	025422	025426			
6811	043464	025424	001204				
6812	043470	172346	172350	172352		.WORD	KIPAR3, KIPAR4, KIPAR5, KIPDR3, KIPDR4, KIPDR5
6813	043476	172306	172310	172312			
6814	043504	001122	001124	001126	DT104:	.WORD	\$BDADR, \$GDDAT, \$BDDAT
6815	043512	025414			DT105:	.WORD	ERCTR
6816	043514	001314			DT111:	.WORD	RETRY
6817							
6818							
6819							
6820	043516	000001					
6821	043520	001			DF1:	.WORD	1
6822	043521	000				.BYTE	1
6823						.BYTE	0
6824	043522	000002					
6825	043524	007			DF2:	.WORD	2
6826	043525	000				.BYTE	7
6827	043526	040410				.BYTE	0
6828	043530	006				.WORD	DH2A
6829	043531	000				.BYTE	6
6830						.BYTE	0
6831	043532	000002					
6832	043534	007			DF5:	.WORD	2
6833	043535	010				.BYTE	7
6834	043536	040552				.BYTE	10
6835	043540	007				.WORD	DH5A
6836	043541	000				.BYTE	7
6837						.BYTE	0
6838	043542	000001			DF11:	.WORD	1

ERROR MESSAGES

6839	043544	006		.BYTE	6
6840	043545	000		.BYTE	0
6841					
6842	043546	000002	DF25:	.WORD	2
6843	043550	005		.BYTE	0
6844	043551	000		.BYTE	0
6845	043552	041236		.WORD	0H25A
6846	043553	003		.BYTE	0
6847	043555	000		.BYTE	0
6848					
6849	043556	000003	DF43:	.WORD	3
6850	043560	006		.BYTE	0
6851	043561	000		.BYTE	0
6852	043562	041375		.WORD	0H43A
6853	043564	005		.BYTE	0
6854	043565	000		.BYTE	0
6855	043566	041462		.WORD	0H43B
6856	043570	003		.BYTE	0
6857	043571	000		.BYTE	0
6858					
6859	043572	000002	DF45:	.WORD	2
6860	043574	006		.BYTE	0
6861	043575	000		.BYTE	0
6862	043576	041637		.WORD	0H45A
6863	043600	004		.BYTE	4
6864	043601	000		.BYTE	0
6865					
6866	043602	000002	DF47:	.WORD	2
6867	043604	005		.BYTE	0
6868	043605	000		.BYTE	0
6869	043606	042100		.WORD	0H47A
6870	043610	005		.BYTE	0
6871	043611	000		.BYTE	0
6872					
6873	043612	000001	DF51:	.WORD	1
6874	043614	010		.BYTE	0
6875	043615	000		.BYTE	0
6876					
6877	043616	000001	DF53:	.WORD	1
6878	043620	005		.BYTE	0
6879	043621	000		.BYTE	0
6880					
6881	043622	000001	DF63:	.WORD	1
6882	043624	007		.BYTE	0
6883	043625	000		.BYTE	0
6884					
6885	043626	000002	DF102:	.WORD	2
6886	043630	010		.BYTE	0
6887	043631	200		.BYTE	200
6888	043632	042514		.WORD	0H102A
6889	043634	000		.BYTE	0
6890	043635	000		.BYTE	0
6891					
6892	043636	000003	DF103:	.WORD	3
6893	043640	010		.BYTE	0
6894	043641	200		.BYTE	200

ERROR MESSAGES

6895	043642	042566		
6896	043644	006		
6897	043645	000		
6898	043646	042645		
6899	043648	000		
6900	043651	000		
6901				
6902	043652	000001		
6903	043654	003		
6904	043655	000		
6905				
6906				
6907				
6908				
6909	043656			
6910				
6911	043656	005015	046412	044501
6912	043664	042116	041505	030455
6913	043672	026461	055104	050122
6914	043700	026531	103	
6915	043703	015	051012	030520
6916	043710	026461	020105	052506
6917	043716	041516	044524	047117
6918	043724	046101	046040	043517
6919	043732	041511	023040	051040
6920	043740	040505	027504	051127
6921	043746	052111	020105	042524
6922	043754	052123	005015	000012
6923		000001		

```

.WORD 0H103
.BYTE 6
.BYTE 0
.WORD 0H103A
.BYTE 0
.BYTE 0

```

```

DF104: .WORD 1
       .BYTE 3
       .BYTE 0

```

.EVEN

BUFFER=. ;BUFFER STARTS HERE

TITLE: .ASCII <15><12><12>@MAINDEC-11-DZRPY-C@

.ASCIIZ <15><12>@RP11-E FUNCTIONAL LOGIC & READ/WRITE TEST@<15><12><12>

.END

KIPAR0 = 172340
 KIPAR1 = 172342
 KIPAR2 = 172344
 KIPAR3 = 172346
 KIPAR4 = 172350
 KIPAR5 = 172352
 KIPAR6 = 172354
 KIPAR7 = 172356
 KIPOR0 = 172300
 KIPOR1 = 172302
 KIPOR2 = 172304
 KIPOR3 = 172306
 KIPOR4 = 172310
 KIPOR5 = 172312
 KIPOR6 = 172314
 KIPOR7 = 172316
 LDRFLG = 001322
 LF = 000012
 LIMIT = 025416
 LMTBK = 001262
 LOOSEC = 024304
 LOMEN = 001240
 LARETRY = 001316
 MAXCYL = 001210
 MAXMEM = 001230
 MAXWC = 001264
 MEMSIZ = 001302
 MEXO = 000020
 MEX1 = 000040
 MNACTV = 001222
 MAVEC = 000250
 MODE = 010000
 MOVLDR = 023346
 MRPADR = 032162
 MSG15A = 027433
 MSG15B = 027456
 MSG15C = 027617
 MSG15D = 027652
 MSG16A = 027743
 MSG16B = 030255
 MSG17A = 030317
 MSG17B = 030362
 MSG17C = 030441
 MSG17D = 030464
 MSG17E = 030506
 MSG17F = 030542
 MSG17G = 030577
 MSG17H = 030714
 MSG17I = 031014
 MSG17J = 031037
 MSG17K = 031176
 MXWRDS = 031447
 NONE = 027372

OFFLIN = 027312
 ONEPAT = 031713
 ONLINE = 027292
 OPASEL = 001260
 PAKSIZ = 001244
 PATNUM = 001204
 PATSEL = 024050
 PATTRN = 001300
 PAT_PT = 001440
 PATO = 001500
 PAT1 = 001540
 PAT10 = 002200
 PAT11 = 002240
 PAT12 = 002300
 PAT13 = 002340
 PAT14 = 002400
 PAT15 = 002440
 PAT2 = 001600
 PAT3 = 001640
 PAT4 = 001700
 PAT5 = 001740
 PAT6 = 002000
 PAT7 = 002040
 PAT8 = 002100
 PAT9 = 002140
 PIRG = 177772
 PIRQVE = 000240
 PRO = 000000
 PR1 = 000040
 PR2 = 000100
 PR3 = 000140
 PR4 = 000200
 PR5 = 000240
 PR6 = 000300
 PR7 = 000340
 PS = 177776
 PSM = 177776
 PWRVEC = 000024
 ROCHR = 104410
 ROERR = 001274
 ROLIN = 104411
 ROSEK = 000005
 ROSM = 016056
 RDY = 000200
 READ = 000017
 RESLDR = 023452
 RESREG = 104413
 RESTAR = 004762
 RESVEC = 000010
 RETRY = 001314
 RPADR = 001324
 RPBA = 000010
 RPCA = 000012

RPCS = 000004
 RPOA = 000014
 RPOS = 000000
 RPRR = 000000
 RPINIT = 022566
 RPM1 = 000016
 RPM2 = 000020
 RPM3 = 000020
 RPPRIO = 001332
 RPVEC = 001326
 RPMC = 000006
 RPO2 = 027327
 RPO3 = 027336
 RP11 = 022770
 RSTAT1 = 004740
 RSTART2 = 004752
 RSTART3 = 004760
 R6 = %000006
 R7 = %000007
 SAVREG = 104412
 SAVRP = 023240
 CYL = 001266
 SECNR = 024002
 FEK = 000011
 SETBUF = 024432
 SILO = 000026
 SIZMEM = 025700
 SLASH = 031322
 SRO = 177572
 SR1 = 177574
 SR2 = 177576
 SR3 = 172516
 SEC = 001272
 TACK = 001100
 TALL = 023202
 TALLT = 001220
 TART = 003710
 TART1 = 003654
 TART2 = 003676
 TART3 = 003644
 TART4 = 003666
 TART5 = 004146
 TART6 = 004360
 TART7 = 004544
 TKLMT = 177774
 TRK = 001270
 TRT1A = 003660
 TRT2A = 003702
 SUCA = 000024
 SUFU = 001000
 SUOL = 040000
 SURDY = 100000
 SURP03 = 020000

SUSI = 004000
 SUMP = 000400
 SWR = 001140
 SWREC = 000176
 SWO = 000001
 SWO0 = 000001
 SWO1 = 000002
 SWO2 = 000004
 SWO3 = 000010
 SWO4 = 000020
 SWO5 = 000040
 SWO6 = 000100
 SWO7 = 000200
 SWO8 = 000400
 SWO9 = 001000
 SW1 = 000002
 SW10 = 002000
 SW11 = 004000
 SW12 = 010000
 SW13 = 020000
 SW14 = 040000
 SW15 = 100000
 SW16 = 000004
 SW17 = 000010
 SW18 = 000020
 SW19 = 000040
 SW20 = 000100
 SW21 = 000200
 SW22 = 000400
 SW23 = 001000
 TBITIVE = 000014
 TESTO = 005160
 TEST1 = 005404
 TEST10 = 010706
 TEST11 = 011652
 TEST12 = 012202
 TEST13 = 013014
 TEST14 = 014066
 TEST15 = 014736
 TEST16 = 015460
 TEST17 = 015716
 TEST2 = 006020
 TEST3 = 006476
 TEST4 = 007154
 TEST5 = 007404
 TEST6 = 010130
 TEST7 = 010464
 TIMEOUT = 001212
 TITLE = 043656
 TKVEC = 000060
 TPVEC = 000064
 TRAPVE = 000034
 TRTVEC = 000014

TSTING = 027401
 TSTNMS = 001276
 TSTO = 005064
 TST1 = 005310
 TST10 = 010612
 TST11 = 011532
 TST12 = 012062
 TST12B = 012534
 TST13 = 012720
 TST13A = 013474
 TST14 = 013574
 TST14A = 014342
 TST15 = 014630
 TST15A = 015112
 TST15B = 015314
 TST15C = 015324
 TST16 = 015372
 TST17 = 015624
 TST2 = 005724
 TST3 = 006402
 TST4 = 007060
 TST5 = 007310
 TST5A = 007634
 TST5B = 007742
 TST6 = 010034
 TST7 = 010370
 TYPDS = 104405
 TYPE = 104401
 TYPERR = 017254
 TYPOC = 104402
 TYPON = 104404
 TYP0S = 104403
 UNSAFE = 027276
 WC14 = 001256
 WRITE = 000013
 WRSW = 016206
 WRSW1 = 016270
 WRSW2 = 016412
 WATCHK = 000007
 WRTSEK = 000003
 SAUTOB = 001134
 SBODADR = 001122
 SBODAT = 001126
 SBELL = 001166
 SBUF = 002506
 SCHARC = 017720
 SCKSWR = 020704
 SCMTAG = 001100
 SCM3 = 000000
 SCM4 = 000001
 SCNTLC = 021604
 SCNTLG = 021616
 SCNTLU = 021611

SCORE	022402	SGDDAT	001124	SOVER	022062	SSIZEX	022436	STRAP	022502
SCRLF	001173	SGET42	017040	SPASS	001100	STUP =	177777	STRAP2	022524
SCRUT	022432	SGTSMR	020774	SQUES	001172	SSUCA	001354	STRK	002513
SCYL	002510	SHO =	000000	SROCHR	021246	SSVLAD	022034	STRP =	000014
SOBLK	020366	SICNT	001104	SROLIN	021336	SSVPC =	000200	STRPAD	022536
SOAGN	017060	SINTAG	001135	SROSZ =	000012	SSWR	167000	STSTNM	001102
SOTBL	020356	SITEMB	001114	SRESRE =	022136	SSWRMK =	000000	STTYIN	021572
SENAD	017050	SKTNEX	022374	SRPBA	001344	STIMES	001162	STYPOS	020152
SENDOCT	017004	SKTOUT	022364	SRPCA	001346	STKB	001146	STYPE	017504
SENULL	017064	SKTII	022232	SRPCS	001340	STKCNT	020376	STYPEC	017654
SEOP	016616	SLF	001174	SRPOA	001350	STKINT	020416	STYPEX	017722
SEOPCT	016776	SLPADR	001106	SRPOS	001354	STKQEN =	020416	STYPOC	017750
SERFLG	001103	SLPERR	001110	SRPER	001336	STKQIN	020400	STYPON	017764
SERMAX	001115	SLSTAD	022476	SRPMI	001352	STKQOU	020402	STYPOS	017724
SERROR	017070	SLSTBK	022500	SRPMC	001342	STKQSR	020404	SWC	002504
SERRPC	001116	SMNEW	021634	SRTNAD	017062	STKS	001144	STXSTR	021660
SERRTB	002514	SMSMR	021623	SSAVRE	022100	STKSRV	020466	SSGET4 =	000000
SERTTL	001112	SMXCNT	022076	SSCOPE	021646	STMPD	001160	SOFILL	020147
SESCAP	001164	SNULL	001154	SSEC	002512	STN =	000020	. =	043762
SFILLC	001156	SNWTST =	000001	SSETUP =	000127	STPB	001152		
SFILLS	001155	SOCNT	020146	SSILO	001356	STPFLG	001157		
SGADR	001120	SOMODE	020150	SSIZE	022174	STPS	001150		

. ABS. 043762 000

ERRORS DETECTED: 0

DSKW: DZRPYC, DSKW: DZRPYC/SOL=DSKW: SYSMAC.SML, DSKM: DZRPYC.P11

RUN-TIME: 16 24 .6 SECONDS

RUN-TIME RATIO: 371/41=8.9

CORE USED: 37K (73 PAGES)

E11