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IDENTIFICATION

PRODUCT CODE: MAINDEC-11-DZKUA-C-D
 PRODUCT NAME: UNIBUS SYSTEMS EXERCISER DIAGNOSTIC
 DATE : NOVEMBER, 75
 MAINTAINER: DIAGNOSTIC GROUP
 AUTHOR: MANUEL SOARES

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UNIBUS EXERCISER
 MAINDEC-11-DZKUA-C-D
 NOVEMBER, 1975
 MANUEL SOARES

UNIBUS EXERCISER
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3.1 SWITCH OPTIONS

THE USE OF THIS PROGRAM ON PROCESSORS HAVING A SOFTWARE SWITCH REGISTER NECESSITATES OPERATOR INTERACTION: THE OPERATOR MUST SET UP LOCATION 176 WITH THE SWITCH REGISTER VALUES DESIRED AND LOCATION 174 WITH THE DISPLAY VALUES DESIRED.

SWITCH -----	USE ---
15	HALT ON ERROR
14	LOOP ON TEST
13	INHIBIT ERROR TYPEOUTS
11	INHIBIT ITERATIONS
10	BELL ON ERROR
9	LOOP ON ERROR
8	LOOP ON TEST IN SWR(7:0)

NOTE: IF YOU WISH TO INHIBIT ALL TYPING EXCEPT "END OF PASS" YOU MUST PUT DOWN SWITCH 7, AFTER LOADING 200.

3.2 TEST 1 THROUGH TEST 16

TEST 1 - NO BUS GRANTS ISSUED WITH PROCESSOR AT HIGHER PRIORITY THAN BUS REQUEST. THIS TEST IS TO INSURE THAT ANY REQUEST IS NOT HONORED AS LONG AS THE PROCESSOR IS AT THE SAME OR HIGHER PRIORITY.

TEST 2 - ISSUING OF NON-PROCESSOR GRANTS AND ARBITRATION TESTS. THIS TEST WILL REQUEST ON NPR THROUGH BR4 LEVELS WITH THE PROCESSOR STATUS INITIALLY AT LEVEL 7 AND MAKE SURE THE DEVICE EXERCISES AN NPG TO DO A FUN 1-DATI, THEN THE REQUESTS WILL BE REPEATED WHILE SEQUENTIALLY LOWERING THE PROCESSOR STATUS FROM 7 TO 0 TO ALLOW ARBITRATION OF ALL REQUESTS AND THE ISSUING OF NPG.

TEST 3 - ISSUING OF BUS GRANT 7 AND ARBITRATION TESTS. THIS TEST WILL ARBITRATE FOR A BG7. THE REQUESTS WILL BE ON LEVELS BR7 THRU BR4, DOING FUN 1-DATI TRANSFERS, AND THE PROCESSOR STATUS LOWERED SEQUENTIALLY FROM 7 TO 0.

TEST 4 - ISSUING OF BUS GRANT 6 AND ARBITRATION TESTS. THIS TEST WILL ARBITRATE FOR A BG6. THE REQUESTS WILL BE ON LEVELS BR6 THRU BR4, DOING FUN 1-DATI TRANSFERS, AND THE PROCESSOR STATUS LOWERED SEQUENTIALLY FROM 6 TO 0.

TEST 5 - ISSUING OF BUS GRANT 5 AND ARBITRATION TESTS. THIS TEST WILL ARBITRATE FOR A BG5. THE REQUESTS WILL BE ON LEVELS BR5 THRU BR4, DOING FUN 1-DATI TRANSFERS, AND THE PROCESSOR STATUS LOWERED SEQUENTIALLY FROM 5 TO 0.

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- TEST 6 - ISSUING OF BUS GRANT 4 AND ARBITRATION TESTS. THIS TEST WILL ARBITRATE FOR A BG4, THE REQUESTS WILL BE ON LEVEL BR4, DOING FUNC 1-DATI TRANSFERS, AND THE PROCESSOR STATUS LOWERED SEQUENTIALLY FROM 4 TO 0.
- TEST 7 - CPU TEST FOR NO SACK TIME OUT. THIS TEST WILL CHECK THAT THE CPU TIMES OUT AND DROPS A GRANT IF NO SACK SIGNAL IS RECEIVED. IF THE CPU TIME OUT IS INOPERATIVE, THE BUS EXERCISER WILL TIME OUT AND SEND THE SACK SIGNAL TO PREVENT A BUS HANG AND SET AN ERROR FLAG IN CR2.
- TEST 10 - CPU TEST FOR RECEIVING SACK. THIS TEST IS TO INSURE THAT THE CPU CAN RECEIVE THE SACK SIGNAL; THE TIME DELAY WILL BE SET ON DEVICE 1 AND SEVERAL DATI TRANSFERS MADE. IF THERE IS NOT BUS LATE ERROR, THE CPU RECEIVED SACK CORRECTLY. IT IS ASSUMED THAT DEV 1 TIME DELAY IS SET FOR 10US.
- TEST 11 - PASSING OF GRANTS AND INTERRUPT TEST. THIS TEST WILL SET OFF ALL AVAILABLE DEVICES SIMULTANEOUSLY WHOSE ONLY FUNCTIONS WILL BE TO INTERRUPT, THE REQUESTS WILL ALL BE AT LEVEL 7 SO THAT THE DEVICE CLOSEST TO THE CPU SHOULD RECEIVE BG7 FIRST AND INTERRUPT FIRST, THE NEXT CLOSEST SHOULD INTERRUPT NEXT AND SO ON.
- TEST 12 - ADDRESS LINES (14 - 17) CHECK. THIS TEST WILL CHECK BUS ADDRESS LINES 14 THRU 17 BY DOING A FUN 1-DATI-NPR TO THOSE ADDRESSES. IF THE ADDRESSES DON'T EXIST THE INTERRUPT ROUTINE WILL IGNORE ANY NO SSYN ERROR.
- TEST 13 - CPU TEST FOR ACLO/DCLO SEQUENCE. THIS TEST CHECKS THE ASSERTION OF ACLO AND DCLO AND THAT THE CPU TRAPS TO THE CORRECT SERVICE ROUTINE. IF THIS PROGRAM IS RUNNING UNDER ACT11 THIS TEST WILL BE SKIPPED.
- TEST 14 - PARITY ERROR TEST. THIS TEST WILL CAUSE PARITY ERROR AND CHECKS THAT THE CPU TRAPS TO THE CORRECT VECTOR. IT FIRST CHECKS THAT THE CPU CAN HANDLE A PARITY TRAP OTHERWISE IT SKIPS THIS TEST.
- TEST 15 - MULTITRANSFERS I. THIS TEST WILL CAUSE ANY BUS EXERCISERS, UP TO 4, TO CREATE A LOT OF TRAFFIC ON THE BUS AND CHECK THAT THE CPU CAN HANDLE IT; ALL DEVICES ARE SET OFF SIMULTANEOUSLY.
- TEST 16 - MULTITRANSFERS II. THIS TEST WILL HAVE THE AVAILABLE EXERCISERS DOING VARIOUS TRANSFERS AND/OR INTERRUPTS AT DIFFERENT REQUEST LEVELS TO FURTHER CHECK CPU HANDLING CAPABILITIES.

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TEST 17 - DUMMY END OF PROGRAM. THIS PORTION OF THE PROGRAM IS JUST TO SEE IF "IH" HAS BEEN TYPED ON THE CONSOLE TO CAUSE A PROGRAM HALT. IF THERE IS NO "IH" THE PROGRAM CONTINUES BY JUMPING TO \$EOP (END-OF-PASS ROUTINE).

3.3 SYSMAC ROUTINES

THE 'END OF PASS ROUTINE' THRU 'POWER DOWN AND UP ROUTINES', AS LISTED IN THE PROGRAM LISTING, ARE THE SYSMAC PACKAGE MACROS. THEY ARE CALLED OUT IN THE SOURCE PROGRAM, SOME WITH ARGUMENTS AND SOME WITHOUT, AND ARE EXPANDED IN THE LISTING. SOME MACROS ARE NECESSARY FOR THE OPERATION OF OTHERS, SO FOR A COMPLETE EXPLANATION OF ALL AVAILABLE SYSMAC MACROS SEE PDP-11 MAINDEC SYSMAC PACKAGE (DZQAC-B-D).

4.0 ERROR REPORTING

THE MINIMUM AMOUNT OF INFORMATION GIVEN WHEN AN ERROR OCCURS IS THE PC OF THE ERROR CALL AND THE TEST NUMBER IN WHICH IT OCCURRED. OTHER PERTINENT DATA WILL BY TYPED OUT DEPENDING ON THE TEST BEING RUN AT THAT TIME.

.REM %

IDENTIFICATION

PRODUCT CODE: MAINDEC-11-DZKUA-C-D
PRODUCT NAME: UNIBUS SYSTEMS EXERCISER DIAGNOSTIC
DATE : DECEMBER, 75
MAINTAINER: DIAGNOSTIC GROUP
AUTHOR: MANUEL SOARES

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1.0 ABSTRACT

THIS PROGRAM WAS CREATED TO TEST PDP-11'S CPU INTERFACE CIRCUITRY. IT USES THE UNIBUS EXERCISER(S) (UBE) TO INSURE PROPER OPERATION BY SIMULATING PERIPHERALS WHICH WOULD REQUIRE THE 11-CPU TO PRODUCE THE NECESSARY SIGNALS. IT SHOULD BE NOTED THAT THE UBE IS A POWERFUL TOOL AND IF IT IS NOT PROGRAMMED CORRECTLY COULD CAUSE VARIOUS PROBLEMS ON THE BUS.

2.0 REQUIREMENTS

2.1 HARDWARE

THIS PROGRAM ASSUMES THE FOLLOWING IN PROPER WORKING CONDITION:
1. THE UNIBUS, 2. MEMORY (8K MINIMUM), AND 3. UBE(S) (4 MAXIMUM).
IF A FOURTH UBE IS BEING USED, ITS TIME DELAY SHOULD BE SET AT 100US TO PREVENT LATENCY PROBLEMS IN ONE OF THE TESTS.

WITH TWO OR MORE UBE(S), ALL SHOULD HAVE W1 JUMPERS EXCEPT THE ONE FURTHEST ELECTRICALLY FROM THE CPU. IF THERE ARE MORE THAN 4 UBE(S) ON THE UNIBUS THE PROGRAM IS NOT RESPONSIBLE FOR ANY PROBLEMS WHICH MIGHT OCCUR, SINCE IT IS PROGRAMMED TO HANDLE A MAXIMUM OF ONLY 4.

2.2 SOFTWARE

AFTER LOADING THE PROGRAM THE STARTING ADDRESS MUST BE 200, SO THAT THE FIRST TIME THROUGH, THE AVAILABLE UBE(S) ARE DETERMINED. IN ADDITION IF ONE OR MORE UBE(S) ARE ADDED OR REMOVED, THE PROGRAM AGAIN MUST BE STARTED AT 200. OTHERWISE, TO AVOID DUPLICATING SOME PRINTOUTS, THE PROGRAM CAN BE RESTARTED AT ADDRESS 220. A SOFTWARE HALT CAN BE CAUSED BY DEPRESSING "H" ON THE CONSOLE.

3.0 PROGRAM DESCRIPTION

THIS PROGRAM WAS ASSEMBLED WITH MACY11 USING PDP-11 MAINDEC SYSMAC PACKAGE .

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3.1 SWITCH OPTIONS

THE USE OF THIS PROGRAM ON PROCESSORS HAVING A SOFTWARE SWITCH REGISTER NECESSITATES OPERATOR INTERACTION: THE OPERATOR MUST SET UP LOCATION 176 WITH THE SWITCH REGISTER VALUES DESIRED AND LOCATION 174 WITH THE DISPLAY VALUES DESIRED.

SWITCH	USE
-----	---
15	HALT ON ERROR
14	LOOP ON TEST
13	INHIBIT ERROR TYPEOUTS
11	INHIBIT ITERATIONS
10	BELL ON ERROR
9	LOOP ON ERROR
8	LOOP ON TEST IN SWR<7:0>

NOTE: IF YOU WISH TO INHIBIT ALL TYPING EXCEPT "END OF PASS" YOU MUST PUT DOWN SWITCH 7, AFTER LOADING 200.

3.2 TEST 1 THROUGH TEST 16

- TEST 1 - NO BUS GRANTS ISSUED WITH PROCESSOR AT HIGHER PRIORITY THAN BUS REQUEST. THIS TEST IS TO INSURE THAT ANY REQUEST IS NOT HONORED AS LONG AS THE PROCESSOR IS AT THE SAME OR HIGHER PRIORITY.
- TEST 2 - ISSUING OF NON-PROCESSOR GRANTS AND ARBITRATION TESTS. THIS TEST WILL REQUEST ON NPR THROUGH BR4 LEVELS WITH THE PROCESSOR STATUS INITIALLY AT LEVEL 7 AND MAKE SURE THE DEVICE EXERCISES AN NPG TO DO A FUN 1-DATI, THEN THE REQUESTS WILL BE REPEATED WHILE SEQUENTIALLY LOWERING THE PROCESSOR STATUS FROM 7 TO 0 TO ALLOW ARBITRATION OF ALL REQUESTS AND THE ISSUING OF NPG.
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- TEST 6 - ISSUING OF BUS GRANT 4 AND ARBITRATION TESTS. THIS TEST WILL ARBITRATE FOR A BG4, THE REQUESTS WILL BE ON LEVEL BR4, DOING FUNC 1-DATI TRANSFERS, AND THE PROCESSOR STATUS LOWERED SEQUENTIALLY FROM 4 TO 0.
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TEST 17 - DUMMY END OF PROGRAM. THIS PORTION OF THE PROGRAM IS JUST TO SEE IF "tH" HAS BEEN TYPED ON THE CONSOLE TO CAUSE A PROGRAM HALT. IF THERE IS NO "tH" THE PROGRAM CONTINUES BY JUMPING TO \$EOP (END-OF-PASS ROUTINE).

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548 001100
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554 177772
555 177570
556 177570
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560 000001
561 000002
562 000003
563 000004
564 000005
565 000006
566 000007
567
568
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571 000000
572 000040
573 000100
574 000140
575 000200

```

$SWR=167400
.TITLE UNIBUS EXERCISER
;*COPYRIGHT (C) MARCH, 75
;*DIGITAL EQUIPMENT CORP.
;*MAYNARD, MASS. 01754
;*
;*PROGRAM BY M.SOARES
;*
;*THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
;*PACKAGE (MAINDEC-11-DZQAC-81),AUG 29,1975.
;*
$TN=1

.SBTTL OPERATIONAL SWITCH SETTINGS
;*
;* SWITCH USE
;* -----
;* 15 HALT ON ERROR
;* 14 LOOP ON TEST
;* 13 INHIBIT ERROR TYPEOUTS
;* 11 INHIBIT ITERATIONS
;* 10 BELL ON ERROR
;* 9 LOOP ON ERROR
;* 8 LOOP ON TEST IN SWR<7:0>

.SBTTL BASIC DEFINITIONS
;*INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***
STACK= 1100
.EQUIV EMT,ERROR ;;BASIC DEFINITION OF ERROR CALL
.EQUIV IOT,SCOPE ;;BASIC DEFINITION OF SCOPE CALL
PS= 177776 ;;PROCESSOR STATUS WORD
.EQUIV PS,PSW
STKLMT= 177774 ;;STACK LIMIT REGISTER
PIRQ= 177772 ;;PROGRAM INTERRUPT REQUEST REGISTER
DSWR= 177570 ;;HARDWARE SWITCH REGISTER
DDISP= 177570 ;;HARDWARE DISPLAY REGISTER

;*GENERAL PURPOSE REGISTER DEFINITIONS
R0= %0 ;;GENERAL REGISTER
R1= %1 ;;GENERAL REGISTER
R2= %2 ;;GENERAL REGISTER
R3= %3 ;;GENERAL REGISTER
R4= %4 ;;GENERAL REGISTER
R5= %5 ;;GENERAL REGISTER
R6= %6 ;;GENERAL REGISTER
R7= %7 ;;GENERAL REGISTER
.EQUIV R6,SP ;;STACK POINTER
.EQUIV R7,PC ;;PROGRAM COUNTER

;*PRIORITY LEVEL DEFINITIONS
PRO= 0 ;;PRIORITY LEVEL 0
PR1= 40 ;;PRIORITY LEVEL 1
PR2= 100 ;;PRIORITY LEVEL 2
PR3= 140 ;;PRIORITY LEVEL 3
PR4= 200 ;;PRIORITY LEVEL 4

```


44 000046 015626
45 000052 000052
46 040000
47 000232

SENDAD
.=52
.WORD 40000
.=SSVPC

::1)SET LOC.46 TO ADDRESS OF SENDAD IN .SEOP
::2)SET LOC.52 TO 40000
::RESTORE PC

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001102 000
001103 000
001104 000000
001106 000000
001110 000000
001112 000000
001114 000
001115 001
001116 000000
001120 000000
001122 000000
001124 000000
001126 000000
001130 000000
001132 000000
001134 000000
001136 177570
001140 177570
001142 177560
001144 177562
001146 177564
001150 177566
001152 000
001153 002
001154 012
001155 000
001156 000000
001160 000000
001162 000000
001164 007000
001166 000000
001170 000000
001172 000000
001174 000000
001176 000000
001200 000000
001202 000000
001204 000000
001206 000000
001210 000000
001212 000000
001214 177607
001220 077
001221 015
001222 000012

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.SBTTL COMMON TAGS

;*THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
;*USED IN THE PROGRAM.

.=1100

SCMTAG: .WORD 0
SPASS: .WORD 0
STSTNM: .BYTE 0
SERFLG: .BYTE 0
SICNT: .WORD 0
SLPADR: .WORD 0
SLPERR: .WORD 0
SERTTL: .WORD 0
SITEMB: .BYTE 0
SERMAX: .BYTE 1
SERRPC: .WORD 0
SGDADR: .WORD 0
SBDADR: .WORD 0
SGDCAT: .WORD 0
SBDDAT: .WORD 0
SWR: .WORD DSWR
DISPLAY: .WORD DDISP
\$TKS: 177560
\$TKB: 177562
\$TPS: 177564
\$TPB: 177566
\$NULL: .BYTE 0
\$FILLS: .BYTE 2
\$FILLC: .BYTE 12
\$TPFLG: .BYTE 0
\$REGAD: .WORD 0
\$REG0: .WORD 0
\$REG1: .WORD 0
\$REG2: .WORD 0
\$REG3: .WORD 0
\$REG4: .WORD 0
\$REG5: .WORD 0
\$TMP0: .WORD 0
\$TMP1: .WORD 0
\$TMP2: .WORD 0
\$TMP3: .WORD 0
\$TMP4: .WORD 0
\$TMP5: .WORD 0
\$TIMES: 0
\$ESCAPE: 0
\$BELL: .ASCIZ <207><377>\377
\$QUES: .ASCII /?/
\$CRLF: .ASCII <15>
\$LF: .ASCIZ <12>

:: START OF COMMON TAGS
:: CONTAINS PASS COUNT
:: CONTAINS THE TEST NUMBER
:: CONTAINS ERROR FLAG
:: CONTAINS SUBTEST ITERATION COUNT
:: CONTAINS SCOPE LOOP ADDRESS
:: CONTAINS SCOPE RETURN FOR ERRORS
:: CONTAINS TOTAL ERRORS DETECTED
:: CONTAINS ITEM CONTROL BYTE
:: CONTAINS MAX. ERRORS PER TEST
:: CONTAINS PC OF LAST ERROR INSTRUCTION
:: CONTAINS ADDRESS OF 'GOOD' DATA
:: CONTAINS ADDRESS OF 'BAD' DATA
:: CONTAINS 'GOOD' DATA
:: CONTAINS 'BAD' DATA
:: RESERVED--NOT TO BE USED

:: ADDRESS OF SWITCH REGISTER
:: ADDRESS OF DISPLAY REGISTER
:: TTY KBD STATUS
:: TTY KBD BUFFER
:: TTY PRINTER STATUS REG. ADDRESS
:: TTY PRINTER BUFFER REG. ADDRESS
:: CONTAINS NULL CHARACTER FOR FILLS
:: CONTAINS # OF FILLER CHARACTERS REQUIRED
:: INSERT FILL CHARS. AFTER A "LINE FEED"
:: "TERMINAL AVAILABLE" FLAG (BIT<07>=0=YES)
:: CONTAINS THE ADDRESS FROM
:: WHICH (\$REG0) WAS OBTAINED
:: CONTAINS ((\$REGAD)+0)
:: CONTAINS ((\$REGAD)+2)
:: CONTAINS ((\$REGAD)+4)
:: CONTAINS ((\$REGAD)+6)
:: CONTAINS ((\$REGAD)+10)
:: CONTAINS ((\$REGAD)+12)
:: USER DEFINED
:: USER DEFINED
:: USER DEFINED
:: USER DEFINED
:: USER DEFINED
:: USER DEFINED
:: MAX. NUMBER OF ITERATIONS
:: ESCAPE ON ERROR ADDRESS
:: CODE FOR BELL
:: QUESTION MARK
:: CARRIAGE RETURN
:: LINE FEED

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860

001224

001224 011374
001226 011442
001230 015252
001232 000000

001234 011473

001236 011606
001240 015262
001242 000000

001244 011676
001246 011730
001250 015302
001252 000000

001254 012011
001256 011730
001260 015302
001262 000000

001264 012047
001266 011730
001270 015302
001272 000000

001274 012105
001276 011730
001300 015302
001302 000000

001304 012143
001306 011730
001310 015302
001312 000000

```
*****
.SBTTL  ERROR POINTER TABLE

*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
*LOCATION $ITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
*NOTE1:      IF $ITEMB IS 0 THE ONLY PERTINENT DATA IS ($ERRPC).
*NOTE2:      EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

*      EM      ;;POINTS TO THE ERROR MESSAGE
*      DH      ;;POINTS TO THE DATA HEADER
*      DT      ;;POINTS TO THE DATA
*      DF      ;;POINTS TO THE DATA FORMAT

$ERRTB:
*****
;ITEM 1
      EM1      ;CPU TRAPPED THRU LOC 4 -TIME OUT
      DH1      ; ADDR  $ERRPC $ERR/TST#
      DT1      ;$REG2,$ERRPC,$STSTNM,0
      0

;ITEM 2
      EM2      ;CPU ISSUED A BUS GRANT WITH PSW = 7
              ;DEV 1 SHOULD NOT HAVE BECOME BUS MASTER
              ;BE1DB BE1CC BE1BA BE1CR1 PSW $ERRPC $ERR/TST#
              ;$REG0,$REG1,$REG2,$REG3,$REG4,$ERRPC,$STSTNM,0
      DH2
      DT2
      0

;ITEM 3
      EM3      ;CPU DID NOT ISSUE A BUS NPG
              ;BE1CR1 BE1CC FM-PS TO-PS $ERRPC $ERR/TST#
              ;$REG0,$REG1,$REG2,$REG3,$ERRPC,$STSTNM,0
      DH3
      DT3
      0

;ITEM 4
      EM4      ;CPU DID NOT ISSUE BUS GRANT 7
      DH3
      DT3
      0

;ITEM 5
      EM5      ;CPU DID NOT ISSUE BUS GRANT 6
      DH3
      DT3
      0

;ITEM 6
      EM6      ;CPU DID NOT ISSUE BUS GRANT 5
      DH3
      DT3
      0

;ITEM 7
      EM7      ;CPU DID NOT ISSUE BUS GRANT 4
      DH3
      DT3
      0

;ITEM 10
```

861	001314	012201	EM10	:ONE OR MORE DEVICES DID NOT INTERRUPT
862	001316	012247	DH10	:THIS IS THE ORDER IN WHICH THEY INTERRUPTED
863				: 1ST 2ND 3RD 4TH \$ERRPC #ERR/TST#
864	001320	015320	DT10	: \$REG1, \$REG2, \$REG3, \$REG4, \$ERRPC, \$STSTNM, 0
865	001322	000000	0	
866			:ITEM 11	
867	001324	012405	EM11	:BUS ADDRESS LINES <A17:A14> DID NOT FUNCTION PROPERLY
868	001326	012473	DH11	:BE1CR1 BE1CR2 BE1BA \$ERRPC #ERR/TST#
869	001330	015336	DT11	: \$REG1, \$REG2, \$REG3, \$ERRPC, \$STSTNM, 0
870	001332	000000	0	
871			:ITEM 12	
872	001334	012544	EM12	:CPU NO SACK TIME OUT LOGIC FAILED(TO NEGATE BUS GRANT)
873	001336	012632	DH12	:BE1CR1 BE1CR2 \$ERRPC #ERR/TST#
874	001340	015352	DT12	: \$REG0, \$REG1, \$ERRPC, \$STSTNM, 0
875	001342	000000	0	
876			:ITEM 13	
877	001344	012673	EM13	:CPU DID NOT PROPERLY EXECUTE AN ACLO/DCLC SEQUENCE
878	001346	012756	DH13	: \$ERRPC #ERR/TST#
879	001350	015364	DT13	: \$ERRPC, \$STSTNM, 0
880	001352	000000	0	
881			:ITEM 14	
882	001354	012777	EM14	:CPU DID NOT TRAP FROM BUS PARITY ERR PA/PB = 0/1
883	001356	012756	DH13	
884	001360	015364	DT13	
885	001362	000000	0	
886			:ITEM 15	
887	001364	013062	EM15	:DEV 1 DID DATIP WITH ROL ON DATOB TO MEMORY
888				:THE TRANSFER TO THE FOLLOWING LOC WAS INCORRECT
889	001366	013225	DH15	:MEMORY ACTUAL CORRECT
890				: LOC DATA DATA \$ERRPC #ERR/TST# \$ICNT #
891	001370	015372	DT15	: \$REG0, \$REG1, \$REG3, \$ERRPC, \$STSTNM, \$ICNT, 0
892	001372	000000	0	
893			:ITEM 16	
894	001374	013337	EM16	:DEV 3'S DATO TO MEMORY DID NOT EQUAL PATTERN IN R3
895	001376	013225	DH15	
896	001400	015372	DT15	
897	001402	000000	0	
898			:ITEM 17	
899	001404	013425	EM17	:DEV 4'S DATO TO MEMORY DID NOT EQUAL PATTERN IN R4
900	001406	013225	DH15	
901	001410	015372	DT15	
902	001412	000000	0	
903			:ITEM 20	
904	001414	013513	EM20	:DEV 1 DID FUN 1-NPR-DATIP; INCORRECT PATTERN IN MEMORY
905	001416	013225	DH15	
906	001420	015372	DT15	
907	001422	000000	0	
908			:ITEM 21	
909	001424	013607	EM21	:DEV 2 DID FUN 2-NPR-DATOB; INCORRECT PATTERN IN MEMORY
910	001426	013225	DH15	
911	001430	015372	DT15	
912	001432	000000	0	
913			:ITEM 22	
914	001434	013703	EM22	:BIT 7 OF CR2 SET-CPU DID NOT TIME OUT WITH SACK INHIBITED
915	001436	013775	DH22	:DEV # PC \$ERRPC #ERR/TST#
916	001440	015410	DT22	: \$TMP4, \$REG5, \$ERRPC, \$STSTNM, 0

917	001442	000000		
918			0	
919	001444	014037	: ITEM 23	
920	001446	013775	EM23	: BIT 11 OF CR2 SET-NO SSYN ON INTR SIGNAL
921	001450	015410	DH22	
922	001452	000000	DT22	
923			0	
924	001454	014110	: ITEM 24	
925	001456	013775	EM24	: BIT 5 OF CR2 SET-RECEIVED WRONG GRANT
926	001460	015410	DH22	
927	001462	000000	DT22	
928			0	
929	001454	014156	: ITEM 25	
930	001466	013775	EM25	: BIT 6 OF CR2 SET-BUS LATE
931	001470	015410	DH22	
932	001472	000000	DT22	
933			0	
934	001474	014210	: ITEM 26	
935	001476	013775	EM26	: BIT 9 OF CR2 SET-DEV DID NOT RECEIVE SSYN
936	001500	015410	DH22	
937	001502	000000	DT22	
938			0	
939	001504	014252	: ITEM 27	
940	001506	013775	EM27	: BIT 9 OF CR2 SET-WRONG ADDR ON BUS
941	001510	015410	DH22	
942	001512	000000	DT22	
943			0	
944	001514	014321	: ITEM 30	
945	001516	013775	EM30	: BIT 10 OF CR2 SET-DEV RECEIVED OTHER THAN ONE GRANT
946	001520	015410	DH22	
947	001522	000000	DT22	
948			0	
949	001524	014410	: ITEM 31	
950			EM31	: BKGRND RTN INSTRUCTIONS OF NEGB'S WERE NOT DONE
951	001526	014550	DH31	: CORRECTLY TO \$REG1 DURING MULTITRANFERS II
952				: ACTUAL CORRECT
953	001530	015422	DT31	: DATA DATA \$ERRPC #ERR/TST# \$ICNT #
954	001532	000000	0	: \$REG1,146463,\$ERRPC.\$TSTNM,\$ICNT,0
955			: ITEM 32	
956	001534	014643	EM32	: DEV 3 DID DATI BUT HAS INCORRECT
957				: VALUES IN DATA REGISTER
958	001536	014550	DH31	
959	001540	015422	DT31	
960	001542	000000	0	
961			: ITEM 33	
962	001544	014727	EM33	: DEV 4 DID NOT INTR THE CORRECT # OF TIMES
963	001546	014550	DH31	
964	001550	015422	DT31	
965	001552	000000	0	
966			: ITEM 34	
967	001554	015001	EM34	: LAST DATI XFER BY DEV 1 WAS INCORRECT-
968				: EITHER DEV DID NOT WORK OR WRONG DATA WASSET UP
969	001556	014550	DH31	
970	001560	015422	DT31	
971	001562	000000	0	
972			: ITEM 35	

973 001564 015155
974
975 001566 011442
976 001570 015252
977 001572 000000
978
979

EM35
DH1
DT1
0

;CPU TRAPPED THRU LOC 0 TO CATCH
;IMPROPERLY LOADED VECTORS
; ADDR \$ERRPC \$ERR/TST#
;SREG2,SERRPC,\$STSTM,0

981 001574 007740
982 001576 170014
983 000114
984 000116
985 001600 000000
986 001602 000000
987 001604 000000
988 001606 000000
989 001610 000000
990 001612 000000
991 001614 000000
992 001616 000000
993 001620 000000
994 001622 000000
995 001624 000000
996 001626 000000
997 001630 000000
998 001632 000000
999 001634 000000
1000 001636 000000
1001 001640 000000
1002 001642 000000
1003 001644 000000
1004 001646 000000
1005 001650 000000
1006 001652 000000
1007 001654 000000
1008 001656 000000
1009 001660 000000
1010 001662 000000
1011 001664 000000
1012 001666 000000
1013 001670 000000
1014 001672 000000
1015 001674 000000
1016 001676 000000
1017 001700 000000
1018 001702 000000
1019 001710 000000
1020 001712 000000
1021 001714 000000
1022 001716 000000
1023 001720 000000
1024 001722 000000
1025

ALLERR :7740
SIMLGO :170014
PBVEC =114
PBPSW =116
BE1DB :0
BE1CC :0
BE1BA :0
BE1CR1 :0
BE1CLR :0
BE1CR2 :0
BE2DB :0
BE2CC :0
BE2BA :0
BE2CR1 :0
BE2CLR :0
BE2CR2 :0
BE3DB :0
BE3CC :0
BE3BA :0
BE3CR1 :0
BE3CLR :0
BE3CR2 :0
BE4DB :0
BE4CC :0
BE4BA :0
BE4CR1 :0
BE4CLR :0
BE4CR2 :0
BE1VEC :0
BE1PSW :0
BE2VEC :0
BE2PSW :0
BE3VEC :0
BE3PSW :0
BE4VEC :0
BE4PSW :0
DEVcnt :0
DEVS :0,0,0,0
DATA1 :0
DATA2 :0
DATA3 :0
DATA4 :0
ENDMEM :0

; ALL ERR BITS OF CR2
; ADDR TO SET OFF ALL DEVICES SIMOLTANEOUSLY
; TRAP VEC FOR PARITY ERROR
; PSW ADDR FOR TRAP ON PARITY ERR
; DATA REG ADDR FOR DEVICE 1
; CYCLE COUNT REG ADDR FOR DEV 1
; ADDR REG ADDR FOR DEV 1
; CONTROL REG 1 ADDR FOR DEV 1
; CLEAR ERRS REG ADDR FOR DEV 1
; CONTROL REG 2 ADDR FOR DEV 1
; DATA REG ADDR FOR DEV 2
; CYCLE COUNT REG ADDR FOR DEV 2
; ADDR REG ADDR FOR DEV 2
; CONTROL REG 1 ADDR FOR DEV 2
; CLEAR ERRS REG ADDR FOR DEV 2
; CONTROL REG 2 ADDR FOR DEV 2
; DATA REG ADDR FOR DEV 3
; CYCLE COUNT REG ADDR FOR DEV 3
; ADDR REG ADDR FOR DEV 3
; CONTROL REG 1 ADDR FOR DEV 3
; CLEAR ERRS REG ADDR FOR DEV 3
; CONTROL REG 2 ADDR FOR DEV 3
; DATA REG ADDR FOR DEV 4
; CYCLE COUNT REG ADDR FOR DEV 4
; ADDR REG ADDR FOR DEV 4
; CONTROL REG 1 ADDR FOR DEV 4
; CLEAR ERRS REG ADDR FOR DEV 4
; CONTROL REG 2 ADDR FOR DEV 4
; TRAP VEC ADDR FOR DEV 1
; PSW ADDR FOR TRAP THRU BE1VEC
; TRAP VEC ADDR FOR DEV 2
; PSW ADDR FOR TRAP THRU BE2VEC
; TRAP VEC ADDR FOR DEV 3
; PSW ADDR FOR TRAP THRU BE3VEC
; TRAP VEC ADDR FOR DEV 4
; PSW ADDR FOR TRAP THRU BE4VEC
; CONTAINS # OF DEVICES ON BUS
; WILL CONTAIN ADDR(S) OF INTR'G DEVS
; MAX ADDR TO WHICH DATA XFERRED BY DEV 1
; MAX ADDR TO WHICH DATA XFERRED BY DEV 2
; MAX ADDR TO WHICH DATA XFERRED BY DEV 3
; MAX ADDR TO WHICH DATA XFERRED BY DEV 4
; TAG ENDING DEFINED LABELS

1026
1027 001724
1028 001724 012703 001600

CLARTN:
MOV #BE1DB,R3 ;R3 IS POINTER TO BUFFER AREAS

```

1029 001730 005023
1030 001732 022703 001722
1031 001736 100374
1032 001740 012703 001160
1033 001744 005023
1034 001746 022703 001206
1035 001752 101374
1036 001754 000207
1037
1038
1039 001756
1040 001756 012706 001100
1041 001762 005026
1042 001764 022706 001126
1043 001770 001374
1044 001772 012706 001100
1045 001776 012737 015646 000020
1046 002004 012737 000340 000022
1047 002012 012737 016116 000030
1048 002020 012737 000340 000032
1049 002026 012737 020104 000034
1050 002034 012737 000340 000036
1051 002042 012737 020150 000024
1052 002050 012737 000340 000026
1053 002056 013737 015472 015464
1054 002064 005037 001210
1055 002070 005037 001212
1056 002074 112737 000001 001115
1057 002102 012737 002102 001106
1058 002110 012737 002110 001110
1059 002116 013746 000004
1060 002122 013746 000006
1061 002126 012737 002142 000004
1062 002134 005777 176776
1063 002140 000407
1064 002142 012737 000176 001136 64$:
1065 002150 012737 000174 001140
1066 002156 022626
1067 002160 012637 000006 65$:
1068 002164 012637 000004
1069 002170 032777 000200 176740
1070 002176 001402
1071 002200 104400 011164
1072 002204
1073 002204 022737 000777 001174 3$:
1074 002212 001002
1075 002214 000137 003506
1076
1077 002220 5$:
1078 002220 012737 010552 000000
1079 002226 012737 000340 000002
1080 002234 032777 000200 176674
1081 002242 001452
1082 002244 104400 002252
1083 002250 000422
1084

```

```

1$: CLR (R3)+ ;CLEAR BUFFER THEN INCREMENT ADDR
   CMP #ENDMEM, R3 ;IF POINTER AT LAST BUFFER, EXIT
   BPL 1$ ;IF PLUS, GO BACK AND CLEAR NEXT ADDR
   MOV #SREG0, R3 ;NOW START TO CLEAR TEMP REGISTERS
2$: CLR (R3)+ ;CLEAR CURRENT ADDR
   CMP #STMP5, R3 ;CHECK FOR LAST TEMP REG ADDR
   BHI 2$ ;IF NOT, CLEAR NEXT TEMP REG
   RTS PC ;EXIT
;*****
;*****
START:
   MOV #SCMTAG, R6 ;;FIRST LOCATION TO BE CLEARED
   CLR (R6)+ ;;CLEAR MEMORY LOCATION
   CMP #SBDDAT, R6 ;;DONE?
   BNE .-E ;;LOOP BACK IF NO
   MOV #STACK, SP ;;SETUP THE STACK POINTER
   MOV #SCOPE, @IOTVEC ;;IOT VECTOR FOR SCOPE ROUTINE
   MOV #340, @IOTVEC+2 ;;LEVEL 7
   MOV #ERROR, @EMTVEC ;;EMT VECTOR FOR ERROR ROUTINE
   MOV #340, @EMTVEC+2 ;;LEVEL 7
   MOV #STRAP, @TRAPVEC ;;TRAP VECTOR FOR TRAP CALLS
   MOV #340, @TRAPVEC+2 ;;LEVEL 7
   MOV #SPWRDN, @PWRVEC ;;POWER FAILURE VECTOR
   MOV #340, @PWRVEC+2 ;;LEVEL 7
   MOV #ENDCT, #SECCT ;;SETUP END-OF-PROGRAM COUNTER
   CLR #TIMES ;;INITIALIZE NUMBER OF ITERATIONS
   MOVB #1, #SERMAX ;;ALLOW ONE ERROR PER TEST
   MOV #., #SLPADR ;;INITIALIZE THE LOOP ADDRESS FOR SCOPE
   MOV #., #SLPERR ;;SETUP THE ERROR LOOP ADDRESS
   MOV @#4, -(SP) ;;SAVE ERROR VECTOR
   MOV @#6, -(SP)
   MOV #64$, 4 ;;SET UP TIME OUT VECTOR
   TST @SWR ;;TRY TO REFERENCE HARDWARE SWR
   BR 65$ ;;BRANCH IF NO TIMEOUT TRAP OCCURS
64$: MOV #SWREG, SWR ;;POINT TO SOFTWARE SWR
   MOV #DISPREG, DISPLAY ;;POINT TO SOFTWARE DISPLAY REG
   CMP (SP)+, (SP)+ ;;RESTORE STACK
65$: MOV (SP)+, @#6 ;;RESTORE ERROR VECTOR
   MOV (SP)+, @#4
   BIT #BIT07, @SWR ;;IS SWITCH 7 UP?
   BEQ 3$ ;;IF NOT, SKIP TYPEOUT
   TYPE ,QNO
3$: CMP #777, #STMP0 ;;IS THIS RESTART FROM LOC 220?
   BNE 5$ ;;IF NOT, SKIP THE JMP INSTR
   JMP @#TST1 ;;ELSE JUMP TO TEST 1
5$: MOV #THRU0, 0 ;;SET UP FOR TRAP THRU LOC 0
   MOV #PR7, 2 ;;SET UP PSW FOR TRAP THRU 0
   BIT #BIT07, @SWR ;;IS SWITCH 7 UP?
   BEQ 33$ ;;IF NOT, SKIP TYPEOUT
   TYPE .+4 ;;TYPE ASCIZ STRING
   BR 66$ ;;GET OVER THE ASCIZ
; ;.ASCIZ <15><12>/IF BUS HANGS WHILE SIZING MEMORY.

```



```

1085 002316
1086 002316 104400 002324
1087 002322 000422
1089 002370
1090 002370
1091 002370 004737 001724
1092 002374 004737 016600
1093 002400 012737 000340 000006
1094 002406 012700 170000
1095 002412 012702 000510
1096 002416 012701 001600
1097 002422 012703 001660
1098 002426
1099 002426 022700 170060
1100 002432 002002
1101 002434 000137 002736
1102 002440
1103 002440 012737 002544 000004
1104 002446 005710
1105 002450 012737 002662 000004
1106 002456 005237 001700
1107 002462 010021
1108 002464 010037 001172
1109 002470 062700 000002
1110 002474 105237 001174
1111 002500 122737 000005 001174
1112 002506 001365
1113 002510 105037 001174
1114 002514 062700 000004
1115 002520 010021
1116 002522 062700 000002
1117 002526
1118 002526 010223
1119 002530 062702 000002
1120 002534 010223
1121 002536 062702 000002
1122 002542 000731
1123
1124
1125 002544
1126 002544 022700 170060
1127 002550 003035
1128 002552 012716 002736
1129 002556 022737 000000 001700
1130 002564 001035
1131 002566 104400 002574
1132 002572 000423
1133
1134 002642
1135 002642 000000
1136 002644 062700 000020
1137 002650 062702 000004
1138 002654 012716 002426
1139 002660
1140 002660 000002

66$:
TYPE .+4 ;TYPE ASCIZ STRING
BR 67$ ;GET OVER THE ASCIZ
;.ASCIZ <15><12>/IT IS DUE TO NO CPU SSYN TIME OUT/

67$:
33$:
JSR PC,CLRTN ;CLEAR BUFFER AREAS
JSR PC,$SIZE ;FIND AVAILABLE MEMORY
MOV #PR7,ERRVEC+2 ;PS=7 FOR TRAP THRU LOC 4
MOV #170000,R0 ;SET UP POINTER FOR 1ST POSSIBLE DEV ADDR
MOV #510,R2 ;SET UP POINTER FOR 1ST POSSIBLE VEC ADDR
MOV #BE10B,R1 ;SET UP POINTER FOR DEVICE ADDR LOCATION
MOV #BE1VEC,R3 ;SET UP POINTER FOR INTR ADDR LOCATION

LODDEV:
CMP #170060,R0 ;IS R0 > LAST POSSIBLE DEV ADDR?
BGE 10$ ;IF NOT,GO TO 10$
JMP BGIN ;ELSE GO TO BGIN

10$:
MOV #NODEV,ERRVEC ;SET UP TRAP VECTOR FOR TIME OUT
TST (R0) ;SEE IF ACTUAL DEVICE ADDRESS EXISTS
MOV #TYMOUT,ERRVEC ;CHANGE TRAP VECTOR FOR ERROR CONDITION
INC DEVCNT ;COUNT DEVICES
MOVREG: MOV R0,(R1)+ ;MOVE ACTUAL DEVICE ADDR TO DEVICE NAME
MOV R0,$REGS ;REGS CONTAINS LAST DEVICE ADDR
ADD #2,R0 ;INCREMENT POINTER BY 2
INCB $TMPD ;COUNT # OF REGISTERS PER DEVICE
CMPB #5,$TMPD ;AFTER 5 REGISTERS
BNE MOVREG ;ARE RECORDED
CLRB $TMPD ;CLEAR THE COUNTING REGISTER
ADD #4,R0 ;ADD 4 TO THE POINTER THEN
MOV R0,(R1)+ ;RECORD THE LAST REGISTER ADDRESS
ADD #2,R0 ;INCREMENT POINTER BY 2

MOVVEC:
MOV R2,(R3)+ ;NOW START RECORDING
ADD #2,R2 ;THE INTR VECTORS
MOV R2,(R3)+ ;INCREMENT POINTER BY 2
ADD #2,R2 ;THE INTR VECTORS
BR LODDEV ;INCREMENT POINTER BY 2
;AND GO SEE IF THER'S ANOTHER DEVICE

;*****
;*****
NODEV:
CMP #170060,R0 ;TIME OUT ROUTINE FOR DEVICE CHECK
BGT ADD20 ;IF ALL POSSIBLE ADDR'S HAVE NOT BEEN CHECKED
MOV #BGIN,(SP) ;OUT-GO BACK AND CHECK FOR MORE.
CMP #0,DEVCNT ;ELSE CHANGE STACK POINTER
BNE EXNO ;CHECK FOR NO EXERCISERS
TYPE .+4 ;IF ONE OR MORE EXERCISERS, EXIT
BR 64$ ;TYPE ASCIZ STRING
;.ASCIZ <15><12>/THERE ARE NO EXERCISERS ON THE BUS/

64$:
HALT
ADD20: ADD #20,R0 ;ADD 20 TO POINTER
ADD #4,R2 ;POINTER=NEXT DEV'S VEC LOCATIONS
MOV #LODDEV,(SP) ;GO BACK TO LODDEV

EXNO: RTI ;EXIT

```



```

1197 003220 001 .BYTE 1 ;;TYPE 1 DIGIT(S)
1198 003221 000 .BYTE 0 ;;SUPPRESS LEADING ZEROS
1199 003222 104400 003230 TYPE .+4 ;;TYPE ASCIZ STRING
1200 003226 000436 BR 66$ ;;GET OVER THE ASCIZ
1201 ;;.ASCIZ <15><12>/THE LOWEST ELECT. PRIORITY UBE SHOULD NOT HAVE W1 JUMPER/
1202 003324 66$:
1203 003324 104400 003332 TYPE .+4 ;;TYPE ASCIZ STRING
1204 003330 000415 BR 67$ ;;GET OVER THE ASCIZ
1205 ;;.ASCIZ <15><12>/DEVICE ADDRESS(ES): /<15><12>
1206 003364 67$:
1207 003364 005037 001174 CLR $TMPD ;CLEAR TMPD(USED AS COUNTER)
1208 003370 012700 001600 MOV #BE1DB,RO ;USE RO AS POINTER TO ADDRESSES
1209 003374 4$:
1210 003374 005237 001174 INC $TMPD ;ADD 1 TO TMPD
1211 003400 011037 001160 MOV (RO), $REGD ;MOVE FOR TYPEOUT REASONS
1212 003404 104400 003412 TYPE .+4 ;;TYPE ASCIZ STRING
1213 003410 000403 BR 68$ ;;GET OVER THE ASCIZ
1214 ;;.ASCIZ / DEV/
1215 003420 68$:
1216 003420 013746 001174 MOV $TMPD, -(SP) ;;SAVE $TMPD FOR TYPEOUT
1217 003424 104402 TYPOS ;;GO TYPE--OCTAL ASCII
1218 003426 002 .BYTE 2 ;;TYPE 2 DIGIT(S)
1219 003427 000 .BYTE 0 ;;SUPPRESS LEADING ZEROS
1220 003430 104400 003436 TYPE .+4 ;;TYPE ASCIZ STRING
1221 003434 000402 BR 69$ ;;GET OVER THE ASCIZ
1222 ;;.ASCIZ / = /
1223 003442 69$:
1224 003442 013746 001160 MOV $REGD, -(SP) ;;SAVE $REGD FOR TYPEOUT
1225 003446 104402 TYPOS ;;GO TYPE--OCTAL ASCII
1226 003450 006 .BYTE 6 ;;TYPE 6 DIGIT(S)
1227 003451 000 .BYTE 0 ;;SUPPRESS LEADING ZEROS
1228 003452 062700 000014 ADD #14, RO ;ADD 14 FOR NEXT ADDR
1229 003456 023737 001174 001700 CMP $TMPD, DEVCNT ;SEE IF TMPD = # OF DEVICES
1230 003464 001343 BNE 4$ ;IF NOT, GO TYPE NEXT ADDR
1231 003466 104400 001221 001700 TYPE , $CR LF ;TYPE <CR><LF>
1232 003472 022737 000004 001700 CMP #4, DEVCNT ;SEE IF THERE ARE 4 DEVICES
1233 003500 001002 BNE 5$ ;IF NOT, SKIP THE TYPE OUT
1234 003502 104400 011267 TYPE , FOR4 ;ELSE TYPE MSSG FOR 4TH DEV
1235 003506 5$:
1236
1237
1238 ;*****
1239 ;*TEST 1 NO BUS GRANTS ISSUED WITH PROCESSOR AT HIGHER PRIORITY THAN BUS REQUEST
1240 ;*THIS TEST IS TO INSURE THAT ANY REQUEST IS NOT
1241 ;*HONORED AS LONG AS THE PROCESSOR IS AT THE SAME OR
1242 ;*HIGHER PRIORITY
1243 ;*****
1244 003506 000004 TST1: SCOPE
1245 003510 004737 002700 JSR PC, CLRREG ;CLEAR CONTENTS OF ALL AVAILABLE DEVS
1246 003514 NG:
1247
1248 003514 012777 004730 176136 MOV #ERRCHK, @BE1VEC ;SET UP DEVICE 1 INTR VECTOR
1249 003522 012777 000340 176132 MOV #PR7, @BE1PSW ;SET UP DEVICE 1 PSW VECTOR
1250 003530 012737 002662 000004 MOV #TYMOUT, ERRVEC ;SET UP TRAP THRU LOC 4(TIME OUT VEC)
1251 003536 012700 000340 MOV #PS7, RO ;MOVE PS=7 TO RO
1252 003542 012701 002021 MOV #2021, R1 ;MOVE FUN 1-DATI-BR7 TO R1

```

NO BUS EXERCISER
NO BUS GRANTS

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NO BUS GRANTS ISSUED WITH PROCESSOR AT HIGHER PRIORITY THAN BUS REQUEST

```

1203 003546 004737 010626
1204 003552 012700 000300
1205 003556 012701 002011
1206 003562 004737 010626
1207 003566 012700 000240
1208 003572 012701 002005
1209 003576 004737 010626
1210 003602 012700 000200
1211 003606 012701 002003
1212 003612 004737 010626
1213 003616 052777 004000 175762
1214 003624 052777 000040 175754
1215 003632 000240
1216 003634 000004
1217 003636 000340
1218 003642 012700 000340
1219 003642 123737 001115 001103
1220 003650 100451
1221 003652 012737 000340 177776
1222 003650 012777 004730 175772
1223 003656 012777 000340 175766
1224 003674 012777 020310 175702
1225 003702 012777 177777 175672
1226 003710 012777 002377 175670
1227 003716 010037 177776
1228 003722 022777 177777 175652
1229 003730 001014
1230 003732 017737 175650 001160
1231 003740 017737 175636 001162
1232 003746 012737 000340 001164
1233 003754 010037 001166
1234 003760 104003
1235 003762
1236 003762 162700 000040
1237 003766 020027 000000
1238 003772 100323

```

```

JSR PC,NOG ;DO NOG
MOV #PR6,RO ;MOVE PS=6 TO RO
MOV #2011,R1 ;MOVE FUN 1-DATI-BR6 TO R1
JSR PC,NOG ;DO NOG
MOV #PR5,RO ;MOVE PS=5 TO RO
MOV #2005,R1 ;MOVE FUN 1-DATI-BR5 TO R1
JSR PC,NOG ;DO NOG
MOV #PR4,RO ;MOVE PS=4 TO RO
MOV #2003,R1 ;MOVE FUN 1-DATI-BR4 TO R1
JSR PC,NOG ;DO NOG
BIS #BIT11,2BE1CR1 ;SET BIT 11 TO DO FUN 3
BIS #BIT05,2BE1CR1 ;SET OFF DEV AT NPR LEVEL
NOP ;ALLOW TIME FOR XFER

```

```

*****
*TEST 2 ISSUING OF NON-PROCESSOR GRANTS AND ARBITRATION TESTS
*THIS TEST WILL REQUEST ON NPR THRU BR4 LEVELS
*WITH THE PROCESSOR STATUS INITIALLY AT LEVEL 7 AND MAKE
*SURE THE DEVICE EXERCISES AN NPG TO DO A FUN 1-DATI,
*THEN THE REQUESTS WILL BE REPEATED WHILE SEQUENTIALLY
*LOWERING THE PROCESSOR STATUS FROM 7 TO 0 TO ALLOW
*ARBITRATION OF ALL REQUESTS AND THE ISSUING OF NPG
*****

```

```

TST2: SCOPE
NPRST:
25: MOV #PR7,RO
CMPB $ERRMAX,$ERFLG ;MAX ERRS FOR THIS TEST OCCURRED
BMI TST3 ;BR IF YES TO NEXT TEST
MOV #PR7,PSW ;INITIAL PS
MOV #ERRCHK,2BE1VEC ;SET UP VECTOR LOCATION
MOV #PR7,2BE1PSW ;SET UP DEVICE INTR PSW
MOV #ATEND,2BE1BA ;SET UP ADDR REG
MOV #-1,2BE1CC ;SET CYCLE COUNT = 1
MOV #2077,2BE1CR1 ;LOAD #2077 FUNCTIONS
MOV RO,PSW ;LOWER PROC STATUS

55: CMP #-1,2BE1CC ;SEE IF DEVICE WENT OFF
BNE $S ;IF IT DID, SKIP ERR TYPEOUT
MOV 2BE1CR1,$REG0 ;NEXT MOVES ARE FOR TYPEOUTS
MOV 2BE1CC,$REG1
MOV #PR7,$REG2
MOV RO,$REG3
ERROR 3 ;TYPE ERROR MESSG

55: SUB #40,RO ;LOWER PS BY 1 LEVEL
CMP RO,#PRO ;SEE IF RO IS LESS THAN 0
BPL 25 ;IF PLUS ,GO BACK AND DO ANOTHER CYCLE

```

```

*****
*TEST 3 ISSUING OF BUS GRANT 7 AND ARBITRATION TESTS
*THIS TEST WILL ARBITRATE FOR A BG7,
*THE REQUESTS WILL BE ON LEVELS BR7 THRU BR4, DOING
*FUN 1-DATI TRANSFERS, AND THE PROCESSOR STATUS

```

```

1309
1310
1311 003774 000004
1312 003776
1313 003776 012700 000300
1314 004002
1315 004002 123737 001115 001103
1316 004010 100451
1317 004012 012737 000340 177776
1318 004020 012777 004730 175632
1319 004026 012777 000340 175626
1320 004034 012777 020310 175542
1321 004042 012777 177777 175532
1322 004050 012777 002037 175530
1323 004055 010037 177776
1324
1325 004062 022777 177777 175512
1326 004070 001014
1327 004072 017737 175510 001160
1328 004100 017737 175476 001162
1329 004106 012737 000340 001164
1330 004114 010037 001165
1331 004120 104004
1332 004122
1333 004122 162700 000043
1334 004126 020027 000000
1335 004132 100323
1336
1337
1338
1339
1340
1341
1342
1343
1344
1345 004134 000004
1346 004136
1347 004136 012700 000240
1348 004142
1349 004142 123737 001115 001103
1350 004150 100451
1351 004152 012737 000300 177776
1352 004160 012777 004730 175472
1353 004166 012777 000340 175466
1354 004174 012777 020310 175402
1355 004202 012777 177777 175372
1356 004210 012777 002017 175370
1357 004216 010037 177776
1358
1359 004222 022777 177777 175352
1360 004230 001014
1361 004232 017737 175350 001160
1362 004240 017737 175336 001162
1363 004246 012737 000300 001164
1364 004254 010037 001166

```

```

: *LOWERED SEQUENTIALLY FROM 7 TO 0.
: *****
†ST3: SCOPE
BR7TST:
25: MOV #PR6,RO ;2ND PS WILL = 6
CMPB $ERMAX,$ERFLG ;MAX ERRS FOR THIS TEST OCCURRED?
BMI TST4 ;;BR IF YES TO NEXT TEST
MOV #PR7,PSW ;INITIAL PS
MOV #ERRCHK,$BE1VEC ;SET UP VECTOR LOCATION
MOV #PR7,$BE1PSW ;SET UP DEVICE INTR PSW
MOV #ATEND,$BE1BA ;SET UP ADDR REG
MOV #-1,$BE1CC ;SET CYCLE COUNT = 1
MOV #2037,$BE1CR1 ;LOAD #2037 FUNTIIONS
MOV RO,PSW ;LOWER PROC STATUS
CMP #-1,$BE1CC ;SEE IF DEVICE WENT OFF
BNE SS ;IF IT DID,SKIP ERR TYPEOLT
MOV $BE1CR1,$REG2 ;NEXT MOVES ARE FOR TYPEOUTS
MOV $BE1CC,$REG1
MOV #PR7,$REG2
MOV RO,$REG3
ERROR 4 ;TYPE ERROR MESSG
55: SUB #40,RO ;LOWER PS BY 1 LEVEL
CMP RO,#PRO ;SEE IF RO IS LESS THAN 0
BPL 25 ;IF PLUS ,GO BACK AND DO ANOTHER CYCLE

```

```

: *****
: *TEST 4 ISSUING OF BUS GRANT 6 AND ARBITRATION TESTS
: *THIS TEST WILL ARBITRATE FOR A BG6.
: *THE REQUESTS WILL BE ON LEVELS BR6 THRU BR4, DOING
: *FUN 1-DATI TRANSFERS, AND THE PROCESSOR STATUS
: *LOWERED SEQUENTIALLY FROM 6 TO 0.
: *****

```

```

†ST4: SCOPE
BR6TST:
25: MOV #PR5,RO ;2ND PS WILL = 5
CMPB $ERMAX,$ERFLG ;MAX ERRS FOR THIS TEST OCCURRED?
BMI TST5 ;;BR IF YES TO NEXT TEST
MOV #PR6,PSW ;INITIAL PS
MOV #ERRCHK,$BE1VEC ;SET UP VECTOR LOCATION
MOV #PR7,$BE1PSW ;SET UP DEVICE INTR PSW
MOV #ATEND,$BE1BA ;SET UP ADDR REG
MOV #-1,$BE1CC ;SET CYCLE COUNT = 1
MOV #2017,$BE1CR1 ;LOAD #2017 FUNTIIONS
MOV RO,PSW ;LOWER PROC STATUS
CMP #-1,$BE1CC ;SEE IF DEVICE WENT OFF
BNE SS ;IF IT DID,SKIP ERR TYPEOLT
MOV $BE1CR1,$REG0 ;NEXT MOVES ARE FOR TYPEOLTS
MOV $BE1CC,$REG1
MOV #PR6,$REG2
MOV RO,$REG3

```

1365 004260 104005
1366 004262
1367 004262 162700 000040
1368 004266 020027 000000
1369 004272 100323

ERROR 5 ;TYPE ERROR MESSG
SS: SUB #40,RO ;LOWER PS BY 1 LEVEL
CMP RO,#PRO ;SEE IF RO IS LESS THAN 0
BPL 2\$;IF PLUS ,GO BACK AND DO ANOTHER CYCLE

1370
1371
1372
1373
1374
1375
1376
1377
1378

;*TEST 5 ISSUING OF BUS GRANT 5 AND ARBITRATION TESTS
;*THIS TEST WILL ARBITRATE FOR A BGS.
;*THE REQUESTS WILL BE ON LEVELS BR5 THRU BR4, DOING
;*FUN 1-DATI TRANSFERS, AND THE PROCESSOR STATUS
;*LOWERED SEQUENTIALLY FROM 5 TO 0.

1379 004274 000004
1380 004276
1381 004276 012700 000200
1382 004302
1383 004302 123737 001115 001103
1384 004310 100451
1385 004312 012737 000240 177776
1386 004320 012777 004730 175332
1387 004326 012777 000340 175326
1388 004334 012777 020310 175242
1389 004342 012777 177777 175232
1390 004350 012777 002007 175230
1391 004356 010037 177776

TST5: SCOPE
BR5TST: MOV #PR4,RO ;2ND PS WILL = 4
2\$: CMPB \$ERMAX,\$ERFLG ;MAX ERRS FOR THIS TEST OCCURRED?
BMI TST6 ;;BR IF YES TO NEXT TEST
MOV #PR5,PSW ;INITIAL PS
MOV #ERRCHK,\$BE1VEC ;SET UP VECTOR LOCATION
MOV #PR7,\$BE1PSW ;SET UP DEVICE INTR PSW
MOV #ATEND,\$BE1BA ;SET UP ADDR REG
MOV #-1,\$BE1CC ;SET CYCLE COUNT = 1
MOV #2007,\$BE1CR1 ;LOAD #2007 FUNCTIONS
MOV RO,PSW ;LOWER PROC STATUS

1392
1393 004362 022777 177777 175212
1394 004370 001014
1395 004372 017737 175210 001160
1396 004400 017737 175176 001162
1397 004406 012737 000240 001164
1398 004414 010037 001166
1399 004420 104006

CMP #-1,\$BE1CC ;SEE IF DEVICE WENT OFF
SS: BNE SS ;IF IT DID,SKIP ERR TYPEOUT
MOV \$BE1CR1,\$REG0 ;NEXT MOVES ARE FOR TYPEOUTS
MOV \$BE1CC,\$REG1
MOV #PR5,\$REG2
MOV RO,\$REG3
ERROR 6 ;TYPE ERROR MESSG

1400 004422
1401 004422 162700 000040
1402 004426 020027 000000
1403 004432 100323

SS: SUB #40,RO ;LOWER PS BY 1 LEVEL
CMP RO,#PRO ;SEE IF RO IS LESS THAN 0
BPL 2\$;IF PLUS ,GO BACK AND DO ANOTHER CYCLE

1404
1405
1406
1407
1408
1409
1410
1411
1412

;*TEST 6 ISSUING OF BUS GRANT 4 AND ARBITRATION TESTS
;*THIS TEST WILL ARBITRATE FOR A BG4.
;*THE REQUESTS WILL BE ON LEVEL BR4, DOING
;*FUNC 1-DATI TRANSFERS, AND THE PROCESSOR STATUS
;*LOWERED SEQUENTIALLY FROM 4 TO 0.

1413 004434 000004
1414 004436
1415 004436 012700 000140
1416 004442
1417 004442 123737 001115 001103
1418 004450 100451
1419 004452 012737 000200 177776
1420 004460 012777 004730 175172

TST6: SCOPE
BR4TST: MOV #PR3,RO ;2ND PS WILL = 3
2\$: CMPB \$ERMAX,\$ERFLG ;MAX ERRS FOR THIS TEST OCCURRED?
BMI TST7 ;;BR IF YES TO NEXT TEST
MOV #PR4,PSW ;INITIAL PS
MOV #ERRCHK,\$BE1VEC ;SET UP VECTOR LOCATION

E03

UNIBUS EXERCISER
DZKUAC.P11 T6

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ISSUING OF BUS GRANT 4 AND ARBITRATION TESTS

```

1421 004466 012777 000340 175166      MOV      #PR7, @BE1PSW      ;SET UP DEVICE INTR PSW
1422 004474 012777 020310 175102      MOV      #ATEND, @BE1BA    ;SET UP ADDR REG
1423 004502 012777 177777 175072      MOV      #-1, @BE1CC      ;SET CYCLE COUNT = 1
1424 004510 012777 002003 175070      MOV      #2003, @BE1CR1   ;LOAD #2003 FUNTIONS
1425 004516 010037 177776      MOV      RO, PSW          ;LOWER PROC STATUS
1426
1427 004522 022777 177777 175052      CMP      #-1, @BE1CC      ;SEE IF DEVICE WENT OFF
1428 004530 001014 177777 175052      BNE      :3              ;IF IT DID, SKIP ERR TYPEOUT
1429 004532 017737 175050 001160      MOV      @BE1CR1, $REG0    ;NEXT MOVES ARE FOR TYPEOUTS
1430 004540 017737 175036 001162      MOV      @BE1CC, $REG1
1431 004546 012737 000200 001164      MOV      #PR4, $REG2
1432 004554 010037 001156      MOV      RO, $REG3
1433 004550 104077 001156      ERROR    7                ;TYPE ERROR MESSG
1434 004552
1435 004562 162700 000040      SS:      SUB      #40, RO      ;LOWER PS BY 1 LEVEL
1436 004566 020027 000000      CMP      RO, #PRO        ;SEE IF RO IS LESS THAN 0
1437 004572 100323 000000      BPL      2$              ;IF PLUS, GO BACK AND DO ANOTHER CYCLE
1438
1439
1440
1441
1442
1443
1444
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1446
1447
1448 004574 000004      *TEST 7  CPU TEST FOR NO SACK TIME OUT
1449 004576 004737 002700      ;*THIS TEST WILL CHECK THAT THE CPU TIMES OUT AND
1450 004602 012777 177777 174772      ;*DROPS A GRANT IF NO SACK SIGNAL IS RECEIVED
1451 004610 012777 020310 174766      ;*IF THE CPU TIME OUT IS INOPERATIVE, THE BUS EXERCISER
1452 004616 012737 000340 177776      ;*WILL TIME OUT AND SEND THE SACK SIGNAL TO PREVENT
1453 004624 012737 002662 000004      ;*A BUS HANG AND SET AN ERROR FLAG IN CR2
1454 004632 012777 004730 175020      *****
1455 004640 012777 000340 175014      †ST7:  SCOPE
1456 004646 052777 000010 174736      JSR      PC, CLREG        ;CLEAR CONTENTS OF ALL AVAILABLE DEVS
1457 004654 012777 006003 174724      MOV      #-1, @BE1CC      ;SET CYCLE COUNT = 1
1458 004662 012737 000140 177776      MOV      #ATEND, @BE1BA   ;SET UP DEVICE REG ADDR
1459 004670 004737 011036 000010 174710      MOV      #PR7, PSW       ;SET PS=7
1460 004674 042777 000010 174710      MOV      #TYMOUT, ERRVEC  ;SET UP TIME OUT VECTOR
1461 004702 105777 174704      MOV      #ERRCHK, @BE1VEC ;SET UP DEVICE INTR VECTOR
1462 004706 100024 174704      MOV      #PR7, @BE1PSW    ;SET UP DEVICE INTR PSW
1463 004710 017737 174672 001160      BIS      #BIT03, @BE1CR2  ;INHIBIT SACK RETURN
1464 004716 017737 174670 001162      MOV      #6003, @BE1CR1  ;DO FUN 3--BR4
1465 004724 104012 000414      MOV      #PR3, PSW       ;LOWER PROC. STATUS TO 3
1466 004726 000414      JSR      PC, CNTR        ;DELAY FOR TIMEOUT
1467
1468
1469 004730      BIC      #BIT03, @BE1CR2 ;ALLOW FUTURE SACKS
1470 004730 033777 001574 174654      TSTB    @BE1CR2         ;CHECK IF NO-NO SACK BIT IS SET
1471 004736 001407 001172 001204      BPL      TST10          ;IF NOT SET, GO TO NEXT TEST
1472 004740 011637 000301 001204      MOV      @BE1CR1, $REG0   ;MOVE FOR TYPEOUT REASONS
1473 004744 012737 000301 001204      MOV      @BE1CR2, $REG1   ;MOVE FOR TYPEOUT
1474 004752 004737 010376 000002      ERROR    12             ;ERROR IF SET-DEVICE FORCED TO SEND SACK
1475 004756 000002      BR       TST10          ;GO TO NEXT TEST
1476 004756 000002      *****
ERRCHK:  BIT      ALLERR, @BE1CR2 ;CHECK FOR ANY ERRS IN CR2
        BEQ      SS        ;IF NONE, EXIT
        MOV      (SP), $REGS ;FOR TYPEOUT OF PC
        MOV      #1, $TMP4  ;INDICATOR FOR DEVICE
        JSR      PC, ERRTN  ;CHECK TO SEE IF ANY ERRORS OCCURED
        SS:
        RTI                ;EXIT TRAP

```

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1487 004760 000004
1488
1489 004752 012737 000340 177776
1490 004770 004737 002700
1491 004774 012702 020310
1492 005000 012705 000010
1493 005004 004737 011020
1494
1495 005010 012777 004730 174642
1496 005016 012777 000340 174636
1497 005024 012777 177770 174550
1498 005032 012777 020310 174544
1499 005040 052777 040000 174544
1500 005046 012777 024441 174532
1501 005054 012737 000000 177776
1502 005062
1503 005062 105777 174520
1504 005066 100375
1505 005070 042777 040000 174514
1506 005076 022777 000010 174474
1507 005104 001407
1508 005106 017737 174466 001162
1509 005114 012737 000010 001164
1510 005122 104034
1511 005124
1512 005124 032777 004000 174460
1513 005132 001402
1514 005134 104023
1515 005136 000400
1516
1517
1518
1519
1520
1521
1522
1523
1524
1525
1526 005140 000004
1527 005142 012737 000340 177776
1528 005150 004737 002700
1529 005154
1530 005154 012704 001702
1531 005160 012777 005402 174472
1532 005166 012777 000340 174466

```

```

:*****
:*TEST 10 CPU TEST FOR RECEIVING SACK
:*THIS TEST IS TO INSURE THAT THE CPU CAN RECEIVE THE
:*SACK SIGNAL; THE TIME DELAY WILL BE SET ON DEVICE 1
:*AND SEVERAL DATA TRANSFERS MADE, IF THERE IS NO BUS
:*LATE ERROR, THE CPU RECEIVED SACK CORRECTLY
:*IT IS ASSUMED THAT DEV 1 TIME DELAY IS SET FOR 10 US

```

```

:*****
†ST10: SCOPE

```

```

MOV #PR7,PSW ;PS = 7
JSR PC,CLRREG ;CLEAR ALL DEVICE REGISTERS
MOV #ATEND,R2 ;R2 WILL POINT TO END OF PROG
MOV #10,R5 ;R5 = # OF TEST WORDS TO CREATE
JSR PC,DOUP ;CREATE THOSE TEST WORDS

```

```

MOV #ERRCHK, @BE1VEC ;SET UP VECTOR LOCATION
MOV #PR7, @BE1PSW ;SET UP DEVICE INTR PSW
MOV #-10, @BE1CC ;SET UP CYCLE COUNT
MOV #ATEND, @BE1BA ;SET UP ADDR REGISTER
BIS #BIT14, @BE1CR2 ;SET BIT 14 OF CR2 FOR TIME DELAY
MOV #24441, @BE1CR1 ;DO FUN 2-DATIP/NO ROL-NPR
MOV #PRO,PSW ;LOWER PS TO ALLOW INTERRUPTS

```

```

SS:
TSTB @BE1CR1 ;SEE IF DONE BIT SET
BPL SS ;IF NOT, GO BACK AND WAIT
BIC #BIT14, @BE1CR2 ;ELSE CLEAR BIT 14 OF CR2
CMP #10, @BE1DB ;DID LAST XFER MOVE 10 INTO DB
BEQ 10$ ;IF IT DID, GO TO 10$
MOV @BE1DB, $REG1 ;ELSE MOVE FOR ERR TYPE OUT
MOV #10, $REG2
ERROR 34 ;TYPE ERR MSSG

```

```

10$:
BIT #BIT11, @BE1CR2 ;SEE IF NO SSYN ON INTR ERR SET
BEQ TST11 ;IF NOT SET, GO TO NEXT TEST
ERROR 23 ;ELSE TYPE ERR MSSG
BR TST11 ;THEN GO TO NEXT TEST

```

```

:*****
:*TEST 11 PASSING OF GRANTS AND INTERRUPT TEST
:*THIS TEST WILL SET OFF ALL AVAILABLE DEVICES SIMULTANEOUSLY
:*WHOSE ONLY FUNCTIONS WILL BE TO INTERRUPT, THE REQUESTS
:*WILL ALL BE AT LEVEL 7 SO THAT THE DEVICE CLOSEST TO THE CPU
:*SHOULD RECEIVE BG7 FIRST AND INTERRUPT FIRST, THE NEXT
:*CLOSEST SHOULD INTERRUPT NEXT AND SO ON.

```

```

:*****
†ST11: SCOPE

```

```

MOV #PR7,PSW ;PS=7
JSR PC,CLRREG ;CLEAR CONTENTS OF ALL AVAILABLE DEVS

```

```

LOAD1:
MOV #DEVS,R4 ;DEVS CONTAINS SEQUENCE OF INTR'G DEVICE ADDRS
MOV #INTR1, @BE1VEC ;SET UP DEVICE 1 INTR VECTOR
MOV #PR7, @BE1PSW ;SET UP INTR PSW

```


1599
1590
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1596
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005502 000004
005504 004737 002700
005510 012737 000140 177776
005516 012777 005604 174134
005524 012777 000340 174130
005532
005532 004737 011064
005536 152777 000001 174046
005544 004737 011064
005550 042777 000001 174034
005556 152777 000002 174026
005564 004737 011064
005570 152777 000003 174014
005576 004737 011064
005602 000431
005604
005604 011637 001172
005610 012737 000001 001204
005616 032777 007340 173766
005624 001003
005626 005077 173756
005632 000414
005634
005634 017737 173746 001162
005642 017737 173744 001164
005650 017737 173730 001166
005656 104011
005660 004737 010376
005664 000002
005666 000004
005670 012737 000001 001210
005676 005737 000042
005702 001061
005704 012705 000001
005710

```
*****
*TEST 12 ADDRESS LINES (14 - 17) CHECK
; *THIS TEST WILL CHECK BUS ADDRESS LINES 14 THRU 17
; *BY DOING A FUN 1-DATI-NPR TO THOSE ADDRESSES
; *IF THE ADDRESSES DON'T EXIST THE INTERRUPT ROUTINE
; *WILL IGNORE ANY NO SSYN ERROR.
*****
†ST12: SCOPE
        JSR PC, CLRREG ; CLEAR CONTENTS OF ALL AVAILABLE DEVS
        MOV #PR3, PSW ; PS=3
        MOV #BRK, @BE1VEC ; SET UP DEVICE INTR VEC
        MOV #PR7, @BE1PSW ; SET UP DEVICE PSW VEC
D014:
        JSR PC, ADLI ; TEST ADDR LINES 14 & 15
        BISB #1, @BE1CR2 ; ELSE SET BIT 0 OF CR2(ADDR LINE 16)
        JSR PC, ADLI
        BIC #1, @BE1CR2 ; CLEAR BIT 0(ADDR LINE 16)
        BISB #2, @BE1CR2 ; SET BIT 1 OF CR2(ADDR LINE 17)
        JSR PC, ADLI
        BISB #3, @BE1CR2 ; ELSE SET BITS 0 AND 1 OF CR2
; SETS ADDR LINES 16 & 17
        JSR PC, ADLI
        BR TST13 ; GO TO NEXT TEST
*****
*****
BRK:
        MOV (SP), $REG5 ; FOR TYPEOUT OF PC
        MOV #1, $TMP4 ; INDICATOR FOR DEVICE 1
        BIT #7340, @BE1CR2 ; CHECK FOR ALL ERRS EXCEPT NO SSYN ERR
        BNE 1$ ; IF ANY ARE SET, SEE WHICH ONES
        CLR @BE1CLR ; ELSE CLEAR THE NO SSYN ERR
        BR EXBRK ; AND EXIT
1$:
        MOV @BE1CR1, $REG1 ; MOVES ARE FOR TYPEOUTS
        MOV @BE1CR2, $REG2
        MOV @BE1BA, $REG3
        ERROR 11 ; ERR ON ACCESSING A14 - A17
        JSR PC, ERRTN ; DO ERR CHECK SUB-ROUTINE
EXBRK: RTI ; EXIT
*****
*TEST 13 CPU TEST FOR ACLO/DCLO SEQUENCE
; *THIS TEST CHECKS THE ASSERTION OF ACLO AND DCLO
; *AND THAT THE CPU TRAPS TO THE CORRECT SERVICE ROUTINE.
; *IF THIS PROGRAM IS RUNNING UNDER ACT11 THIS TEST
; *WILL BE SKIPPED.
*****
†ST13: SCOPE
        MOV #1, $TIMES ; DO 1 ITERATION
        TST 42 ; SEE IF PROGRAM IS UNDER ACT11
        BNE TST14 ; IF UNDER ACT, DO NOT PERFORM THIS TEST
        MOV #1, R5 ; INIT R5 WITH A VALUE OF 1
E$:
```

```

1645 005710 005205          INC      R5          ;ADD 1 TO R5
1646 005712 100376          BPL      6$         ;KEEP ADDING AS LONG AS R5 POS
1647 005714 012737 000001 001204  MOV     #1,$TMP4    ;INDICATOR FOR DEVICE 1
1648 005722 012737 000340 177776  MOV     #PR7,PSW    ;SET PS=7
1649 005730 012777 004730 173722  MOV     #ERRCHK,$BE1VEC ;SET UP INTR VECTOR
1650 005736 012777 000340 173716  MOV     #PR7,$BE1PSW  ;SET UP DEVICE INTR PSW
1651 005744 005037 001176          CLR     $TMP1       ;CLEAR TEMPORARY REGISTER(TMP1)
1652 005750 012737 006026 000024  MOV     #TMPPWR,PWRVEC ;SET UP SPECIAL POWER RTN
1653 005756 052777 000020 173626  BIS     #BIT04,$BE1CR2 ;INDICATE PWR FAILURE BY SETTING BIT 4
1654 005764 004737 011036          JSR     PC,CNTR     ;PAUSE FOR TIME
1655 005770 012737 010332 000024  MOV     #PWRFAL,PWRVEC ;RESTORE PWRFAL SEQ FOR A PWR FAIL
1656 005776 042777 000020 173606  BIC     #BIT04,$BE1CR2 ;MAKE SURE BIT 4 IS CLEARED
1657 006004
1658 006004 022737 001221 020266  FAILCK: CMP     #$CRLF,$PWRMG ;IF THIS TEST IS CAUSE OF
1659                                ;PWR FAIL --TYPE <CR><LF>
1660 006012 001401          BEQ     XTST       ;IF EQUAL, EXIT TEST
1661 006014 104013          ERROR   13        ;TYPE ERR MMSG IF FAILURE
1662 006016
1663 006016 012737 020300 020266  XTST:  MOV     #$POWER,$PWRMG ;RESTORE TYPE OUT OF 'POWER'
1664 006024 000410          BR      TST14     ;GO TO NEXT TEST
1665 ;*****
1666 ;*****
1667 006026          TMPPWR:          ;SPECIAL PWR RTN; OTHER THAN SYSMAC'S
1668 006026 012737 001221 020266  MOV     #$CRLF,$PWRMG ;CHANGE PWR MMSG TO <CR><LF>
1669 005034 042777 000020 173550  BIC     #BIT04,$BE1CR2 ;CLEAR POWER DOWN/UP BIT
1670 006042 000137 020150          JMP     $PWRDN    ;GO TO THAT RTN
1671
1672
1673 ;*****
1674 ;*TEST 14          PARITY ERROR TEST
1675 ;*THIS TEST WILL CAUSE PARITY ERROR AND CHECKS
1676 ;*THAT THE CPU TRAPS TO THE CORRECT VECTOR, IT
1677 ;*FIRST CHECKS THAT THE CPU CAN HANDLE A PARITY
1678 ;*TRAP OTHERWISE IT SKIPS THIS TEST
1679 ;*****
1680 006046 000004          TST14: SCOPE
1681 006050 012737 006260 000010  MOV     #NOD0,10   ;SET UP RESERVED INSTR VECTOR
1682 006056 012737 000340 000C12  MOV     #PR7,12    ;PSW=7
1683 006064 005037 001202          CLR     $TMP3     ;SET $TMP3 = 0
1684 006070 000270          SEN          ;SET N BIT OF CC
1685 005072 006737 001202  SXT     $TMP3     ;IF VALID INSTR, $TMP3 WILL = -1
1686 006076 005737 001202  TST     $TMP3     ;IF INVALID, $TMP3 WILL REMAIN 0
1687 006102 100033          BPL     NXT       ;IF CP NOT= 35,40,45,OR 70.GO TO NEXT TEST
1688 006104 012737 000140 177776  MOV     #PR3,PSW   ;PS=3
1689 006112 012777 006224 173540  MOV     #PBERR,$BE1VEC ;SET UP DEVICE INTR
1690 006120 012737 006246 000114  MOV     #PBRTN,PBVEC ;SET UP PARITY BIT VECTOR
1691 006126 012737 000340 000116  MOV     #340,PBPSW ;SET UP PARITY BIT PSW
1692 006134 012777 020310 173442  MOV     #ATEND,$BE1BA ;SET UP ADDR REG
1693 006142 012777 177777 173432  MOV     #-1,$BE1CC ;SET UP CYCLE COUNT
1694 006150 052777 010000 173434  BIS     #BIT12,$BE1CR2 ;SET BIT 12 FOR PARITY ERROR
1695 006156 005777 173430          TST     $BE1CR2  ;SET OFF PARITY ERR SEQUENCE
1696 006162 012777 013161 173416  MOV     #13161,$BE1CR1 ;TRY FUN 1-DATO FROM CC-NPR-INTR ON DONE(?)
1697 006170 000240          NOP          ;ALLOW TIME FOR ATTEMPTED XFER
1698 006172
1699 006172 012737 000116 000114  NXT:  MOV     #PBPSW,PBVEC ;RESTORE
1700 006200 012737 000000 000116  MOV     #0,PBPSW   ;TRAP CATCHER HERE AND

```

UNIBUS EXERCISER
DZKUAC.P11

T14

MACY11 27(732) 10-SEP-76 13:08 PAGE 36
PARITY ERROR TEST

```

1701 006206 012737 000012 000010      MOV      #12,10      ;AT RESERVED
1702 006214 012737 000000 000012      MOV      #0,12      ;INSTRUCTION VECTOR
1703 006222 000417          BR      TST15      ;;BRANCH TO NEXT TEST IF PARITY TRAP OCCURRED
1704 006224          FBERR:
1705 006224 011637 001172      MOV      (SP),SREGS ;FOR TYPEOUT OF PC
1706 006230 104014      ERROR  14          ;TYPE ERR MSSG IF DEVICE INTERRUPTED
1707 006232 012737 000001 001204      MOV      #1,$TMP4   ;INDICATOR FOR DEVICE 1
1708 006240 004737 010376      JSR      PC,ERRTN   ;CHECK TO SEE IF ANY ERRORS OCCURED
1709 006244 000002      RTI          ;EXIT TRAP
1710          ;*****
1711          ;*****
1712 006246          PBRTN:
1713 006246 012777 000000 173336      MOV      #0,$BE1CR2 ;PARITY BIT TRAP RTN
1714          ;CLEAR PARITY BIT ERROR-MUST BE DONE
1715 006254 012716 006172      MOV      #NXT,(SP) ;BY MOVING 0(S) TO BE1CR2
1716 006260 000002      NODO: RTI          ;SET STACK FOR NEXT TEST
1717
1718
1719          ;*****
1720          ;*TEST 15      MULTITRANSFERS I
1721          ;*THIS TEST WILL CAUSE ANY BUS EXERCISERS, UP TO 4,
1722          ;*TO CREATE A LOT OF TRAFFIC ON THE BUS AND
1723          ;*CHECK THAT THE CPU CAN HANDLE IT; ALL DEVICES
1724          ;*ARE SET OFF SIMULTANEOUSLY
1725          ;*****
1726 006262 000004      TST15: SCOPE
1727 006264 004737 002700      JSR      PC,CLREG   ;CLEAR CONTENTS OF ALL AVAILABLE DEVS
1728 006270 012703 000000      MOV      #0,R3      ;SET DATA PATTERN = 0
1729 006274 012704 177777      MOV      #177777,R4 ;SET DATA PATTERN = ALL 1'S
1730 006300 004737 007470      JSR      PC,MULT1   ;LOAD & EXECUTE ALL DEVICES
1731 006304 022737 000002 001700      CMP      #2,DEVcnt ;ARE THERE MORE THAN 2 DEVICES?
1732 006312 100115      BPL     TST16      ;IF 2 OR LESS, GO TO NEXT TEST
1733 006314 012703 161610      MOV      #161610,R3 ;ELSE LOAD R3 AND R4 WITH
1734 006320 012704 016161      MOV      #016161,R4 ;ANOTHER PATTERN
1735          ;*****
1736 006324 123737 001115 001103      SS:      CMPB     $ERMAX,$ERFLG ;MAX ERRS FOR THIS TEST OCCURRED?
1737 006332 100505      BMI     TST16      ;BR IF YES TO NEXT TEST
1738 006334 004737 010570      JSR      PC,ROTATE  ;ROTATE DATA PATTERNS
1739 006340 004737 007470      JSR      PC,MULT1   ;LOAD & EXECUTE ALL DEVICES
1740 006344 022703 107070      CMP      #107070,R3 ;IS R3 = 107070?
1741 006350 001365      BNE     SS         ;IF NOT,ROTATE AND DO AGAIN
1742 006352 012703 167777      MOV      #167777,R3 ;ELSE MOVE NEW PATTERNS
1743 006356 012704 010000      MOV      #010000,R4 ;INTO R3 AND R4
1744          ;*****
1745 006362 123737 001115 001103      10$:     CMPB     $ERMAX,$ERFLG ;MAX ERRS FOR THIS TEST OCCURRED?
1746 006370 100466      BMI     TST16      ;BR IF YES TO NEXT TEST
1747 006372 004737 010570      JSR      PC,ROTATE  ;ROTATE DATA PATTERNS
1748 006376 004737 007470      JSR      PC,MULT1   ;LOAD & EXECUTE ALL DEVICES
1749 006402 022703 167777      CMP      #167777,R3 ;IS R3 = 167777 AGAIN?
1750 006406 001365      BNE     10$        ;IF NOT,ROTATE AND DO AGAIN
1751 006410 000456      BR      TST16      ;GO TO NEXT TEST
1752          ;*****
1753          ;*****
1754          ;*****
1755 006412          SERV1:
1756 006412 017737 173166 001712      MOV      $BE1BA,DATA1 ;MOVE ADDR IN BE1BA TO DATA1 AND

```

```

1757 006420 162737 000001 001712      SUB      #1,DATA1      ;SUB 1 TO GET ACTUAL ADDR
1758 006426 012737 000001 001204      MOV      #1,$TMP4     ;INDICATOR FOR DEVICE 1
1759 006434 000435      BR       INK          ;BRANCH TO INK
1760 006436      SERV2:
1761 006436 017737 173156 001714      MOV      @BE2BA,DATA2 ;MOVE ADDR IN BE2BA TO DATA2 AND
1762 006444 162737 000002 001714      SUB      #2,DATA2     ;SUB 2 TO GET ACTUAL ADDR
1763 006452 012737 000002 001204      MOV      #2,$TMP4     ;INDICATOR FOR DEVICE 2
1764 006460 000423      BR       INK          ;BRANCH TO INK
1765 006462      SERV3:
1766 006462 017737 173146 001716      MOV      @BE3BA,DATA3 ;MOVE ADDR IN BE3BA TO DATA3 AND
1767 006470 162737 000002 001716      SUB      #2,DATA3     ;SUB 2 TO GET ACTUAL ADDR
1768 006476 012737 000003 001204      MOV      #3,$TMP4     ;INDICATOR FOR DEVICE 3
1769 006504 000411      BR       INK          ;BRANCH TO INK
1770 006506      SERV4:
1771 006506 017737 173136 001720      MOV      @BE4BA,DATA4 ;MOVE ADDR IN BE4BA TO DATA4 AND
1772 006514 162737 000002 001720      SUB      #2,DATA4     ;SUB 2 TO GET ACTUAL ADDR
1773 006522 012737 000004 001204      MOV      #4,$TMP4     ;INDICATOR FOR DEVICE 4
1774 006530      INK:
1775 006530 005237 001164      INC      $REG2        ;INCREMENT REG
1776 006534 011637 001172      MOV      (SP),$REG5   ;FOR TYPEOUT OF PC
1777 006540 004737 010376      JSR      PC,ERRTN     ;CHECK FOR ANY ERRS
1778 006544 000002      RTI                 ;EXIT
1779
;*****
1780
;*****
1781
;*TEST 16      MULTITRANSFERS II
1782
;*THIS TEST WILL HAVE THE AVAILABLE EXERCISERS DOING
1783
;*VARIOUS TRANSFERS AND/OR INTERRUPTS AT DIFFERENT
1784
;*REQUEST LEVELS TO FURTHER CHECK CPU HANDLING CAPABILITIES
1785
;*****
1786
†ST16: SCOPE
1787 006546 000004
1788 006550 012702 020310      MOV      #ATEND,R2   ;R2 = END OF PROG
1789 006554 012705 005000      MOV      #5000,R5    ;R5 = THE # OF DATA WORDS
1790 006560 004737 011020      JSR      PC,DOUP     ;CREATE THOSE WORDS IN BUFFER MEMORY
1791 006564 004737 011132      JSR      PC,TSTCVR   ;SET UP PATTERN IN MEMORY BUFFER AREA
1792 006570 004737 002700      JSR      PC,CLRREG   ;CLEAR CONTENTS OF ALL AVAILABLE DEVS
1793
1794 006574 012737 000000 177776      MOV      #PRO,PSW    ;PS=0
1795 006602 012777 007334 173050      MOV      #S1,@BE1VEC ;SET UP DEVICE 1 INTR VECTOR
1796 006610 012777 000340 173044      MOV      #PR7,@BE1PSW ;SET UP DEVICE 1 PSW VECTOR
1797 006616 012777 022310 172760      MOV      #ATEND+2000,@BE1BA ;SET UP ADDR REG
1798 006624 012777 176000 172750      MOV      #-2000,@BE1CC ;SET UP CYCLE COUNT
1799 006632 012777 015551 172746      MOV      #15551,@BE1CR1 ;DO FUN 2-DATOB FROM CC-NPR-INTR ON DONE(6)
1800 006640 022737 000001 001700      CMP      #1,DEVCNT   ;CHECK FOR MORE THAN 1 DEVICE
1801 006646 001467      BEQ      IS          ;IF NOT, GO CHECK RESULTS
1802
1803 006650 012777 007366 173006      MOV      #S2,@BE2VEC ;SET UP DEVICE 2 INTR VECTOR
1804 006656 012777 000340 173002      MOV      #PR7,@BE2PSW ;SET UP DEVICE 2 PSW VECTOR
1805 006664 012777 177030 172724      MOV      #-1000,@BE2CC ;SET UP CYCLE COUNT FOR 1000 XFERS
1806 006672 012777 020310 172720      MOV      #ATEND,@BE2BA ;SET UP ADDR REG=1ST LOCATION AFTER PROG
1807 006700 012777 002551 172714      MOV      #2551,@BE2CR1 ;DO FUN 1-DATIP-NPR-INTR ON DONE(7)
1808 006706 022737 000002 001700      CMP      #2,DEVCNT   ;CHECK FOR MORE THAN 2 DEVICES
1809 006714 001444      BEQ      IS          ;IF NOT, GO CHECK RESULTS
1810
1811 006716 012777 007420 172744      MOV      #S3,@BE3VEC ;SET UP DEVICE 3 INTR VECTOR
1812 006724 012777 000340 172740      MOV      #PR7,@BE3PSW ;SET UP PSW VECTOR
    
```

```

1813 006732 012777 176776 172672      MOV      #-1002, @BE3CC      ;SET UP CYCLE COUNT
1814 006740 012777 020310 172666      MOV      @ATEND, @BE3BA    ;SET UP ADDR REG
1815 006746 012777 004005 172662      MOV      #4005, @BE3CR1   ;DO FUN 2-DATI-BR5
1816 006754 022737 000003 001700      CMP      #3, DEVCNT       ;CHECK FOR MORE THAN 3 DEVICES
1817 006762 001421                                BEQ      1$              ;IF NOT, GO CHECK RESULTS
1819
1819 006764 005037 001170                                CLR      $REG4            ;USE REG4 TO COUNT INTRS
1820 006770 012777 007436 172676      MOV      #54, @BE4VEC     ;SET UP DEVICE 4 INTR VECTOR
1821 006776 012777 000340 172672      MOV      #PR7, @BE4PSW    ;SET UP PSW VECTOR
1822 007004 012777 175000 172634      MOV      #-3000, @BE4CC   ;SET UP CYCLE COUNT
1823 007012 052777 040000 172636      BIS      #BIT14, @BE4CR2  ;SET TIME DELAY BIT OF DEVICE 4
1824 007020 012777 000021 172624      MOV      #21, @BE4CR1     ;DO FUN 0-BR7
1825 007026
1826 007026 012700 001606                                MOV      #BE1CR1, R0      ;USE R0 TO POINT TO DEVICE 1'S CR1
1827 007032 004737 010744                                JSR      PC, BKGD        ;PERFORM BACKGROUND ROUTINE

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; /*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:
; /*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:
;BE1 TRANSFER CHECKS
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; /*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:

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1835 007036 012702 022310                                MOV      @ATEND+2000, R2  ;USE R2 TO POINT TO 2000 BYTES AFTER END OF PROGRAM
1836 007042 012737 176000 001164      MOV      #-2000, $REG2   ;SET UP $REG2 WITH EXPECTED RESULTS
1837 007050 023702 001712 10$:      CMP      DATA1, R2      ;CHECK FOR 4000 BYTES PAST END OF PROGRAM
1838 007054 001417                                BEQ      14$             ;IF EQUAL, GO CHECK FOR ANOTHER DEVICE
1839 007056 023722 001164      CMP      $REG2, (R2)+    ;ELSE COMPARE EXPECTED RESULTS WITH THAT IN R2
1840 007062 001004                                BNE      12$            ;IF NOT EQUAL, GO TO ERR MSSG
1841 007064 062737 000002 001164      ADD      #2, $REG2       ;INCREMENT REG2 AS CC WOULD
1842 007072 000766                                BR       10$            ;AND GO BACK TO CHECK NEXT LOC
1843 007074 12$:
1844 007074 014237 001162      MOV      -(R2), $REG1    ;MOVE R2 TO REG 5 FOR TYPEOUT
1845 007100 010237 001160      MOV      R2, $REG0
1846 007104 013737 001164 001163      MOV      $REG2, $REG3
1847 007112 104021                                ERROR    21              ;TYPE ERR MSSG
1848 007114 14$:
1849 007114 022737 000001 001700      CMP      #1, DEVCNT     ;CHECK IF ONLY 1 DEVICE SHOULD HAVE OPERATED
1850 007122 001470                                BEQ      TS17           ;<IF EQUAL, GO TO NEXT TEST>

```

```

; /*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:
; /*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:
;BE2 TRANSFER CHECKS
; /*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:/*:
; /*:/*:/*:/*:~/*:~/*:~/*:~/*:~/*:~/*:~/*:~/*:~/*:~/*:~/*:~/*:~/*:~/*:~/*:~/*:~/*:

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1858 007124 012701 020310                                MOV      @ATEND, R1      ;USE R1 TO POINT TO END OF PROG
1859 007130 023701 001714 5$:      CMP      DATA2, R1     ;CHECK FOR 2000 BYTES PAST END
1860 007134 001413                                BEQ      6$              ;IF EQUAL, EXIT
1861 007136 022721 052525      CMP      #052525, (R1)+ ;ELSE COMPARE EXPECTED RESULTS WITH R1
1862 007142 001772                                BEQ      5$             ;IF EQUAL, GO CHECK NEXT LOC
1863 007144 014137 001162      MOV      -(R1), $REG1   ;MOVE R1 TO REG1 FOR TYPEOUT
1864 007150 010137 001160      MOV      R1, $REG0
1865 007154 012737 052525 001166      MOV      #052525, $REG3
1866 007162 104020                                ERROR    20              ;ELSE TYPE ERR MSSG
1867 007164 6$:
1868 007164 022737 000002 001700      CMP      #2, DEVCNT     ;CHECK IF ONLY 2 DEVICES OPERATED

```


1925	007356	005777	172224		TST	0BE1CR1	: TEST FOR ERROR
1926	007362	100041			BPL	EXS	: IF PLUS, EXIT
1927	007364	000434			BR	CHEX	: ELSE FIND CAUSE OF INTR
1929	007366			S2:			
1929	007366	017737	172226	001714	MOV	0BE2BA, DATA2	: MOVE ADDR IN BE2BA TO DATA2 AND
1930	007374	162737	000002	001714	SUB	#2, DATA2	: SUB 2 TO GET ACTUAL ADDR
1931	007402	012737	000002	001204	MOV	#2, \$TMP4	: SET INDICATOR FOR DEVICE 2
1932	007410	005777	172206		TST	0BE2CR1	: TEST FOR ERROR
1933	007414	100024			BPL	EXS	: IF PLUS EXIT
1934	007416	000417			BR	CHEX	: ELSE FIND CAUSE OF INTR
1935	007420			S3:			
1936	007420	012737	000003	001204	MOV	#3, \$TMP4	: SET INDICATOR FOR DEVICE 3
1937	007426	005777	172204		TST	0BE3CR1	: TEST FOR ERROR
1938	007432	100015			BPL	EXS	: IF PLUS, EXIT
1939	007434	000410			BR	CHEX	: ELSE FIND CAUSE OF INTR
1940	007436			S4:			
1941	007436	005237	001170		INC	\$REG4	: COUNT DEVICE 4'S INTR


```

1998 007766 012701 001162           MOV    #SREG1,R1    ;SET UP R1 AS POINTER
1999 007772 005121             7$:    COM    (R1)+    ;COMPLEMENT TEMP REG
2000 007774 006041           ROR    -(R1)       ;ROTATE CONTENTS RIGHT
2001 007776 123737 001700 001164    CMPB   DEVCNT,$REG2 ;CHECK IF ALL DEVICES ARE DONE
2002 010204 101372           BHI    7$         ;IF NOT, CONTINUE WITH BACKGROUND RTN
2003                                     ;*****

```

:*:*:*:*:*:*:*:*:*:*:*:*:*:*:*:*:*:*:*:*:*:*:*:*:**
:*:*:*:*:*:*:*:*:*:*:*:*:*:*:*:*:*:*:**
:DEVICES TRANSFER CHECKS

:THERE ARE NO CHECKS FOR BE2
:*:*:*:*:*:*:*:*:*:*:*:*:*:*:*:*:*:*:**
:*:*:*:*:*:*:*:*:*:*:*:*:*:**
:*

```

2012 010006 042777 040000 171576    BIC    #BIT14,$BE1CR2 ;CLEAR TIME DELAY BIT
2013 010014 012700 020310           MOV    #ATEND,RO    ;START CHECKING FOR CORRECT XFRS
2014 010020             9$:    ;
2015 010020 122720 000125    CMPB   #125,(RO)+    ;COMPARE LOWER BYTE
2016 010024 001012           BNE    20$         ;IF NOT EQUAL, BR TO ERR MSSG
2017 010026 023700 001712    CMP    DATA1,RO    ;IS THIS LAST BYTE COMPARE?
2018 010032 001422           BEQ    9$         ;IF SO, BR TO 9$
2019 010034 122720 000124    CMPE   #124,(RO)+    ;ELSE COMPARE UPPER BYTE
2020 010040 001006           BNE    22$         ;IF NOT EQUAL, BR TO ERR MSSG
2021 010042 023700 001712    CMP    DATA1,RO    ;IS THIS LAST BYTE COMPARE?
2022 010046 001414           BEQ    9$         ;IF SO, BR TO 9$
2023 010050 000763           BR     8$         ;ELSE CHECK NEXT ADDR
2024 010052             20$:   ;
2025 010052 105740           TSTB   -(RO)       ;SUB 1 TO GET ERR ADDR
2026 010054 000401           BR     24$        ;GO DO MOVES FOR ERR MSSG
2027 010056             22$:   ;
2028 010056 005740           TST    -(RO)       ;SUB 2 TO GET ERR ADDR
2029 010060             24$:   ;
2030 010060 011037 001162    MOV    (RO),SREG1   ;MOVES ARE FOR ERR MSSG
2031 010064 010037 001160    MOV    RO,$REG0
2032 010070 012737 052125 001166    MOV    #052125,$REG3
2033 010076 104015           ERROR  15         ;ELSE TYPE ERR MSSG
2034 010100             9$:    ;
2035 010100 022737 000001 001700    CMP    #1,DEVCNT   ;IF ONLY ONE DEVICE
2036 010106 001447           BEQ    13$         ;IF NO MORE DEVICES, EXIT RTN
2037 010110 122737 000002 001700    CMPB   #2,DEVCNT   ;CHECK FOR MORE THAN 2 DEVICES
2038 010116 001773           BEQ    25$         ;IF NOT, EXIT TEST

```

:*:*:*:*:*:*:*:*:*:*:*:*:**
:BE3 TRANSFER CHECKS
:*:*:*:*:*:*:*:**
:*

```

2046 010120 042777 040000 171514    BIC    #BIT14,$BE3CR2 ;CLEAR TIME DELAY BIT OF DEVICE 3
2047 010126 012700 022310           MOV    #ATEND+2000,RO ;CHECK NEXT 2000 LOCATIONS
2048 010132 023700 001716             10$:  CMP    DATA3,RO    ;CHECK FOR 2000 XFRS
2049 010136 001411           BEQ    11$         ;IF SO, CHECK NEXT BLOCK
2050 010140 020320           CMP    R3,(RO)+    ;TEST FOR CORRECT PATTERNS
2051 010142 001773           BEQ    10$         ;IF NO ERR, CHECK ANOTHER LOC
2052 010144 010337 001166    MCV    R3,$REG3    ;THE MOVE IS FOR TYPEOUT REASONS
2053 010150 014037 001162    MOV    -(RO),SREG1

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2054 010154 010037 001160      MOV    RO,$REGO
2055 010160 104016      ERROR  16      ;ELSE TYPE ERR MSSG
2056 010162      11$:
2057 010162 122737 000003 001700  CMPB   #3,DEVCNT ;CHECK FOR MORE THAN 3 DEVICES
2058 010170 001416      BEQ    13$      ;IF NO MORE DEVICES GO DO NEXT PATTERN

: *:/*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:
: *:/*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:
;BE4 TRANSFER CHECKS
: :*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:
: :*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:*/:

2056 010172 012700 024310      MOV    #ATEND+4000,RO ;CHECK NEXT BLOCK OF 2000 LOCATIONS
2057 010176 023700 001720      12$:  CMP    DATA4,RO     ;CHECK FOR 1000 XFRS
2058 010202 001411      BEQ    13$          ;IF SO, CHANGE DATA PATTERNS & START OVER AGAIN
2059 010204 020420      CMP    R4,(RO)+    ;ELSE CHECK FOR CORRECT PATTERNS
2070 010206 001773      BEQ    12$          ;IF NO ERR, CHECK ANOTHER LOCATION
2071 010210 010437 001166      MOV    R4,$REG3    ;THE MOVE IS FOR TYPEOUT REASONS
2072 010214 014037 001162      MOV    -(RO),$REG1
2073 010220 010037 001160      MOV    RO,$REGO
2074 010224 104017      ERROR  17      ;ELSE TYPE ERR MSSG
2075 010226      13$:
2076 010226 000207      RTS    PC

:////////////////////////
:////////////////////////
:////////////////////////
STVEC:
2081 010230 005000      CLR    RO          ;RO IS COUNTER
2082 010232 012701 010266      MOV    #Q1,R1     ;R1 IS ADDR OF INTR RTN
2083 010236 012702 000510      MOV    #S10,R2    ;R2 IS DEV INTR VEC ADDR
2084 010242      5$:
2085 010242 010122      MOV    R1,(R2)+   ;LOAD INTR RTN INTO INTR VEC
2086 010244 012722 000340      MOV    #PR7,(R2)+;LOAD INTR PSW
2087 010250 062701 000006      ADD    #6,R1      ;GET NEXT INTR RTN
2088 010254 005200      INC    RO         ;INC COUNTER
2089 010256 020037 001700      CMP    RO,DEVCNT ;ARE ALL AVAILABLE VECs LOADED?
2090 010262 001367      BNE   5$         ;IF NOT, GO AND LOAD NEXT ONE
2091 010264 000207      RTS    PC        ;ELSE EXIT

:*****
:*****

; THE FOLLOWING INTR ROUTINES SHOULD HANDLE ANY ERRORS
; WHICH MIGHT OCCUR BEFORE THE PROGRAM HAS A CHANCE TO
; PROPERLY CHECK OUT ALL DEVICES AND SET UP THEIR INTR VECTORS

2100 010266      01:
2101 010266 012737 000001 001204  MOV    #1,$TMP4   ;INDICATOR FOR DEV 1
2102 010274 000413      BR     XQ
2103 010276      02:
2104 010276 012737 000002 001204  MOV    #2,$TMP4   ;INDICATOR FOR DEV 2
2105 010304 000407      BR     XQ
2106 010306      03:
2107 010306 012737 000003 001204  MOV    #3,$TMP4   ;INDICATOR FOR DEV 3
2108 010314 000403      BR     XQ
2109 010316      04:

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2110 010316 012737 000004 001204      MOV      #4,STMP4      ;INDICATOR FOR DEV 4
2111 010324      XQ:      JSR      PC,ERRTN      ;GO TO ERR ROUTINE
2112 010324 004737 010376      RTI
2113 010330 000002
2114
2115 :*****
2116 :*****
2116 010332      PWRFAL:
2117 010332 010146      MOV      R1,-(SP)      ;SAVE CONTENTS OF R1
2118 010334 010046      MOV      R0,-(SP)      ;SAVE CONTENTS OF R0
2119 010336 012700 001612      MOV      #BE1CR2,R0    ;R0 POINTS TO DEV 1 CR2 ADDR
2120 010342 005001      CLR      R1            ;CLEAR R1
2121 010344      S$:
2122 010344 042770 000020 000000      BIC      #BIT04,@(R0)  ;CLR BIT 4 OF CURRENT CR2
2123 010352 062700 000014      ADD      #14,R0        ;ADD 14 TO POINT TO NEXT CR2
2124 010356 005201      INC      R1            ;COUNT THE NUMBER OF DEVS
2125 010360 020137 001700      CMP      R1,DEV CNT    ;REACHED MAX # ON BUS?
2126 010364 103767      BLO      S$            ;IF NOT, CLR NEXT CR2
2127 010366 012600      MOV      (SP)+,R0      ;ELSE RESTORE R0
2128 010370 012601      MOV      (SP)+,R1      ;AND R1
2129 010372 000137 020150      JMP      $PWRDN        ;THEN DO REGULAR PWR DOWN RTN
2130
2131 :*****
2131 :*****
2131      ERRTN:
2132 010376      SAVREG
2133 010376 104407      MOV      #BE1CR2-14,R0 ;SAVE REGISTERS
2134 010400 012700 001576      CLR      R5            ;INITIALIZE R0
2135 010404 105005      CLRB     R5            ;CLEAR DEVICE COUNTER
2136 010406      I$:
2137 010406 105205      INCB     R5            ;ADD 1 TO COUNTER
2138 010410 062700 000014      ADD      #14,R0        ;SET R0=ADDR OF CR2 OF NEXT DEVICE
2139 010414 120537 001204      CMPB     R5,$TMP4      ;IF COUNTER NOT EQUAL TO INDICATOR
2140 010420 001372      BNE      I$            ;ADD 1 TO COUNTER & CHECK AGAIN
2141 010422      CHKERR:
2142 010422 105770 000000      TSTB     @(R0)         ;CHECK FOR NO NOSACK TIMEOUT
2143 010426 100001      BPL      I$            ;IF NOT, SEE IF THERE ARE ANY ERRS
2144 010430 104022      ERROR    22           ;TYPE ERR MESSG FOR NO NOSACK
2145 010432      I$:
2146 010432 032770 007540 000000      BIT      #7540,@(R0)   ;CHECK FOR OTHER ERRORS
2147 010440 001436      BEQ      LEEV          ;IF NO ERRORS,EXIT
2148 010442 032770 004000 000000      BIT      #BIT11,@(R0)  ;CHECK FOR NO SSYN ON INTR
2149 010450 001401      BEQ      I0$          ;IF NOT SET, CHECK FOR NEXT ERR
2150 010452 104023      ERROR    23           ;TYPE ERR MESSG FOR NO SSYN ON INTR
2151 010454      I0$:
2152 010454 132770 000040 000000      BITB     #BIT05,@(R0)  ;CHECK FOR WRONG GRANT ERR
2153 010462 001401      BEQ      I2$          ;IF NOT, CHECK BIT 6
2154 010464 104024      ERROR    24           ;ELSE TYPE ERR MESSG FOR WRONG GRANT
2155 010466      I2$:
2156 010466 132770 000100 000000      BITB     #BIT06,@(R0)  ;CHECK FOR BUS LATE ERR
2157 010474 001401      BEQ      I3$          ;IF NOT, CHECK BIT 8
2158 010476 104025      ERROR    25           ;TYPE ERR MESSG FOR BUS LATE
2159 010500      I3$:
2160 010500 032770 000400 000000      BIT      #BIT08,@(R0)  ;CHECK FOR NO SSYN ERR
2161 010506 001401      BEQ      I4$          ;IF NOT, CHECK BIT 9
2162 010510 104026      ERROR    26           ;TYPE ERR MESSG FOR NO SSYN
2163 010512      I4$:
2164 010512 032770 001000 000000      BIT      #BIT09,@(R0)  ;CHECK FOR WRONG ADDR ERR
2165 010520 001401      BEQ      I5$          ;IF NOT, CHECK BIT 10

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2166 010522 104027          ERROR 27          ;TYPE ERR MSSG FOR WRONG ADDR
2167 010524                SS:                ;
2168 010524 032770 002000 000000  BIT #BIT10,2(R0) ;CHECK FOR NO GRANT ERR
2169 010532 001401          BEQ LEEV          ;IF NOT, EXIT
2170 010534 104030          ERROR 30          ;TYPE ERR MSSG FOR NO GRANT
2171 010536                LEEV:                ;
2172 010536 162700 000002  SUB #2,R0          ;POINT TO DEVICE CLEAR REG
2173 010542 005070 000000  CLR 2(R0)         ;CLEAR ALL ERRORS
2174 010546 104410          RESREG          ;RESTORE REGISTERS
2175 010550 000207          RTS PC           ;EXIT
2176
2177
2178
2179 010552                THRU:                ;
2180 010552 011637 001164  MOV (SP),SREG2    ;MOVE FOR ERR TYPE OUT
2181 010556 162737 000002 001164  SUB #2,SREG2     ;SUB 2 FOR ACTUAL ADDR
2182 010564 104035          ERROR 35          ;TYPE ERR MSSG
2183 010566 000002          RTI
2184
2185
2186 010570                ROTATE:                ;
2187 010570 032703 000001  BIT #BIT00,R3        ;IS LSB A 1 OR 0 ?
2188 010574 001402          BEQ SS             ;IF 0, GO TO SS
2189 010576 000261          SEC             ;ELSE SET C BIT OF COND CODES
2190 010600 000401          BR 10$           ;AND GO ROTATE
2191 010602                SS:                ;
2192 010602 000241          CLC             ;CLEAR C BIT OF COND CODES
2193 010604                10$:                ;
2194 010604 006003          ROR R3           ;ROTATE R3
2195 010606 032704 000001  BIT #BIT00,R4        ;IS LSB A 1 OR 0 ?
2196 010612 001402          BEQ 15$           ;IF 0, GO TO 15$
2197 010614 000261          SEC             ;ELSE SET C BIT OF COND CODES
2198 010616 000401          BR 20$           ;AND GO ROTATE
2199 010620                15$:                ;
2200 010620 000241          CLC             ;CLEAR C BIT OF COND CODES
2201 010622                20$:                ;
2202 010622 006004          ROR R4           ;ROTATE R4
2203 010624          RTS PC
2204
2205
2206 010626                NOG:                ;
2207 010626 25:                ;
2208 010626 010037 177776  MOV R0,PSW        ;SET UP PROCESSOR STATUS
2209 010632 012777 020310 170744  MOV #ATEND,2BE1BA ;SET UP ADDR REG
2210 010640 012777 177777 170734  MOV #-1,2BE1CC   ;SET UP CYCLE COUNT FOR 1 CYCLE
2211 010646 010177 170734  MOV R1,2BE1CR1    ;DO FUN 1 ON BR LEVELS IN R1
2212 010652 000240          NOP             ;WAIT FOR DEVICE TO ATTEMPT TO DO XFER
2213 010654 022777 177777 170720  CMP #-1,2BE1CC   ;SEE IF DEVICE OPERATED
2214 010662 001005          BNE 4$           ;IF IT DID, GO TYPE ERR MSSG
2215 010664 106201          ASRB R1         ;SHIFT BYTE RIGHT TO LOWER BR0
2216 010666 122701 000001  CMPB #1,R1       ;IF BYTE IS NOT EQUAL TO 1
2217 010672 001355          BNE 2$           ;GO TO 2$
2218 010674 000402          BR EXNOG        ;EXIT
2219 010676                4$:                ;
2220 010676 004737 010704  JSR PC,ERRS      ;
2221 010702 000207          EXNOG: RTS PC         ;EXIT SUB RTN

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22222 *****
22223 ERRS:
22224 010704 017737 170670 001160      MOV     JBE1DB,$REG0      ;MOVES ARE FOR TYPEOUTS
22225 010712 017737 170664 001162      MOV     JBE1CC,$REG1
22226 010720 017737 170660 001164      MOV     JBE1BA,$REG2
22227 010726 017737 170654 001166      MOV     JBE1CR1,$REG3
22228 010734 010037 001170              MOV     RO,$REG4
22229 010740 104002                      ERROR   2
22230 010742 000207                      RTS     PC                ;EXIT ERROR RTN
22231 *****
22232 *****
22233 BKGD:
22234 010744 012737 031463 001162      MOV     #031463,$REG1    ;START OF BACKGROUND ROUTINE
22235 010752 012701 001163              MOV     #R1+1,R1        ;USE R1 TO POINT TO TEST PATTERN
22236 010756 105441                      1$:    NEGB    -(R1)      ;DECREMENT LOC AND NEGATE BYTE=(031715)
22237 010760 105421                      NEGB    (R1)+           ;NEGATE BYTE THEN INCREMENT LOC=(031463)
22238 010762 105421                      NEGB    (R1)+           ;NEGATE BYTE THEN INCREMENT LOC=(146463)
22239 010764 105770 000000              TSTB   J(RO)           ;TEST FOR DONE BIT OF DEVICE IN RO
22240 010770 100402                      BMI     2$              ;IF DONE, GO CHECK RESULTS
22241 010772 105441                      NEGB    -(R1)          ;ELSE DECREMENT LOC AND NEGATE BYTE=(031463)
22242 010774 000770                      BR      1$              ;CONTINUE WITH BACKGROUND
22243 010776
22244
22245 010776 005741                      TST     -(R1)           ;BRING POINTER DOWN TO REG1
22246 011000 022711 146463              CMP     #146463,(R1)    ;COMPARE EXPECTED PATTERN WITH THAT IN R1
22247 011004 001404                      BEQ     BKEX            ;IF EQUAL, EXIT THIS RTN
22248 011006 012737 146463 001164      MOV     #146463,$REG2   ;MOVE FOR TYPE OUT
22249 011014 104031                      ERROR   31              ;ELSE TYPE ERR MSSG
22250 011016 000207                      SKEX:  RTS     PC
22251 *****
22252 *****
22253 DOUP:
22254 011020 012701 000001              MOV     #1,R1           ;INIT R1 TO 1
22255 011024
22256 011024 010122                      5$:    MOV     R1,(R2)+    ;MOVE CONTENTS OF R1 TO AREA IN R2
22257 011026 005201                      INC     R1              ;ADD 1 TO R1
22258 011030 020105                      CMP     R1,R5           ;IS # OF MOVES = TO # IN R5?
22259 011032 101774                      BLOS   5$              ;IF NOT, DO ANOTHER MOVE
22260 011034 000207                      RTS     PC              ;ELSE EXIT
22261 *****
22262 *****
22263
22264 011036
22265 C 1036 012737 000001 001170      CNTR:  MOV     #1,$REG4    ;INITIALIZE COUNTER REG
22266 011044 062737 000001 001170      1$:    ADD     #1,$REG4        ;ADD 1 TO IT
22267 011052 022737 000106 001170      CMP     #70,$REG4      ;COUNT OF 70 IS APPROX 20 US
22268 011060 001371                      BNE    1$              ;IF NOT, GO BACK AND ADD 1 TO REG4
22269 011062 000207                      RTS     PC              ;EXIT
22270 *****
22271 *****
22272 ADLI:
22273 011064 012700 040000              MOV     #40000,RO       ;USE RO TO SET BIT 14
22274 011070
22275 011070 012777 177777 170504      1$:    MOV     #-1,JBE1CC     ;SET CYCLE COUNT = 1 XFER
22276 011076 010077 170502              MOV     RO,JBE1BA       ;SET ADDR AS SPECIFIED IN RO
22277 011102 012777 002041 170476      MOV     #2041,JBE1CR1   ;DO DATI-FUN 1-NPR

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2278 011110 004737 011036
2279 011114 022700 100000
2280 011120 001403
2281 011122 012700 100000
2282 011126 000760
2283 011130
2284 011130 000207
2285
2286
2287 011132
2288 011132 012737 000140 177776
2289 011140 005037 001164
2290 011144 012700 020310
2291 011150 012720 125252
2292 011154 022700 022310
2293 011160 001373
2294 011162 000207
2295
2296
2297
2298 011164 005015 005015 047125
011267 015 042012 053105
011374 050103 020125 051124
011442 040440 042104 020122
011473 103 052520 044440
011536 042504 020126 020061
011606 042502 042061 020102
011676 050103 020125 044504
011730 042502 041461 030522
012011 103 052520 042040
012047 103 052520 042040
012105 103 052520 042040
012143 103 052520 042040
012201 117 042516 047440
012247 124 044510 020123
012324 020040 051461 020124
012405 102 051525 040440
012473 103 030505 051103
012544 050103 020125 047516
012632 042502 041461 030522
012673 103 052520 042040
012756 042444 051122 041520
012777 103 052520 042040
013062 042504 020126 020061
013137 124 042510 052040
013225 115 046505 051117
013256 046040 041517 020040
013337 104 053105 041511
013425 104 053105 041511
013513 104 053105 041511
013607 104 053105 041511
013703 102 052111 033440
013775 104 053105 021440
014037 102 052111 030440
014110 044502 020124 020065
014156 044502 020124 020066

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JSR PC_CNTR ;ALLOW TIME FOR RDY BIT TO SET
CMP #100000,RO ;CHECK IF BIT 15 OF RO IS SET
BEQ EXAD ;IF SET, GO SET NEXT ADDR LINE
MOV #100000,RO ;ELSE, NOW SET BIT 15 OF RC
BR IS ;GO BACK AND CHECK THAT ADDR

EXAD:
RTS PC ;EXIT SUB ROUTINE
;*****
;*****
†STOVR:
MOV #PR3,PSW ;PS=3
CLR $REG2 ;CLEAR REG FOR INTR ON DONE COUNTER
MOV #ATEND,RO ;SET UP RO AS POINTER
IS: MOV #125252,(RO)+ ;MOVE DATA PATTERN TO AVAILABLE MEMORY
CMP #ATEND+2000,RO ;CHECK FOR A 2000 MOVES
BNE IS ;IF NOT, GO BACK AND MOVE AGAIN
RTS PC ;EXIT
;*****
;*****

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QNO: .ASCIZ <15><12><15><12>\UNIBUS SYSTEMS EXERCISER DIAGNOSTIC - DZKUA-C BY:M.S
FOR4: .ASCIZ <15><12>\DEV 4 MUST HAVE TIME DELAY SET @ 100 US OR LATENCY ERR MAY OCCU
EM1: .ASCIZ \CPU TRAPPED THRU LOCATION 4 -TIMEOUT \
DH1: .ASCIZ \ ADDR $ERRPC #ERR/TST#\
EM2: .ASCIZ \CPU ISSUED A BUS GRANT WITH PSW = 7\
.ASCIZ \DEV 1 SHOULD NOT HAVE BECOME BUS MASTER\
DH2: .ASCIZ \BE1DB BE1CC BE1BA BE1CR1 PSW $ERRPC #ERR/TST#\
EM3: .ASCIZ \CPU DID NOT ISSUE BUS NPG\
DH3: .ASCIZ \BE1CR1 BE1CC FM-PS TO-PS $ERRPC #ERR/TST#\
EM4: .ASCIZ \CPU DID NOT ISSUE BUS GRANT 7\
EM5: .ASCIZ \CPU DID NOT ISSUE BUS GRANT 6\
EM6: .ASCIZ \CPU DID NOT ISSUE BUS GRANT 5\
EM7: .ASCIZ \CPU DID NOT ISSUE BUS GRANT 4\
EM10: .ASCIZ \ONE OR MORE DEVICES DID NOT INTERRUPT\
DH10: .ASCIZ \THIS IS THE ORDER IN WHICH THEY INTERRUPTED\<15><12>
.ASCIZ \ 1ST 2ND 3RD 4TH $ERRPC #ERR/TST#\
EM11: .ASCIZ \BUS ADDRESS LINES <A17:A14> DID NOT FUNCTION PROPERLY\
DH11: .ASCIZ \BE1CR1 BE1CR2 BE1BA $ERRPC #ERR/TST#\
EM12: .ASCIZ \CPU NO SACK TIMEOUT LOGIC FAILED(TO NEGATE BUS GRANT)\
DH12: .ASCIZ \BE1CR1 BE1CR2 $ERRPC #ERR/TST#\
EM13: .ASCIZ \CPU DID NOT PROPERLY EXECUTE AN ACLO/DCLO SEQUENCE\
DH13: .ASCIZ \ $ERRPC #ERR/TST#\
EM14: .ASCIZ \CPU DID NOT TRAP FROM BUS PARITY ERROR PA/PB = 0/1\
EM15: .ASCIZ \DEV 1 DID DATIP WITH ROL ON DATOB TO MEMORY\<15><12>
.ASCIZ \THE TRANSFER TO THE FOLLOWING LOCATION WAS INCORRECT\
DH15: .ASCIZ \MEMORY ACTUAL CORRECT\<15><12>
.ASCIZ \ LOC DATA DATA $ERRPC #ERR/TST# SICNT #\
EM16: .ASCIZ \DEVICE 3'S DATO TO MEMORY DID NOT EQUAL PATTERN IN R3\
EM17: .ASCIZ \DEVICE 4'S DATO TO MEMORY DID NOT EQUAL PATTERN IN R4\
EM20: .ASCIZ \DEVICE 1 DOING FUN 1-NPR-DATIP; INCORRECT PATTERN IN MEMORY\
EM21: .ASCIZ \DEVICE 2 DOING FUN 2-NPR-DATOB; INCORRECT PATTERN IN MEMORY\
EM22: .ASCIZ \BIT 7 OF CR2 SET-CPU DID NOT TIME OUT WITH SACK INHIBITED\
DH22: .ASCIZ \DEV # PC $ERRPC #ERR/TST#\
EM23: .ASCIZ \BIT 11 OF CR2 SET-NO SSYN ON INTR SIGNAL\
EM24: .ASCIZ \BIT 5 OF CR2 SET-RECEIVED WRONG GRANT\
EM25: .ASCIZ \BIT 6 OF CR2 SET-BUS LATE\

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014210 044502 020124 020070 EM26: .ASCIZ \BIT 8 OF CR2 SET-NO SSYN OCCURRED\
014252 044502 020124 020071 EM27: .ASCIZ \BIT 9 OF CR2 SET-WRONG ADDRESS ON BUS\
014321 102 052111 030440 EM30: .ASCIZ \BIT 10 OF CR2 SET-DEVICE RECEIVED OTHER THAN ONE GRANT
014410 045502 047107 020104 EM31: .ASCIZ \BKGND ROUTINE INSTRUCTIONS OF NEGB'S WERE NOT DONE\<15><12>
014474 047503 051122 041505 .ASCIZ \CORRECTLY TO $REG1 DURING MULTITRANSFERS II\
014550 041501 052524 046101 DH31: .ASCIZ \ACTUAL CORR'T \<15><12>
014572 040504 040524 020040 .ASCIZ \DATA DATA $ERRPC #ERR/TST# $ICNT #\
014643 104 053105 031440 EM32: .ASCIZ \DEV 3 DID DATI BUT HAS INCORRECT VALUES IN DATA REG\
014727 104 053105 032040 EM33: .ASCIZ \DEV 4 DID NOT INTR THE CORRECT # OF TIMES\
015001 114 051501 020124 EM34: .ASCIZ \LAST DATIP TO DEVICE 1 DB WAS INCORRECT- EITHER DEVICE DID\<15><12>
015075 116 052117 053440 .ASCIZ \NOT WORK OR BUFFER AREA WAS NOT SET UP PROPERLY\
015155 015 041412 052520 EM35: .ASCIZ <15><12>\CPU TRAPPED THRU LOC 0 TO CATCH IMPROPERLY LOADED VECTORS\
.EVEN
015252 001164 001116 001102 DT1: .WORD $REG2,$ERRPC,$STNM,0
015262 001160 001162 001164 DT2: .WORD $REG0,$REG1,$REG2,$REG3,$REG4,$ERRPC,$STNM,0
015302 001160 001162 001164 DT3: .WORD $REG0,$REG1,$REG2,$REG3,$ERRPC,$STNM,0
015320 001162 001164 001166 DT10: .WORD $REG1,$REG2,$REG3,$REG4,$ERRPC,$STNM,0
015336 001162 001164 001166 DT11: .WORD $REG1,$REG2,$REG3,$ERRPC,$STNM,0
015352 001160 001162 001116 DT12: .WORD $REG0,$REG1,$ERRPC,$STNM,0
015364 001116 001102 000000 DT13: .WORD $ERRPC,$STNM,0
015372 001160 001162 001166 DT15: .WORD $REG0,$REG1,$REG3,$ERRPC,$STNM,$ICNT,0
015410 001204 001172 001116 DT22: .WORD $TMP4,$REG5,$ERRPC,$STNM,0
015422 001162 001164 001116 DT31: .WORD $REG1,$REG2,$ERRPC,$STNM,$ICNT,0

```

```

;*****
;*****
;*****
;*****

```

.SBTTL END OF PASS ROUTINE

```

;*INCREMENT THE PASS NUMBER ($PASS)
;*TYPE "END PASS #XXXXX TOTAL NUMBER OF ERRORS SINCE LAST REPORT YYYY"
;*WHERE XXXXX AND YYYY ARE DECIMAL NUMBERS
;*IF THERES A MONITOR GO TO IT
;*IF THERE ISN'T JUMP TO TST1

```

\$EOP:

```

SCOPE
CLR $STNM ;;ZERO THE TEST NUMBER
CLR $TIMES ;;ZERO THE NUMBER OF ITERATIONS
INC $PASS ;;INCREMENT THE PASS NUMBER
BIC #100000,$PASS ;;DON'T ALLOW A NEG. NUMBER
DEC (PC)+ ;;LOOP?

```

\$EOPCT:

```

.WORD 1
BGT $DOAGN ;;YES
MOV (PC)+,2(PC)+ ;;RESTORE COUNTER

```

\$ENDCT:

```

.WORD 1
$EOPCT
TYPE ..+4 ;;TYPE ASCIZ STRING
BR 64$ ;;GET OVER THE ASCIZ
; .ASCIZ <12><15>/END PASS #/

```

64\$:

```

MOV $PASS,-(SP) ;;SAVE $PASS FOR TYPEOUT
;;TYPE PASS NUMBER
TYPDS ;;GO TYPE--DECIMAL ASCII WITH SIGN
TYPE ..+4 ;;TYPE ASCIZ STRING

```

(2)
2299
2300
2301
2302
2303
2304
2305
2306
2307
2308
2309
2310
2311
2312
2313
2314
2315
2316
2317
2318
2319
2320
2321
2322
2323
2324
2325
2326
2327
2328
2329
2330

```

015436  
015436 000004  
015440 005037 001102  
015444 005037 001210  
015450 005237 001100  
015454 042737 100000 001100  
015462 005327  
015464 000001  
015466 003063  
015470 012737  
015472 000001  
015474 015464  
015476 104400 015504  
015502 000407  
015522  
015522 013746 001100  
015526 104404  
015530 104400 015536

```



```

2331 015534 000421          BR      65$          ;;GET OVER THE ASCIZ
2332          ;;.ASCIZ / TOTAL ERRORS SINCE LAST REPORT /
2333 015600          65$:
2334 015600 013746 001112    MOV     $ERTTL,-(SP)   ;;SAVE $ERTTL FOR TYPEOUT
2335          ;;TOTAL NUMBER OF ERRORS
2336 015604 104404          TYPDS          ;;GO TYPE--DECIMAL ASCII WITH SIGN
2337 015606 104400 001221    TYPE     $CRLF        ;;TYPE CARRIAGE RETURN, LINE FEED
2338 015612 005037 001112    CLR     $ERTTL       ;;CLEAR ERROR TOTAL
2339 015616          $GET42:
2340
2341 015616 013700 000042    MOV     @#42,R0      ;;GET MONITOR ADDRESS
2342 015622 001405          BEQ     $DOAGN       ;;BRANCH IF NO MONITOR
2343 015624 000005          RESET          ;;CLEAR THE WORLD
2344 015626 004710          SENDAD: JSR    PC,(R0) ;;GO TO MONITOR
2345 015630 000240          NOP           ;;SAVE ROOM
2346 015632 000240          NOP           ;;FOR
2347 015634 000240          NOP           ;;ACT11
2348 015636          $DOAGN:
2349 015636 000137 003506    JMP     @#TST1       ;;RETURN
2350 015642          377          377          000 $ENULL: .BYTE  -1,-1,0  ;;NULL CHARACTER STRING
2351          015646          .EVEN
2352          ;*****
2353          .SBTTL  SCOPE HANDLER ROUTINE
2354
2355          ;*THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
2356          ;*AND LOAD THE TEST NUMBER($TSTNM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
2357          ;*AND LOAD THE ERROR FLAG ($ERFLG) INTO DISPLAY<15:08>
2358          ;*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
2359          ;*SW14=1      LOOP ON TEST
2360          ;*SW11=1      INHIBIT ITERATIONS
2361          ;*SW09=1      LOOP ON ERROR
2362          ;*SW08=1      LOOP ON TEST IN SWR<7:0>
2363          ;*CALL
2364          ;*          SCOPE          ;;SCOPE=IOT
2365
2366          $SCOPE:
2367 015646          1$: BIT     @BIT14,@SWR          ;;LOOP ON PRESENT TEST?
2368 015646 032777 040000 163262  BNE     $OVER        ;;YES IF SW14=1
2369 015654 001111          ;*****START OF CODE FOR THE XOR TESTER*****
2370          $XTSTR: BR     6$
2371 015656 000416          ;;IF RUNNING ON THE "XOR" TESTER CHANGE
2372          ;;THIS INSTRUCTION TO A "NOP" (NOP=240)
2373 015660 013746 000004          MOV     @#ERRVEC,-(SP) ;;SAVE THE CONTENTS OF THE ERROR VECTOR
2374 015664 012737 015704 000004    MOV     #5$,@#ERRVEC  ;;SET FOR TIMEOUT
2375 015672 005737 177060          TST     @#177060     ;;TIME OUT ON XOR?
2376 015676 012637 000004          MOV     (SP)+,@#ERRVEC ;;RESTORE THE ERROR VECTOR
2377 015702 000463          BR     $$VLAD        ;;GO TO THE NEXT TEST
2378 015704 022626          5$: CMP     (SP)+,(SP)+  ;;CLEAR THE STACK AFTER A TIME OUT
2379 015706 012637 000004          MOV     (SP)+,@#ERRVEC ;;RESTORE THE ERROR VECTOR
2380 015712 000423          BR     7$           ;;LOOP ON THE PRESENT TEST
2381 015714          6$: ;*****END OF CODE FOR THE XOR TESTER*****
2382 015714 032777 000400 163214    BIT     @BIT08,@SWR  ;;LOOP ON SPEC. TEST?
2383 015722 001404          BEQ     2$           ;;BR IF NO
2384 015724 127737 163206 001102    CMPB   @SWR,$TSTNM  ;;ON THE RIGHT TEST? SWR<7:0>
2385 015732 001462          BEQ     $OVER        ;;BR IF YES
2386 015734 105737 001103          2$: TSTB   $ERFLG      ;;HAS AN ERROR OCCURRED?

```

```

2387 015740 001421          BEQ      3$          ;; BR IF NO
2388 015742 123737 001115 001103  CMPB    $ERMAX,$ERFLG  ;; MAX. ERRORS FOR THIS TEST OCCURRED?
2389 015750 101015          BHI     3$          ;; BR IF NO
2390 015752 032777 001000 163156  BIT     #BIT09,$SWR    ;; LOOP ON ERROR?
2391 015760 001404          BEQ     4$          ;; BR IF NO
2392 015762 013737 001110 001106 7$:  MOV    $LPERR,$LPADR  ;; SET LOOP ADDRESS TO LAST SCOPE
2393 015770 000443          BR      $OVER
2394 015772 105037 001103          4$:  CLRB   $ERFLG        ;; ZERO THE ERROR FLAG
2395 015776 005037 001210          CLR    $TIMES        ;; CLEAR THE NUMBER OF ITERATIONS TO MAKE
2396 016002 000415          BR     1$          ;; ESCAPE TO THE NEXT TEST
2397 016004 032777 004000 163124 3$:  BIT     #BIT11,$SWR    ;; INHIBIT ITERATIONS?
2398 016012 001011          BNE    1$          ;; BR IF YES
2399 016014 005737 001100          TST   $PASS         ;; IF FIRST PASS OF PROGRAM
2400 016020 001406          BEQ    1$          ;; INHIBIT ITERATIONS
2401 016022 005237 001104          INC   $ICNT         ;; INCREMENT ITERATION COUNT
2402 016026 023737 001210 001104  CMP    $TIMES,$ICNT   ;; CHECK THE NUMBER OF ITERATIONS MADE
2403 016034 002021          BGE    $OVER        ;; BR IF MORE ITERATION REQUIRED
2404 016036 012737 000001 001104 1$:  MOV    #1,$ICNT      ;; REINITIALIZE THE ITERATION COUNTER
2405 016044 013737 016114 001210  MOV    $MXCNT,$TIMES  ;; SET NUMBER OF ITERATIONS TO DO
2406 016052 105237 001102          $SVLAD: INCB   $TSTNM     ;; COUNT TEST NUMBERS
2407 016056 011637 001106          MOV   (SP),$LPADR    ;; SAVE SCOPE LOOP ADDRESS
2408 016062 011637 001110          MOV   (SP),$LPERR    ;; SAVE ERROR LOOP ADDRESS
2409 016066 005037 001212          CLR   $ESCAPE       ;; CLEAR THE ESCAPE FROM ERROR ADDRESS
2410 016072 112737 000001 001115  MOVB   #1,$ERMAX     ;; ONLY ALLOW ONE(1) ERROR ON NEXT TEST
2411 016100 013777 001102 163032 $OVER: MOV   $TSTNM,$DISPLAY  ;; DISPLAY TEST NUMBER
2412 016106 013716 001106          MOV   $LPADR,(SP)   ;; FUDGE RETURN ADDRESS
2413 016112 000002          RTI
2414 016114 000040          $MXCNT: 40          ;; FIXES PS
2415          ;; MAX. NUMBER OF ITERATIONS
2416          *****
2417          .SBTTL 'ERROR HANDLER ROUTINE
2418          ;; *THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT,
2419          ;; *SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
2420          ;; *AND GO TO $ERRTYP ON ERROR
2421          ;; *THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
2422          ;; *SW15=1 HALT ON ERROR
2423          ;; *SW13=1 INHIBIT ERROR TYPEOUTS
2424          ;; *SW10=1 BELL ON ERROR
2425          ;; *SW09=1 LOOP ON ERROR
2426          ;; *CALL
2427          ;; * ERROR N ;;ERROR=EMT AND N=ERROR ITEM NUMBER
2428
2429
2430 $ERROR:
2431 016116 105237 001103 7$:  INCB   $ERFLG        ;; SET THE ERROR FLAG
2432 016122 001775          BEQ    7$          ;; DON'T LET THE FLAG GO TO ZERO
2433 016124 013777 001102 163006  MOV    $TSTNM,$DISPLAY  ;; DISPLAY TEST NUMBER AND ERROR FLAG
2434 016132 032777 002000 162776  BIT    #BIT10,$SWR     ;; BELL ON ERROR?
2435 016140 001402          BEQ    1$          ;; NO - SKIP
2436 016142 104400 001214          TYPE  $BELL         ;; RING BELL
2437 016146 005237 001112 1$:  INC   $ERTTL        ;; COUNT THE NUMBER OF ERRORS
2438 016152 011637 001116          MOV   (SP),$ERRPC    ;; GET ADDRESS OF ERROR INSTRUCTION
2439 016156 162737 000002 001116  SUB    #2,$ERRPC
2440 016164 117737 162726 001114  MOVB  $ERRPC,$ITEMB  ;; STRIP AND SAVE THE ERROR ITEM CODE
2441 016172 032777 020000 162736  BIT    #BIT13,$SWR    ;; SKIP TYPEOUT IF SET
2442 016200 001004          BNE   20$          ;; SKIP TYPEOUTS

```

```

2443 016202 004737 016264 JSR PC,$ERRTYP ;;GO TO USER ERROR ROUTINE
2444 016206 104400 001221 TYPE , $CRLF
2445 016212 20$:
2446 016212 005777 162720 2$: TST $SWR ;;HALT ON ERROR
2447 016216 100006 BPL 3$ ;;SKIP IF CONTINUE
2448 016220 000000 HALT ;;HALT ON ERROR!
2449 016222 022737 015626 000042 CMP #SENDAD,$#42 ;;ACT-11 AUTO-ACCEPT?
2450 016230 001001 BNE 3$ ;;BRANCH IF NO
2451 016232 000000 HALT ;;YES
2452 016234 032777 001000 162674 3$: BIT #BIT09,$SWR ;;LOOP ON ERROR SWITCH SET?
2453 016242 001402 BEQ 4$ ;;BR IF NO
2454 016244 013716 001110 MOV $LPERR,(SP) ;;FUDGE RETURN FOR LOOPING
2455 016250 005737 001212 4$: TST $ESCAPE ;;CHECK FOR AN ESCAPE ADDRESS
2456 016254 001402 BEQ 5$ ;;BR IF NONE
2457 016256 013716 001212 MOV $ESCAPE,(SP) ;;FUDGE RETURN ADDRESS FOR ESCAPE
2458 016262 5$:
2459 016262 000002 RTI ;;RETURN

```

.SBTTL ERROR MESSAGE TYPEOUT ROUTINE

;*THIS ROUTINE USES THE "ITEM CONTROL BYTE" (\$ITEMB) TO DETERMINE WHICH
;*ERROR IS TO BE REPORTED. IT THEN OBTAINS, FROM THE "ERROR TABLE" (\$ERRTB),
;*AND REPORTS THE APPROPRIATE INFORMATION CONCERNING THE ERROR.

```

2468 016264 $ERRTYP:
2469 016264 104400 001221 TYPE , $CRLF ;; "CARRIAGE RETURN" & "LINE FEED"
2470 016270 010046 MOV $J,-(SP) ;;SAVE RO
2471 016272 005000 CLR RO ;;PICKUP THE ITEM INDEX
2472 016274 153700 001114 BISB $#ITEMB,RO
2473 016300 001004 BNE 1$ ;;IF ITEM NUMBER IS ZERO, JUST
2474 016302 013746 001116 MOV $ERRPC,-(SP) ;;TYPE THE PC OF THE ERROR
2475 016306 104401 TYPOC ;;SAVE $ERRPC FOR TYPEOUT
2476 016310 000426 BR 6$ ;;ERROR ADDRESS
2477 016312 005300 1$: DEC RO ;;GO TYPE--OCTAL ASCII(ALL DIGITS)
2478 016314 006300 ASL RO ;;GET OUT
2479 016316 006300 ASL RO ;;ADJUST THE INDEX SO THAT IT WILL
2480 016320 006300 ASL RO ;; WORK FOR THE ERROR TABLE
2481 016322 062700 001224 ADD #ERRTB,RO ;;FORM TABLE POINTER
2482 016326 012037 016336 MOV (RO)+,2$ ;;PICKUP "ERROR MESSAGE" POINTER
2483 016332 001404 BEQ 3$ ;;SKIP TYPEOUT IF NO POINTER
2484 016334 104400 TYPE ;;TYPE THE "ERROR MESSAGE"
2485 016336 000000 2$: .WORD 0 ;;"ERROR MESSAGE" POINTER GOES HERE
2486 016340 104400 001221 TYPE , $CRLF ;; "CARRIAGE RETURN" & "LINE FEED"
2487 016344 012037 016354 3$: MOV (RO)+,4$ ;;PICKUP "DATA HEADER" POINTER
2488 016350 001404 BEQ 5$ ;;SKIP TYPEOUT IF 0
2489 016352 104400 TYPE ;;TYPE THE "DATA HEADER"
2490 016354 000000 4$: .WORD 0 ;;"DATA HEADER" POINTER GOES HERE
2491 016356 104400 001221 TYPE , $CRLF ;; "CARRIAGE RETURN" & "LINE FEED"
2492 016362 011000 5$: MOV (RO),RO ;;PICKUP "DATA TABLE" POINTER
2493 016364 001004 BNE 7$ ;;GO TYPE THE DATA
2494 016366 012600 6$: MOV (SP)+,RO ;;RESTORE RO
2495 016370 104400 001221 TYPE , $CRLF ;; "CARRIAGE RETURN" & "LINE FEED"
2496 016374 000207 RTS PC ;;RETURN

```

```

2499 016376          7$:
2500 016376 013046      MOV      2(RO)+,-(SP)    ;;SAVE 2(RO)+ FOR TYPEOUT
2501 016400 104401      TYPOC          ;;GO TYPE--OCTAL ASCII(ALL DIGITS)
2502 016402 005710      TST      (RO)        ;;IS THERE ANOTHER NUMBER?
2503 016404 001770      BEQ      6$          ;;BR IF NO
2504 016406 104400 016414 TYPE      9$          ;;TYPE TWO(2) SPACES
2505 016412 000771      SR      7$          ;;LOOP
2506 016414 020040 000    8$:      .ASCIZ  / /        ;;TWO(2) SPACES
2507          016420      .EVEN
2508
2509 ;*****
2510 .SBTTL  TTY INPUT ROUTINE
2511
2512 ;*THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY
2513 ;*CALL:
2514 ;*      RDCHR          ;;INPUT A SINGLE CHARACTER FROM THE TTY
2515 ;*      RETURN HERE    ;;CHARACTER IS ON THE STACK
2516
2517
2518 016420 011646          $RDCHR: MOV      (SP),-(SP)    ;;PUSH DOWN THE PC
2519 016422 016666 000004 000002 MOV      4(SP),2(SP)    ;;SAVE THE PS
2520 016430 105777 162506 1$:      TSTB     2$TKS        ;;WAIT FOR
2521 016434 100375          BPL      1$          ;;A CHARACTER
2522 016436 117766 162502 000004 MOVB     2$TKB,4(SP)    ;;READ THE TTY
2523 016444 042766 177600 000004 BIC      #1C<177>,4(SP) ;;GET RID OF JUNK IF ANY
2524 016452 000002          RTI          ;;GO BACK TO USER
2525
2526 ;*****
2527 ;*THIS ROUTINE WILL INPUT A STRING FROM THE TTY
2528 ;*CALL:
2529 ;*      RDLIN          ;;INPUT A STRING FROM THE TTY
2530 ;*      RETURN HERE    ;;ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK
2531 ;*      TERMINATOR WILL BE A BYTE OF ALL 0'S
2532
2533 016454 010346          $RDLIN: MOV      R3, -(SP)    ;;SAVE R3
2534 016456 012703 016562 1$:      MOV      #$TTYIN,R3    ;;GET ADDRESS
2535 016462 022703 016600 2$:      CMP      #$TTYIN+16,R3 ;;BUFFER FULL?
2536 016466 101405          BLOS     4$          ;;BR IF YES
2537 016470 104405          RDCHR          ;;GO READ ONE CHARACTER FROM THE TTY
2538 016472 112613          MOVB     (SP)+,(R3)    ;;GET CHARACTER
2539 016474 122713 000177 3$:      CMPB     #177,(R3)    ;;IS IT A RUBOUT
2540 016500 001003          BNE      3$          ;;SKIP IF NOT
2541 016502 104400 001220 4$:      TYPE     $QUES        ;;TYPE A '?'
2542 016506 000763          BR      1$          ;;CLEAR THE BUFFER AND LOOP
2543 016510 111337 016560 3$:      MOVB     (R3),9$      ;;ECHO THE CHARACTER
2544 016514 104400 016560          TYPE     9$
2545 016520 122723 000015          CMPB     #15,(R3)+    ;;CHECK FOR RETURN
2546 016524 001356          BNE      2$          ;;LOOP IF NOT RETURN
2547 016526 105063 177777          CLRB     -1(R3)      ;;CLEAR RETURN (THE 15)
2548 016532 104400 001222          TYPE     $LF         ;;TYPE A LINE FEED
2549 016536 012603          MOV      (SP)+,R3    ;;RESTORE R3
2550 016540 011646          MOV      (SP),-(SP)   ;;ADJUST THE STACK AND PUT ADDRESS OF THE
2551 016542 016666 000004 000002 MOV      4(SP),2(SP)   ;;FIRST ASCII CHARACTER ON IT
2552 016550 012766 016562 000004 MOV      #$TTYIN,4(SP)
2553 016556 000002          RTI          ;;RETURN
2554 016560 000          9$:      .BYTE    0          ;;STORAGE FOR ASCII CHAR. TO TYPE
2555 016561 000          .BYTE    0          ;;TERMINATOR

```

```

2555 016562 000016 $TTYIN: .BLKB 16 ;;RESERVE 16 BYTES FOR TTY INPUT
2556 ;*****
2557
2559 .SBTTL ROUTINE TO SIZE MEMORY
2559
2560 ;*CALL:
2561 ;* JSR PC,$SIZE
2562 ;* RETURN
2563 ;*$LSTAD WILL CONTAIN:
2564 ;* WITH KT11 OPTION -- LAST VIRTUAL ADDRESS OF THE LAST BANK
2565 ;* WITHOUT KT11 OPTION -- LAST ABSOLJTE ADDRESS OF AVAILABLE MEMORY
2566 ;*$LSTBK WILL CONTAIN THE LAST BANK AS A SAF
2567 ;*$KT11 IS THE MEMORY MANAGEMENT KEY
2568 ;*$BIT07 = 0 DON'T USE MEMORY MANAGEMENT
2569 ;* MUST BE SETUP BEFORE THE CALL
2570 ;*$BIT15 = 0 DON'T HAVE MEMORY MANAGEMENT OPTION
2571 ;*
2572 ;* DETERMINED BY ROUTINE
2573
2573 016600 010046 $SIZE: MOV R0,-(SP) ;;SAVE R0 ON THE STACK
2574 016602 010146 MOV R1,-(SP) ;;SAVE R1 ON THE STACK
2575 016604 010246 MOV R2,-(SP) ;;SAVE R2 ON THE STACK
2576 016606 010346 MOV R3,-(SP) ;;SAVE R3 ON THE STACK
2577 016610 013746 000004 MOV @#ERRVEC,-(SP) ;;SAVE PRESENT ERROR VECTOR PS & PC
2578 016614 013746 000006 MOV @#ERRVEC+2,-(SP)
2579 016620 010600 MOV SP,R0 ;;SAVE THE STACK POINTER
2580 016622 013746 000034 MOV @#34,-(SP) ;;SETUP THE TRAP VECTOR
2581 016626 012737 016636 000034 MOV #1$.@#34 ;; TO GET THE PS
2582 016634 104400 TRAP
2583 016636 016637 000002 000006 1$: MOV 2(SP),@#ERRVEC+2 ;;SET ERRVEC PS
2584 016644 012716 016652 MOV #$$SIZE1,(SP) ;; TO PRESENT PS
2585 016650 000002 RTI
2586 016652 012637 000034 $SIZE1: MOV (SP)+,@#34 ;;RESTORE TRAP VECTOR
2587 016656 012701 003776 MOV #3776,R1 ;;SETUP ADDRESS
2588 016662 105727 TSTB (PC)+ ;;USE MEMORY MANAGEMENT?
2589 016664 000200 $KT11: .WORD 200 ;;SET TO USE MEMORY MANAGEMENT
2590 016666 100055 BPL $SCORE ;;BR IF NO
2591 016670 012737 017014 000004 MOV #$$KTNEX,@#ERRVEC ;;SET FOR TIMEOUT
2592 016676 005737 177572 TST @#SRO ;;KT11 ARE YOU THERE?
2593 016702 052737 100000 016664 BIS #100000,$KT11 ;;YES--SET KT11 KEY
2594 016710 005046 CLR -(SP) ;;INITIALIZE FOR "PAR" LOADING
2595 016712 012702 172340 MOV #KIPAR0,R2 ;;ADDRESS OF FIRST "PAR"
2596 016716 012703 000010 MOV #108,R3 ;;LOAD EIGHT "PAR.'S" AND EIGHT "PDR.'S"
2597 016722 012762 077406 177740 1$: MOV #77406,-40(R2) ;;PDR = 4K, UP, READ/WRITE
2598 016730 011622 MOV (SP),(R2)+ ;;LOAD "PAR"
2599 016732 062716 000200 ADD #200,(SP) ;;UPDATE FOR NEXT "PAR"
2600 016736 077307 SOB R3,1$ ;;LOOP UNTIL ALL EIGHT ARE LOADED
2601 016740 012742 177600 MOV #177600,-(R2) ;;SETUP KIPAR7 FOR I/O
2602 016744 005042 CLR -(R2) ;;SETUP KIPAR6 FOR TESTING
2603 016746 012737 000020 172516 MOV #20,@#SR3 ;;ENABLE 22 BIT MODE
2604 016754 005237 177572 INC @#SRO ;;TURN ON MEMORY MANAGEMENT
2605 016760 012737 017004 000004 MOV #$$KTOUT,@#ERRVEC ;;SET FOR TIME OUT
2606 016766 005737 143776 2$: TST @#143776 ;;TRAP ON NON-EX-MEM
2607 016772 062712 000040 ADD #40,(R2) ;;MAKE A 1K STEP
2608 016776 023712 172356 CMP @#KIPAR7,(R2) ;;LAST ONE?
2609 017002 101371 BHI 2$ ;;NO--TRY IT
2610 017004 011202 $KTOUT: MOV (R2),R2 ;;GET LAST BANK+1
    
```

```

017006 005037 177572
017012 000421
017014 042737 100000 016564
017022 012737 017052 000004
017030 005002
017032 062701 004000
017036 062702 000040
017042 005711
017044 022701 177776
017050 001370
017052 162701 004000
017056 162702 000040
017062 010006
017064 012637 000006
017070 012637 000004
017074 010137 017116
017100 010237 017120
017104 012603
017106 012602
017110 012601
017112 012600
017114 000207
017116 000000
017120 000000

```

```

CLR JMSRO ;;TURN OFF MEMORY MANAGEMENT
BR $SIZE
BIC #100000,$K11 ;;K11 NON-EXISTENT
MOV #SCROLL,$ERRVEC ;;SET FOR TIMEOUT
CLR R2 ;;SET UP BANK
ADD #4000,R1 ;;INCREMENT BY 1K
ADD #40,R2 ;;1K STEP
TS (R1) ;;TRAP ON TIME OUT
CMP #177776,R1 ;;LAST ONE
BNE $S ;;NO--TRY AGAIN
$SCROLL: SUB #4000,R1
$SIZE: SUB #40,R2 ;;DROP BACK
MOV R0,SP ;;RESTORE THE STACK
MOV (SP)+,$ERRVEC+2 ;;RESTORE ERROR VECTOR
MOV (SP)+,$ERRVEC
MOV R1,$LSTAD ;;LAST ADDRESS
MOV R2,$LSTBK ;;LAST BANK
MOV (SP)+,R3 ;;RESTORE R3
MOV (SP)+,R2 ;;RESTORE R2
MOV (SP)+,R1 ;;RESTORE R1
MOV (SP)+,R0 ;;RESTORE R0
RTS PC
$LSTAD: .WORD 0 ;;CONTAINS THE LAST ADDRESS
$LSTBK: .WORD 0 ;;CONTAINS THE LAST BANK
;*****

```

.SBTTL SAVE AND RESTORE R0-R5 ROUTINES

```

;*SAVE R0-R5
;*CALL:
;* SAVREG
;*UPON RETURN FROM $SAVREG THE STACK WILL LOOK LIKE:
;*
;*TOP---(+16)
;* +2---(+18)
;* +4---R5
;* +6---R4
;* +8---R3
;*+10---R2
;*+12---R1
;*+14---R0

```

```

017122 010046
017124 010146
017126 010246
017130 010346
017132 010446
017134 010546
017136 016646 000022
017142 016646 000022
017146 016646 000022
017152 016646 000022
017156 000002

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```

$SAVREG:
MOV R0,-(SP) ;;PUSH R0 ON STACK
MOV R1,-(SP) ;;PUSH R1 ON STACK
MOV R2,-(SP) ;;PUSH R2 ON STACK
MOV R3,-(SP) ;;PUSH R3 ON STACK
MOV R4,-(SP) ;;PUSH R4 ON STACK
MOV R5,-(SP) ;;PUSH R5 ON STACK
MOV 22(SP),-(SP) ;;SAVE PS OF MAIN FLOW
MOV 22(SP),-(SP) ;;SAVE PC OF MAIN FLOW
MOV 22(SP),-(SP) ;;SAVE PS OF CALL
MOV 22(SP),-(SP) ;;SAVE PC OF CALL
RTI

```

;*RESTORE R0-R5

```

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2669 017160
2670 017163 012666 000022
2671 017164 012666 000022
2672 017170 012666 000022
2673 017174 012666 000022
2674 017200 012605
2675 017202 012604
2676 017204 012603
2677 017206 012602
2678 017210 012601
2679 017212 012600
2680 017214 000002
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2699 017216 105737 001155
2700 017222 100002
2701 017224 000000
2702 017226 000407
2703 017230 010046
2704 017232 017600 000002
2705 017236 112046
2706 017240 001005
2707 017242 005726
2708 017244 012600
2709 017246 062716 000002
2710 017252 000002
2711 017254 122716 000011
2712 017260 001426
2713 017262 122716 000200
2714 017266 001004
2715 017270 005726
2716 017272 104400
2717 017274 001221
2718 017276 000757
2719 017300 004737 017362
2720 017304 123726 001154
2721 017310 001352
2722 017312 013746 001152

```

```

:*CALL:
:* RESREG
$RESREG:
MOV (SP)+,R2(SP) ;;RESTORE PC OF CALL
MOV (SP)+,R2(SP) ;;RESTORE PS OF CALL
MOV (SP)+,R2(SP) ;;RESTORE PC OF MAIN FLOW
MOV (SP)+,R2(SP) ;;RESTORE PS OF MAIN FLOW
MOV (SP)+,R5 ;;POP STACK INTO R5
MOV (SP)+,R4 ;;POP STACK INTO R4
MOV (SP)+,R3 ;;POP STACK INTO R3
MOV (SP)+,R2 ;;POP STACK INTO R2
MOV (SP)+,R1 ;;POP STACK INTO R1
MOV (SP)+,R0 ;;POP STACK INTO R0
RTI

```

.SBTTL TYPE ROUTINE

```

:*ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
:*THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
:*NOTE1: $NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
:*NOTE2: $FILLC CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
:*NOTE3: $FILLC CONTAINS THE CHARACTER TO FILL AFTER.

```

```

:*CALL:
:*1) USING A TRAP INSTRUCTION
:* TYPE ,MESADR ;;MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
:*OR
:* TYPE
:* MESADR

```

```

$TYPE: TSTB $TPFLG ;; IS THERE A TERMINAL?
BPL IS ;;BR IF YES
HALT ;;HALT HERE IF NO TERMINAL
BR 3$ ;;LEAVE
1$: MOV RO,-(SP) ;;SAVE RO
MOV 2$(SP),RO ;;GET ADDRESS OF ASCIZ STRING
2$: MOVB (RO)+,-(SP) ;;PUSH CHARACTER TO BE TYPED ONTO STACK
BNE 4$ ;;BR IF IT ISN'T THE TERMINATOR
TST (SP)+ ;;IF TERMINATOR POP IT OFF THE STACK
60$: MOV (SP)+,RO ;;RESTORE RO
3$: ADD #2,(SP) ;;ADJUST RETURN PC
RTI ;;RETURN
4$: CMPB #HT,(SP) ;;BRANCH IF <HT>
BEQ 8$
CMPB #TCRLF,(SP) ;;BRANCH IF NOT <CRLF>
BNE 5$
TST (SP)+ ;;POP <CR><LF> EQUIV
TYPE ;;TYPE A CR AND LF
$CRLF
BR 2$ ;;GET NEXT CHARACTER
5$: JSR PC,$TYPEC ;;GO TYPE THIS CHARACTER
6$: CMPB $FILLC,(SP)+ ;;IS IT TIME FOR FILLER CHARS.?
BNE 2$ ;;IF NO GO GET NEXT CHAR.
MOV $NULL,-(SP) ;;GET # OF FILLER CHARS. NEEDED

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017316 105366 000001
017322 002770
017324 004737 017362
017330 105337 017426
017334 000770

017336 112716 000040
017342 004737 017362
017346 132737 000007 017426
017354 001372
017356 005726
017360 000726
017362 105777 161560
017366 100375
017370 116677 000002 161552
017376 122766 000015 000002
017404 001003
017406 105037 017426
017412 000406
017414 122766 000012 000002
017422 002002
017424 105227
017426 000000
017430 000207

000011
000200

```
7$:   DECB    1(SP)           ;; AND THE NULL CHAR.
      BLT     6$              ;; DOES A NULL NEED TO BE TYPED?
      JSR     PC,$TYPEC       ;; BR IF NO--GO POP THE NULL OFF OF STACK
      DECB   $CHARCNT         ;; GO TYPE A NULL
      BR     7$              ;; DO NOT COUNT AS A COUNT
      ;; LOOP

;HORIZONTAL TAB PROCESSOR

8$:   MOVB    #40,(SP)        ;; REPLACE TAB WITH SPACE
9$:   JSR     PC,$TYPEC       ;; TYPE A SPACE
      BITB    #7,$CHARCNT     ;; BRANCH IF NOT AT
      BNE     9$              ;; TAB STOP
      TST     (SP)+           ;; POP SPACE OFF STACK
      BR     2$              ;; GET NEXT CHARACTER
      $TYPEC: TSTB   2$TPS     ;; WAIT UNTIL PRINTER IS READY
      BPL     $TYPEC
      MOVB    2(SP),2$TPB     ;; LOAD CHAR TO BE TYPED INTO DATA REG.
      CMPB    #15,2(SP)       ;; BRANCH IF
      BNE     1$              ;; NOT <CR>
      CLRB   $CHARCNT
      BR     $TYPEC
      $CHARCNT: .WORD 0
      $TYPEX: RTS    PC
      ;; EQUATES
      THT=11
      TCRLF=200
```

```
*****
.SBTTL  BINARY TO OCTAL (ASCII) AND TYPE
; *THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT
; *OCTAL (ASCII) NUMBER AND TYPE IT.
; *$TYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE
; *CALL:
; *   MOV     NUM,-(SP)        ;; NUMBER TO BE TYPED
; *   TYPOS   N                ;; CALL FOR TYPEOUT
; *   .BYTE  N                ;; N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE
; *   .BYTE  M                ;; M=1 OR 0
; *                               ;; 1=TYPE LEADING ZEROS
; *                               ;; 0=SUPPRESS LEADING ZEROS
; *
; *$TYPON----ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST
; *$TYPOS OR $TYPOC
; *CALL:
; *   MOV     NUM,-(SP)        ;; NUMBER TO BE TYPED
; *   TYPON   N                ;; CALL FOR TYPEOUT
; *
; *$TYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER
; *CALL:
; *   MOV     NUM,-(SP)        ;; NUMBER TO BE TYPED
; *   TYPOC   N                ;; CALL FOR TYPEOUT
```



```

2779
2780 017432 017646 000000 $TYPOS: MOV 2(SP),-(SP) ;; PICKUP THE MODE
2781 017436 116637 000001 C17655 MOVB 1(SP),$OFILL ;; LOAD ZERO FILL SWITCH
2782 017444 112637 017657 MOVB (SP)+,$SOMODE+1 ;; NUMBER OF DIGITS TO TYPE
2783 017450 062716 000002 ADD #2,(SP) ;; ADJUST RETURN ADDRESS
2784 017454 000406 BR $TYPON
2785 017456 112737 000001 017655 $TYPOC: MOVB #1,$OFILL ;; SET THE ZERO FILL SWITCH
2786 017464 112737 000006 017657 MOVB #6,$SOMODE+1 ;; SET FOR SIX(6) DIGITS
2787 017472 112737 000005 017654 $TYPON: MOVB #5,$OCNT ;; SET THE ITERATION COUNT
2788 017500 010346 MOV R3,-(SP) ;; SAVE R3
2789 017502 010446 MOV R4,-(SP) ;; SAVE R4
2790 017504 010546 MOV R5,-(SP) ;; SAVE R5
2791 017506 113704 017657 MOVB $SOMODE+1,R4 ;; GET THE NUMBER OF DIGITS TO TYPE
2792 017512 005404 NEG R4
2793 017514 062704 000006 ADD #6,R4 ;; SUBTRACT IT FOR MAX. ALLOWED
2794 017520 110437 017656 MOVB R4,$SOMODE ;; SAVE IT FOR USE
2795 017524 113704 017655 MOVB $OFILL,R4 ;; GET THE ZERO FILL SWITCH
2796 017530 016605 000012 MOV 12(SP),R5 ;; PICKUP THE INPUT NUMBER
2797 017534 005003 CLR R3 ;; CLEAR THE OUTPUT WORD
2798 017536 006105 1$: ROL R5 ;; ROTATE MSB INTO "C"
2799 017540 000404 BR 3$ ;; GO DO MSB
2800 017542 006105 2$: ROL R5 ;; FORM THIS DIGIT
2801 017544 006105 ROL R5
2802 017546 006105 ROL R5
2803 017550 010503 MOV R5,R3
2804 017552 006103 3$: ROL R3 ;; GET LSB OF THIS DIGIT
2805 017554 105337 C17656 DECB $SOMODE ;; TYPE THIS DIGIT?
2806 017560 100016 BPL 7$ ;; BR IF NO
2807 017562 042703 177770 BIC #177770,R3 ;; GET RID OF JUNK
2808 017566 001002 BNE 4$ ;; TEST FOR 0
2809 017570 005704 TST R4 ;; SUPPRESS THIS 0?
2810 017572 001403 BEQ 5$ ;; BR IF YES
2811 017574 005204 4$: INC R4 ;; DON'T SUPPRESS ANYMORE 0'S
2812 017576 052703 000060 BIS #'0,R3 ;; MAKE THIS DIGIT ASCII
2813 017602 052703 000040 5$: BIS #'R3 ;; MAKE ASCII IF NOT ALREADY
2814 017606 110337 017652 MOVB R3,$S ;; SAVE FOR TYPING
2815 017612 104400 017652 TYPE $S ;; GO TYPE THIS DIGIT
2816 017616 105337 017654 7$: DECB $OCNT ;; COUNT BY 1
2817 017622 003347 BGT 2$ ;; BR IF MORE TO DO
2818 017624 002402 BLT 6$ ;; BR IF DONE
2819 017626 005204 INC R4 ;; INSURE LAST DIGIT ISN'T A BLANK
2820 017630 000744 BR 2$ ;; GO DO THE LAST DIGIT
2821 017632 012605 6$: MOV (SP)+,R5 ;; RESTORE R5
2822 017634 012604 MOV (SP)+,R4 ;; RESTORE R4
2823 017636 012603 MOV (SP)+,R3 ;; RESTORE R3
2824 017640 016666 000002 000004 MOV 2(SP),4(SP) ;; SET THE STACK FOR RETURNING
2825 017646 012616 MOV (SP)+,(SP)
2826 017650 000002 RTI ;; RETURN
2827 017652 000 8$: .BYTE 0 ;; STORAGE FOR ASCII DIGIT
2828 017653 000 .BYTE 0 ;; TERMINATOR FOR TYPE ROUTINE
2829 017654 000 $OCNT: .BYTE 0 ;; OCTAL DIGIT COUNTER
2830 017655 000 $OFILL: .BYTE 0 ;; ZERO FILL SWITCH
2831 017656 000000 $SOMODE: .WORD 0 ;; NUMBER OF DIGITS TO TYPE
;*****
.SBTTL CONVERT BINARY TO DECIMAL AND TYPE ROUTINE
    
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017660
017660 010046
017662 010146
017664 010246
017666 010346
017670 010546
017672 012746 020200
017676 016605 000020
017702 100004
017704 005405
017706 112766 000055 000001
017714 005000
017716 012703 020074
017722 112723 000040
017726 005002
017730 016001 020064
017734 160105
017736 002402
017740 005202
017742 000774
017744 060105
017746 005702
017750 001002
017752 105716
017754 100407
017756 106316
017760 103003
017762 116663 000001 177777
017770 052702 000060
017774 052702 000040
020000 110223
020002 005720
020004 020027 000010
020010 002746
020012 003002
020014 010502
020016 000764
020020 105726
020022 100003
020024 115663 177777 177776
020032 105013
020034 012605
020036 012603
020040 012602
020042 012601
020044 012600

```

; *THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 5-DIGIT
; *SIGNED DECIMAL (ASCII) NUMBER AND TYPE IT. DEPENDING ON WHETHER THE
; *NUMBER IS POSITIVE OR NEGATIVE A SPACE OR A MINUS SIGN WILL BE TYPED
; *BEFORE THE FIRST DIGIT OF THE NUMBER. LEADING ZEROS WILL ALWAYS BE
; *REPLACED WITH SPACES.
; *CALL:
; *      MOV      NUM, -(SP)      ;; PUT THE BINARY NUMBER ON THE STACK
; *      TYPDS                    ;; GO TO THE ROUTINE

$TYPDS:
MOV      R0, -(SP)      ;; PUSH R0 ON STACK
MOV      R1, -(SP)      ;; PUSH R1 ON STACK
MOV      R2, -(SP)      ;; PUSH R2 ON STACK
MOV      R3, -(SP)      ;; PUSH R3 ON STACK
MOV      R5, -(SP)      ;; PUSH R5 ON STACK
MOV      #20200, -(SP)    ;; SET BLANK SWITCH AND SIGN
MOV      20(SP), R5      ;; GET THE INPUT NUMBER
BPL      1$             ;; BR IF INPUT IS POS.
NEG      R5             ;; MAKE THE BINARY NUMBER POS.
MOVB    #'-, 1(SP)     ;; MAKE THE ASCII NUMBER NEG.
CLR      R0             ;; ZERO THE CONSTANTS INDEX
MOV      #5DBLK, R3     ;; SETUP THE OUTPUT POINTER
MOVB    #' , (R3)+     ;; SET THE FIRST CHARACTER TO A BLANK
CLR      R2             ;; CLEAR THE BCD NUMBER
MOV      $DTBL(R0), R1  ;; GET THE CONSTANT
SUB      R1, R5         ;; FORM THIS BCD DIGIT
BLT     4$             ;; BR IF DONE
INC      R2             ;; INCREASE THE BCD DIGIT BY 1
BR      3$

4$:
ADD      R1, R5        ;; ADD BACK THE CONSTANT
TST     R2             ;; CHECK IF BCD DIGIT=0
BNE     5$            ;; FALL THROUGH IF 0
TSTB   (SP)           ;; STILL DOING LEADING 0'S?
BMI     7$            ;; BR IF YES
ASLB   (SP)           ;; MSD?
BCC     6$            ;; BR IF NO
MOVB   1(SP), -1(R3)  ;; YES--SET THE SIGN
BIS    #'0, R2        ;; MAKE THE BCD DIGIT ASCII
BIS    #' , R2        ;; MAKE IT A SPACE IF NOT ALREADY A DIGIT
MOVB   R2, (R3)+     ;; PUT THIS CHARACTER IN THE OUTPUT BUFFER
TST    (R0)+         ;; JUST INCREMENTING
CMP    R0, #10       ;; CHECK THE TABLE INDEX
BLT    2$            ;; GO DO THE NEXT DIGIT
BGT    8$            ;; GO TO EXIT
MOV    R5, R2        ;; GET THE LSD
BR     6$            ;; GO CHANGE TO ASCII
TSTB  (SP)+         ;; WAS THE LSD THE FIRST NON-ZERO?
BFL   9$            ;; BR IF NO
MOVB  -1(SP), -2(R3) ;; YES--SET THE SIGN FOR TYPING
CLRB  (R3)           ;; SET THE TERMINATOR
MOV   (SP)+, R5     ;; POP STACK INTO R5
MOV   (SP)+, R3     ;; POP STACK INTO R3
MOV   (SP)+, R2     ;; POP STACK INTO R2
MOV   (SP)+, R1     ;; POP STACK INTO R1
MOV   (SP)+, R0     ;; POP STACK INTO R0
    
```

2891 020046 104400 020074
 2892 020052 016666 000002 000004
 2893 020060 012616
 2894 020062 000002
 2895 020064 023420
 2896 020066 001750
 2897 020070 000144
 2898 020072 000012
 2899 020074 000004
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 2909 020104 010046
 2910 020106 016600 000002
 2911 020112 005740
 2912 020114 111000
 2913 020116 006300
 2914 020120 016000 020126
 2915 020124 000200
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 2926 020126 017216
 2927 020130 017456
 2928 020132 017432
 2929 020134 017472
 2930 020136 017660
 2931 020140 016420
 2932 020142 016454
 2933 020144 017122
 2934 020146 017160
 2935
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 2939
 2940 020150 012737 020272 000024
 2941 020156 012737 000340 000026
 2942 020164 010046
 2943 020166 010146
 2944 020170 010246
 2945 020172 010346
 2946 020174 010446

```

TYPE $DBLK ;; NOW TYPE THE NUMBER
MOV 2(SP),4(SP) ;; ADJUST THE STACK
MOV (SP)+,(SP)
RTI ;; RETURN TO USER

$DTBL: 10000.
      1000.
      100.
      10.

$DBLK: .BLKW 4
:*****

.SBTTL TRAP DECODER

; *THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
; *AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
; *OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL
; *GO TO THAT ROUTINE.

$TRAP: MOV RO,-(SP) ;; SAVE RO
      MOV 2(SP),RO ;; GET TRAP ADDRESS
      TST -(RO) ;; BACKUP BY 2
      MOVB (RO),RO ;; GET RIGHT BYTE OF TRAP
      ASL RO ;; POSITION FOR INDEXING
      MOV $TRPAD(RO),RO ;; INDEX TO TABLE
      RTS RO ;; GO TO ROUTINE

.SBTTL TRAP TABLE

; *THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
; *BY THE "TRAP" INSTRUCTION.

: ROUTINE
: -----
$TRPAD:
$TYPE ;; CALL=TYPE TRAP+0(104400) TTY TYPEOUT ROUTINE
$TYPOC ;; CALL=TYPOC TRAP+1(104401) TYPE OCTAL NUMBER (WITH LEADING ZERCS)
$TYPOS ;; CALL=TYPOS TRAP+2(104402) TYPE OCTAL NUMBER (NO LEADING ZERCS)
$TYPON ;; CALL=TYPON TRAP+3(104403) TYPE OCTAL NUMBER (AS PER LAST CALL)
$TYPDS ;; CALL=TYPCS TRAP+4(104404) TYPE DECIMAL NUMBER (WITH SIGN)
$RDCHR ;; CALL=RDCHR TRAP+5(104405) TTY TYPEIN CHARACTER ROUTINE
$RDLIN ;; CALL=RDLIN TRAP+6(104406) TTY TYPEIN STRING ROUTINE
$SAVREG ;; CALL=SAVREG TRAP+7(104407) SAVE RO-R5 ROUTINE
$RESREG ;; CALL=RESREG TRAP+10(104410) RESTORE RO-R5 ROUTINE
:*****

.SBTTL POWER DOWN AND UP ROUTINES

.POWER DOWN ROUTINE
$PWRDN: MOV # $ILLUP,2#PWRVEC ;; SET FOR FAST UP
      MOV #340,2#PWRVEC+2 ;; PRIO:7
      MOV RO,-(SP) ;; PUSH RO ON STACK
      MOV R1,-(SP) ;; PUSH R1 ON STACK
      MOV R2,-(SP) ;; PUSH R2 ON STACK
      MOV R3,-(SP) ;; PUSH R3 ON STACK
      MOV R4,-(SP) ;; PUSH R4 ON STACK

```

```

2947 020176 010546          MOV      R5,-(SP)          ;; PUSH R5 ON STACK
2948 020200 010637 020276    MOV      SP,$SAVR6        ;; SAVE SP
2949 020204 012737 020216 000024  MOV      #SPWRUP,#PWRVEC ;; SET UP VECTOR
2950 020212 000000          HALT
2951 020214 000776          BR       .-2              ;; HANG UP
2952
2953
2954 020216 013706 020276    :POWER UP ROUTINE
$PWRUP: MOV      $SAVR6,SP    ;; GET SP
2955 020222 005037 020276    CLR      $SAVR6          ;; WAIT LOOP FOR THE TTY
2956 020226 005237 020276    IS:     INC      $SAVR6    ;; WAIT FOR THE INC
2957 020232 001375          BNE     IS              ;; OF WORD
2958 020234 012605          MOV     (SP)+,R5        ;; POP STACK INTO R5
2959 020236 012604          MOV     (SP)+,R4        ;; POP STACK INTO R4
2960 020240 012603          MOV     (SP)+,R3        ;; POP STACK INTO R3
2961 020242 012602          MOV     (SP)+,R2        ;; POP STACK INTO R2
2962 020244 012601          MOV     (SP)+,R1        ;; POP STACK INTO R1
2963 020246 012600          MOV     (SP)+,R0        ;; POP STACK INTO R0
2964 020250 012737 020150 000024  MOV      #SPWRDN,#PWRVEC ;; SET UP THE POWER DOWN VECTOR
2965 020256 012737 000340 000026  MOV      #340,#PWRVEC+2 ;; PRIO:7
2966 020264 104400          TYPE
2967 020266 020300          $PWRMG: .WORD $POWER    ;; REPORT THE POWER FAILURE
2968 020270 000002          RTI                    ;; POWER FAIL MESSAGE POINTER
2969 020272 000000          $ILLUP: HALT
2970 020274 000776          BR       .-2              ;; THE POWER UP SEQUENCE WAS STARTED
2971 020276 000000          $SAVR6: 0              ;; BEFORE THE POWER DOWN WAS COMPLETE
2972 020300 005015 047520 042527  $POWER: .ASCIZ <15><12>"POWER" ;; PUT THE SP HERE
2973 020306 000122
2974
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2976
2977
2978 020310 000001          .EVEN
2979
;*****
;*****
;*****
ATEND:
.END

```


SSUES	001220	802#	2460	2540	2555	2754								
SROCHR	016420	2518#	2931											
SRODEC=	***** U	2933												
SROLYN	016454	2532#	2932											
SROCT=	***** U	2933												
SROSZ =	000016	2525#												
SREGAD	001156	795#												
SREGC	001160	787#	1032	1173*	1178*	1180	1211*	1224	1294*	1327*	1361*	1395*	1429*	1463*
		1845*	1864*	2031*	2054*	2073*	2224*	2298						
SREG1	001162	788#	1295*	1328*	1362*	1396*	1430*	1464*	1508*	1557*	1624*	1844*	1863*	1883*
		1901*	1997*	1998	2030*	2053*	2072*	2225*	2234*	2235	2298			
SREG2	001164	789#	1145*	1146*	1296*	1329*	1363*	1397*	1431*	1509*	1558*	1625*	1775*	1836*
		1839	1841*	1846	1894*	1902*	2001	2180*	2181*	2226*	2248*	2289*	2298	
SREG3	001166	790#	1297*	1330*	1364*	1398*	1432*	1559*	1626*	1846*	1865*	2032*	2052*	2071*
		2227*	2298											
SREG4	001170	791#	1560*	1819*	1899	1901	1941*	2228*	2265*	2266*	2267	2298		
SREG5	001172	792#	1108*	1472*	1585*	1617*	1705*	1776*	1946*	2298				
SRESRE	017160	2669#	2934											
SRAA =	***** U	2935												
SSAVRE	017122	2653#	2933											
SSAVR6	020276	2948*	2954	2955*	2956*	2971*								
SSCOPE	015646	1045	2367#											
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		1640	1681	1727	1788	1910	2308	2314	2340	2349	2350	2359	2360	2361
		2362	2363	2368	2380	2382	2383	2386	2387	2388	2395	2396	2397	2408
		2411	2414	2422	2423	2424	2425	2426	2434	2441	2446	2452	2460	2958
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STMP0	001174	718*	735*	793#	1073	1110*	1111	1113*	1172*	1174*	1179*	1182*	1184	1207*
		1210*	1216	1229										
STMP1	001176	794#	1651*											
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STMP4	001204	797#	1473*	1568*	1573*	1578*	1583*	1618*	1647*	1707*	1758*	1763*	1768*	1773*
		1924*	1931*	1936*	1942*	2101*	2104*	2107*	2110*	2139	2298			
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		1518	1527#	1556	1562	1590	1598#	1613	1632	1640#	1642	1664	1673	1681#
		1703	1719	1727#	1732	1737	1746	1751	1781	1788#	1850	1869	1888	1906
		1910#												
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	2058	2068	2070	2147	2149	2153	2157	2161	2165	2169	2188	2196	2247	2280	2342
	2383	2385	2387	2391	2400	2432	2435	2453	2456	2485	2490	2503	2712	2810	
BGE	1100	2403	2746												
BGT	1127	2319	2817	2879											
B4I	1035	2002	2389	2609											
BIC	1460	1505	1607	1656	1669	1905	2012	2046	2122	2316	2523	2613	2807		
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BISB	1605	1608	1610	2472											
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BITB	2152	2156	2734												
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	1944	2143	2447	2521	2590	2700	2739	2806	2853	2883					
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	1651	1683	1819	2081	2120	2173	2289	2313	2314	2338	2395	2409	2471	2594	2602
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	2048	2050	2067	2069	2089	2125	2213	2246	2258	2267	2279	2292	2378	2402	2449
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JSR	1091 1554 1748 2726	1092 1586 1777 2733	1170 1599 1790	1171 1604 1791	1245 1606 1792	1253 1609 1827	1256 1612 1947	1259 1628 1955	1262 1654 1956	1449 1708 2112	1459 1727 2220	1474 1730 2278	1490 1738 2344	1493 1739 2443	1528 1747 2719
MOV	725 1058 1103 1208 1280 1319 1356 1396 1453 1495 1538 1573 1643 1692 1742 1796 1821 1901 1959 1985 2066 2127 2248 2341 2457 2551 2595 2629 2672 2790 2857 2942 2964	1029 1059 1105 1211 1284 1320 1357 1397 1451 1496 1541 1577 1647 1693 1743 1797 1822 1902 1960 1986 2071 2128 2254 2373 2470 2573 2596 2630 2673 2796 2860 2943 2965	1032 1060 1107 1216 1285 1321 1361 1398 1452 1497 1542 1578 1648 1696 1756 1798 1824 1910 1962 1987 2072 2134 2256 2374 2475 2574 2597 2631 2674 2803 2880 2944	1040 1061 1108 1224 1286 1322 1362 1415 1453 1498 1543 1582 1649 1699 1758 1799 1826 1912 1966 1988 2073 2180 2265 2376 2484 2575 2598 2654 2675 2821 2886 2945	1044 1064 1115 1248 1287 1323 1363 1419 1454 1500 1547 1583 1650 1700 1761 1803 1835 1922 1967 1989 2082 2208 2273 2379 2489 2576 2601 2655 2676 2822 2887 2946	1045 1065 1118 1249 1288 1327 1364 1420 1455 1501 1548 1585 1652 1701 1763 1804 1836 1924 1968 1997 2083 2209 2275 2392 2494 2577 2603 2656 2677 2823 2888 2947	1046 1067 1120 1250 1289 1328 1381 1421 1457 1508 1549 1585 1655 1702 1766 1805 1844 1929 1969 1998 2085 2210 2276 2404 2496 2578 2605 2657 2678 2824 2889 2948	1047 1068 1128 1251 1290 1329 1385 1422 1458 1509 1549 1585 1663 1705 1768 1806 1845 1931 1970 1998 2086 2211 2277 2405 2496 2579 2610 2658 2679 2825 2890 2949	1048 1078 1138 1252 1294 1330 1386 1423 1463 1527 1557 1602 1668 1707 1771 1807 1846 1936 1974 1998 2030 2224 2281 2407 2518 2580 2614 2659 2703 2846 2892 2954	1049 1079 1145 1254 1295 1347 1386 1424 1464 1530 1558 1617 1681 1713 1773 1811 1858 1942 1975 1998 2031 2225 2288 2408 2519 2581 2623 2660 2704 2847 2893 2958	1050 1093 1155 1255 1296 1351 1388 1425 1464 1531 1559 1618 1682 1715 1776 1812 1863 1946 1976 1998 2032 2226 2290 2411 2532 2583 2624 2661 2708 2848 2909 2959	1051 1094 1169 1257 1297 1352 1389 1429 1472 1532 1560 1624 1688 1728 1788 1813 1864 1953 1977 1998 2047 2227 2291 2412 2533 2584 2625 2662 2722 2849 2910 2960	1052 1095 1174 1258 1313 1353 1390 1430 1489 1533 1567 1625 1689 1729 1789 1814 1865 1954 1978 1998 2052 2228 2320 2433 2548 2586 2626 2663 2780 2850 2914 2961	1053 1096 1184 1260 1317 1354 1391 1431 1491 1536 1568 1626 1690 1733 1794 1815 1883 1957 1980 1998 2053 2234 2327 2438 2549 2587 2627 2670 2788 2851 2940 2962	1057 1097 1195 1261 1318 1355 1395 1432 1492 1537 1572 1640 1691 1734 1795 1820 1884 1958 1984 1998 2054 2235 2334 2454 2550 2591 2628 2671 2789 2852 2941 2963
MOVE	1056 2794 2792	2410 2795 2854	2440 2814	2522 2855	2537 2858	2542 2872	2705 2875	2732 2884	2740 2912	2781	2782	2785	2786	2787	2791
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RESET	2343														
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ROR	2000	2194	2202												
RTI	1140 2585	1148 2664	1476 2680	1587 2710	1629 2826	1709 2894	1716 2968	1778	1949	2113	2183	2413	2459	2524	2552
RTS	1036 2749	1164 2915	2076	2091	2175	2203	2221	2230	2250	2260	2269	2284	2294	2498	2632
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TST	1062	1104	1641	1686	1695	1925	1932	1937	1943	2029	2245	2375	2399	2446	2455
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	806	822	978	1025	1037	1040	1044	1045	1047	1049	1051	1053	1054	1055	1057
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ERRORS DETECTED: 0
 DEFAULT GLOBALS GENERATED: 0

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 RUN-TIME: 52 47 6 SECONDS
 RUN-TIME RATIO: 150/107=1.3
 CORE USED: 30K (59 PAGES)

