

# MRV11-BA

LSI-11-UV PROM-RAM TEST  
MD-11-DVMRA-A

EP-DVMRA-A-DL-A  
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**digital**  
MADE IN USA

The image displays a grid of 48 small test data tables, arranged in 8 rows and 6 columns. Each table contains numerical data and headers, likely representing test results for different components or configurations. The data is organized into columns, with some headers appearing to be 'ADDRESS', 'DATA', and 'TEST'. The tables are arranged in a grid that covers the left side of the page, leaving the right side mostly blank.

A small table located in the bottom right corner of the page, containing a few lines of data. It appears to be a continuation of the test data from the main grid.

B01

EOF10M9RASE0

00010000

770526

PDP10 411

HDR10M9RASE0

00010000

770526

.REM x

IDENTIFICATION

PRODUCT CODE: MAINDEC-11-DVMRA-A-D  
PRODUCT NAME: LSI11 UV PROM-RAM (MRV11-BA) TEST.  
DATE: APRIL 1977  
MAINTAINER: DIAGNOSTIC GROUP  
AUTHOR: GUS PASQUANTONIO

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1.0 ABSTRACT

THIS PROGRAM CONSISTS OF A SERIES OF ROUTINES DESIGNED TO TEST THE MRV11-BA UV PROM-RAM OPTION ON AN LSI11 OR 11/03 SYSTEM. THE PROGRAM ALSO PROVIDES OPTIONAL MEMORY DUMP ROUTINES WHICH WILL OUTPUT THE CONTENTS OF ANY GIVEN MEMORY RANGE, IN EITHER OCTAL OR BINARY FORMAT, OR OUTPUT A 32K MEMORY MAP. SEE 7.0 FOR DETAILS.

THE PROGRAM WILL RUN UNDER ACT, APT, AND/OR XXDP WITH THE FOLLOWING RESTRICTIONS (SEE 4.3):

1. THE 256 WORD RAM ADDRESS JUMPERS MUST BE SET TO THE DEFAULT ADDRESS 020000.
2. THE 4K PROM ADDRESS JUMPERS MUST BE SET TO THE DEFAULT ADDRESS 140000, AND THE PROM SIZE JUMPERS SET FOR 4K.

2.0 REQUIREMENTS2.1 EQUIPMENT REQUIRED:

1. LSI11 OR 11/03 WITH 4K READ/WRITE MEMORY.
2. CONSOLE TERMINAL (LA36, OR TTY).
3. MRV11-BA UV PROM-RAM.

2.2 MEMORY REQUIRED:

1. 4K READ/WRITE FOR PROGRAM STORAGE AND EXECUTION.
2. 256 WORD RAM (STANDARD IN MRV11-BA).
3. 1K TO 4K UV PROM (OPTIONAL IN MRV11-BA).
4. SPECIAL 4K TEST PROM (IN-HOUSE ENGINEERING USE ONLY).

3.0 LOADING PROCEDURE

USE STANDARD LOAD PROCEDURE FOR PDP-11 ABSOLUTE BINARY FORMATTED TAPE.

4.0 STARTING PROCEDURE

FIVE START-RESTART ADDRESS ARE PROVIDED:

00200 = INITIAL START. INITIALIZE PROM-RAM ADDRESSES.  
00204 = RESTART. RE-USE OLD PROM-RAM ADDRESSES.  
00210 = OCTAL MEMORY DUMP START (SEE 7.1).  
00214 = BINARY MEMORY DUMP START (SEE 7.1).  
00220 = MEMORY MAP DUMP START (SEE 7.2).

STARTING PROCEDURE (CONT'D)

4.1 UPON INITIAL START THE PROGRAM WILL IDENTIFY ITSELF, TYPE THE CURRENT SWITCH REGISTER CONTENTS (SEE 5.0), AND BEGIN A BRIEF CONVERSATION IN WHICH THE RAM ADDRESS, PROM SIZE, AND PROM ADDRESS ARE ESTABLISHED. RESPOND TO EACH QUERY AS FOLLOWS:

"SWR = 00000 NEW = "  
ENTER THE DESIRED SWITCH REGISTER CONTENTS AND TERMINATE WITH <CR>. SEE 5.0 FOR DETAILS.

"RAM ADDRESS = "  
ENTER THE OCTAL STARTING ADDRESS OF THE 256 WORD RAM, AND TERMINATE WITH <CR>. ADDRESS MUST BE ON A 256 WORD (1000 OCTAL BYTE) BOUNDARY. IF <CR> ONLY, THE DEFAULT ADDRESS 020000 IS USED.

"PROM SIZE = "  
ENTER THE PROM SIZE AND TERMINATE WITH <CR>, AS FOLLOWS:  
1 (OR 1024) = 1K OPTIONAL PROM INSTALLED.  
2 (OR 2048) = 2K OPTIONAL PROM INSTALLED.  
3 (OR 3072) = 3K OPTIONAL PROM INSTALLED.  
4 (OR 4096) = 4K OPTIONAL PROM INSTALLED.  
X = SPECIAL 4K TEST PROM INSTALLED.  
(IN-HOUSE ENGINEERING USE ONLY)  
IF <CR> ONLY, THE DEFAULT SIZE 4K IS USED.

"PROM ADDRESS = "  
ENTER THE OCTAL STARTING ADDRESS OF THE 4K PROM, AND TERMINATE WITH <CR>. THE ADDRESS MUST BE ON A 1K (4000 OCTAL BYTE) BOUNDARY. IF <CR> ONLY, THE DEFAULT ADDRESS 140000 IS USED.

ALL INPUT DATA ARE TESTED FOR VALIDITY (ON PROPER BOUNDARY AND NOT NON-EXISTANT ADDRESS) BEFORE PROCEEDING TO THE NEXT QUERY. WHEN AN INVALID INPUT IS MADE, THAT INPUT REQUEST WILL BE REPEATED.

\*\*\*\*\*  
NOTE: VALID PROM ENTRIES MUST BE MADE EVEN THOUGH THERE MAY BE NO PROM PHYSICALLY INSTALLED.  
THE PROM TESTS (11 AND 12) WILL RUN WITH OR WITHOUT PROM CHIPS INSTALLED.  
\*\*\*\*\*

4.2 SUBSEQUENT RE-STARTS FROM LOC 204 WILL OMIT THE INITIAL DIALOGUE AND USE THE SIZE AND ADDRESS PARAMETERS OBTAINED AT THE LAST INITIALIZATION.

STARTING PROCEDURE (CONT'D)

4.3 THE FOLLOWING PROM-RAM PARAMETERS ARE ASSUMED BY THE PROGRAM UNLESS OTHERWISE ESTABLISHED AS IN 4.1 ABOVE:

1. THE FIRST RAM ADDRESS IS 020000.
2. THE FIRST PROM ADDRESS IS 140000.
3. THE PROM SIZE IS 4K.

THESE DEFAULT VALUES MAY BE ALTERED IF NECESSARY TO MEET SOME SPECIFIC CONFIGURATION. CHANGE THE FOLLOWING MEMORY LOCATIONS TO CONFORM TO YOUR PARTICULAR SYSTEM:

LOC	TAG	CONTENT	COMMENT
1250	\$BASE:	020000	; DEFAULT RAM ADDRESS.
1254	\$CDW1:	140000	; DEFAULT PROM ADDRESS.
1256	\$CDW2:	000004	; DEFAULT PROM SIZE (K).

5.0 SWITCH REGISTER OPTIONS AND CONTROL.

THE FOLLOWING SWITCH OPTIONS ARE PROVIDED:

SWITCH	OCTAL	FUNCTION
SWR15=1	100000	HALT ON ERROR.
SWR14=1	040000	LOOP ON TEST.
SWR13=1	020000	INHIBIT ERROR TYPEOUTS
SWR12=1		NOT USED.
SWR11=1	004000	INHIBIT ITERATIONS.
SWR10=1	002000	BELL ON ERROR.
SWR09=1	001000	LOOP ON ERROR.
SWR08=1	000400	LOOP ON TEST SWR <7:0>
SWR<7:0>	0004XX	TEST # FOR SWR B OPTION.

THE LSI11 USES A DEDICATED MEMORY LOCATION (00176) AS A SOFTWARE SWITCH REGISTER. THIS LOCATION FUNCTIONS THE SAME AS THE HARDWARE SWITCH REGISTER IN OTHER PDP-11'S. THE SOFT SWITCH REGISTER IS DISPLAYED AND/OR CHANGED AT INITIAL START TIME, AND THEREAFTER MAY BE ALTERED BY TYPING "CNTRL G" <↑G>. THE PROGRAM WILL TYPE THE CURRENT SWR CONTENTS. ENTER THE NEW VALUE, IF ANY, AND TERMINATE WITH "CARRIAGE RETURN" <CR>.

6.0 ERROR REPORTING

ANY ERRORS ENCOUNTERED ARE REPORTED ON THE CONSOLE TTY.  
EACH ERROR SIGNATURE IS CONSISTENT WITH THE FOLLOWING  
EXAMPLES;

```
RAM READ-WRITE ERROR.  
TEST#  ERRPC  ADDR  GOOD  BAD  
0000NN XXXXXX YYYYYY 177777 000000
```

```
PROM-RAM COPY ERROR.  
TEST#  ERRPC  PRMADD  PRMDAT  RAMADD  RANDAT  
0000NN XXXXXX 140000 010000 023000 000000
```

TESTING CONTINUES FOLLOWING ANY ERROR UNLESS THE  
LOOP (SWR 9) OR HALT (SWR 15) OPTIONS ARE SELECTED.

IF AN UNFORSEEN BUS TIME-OUT TRAP OCCURS AT ANY TIME,  
AN APPROPRIATE ERROR MESSAGE WILL BE OUTPUT, AND THE  
PROCESSOR WILL HALT AT LOC "STOP" (SEE LISTING).

THE FOLLOWING KEY WORDS ARE USED TO IDENTIFY THE  
VARIOUS DATA WORDS THAT MAKE UP AN ERROR  
SIGNATURE:

ADDR THE WORD ADDRESS CURRENTLY UNDER TEST.  
ADDR-2 THE NEXT LOWER WORD ADDRESS (TESTS 5 AND 6).  
ADDR+2 THE NEXT HIGHER WORD ADDRESS (TESTS 5 AND 6).  
BAD THE BAD (FAILING) DATA WORD.  
BUFDAT THE CONTENTS OF AN INTERNAL BUFFER COPY OF RAM  
LOCATION "ADDR" (TEST 4).  
BYTE THE FAILING BYTE ADDRESS (TEST 10).  
DATIS THE CONTENTS OF PROM LOC "ADDR" AFTER A WRITE  
ATTEMPT (TEST 12).  
DATWAS THE CONTENTS OF PROM LOC "ADDR" BEFORE A WRITE  
ATTEMPT (TEST 12).  
ERRPC THE PC AT WHICH THE ERROR CALL OCCURRED.  
GOOD THE GOOD (EXPECTED) DATA WORD.  
PATRN THE CURRENT TEST BIT PATTERN (TESTS 5 AND 6).  
PRMADD THE CURRENT PROM ADDRESS (TEST 11).  
PRMDAT THE CONTENTS OF "PRMADD".  
RAMADD THE CURRENT RAM ADDRESS (TEST 11).  
RANDAT THE CONTENTS OF "RAMADD" (TESTS 4 AND 11).  
TEST# THE CURRENT TEST NUMBER.  
TRAPPC THE PC AT WHICH A BUS-ERROR OCCURRED.  
TRAPPS THE PROCESSOR STATUS WHEN BUS-ERROR OCCURRED.



7.0 MISCELLANEOUS  
-----

TWO MEMORY DUMP ROUTINES ARE INCLUDED IN THE PROM-RAM TEST FOR CONVENIENCE. THESE ROUTINES WILL DUMP THE CONTENTS OF ANY GIVEN MEMORY RANGE, OR DUMP A 32K MEMORY MAP ON THE CONSOLE TTY.

7.1 MEMORY DATA DUMP.  
-----

THIS ROUTINE IS STARTED FROM LOC 00210 (FOR OCTAL OUTPUT) OR 00214 (FOR BINARY OUTPUT). THE OPERATOR WILL BE ASKED TO INPUT THE STARTING ADDRESS AND THE WORD COUNT OF THE RANGE TO BE DUMPED. 000 ADDRESS INPUTS ARE ACCEPTED, BUT THE DUMP RANGE WILL START AT THAT ADDRESS -1. WORD COUNT INPUT MUST BE GREATER THAN ZERO. THE DUMP RANGE IS CHECKED FOR VALIDITY, AND IF OK, THAT RANGE WILL BE TYPED ON THE TTY, 4 WORDS/LINE OCTAL, OR 2 WORDS/LINE BINARY. IF THE DUMP RANGE IS INVALID (NON-EXISTANT ADDRESS OR WORD COUNT = 0) THE STARTING ADDRESS AND WORD COUNT QUERIES WILL BE REPEATED.

ON OUTPUT, EACH DATA SET IS PRECEDED BY THE ADDRESS OF THE FIRST WORD OF THE SET. I.E.

```
0      000000 111111 022222 133333
10     044444 155555 066666 177777
      OR
0      0000000000000000 1001001001001001
4      0010010010010010 0011011011011011
      ETC.
```

7.2 MEMORY MAP DUMP.  
-----

NORMALLY A 32K MEMORY MAP IS OUTPUT AS AN INTEGRAL PART OF TEST #1. FOR MAINTENANCE PURPOSES, A START FROM LOC 00220 WILL FORCE A SINGLE PASS ENTRY TO TEST #1, OUTPUT THE RESULTING MAP, AND HALT. SEE 9.1 FOR A DESCRIPTION OF THE MAP FORMAT.

8.0 EXECUTION TIME AND END PASS  
-----

THE FIRST PASS THRU THE PROGRAM IS A "QUICK VERIFY" WITH NO ITERATIONS. THEREAFTER, EACH TEST IS REPEATED 100 OCTAL TIMES. EXECUTION TIME WILL VARY FROM 70 TO 150 SECONDS PER PASS DEPENDING ON PROM SIZE. THE PROGRAM WILL TYPE "END PASS # X" AT THE CONCLUSION OF EACH PASS.

JO1

9.0 TEST DESCRIPTION

AFTER INITIALIZATION, THE TEST SEQUENCE IS CONDUCTED IN THE FOLLOWING ORDER. EACH TEST IS REPEATED 100 OCTAL TIMES BEFORE PROCEEDING TO THE NEXT. ANY ERRORS OR DISCREPANCIES ARE REPORTED ON THE CONSOLE TTY AS DESCRIBED IN 6.0 ABOVE.

9.1 TEST 1. 32K MEMORY MAP TEST.

FOR THIS TEST A MEMORY MAP IS CONSTRUCTED BY TESTING MEMORY FROM 0 THRU 32K, IN 256 WORD INCREMENTS. TESTING IS DONE VIA A READ/WRITE SEQUENCE ON THE FIRST WORD OF EACH 256 WORD SEGMENT. THE MAP THUS OBTAINED DESCRIBES EACH 256 WORD SEGMENT AS NON-EXISTANT, READ-ONLY, OR READ/WRITE MEMORY. THE MAP INVOLVES THE USE OF 2 WORDS FOR EACH 4K BANK (16 WORDS). ON THE INITIAL PASS EACH MAP PAIR IS TRANSLATED INTO A 16 CHARACTER STRING COMPOSED OF THE CHARACTERS (R) FOR READ ONLY, (W) FOR READ/WRITE, AND (-) FOR NON-EXISTANT, AND OUTPUT AS SHOWN BELOW. ON SUBSEQUENT PASSES, A NEW MAP IS CONSTRUCTED AND COMPARED AGAINST THE ORIGINAL (REFERENCE) MAP. ANY DIFFERENCES ARE INTERPRETED AS ERRORS AND REPORTED ACCORDINGLY. NORMALLY THE MAP IS PRINTED ONLY ONCE, UPON INITIAL START FROM LOC 00200, AND NOT AT ALL IF RUNNING UNDER ACT11, RPT11, OR XXDP.

A TYPICAL MAP FOR A 4K LSI-11, WITH MRV11-BA, AND 4K PROM MIGHT APPEAR AS FOLLOWS (COMMENTS ADDED):

```

WWWWWWWWWWWWWWWWW  BANK 0 IS R/W MEMORY.
W-----           RAM AT DEFAULT 020000
-----           BANKS 2-5 ARE NEXM
-----
-----
RRRRRRRRRRRRRRRRR  PROM AT DEFAULT 140000
----R-----R----- BANK 7 (I/O PAGE) WITH
                        BOOTSTRAP ROM AT 165000,
                        AND 173000.
    
```

NOTE THAT EACH CHARACTER IN A LINE REPRESENTS A 256 WORD (1000 OCTAL BYTE) SEGMENT OF THAT BANK, STARTING AT ADDRESS X+00000 (LEFTMOST CHAR), AND ENDING AT ADDRESS X+17000 (RIGHTMOST CHAR), WHERE X = THE BANK BITS FOR THAT BANK (00, 02, 04, 06, 10, 12, 14, OR 16).

IF A MAP COMPARE ERROR OCCURS, THE MAP PAIR FOR THE FAILING BANK ARE TRANSLATED AS ABOVE AND THE RESULTING MAP LINE IS OUTPUT ON THE TTY. ONE SHOULD ALWAYS SAVE A COPY OF THE 1ST PASS MAP FOR REFERENCE.

9.2 TEST 2. RAM ONES AND ZEROS TEST.

THIS TEST VERIFIES THAT THE RAM CAN HOLD ALL ONES, AND THEN ALL ZEROS. THE RAM IS FILLED WITH THE APPROPRIATE DATA PATTERN, THEN EACH LOCATION IS READ AND VERIFIED.

9.3 TEST 3. RAM BASIC ADDRESS TEST.

THE RAM IS FILLED WITH AN ADDRESS PATTERN WHERE EACH LOCATION CONTAINS ITS OWN ADDRESS WORD. EACH ADDRESS IS THEN READ AND VERIFIED.

9.4 TEST 4. RAM FAST READ TEST.

THIS TEST IS SIMILAR TO THE ADDRESS TEST ABOVE, IN THAT THE DATA PATTERN USED IS AN ADDRESS PATTERN. HOWEVER, IN THIS CASE THE ENTIRE RAM IS COPIED INTO AN INTERNAL BUFFER USING 256 CONSECUTIVE "MOV" INSTRUCTIONS. THE INTENT HERE IS TO HIT THE RAM AS FAST AS POSSIBLE. THE CONTENTS OF THE RAM ARE THEN COMPARED TO THAT OF THE INTERNAL BUFFER.

9.5 TEST 5. RAM SLIDING 0 BIT TEST.

IN THIS TEST THE RAM IS FILLED WITH 1'S, THEN A WORD CONTAINING A SINGLE 0 BIT IS WRITTEN, BEGINNING WITH BIT 0 OF WORD 0, AND ENDING WITH BIT 15 OF WORD 255. AS EACH 0 BIT IS WRITTEN, THAT LOCATION (N) IS VERIFIED, AND THE TWO ADJACENT LOCATIONS (N-2 AND N+2) ARE TESTED TO VERIFY THAT THEY WERE UNAFFECTED BY THE WRITING INTO LOCATION N.

9.6 TEST 6. RAM SLIDING 1 BIT TEST.

SAME AS TEST 4 ABOVE, EXCEPT THAT THE RAM IS INITIALIZED WITH 0'S, AND A WORD CONTAINING A SINGLE 1 BIT IS WRITTEN.

9.7 TEST 7. RAM ALTERNATE BIT/WORD TEST.

THE RAM IS FILLED WITH 052525, 125252, 052525, 125252, ETC., AND VERIFIED. THE PATTERN IS THEN COMPLIMENTED 125252, 052525, 125252, 052525, ETC., AND VERIFIED AGAIN.

9.8 TEST 10. RAM WRITE BYTE TEST.

THE RAM IS FILLED WITH 1'S, AND A LO BYTE 0 IS WRITTEN INTO EACH WORD. EACH ADDRESS IS THEN CHECKED TO SEE THAT THE HI BYTE = -1, AND THE LO BYTE = 0.

REFILL WITH 1'S, AND WRITE AND CHECK A HI BYTE 0.  
 REFILL WITH 0'S, AND WRITE AND CHECK A LO BYTE -1.  
 REFILL WITH 0'S, AND WRITE AND CHECK A HI BYTE -1.

9.9 TEST 11. PROM TO RAM COPY TEST.

THE CONTENTS OF THE PROM IS COPIED INTO THE RAM, IN 256 WORD PACKETS. THEN THE RAM DATA IS COMPARED WITH THE PROM DATA. THE COPY-COMPARE CONTINUES UNTIL THE PROM IS EXHAUSTED.

9.10 TEST 12. PROM WRITE-TRAP TEST.

THIS TEST CHECKS THAT ANY ATTEMPT TO WRITE INTO THE PROM RESULTS IN A "BUS ERROR TRAP". ALL PROM ADDRESSES AS DETERMINED BY THE "PROM SIZE", ARE TESTED.

9.11 TEST 13. PROM DATA PATTERN TEST  
 (IN-HOUSE ENGINEERING USE ONLY)

TEST 13 IS INCLUDED FOR ENGINEERING PURPOSES ONLY, AND REQUIRES THAT A SPECIAL 4K PROGRAMMED PROM CHIP SET BE INSTALLED. ENTRY INTO THE TEST IS ONLY MADE IF THE "X" RESPONSE WAS GIVEN TO THE "PROM SIZE" QUERY DURING INITIALIZATION.

THE TEST READS EACH PROM LOCATION AND VERIFIES THAT THE FOLLOWING BINARY COUNT PATTERN IS READ:

1ST K 010000 THRU 011777  
 2ND K 022000 THRU 023777  
 3RD K 044000 THRU 045777  
 4TH K 106000 THRU 107777

10.0 PROGRAM LISTING FOLLOWS:

x

487  
488  
489  
490  
491  
492  
493  
494  
495  
496  
497  
498  
499  
500  
501  
502  
503  
504  
505  
506  
507  
508  
509  
510  
511  
512  
513  
514  
515

000001  
160000  
  
167400  
020000  
140000  
000004

```

:TITLE LSI-11 UVPROM-RAM TEST. MD-11-DVMRA-A.
:*COPYRIGHT (C) 1977
:*DIGITAL EQUIPMENT CORP.
:*MAYNARD, MASS. 01754
:*
:*PROGRAM BY GUS PASQUANTONIO
:*
:*THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
:*PACKAGE (MAINDEC-11-DZQAC-C2), SEPT 14, 1976.
:*
$TN=1
$SWR=160000 ;;HALT ON ERROR, LOOP ON TEST, INHIBIT ERROR TYP0UT

$SWR=167400 ;REDEFINE SWITCHES USED.
ABASE=020000 ;DEFAULT RAM ADDRESS
ACDW1=140000 ;DEFAULT PROM ADDRESS
ACDW2=4 ;DEFAULT PROM SIZE IN K.

.SBTTL OPERATIONAL SWITCH SETTINGS
:*
:* SWITCH USE
:* -----
:* 15 HALT ON ERROR
:* 14 LOOP ON TEST
:* 13 INHIBIT ERROR TYPEOUTS
:* 11 INHIBIT ITERATIONS
:* 10 BELL ON ERROR
:* 9 LOOP ON ERROR
:* 8 LOOP ON TEST IN SWR<7:0>

```

```

516 .SBTTL TRAP CATCHER
517
518 000000
519
520
521
522
523 000174 000174
524 000176 000000
525
526
527 000004 000004 000000
528
529
530 000100 000100 000200 000002
531
532
533 000200 000137 001436
534 000204 000137 002620
535
536 000210 000137 007174
537 000214 000137 007276
538 000220 000137 007124
539
540 .SBTTL BASIC DEFINITIONS
541
542
543 001100
544
545
546
547
548 000011
549 000012
550 000015
551 000200
552 177776
553
554 177774
555 177772
556 177570
557 177570
558
559
560 000000
561 000001
562 000002
563 000003
564 000004
565 000005
566 000006
567 000007
568 000006
569 000007

;#ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A "+2,HALT"
;#SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS
;#LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS

;#174
DISPREG: .WORD 0 ;:SOFTWARE DISPLAY REGISTER
SWREG: .WORD 0 ;:SOFTWARE SWITCH REGISTER

;#4
.TIMOUT,0 ;BUS ERROR PROC ER.

;#100
.TIMOUT,104,200,2 ;IGNORE ANY "BEVNT" INTERRUPTS

;#200
JMP @#START ;:INITIAL START AND INIT.
JMP @#RSTRT ;:RESTART. USE SAME PROM/RAM
;:PARAMETERS. NO MAP OUTPUT.
JMP @#ODUMP ;:OCTAL DUMP START
JMP @#BDUMP ;:BINARY DUMP START
JMP @#MDUMP ;:MAP DUMP START

;#INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***
STACK= 1100
.EQUIV EMT,ERROR ;:BASIC DEFINITION OF ERROR CALL
.EQUIV IOT,SCOPE ;:BASIC DEFINITION OF SCOPE CALL

;#MISCELLANEOUS DEFINITIONS
HT= 11 ;:CODE FOR HORIZONTAL TAB
LF= 12 ;:CODE FOR LINE FEED
CR= 15 ;:CODE FOR CARRIAGE RETURN
CRLF= 200 ;:CODE FOR CARRIAGE RETURN-LINE FEED
PS= 177776 ;:PROCESSOR STATUS WORD
.EQUIV PS,PSW
STKLMT= 177774 ;:STACK LIMIT REGISTER
PIRQ= 177772 ;:PROGRAM INTERRUPT REQUEST REGISTER
DSWR= 177570 ;:HARDWARE SWITCH REGISTER
DDISP= 177570 ;:HARDWARE DISPLAY REGISTER

;#GENERAL PURPOSE REGISTER DEFINITIONS
R0= %0 ;:GENERAL REGISTER
R1= %1 ;:GENERAL REGISTER
R2= %2 ;:GENERAL REGISTER
R3= %3 ;:GENERAL REGISTER
R4= %4 ;:GENERAL REGISTER
R5= %5 ;:GENERAL REGISTER
R6= %6 ;:GENERAL REGISTER
R7= %7 ;:GENERAL REGISTER
SP= %6 ;:STACK POINTER
PC= %7 ;:PROGRAM COUNTER
    
```

570  
571  
572 000000  
573 000040  
574 000100  
575 000140  
576 000200  
577 000240  
578 000300  
579 000340  
580  
581  
582 100000  
583 040000  
584 020000  
585 010000  
586 004000  
587 002000  
588 001000  
589 000400  
590 000200  
591 000100  
592 000040  
593 000020  
594 000010  
595 000004  
596 000002  
597 000001  
598  
599  
600  
601  
602  
603  
604  
605  
606  
607  
608  
609  
610 100000  
611 040000  
612 020000  
613 010000  
614 004000  
615 002000  
616 001000  
617 000400  
618 000200  
619 000100  
620 000040  
621 000020  
622 000010  
623 000004

..#PRIORITY LEVEL DEFINITIONS  
PR0= 0  
PR1= 40  
PR2= 100  
PR3= 140  
PR4= 200  
PR5= 240  
PR6= 300  
PR7= 340  
: : PRIORITY LEVEL 0  
: : PRIORITY LEVEL 1  
: : PRIORITY LEVEL 2  
: : PRIORITY LEVEL 3  
: : PRIORITY LEVEL 4  
: : PRIORITY LEVEL 5  
: : PRIORITY LEVEL 6  
: : PRIORITY LEVEL 7

..# "SWITCH REGISTER" SWITCH DEFINITIONS  
SW15= 100000  
SW14= 40000  
SW13= 20000  
SW12= 10000  
SW11= 4000  
SW10= 2000  
SW9= 1000  
SW8= 400  
SW7= 200  
SW6= 100  
SW5= 40  
SW4= 20  
SW3= 10  
SW2= 4  
SW1= 2  
SW0= 1  
.EQUIV SW09, SW9  
.EQUIV SW08, SW8  
.EQUIV SW07, SW7  
.EQUIV SW06, SW6  
.EQUIV SW05, SW5  
.EQUIV SW04, SW4  
.EQUIV SW03, SW3  
.EQUIV SW02, SW2  
.EQUIV SW01, SW1  
.EQUIV SW00, SW0

..#DATA BIT DEFINITIONS (BIT00 TO BIT15)  
BIT15= 100000  
BIT14= 40000  
BIT13= 20000  
BIT12= 10000  
BIT11= 4000  
BIT10= 2000  
BIT09= 1000  
BIT08= 400  
BIT07= 200  
BIT06= 100  
BIT05= 40  
BIT04= 20  
BIT03= 10  
BIT02= 4

624 000002  
625 000001  
626  
627  
628  
629  
630  
631  
632  
633  
634  
635  
636  
637  
638 000004  
639 000010  
640 000014  
641 000014  
642 000014  
643 000020  
644 000024  
645 000030  
646 000034  
647 000060  
648 000064  
649 000240  
650  
651  
652  
653  
654  
655 000224  
656 000046  
657 000046 000626  
658 000052  
659 000052 000000  
660 000224  
661 001000  
662  
663  
664  
665  
666  
667 001000  
668 000024  
669 000024 000200  
670 000044  
671 000044 001000  
672 001000  
673  
674  
675  
676  
677 001000

BIT01= 2  
BIT00= 1  
.EQUIV BIT09,BIT9  
.EQUIV BIT08,BIT8  
.EQUIV BIT07,BIT7  
.EQUIV BIT06,BIT6  
.EQUIV BIT05,BIT5  
.EQUIV BIT04,BIT4  
.EQUIV BIT03,BIT3  
.EQUIV BIT02,BIT2  
.EQUIV BIT01,BIT1  
.EQUIV BIT00,BIT0

.\*BASIC "CPU" TRAP VECTOR ADDRESSES  
ERRVEC= 4 TIME OUT AND OTHER ERRORS  
RESVEC= 10 RESERVED AND ILLEGAL INSTRUCTIONS  
TBITVEC= 14 "T" BIT  
TRTVEC= 14 TRACE TRAP  
BPTVEC= 14 BREAKPOINT TRAP (BPT)  
IOTVEC= 20 INPUT/OUTPUT TRAP (IOT) \*\*SCOPE\*\*  
PWRVEC= 24 POWER FAIL  
EMTVEC= 30 EMULATOR TRAP (EMT) \*\*ERROR\*\*  
TRAPVEC= 34 "TRAP" TRAP  
TKVEC= 60 TTY KEYBOARD VECTOR  
TPVEC= 64 TTY PRINTER VECTOR  
PIRQVEC= 240 PROGRAM INTERRUPT REQUEST VECTOR

.SBTTL ACT11 HOOKS

\*\*\*\*\*  
;HOOKS REQUIRED BY ACT11  
\$SVPC=. ;SAVE PC  
.=46  
SENDAD ;;1)SET LOC.46 TO ADDRESS OF SENDAD IN .SEOP  
.=52  
.WORD 0 ;;2)SET LOC.52 TO ZERO  
.= \$SVPC ;; RESTORE PC  
.=1000

.SBTTL APT PARAMETER BLOCK

\*\*\*\*\*  
;SET LOCATIONS 24 AND 44 AS REQUIRED FOR APT  
\*\*\*\*\*  
.\$X=. ;SAVE CURRENT LOCATION  
.=24 ;SET POWER FAIL TO POINT TO START OF PROGRAM  
200 ;FOR APT START UP  
.=44 ;POINT TO APT INDIRECT ADDRESS PNTR.  
\$APTHDR ;POINT TO APT HEADER BLOCK  
.= \$X ;RESET LOCATION COUNTER  
\*\*\*\*\*  
;SETUP APT PARAMETER BLOCK AS DEFINED IN THE APT-PDPI1 DIAGNOSTIC  
;INTERFACE SPEC.

\$APTHD:



678 001000 000000  
679 001002 001174  
680 001004 000055  
681 001006 000005  
682 001010 000005  
683 001012 000032

SHIBTS: .WORD 0 ;; TWO HIGH BITS OF 18 BIT MAILBOX ADDR.  
\$MADR: .WORD \$MAIL ;; ADDRESS OF APT MAILBOX (BITS 0-15)  
\$TSM: .WORD 45. ;; RUN TIM OF LONGEST TEST  
\$PASTM: .WORD 5. ;; RUN TIME IN SECS. OF 1ST PASS ON 1 UNIT (QUICK VERIFY)  
\$UNITH: .WORD 5. ;; ADDITIONAL RUN TIME (SECS) OF A PASS FOR EACH ADDITIONAL UNIT  
SETEND-\$MAIL/2 ;; LENGTH MAILBOX-ETABLE(WORDS)

.SBTTL COMMON TAGS

\*\*\*\*\*  
: THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS  
: USED IN THE PROGRAM.

684  
685  
686  
687  
688  
689  
690 001100  
691 001100  
692 001100 000000  
693 001102 000  
694 001103 000  
695 001104 000000  
696 001106 000000  
697 001110 000000  
698 001112 000000  
699 001114 000  
700 001115 001  
701 001116 000000  
702 001120 000000  
703 001122 000000  
704 001124 000000  
705 001126 000000  
706 001130 000000  
707 001132 000000  
708 001134 000  
709 001135 000  
710 001136 000000  
711 001140 177570  
712 001142 177570  
713 001144 177560  
714 001146 177562  
715 001150 177564  
716 001152 177566  
717 001154 000  
718 001155 002  
719 001156 012  
720 001157 000  
721 001160 000000  
722 001162 000000  
723 001164 177607 000377  
724 001170 077  
725 001171 015  
726 001172 000012  
727  
728  
729  
730  
731  
732 001174  
733 001174 000000  
734 001176 000000  
735 001200 000000  
736 001202 000000  
737 001204 000000

.=1100

SCMTAG: .WORD 0  
STSTNM: .BYTE 0  
SERFLG: .BYTE 0  
\$ICNT: .WORD 0  
\$LPADR: .WORD 0  
\$LPERR: .WORD 0  
\$ERTTL: .WORD 0  
\$ITEMB: .BYTE 0  
\$ERMAX: .BYTE 1  
\$ERRPC: .WORD 0  
\$GDADR: .WORD 0  
\$BDADR: .WORD 0  
\$GOODAT: .WORD 0  
\$BDOAT: .WORD 0  
\$AUTOB: .BYTE 0  
\$INTAG: .BYTE 0  
\$SWR: .WORD DSWR  
DISPLAY: .WORD DDISP  
\$TKS: 177560  
\$TKB: 177562  
\$TPS: 177564  
\$TPB: 177566  
\$NULL: .BYTE 0  
\$FILLS: .BYTE 2  
\$FILLC: .BYTE 12  
\$TPFLG: .BYTE 0  
\$TIMES: 0  
\$ESCAPE: 0  
\$BELL: .ASCIZ <207><377><377>  
\$QUES: .ASCII /?/  
\$CRLF: .ASCII <15>  
\$LF: .ASCIZ <12>

:: START OF COMMON TAGS

:: CONTAINS THE TEST NUMBER  
:: CONTAINS ERROR FLAG  
:: CONTAINS SUBTEST ITERATION COUNT  
:: CONTAINS SCOPE LOOP ADDRESS  
:: CONTAINS SCOPE RETURN FOR ERRORS  
:: CONTAINS TOTAL ERRORS DETECTED  
:: CONTAINS ITEM CONTROL BYTE  
:: CONTAINS MAX. ERRORS PER TEST  
:: CONTAINS PC OF LAST ERROR INSTRUCTION  
:: CONTAINS ADDRESS OF 'GOOD' DATA  
:: CONTAINS ADDRESS OF 'BAD' DATA  
:: CONTAINS 'GOOD' DATA  
:: CONTAINS 'BAD' DATA  
:: RESERVED--NOT TO BE USED  
  
:: AUTOMATIC MODE INDICATOR  
:: INTERRUPT MODE INDICATOR  
  
:: ADDRESS OF SWITCH REGISTER  
:: ADDRESS OF DISPLAY REGISTER  
:: TTY KBD STATUS  
:: TTY KBD BUFFER  
:: TTY PRINTER STATUS REG. ADDRESS  
:: TTY PRINTER BUFFER REG. ADDRESS  
:: CONTAINS NULL CHARACTER FOR FILLS  
:: CONTAINS # OF FILLER CHARACTERS REQUIRED  
:: INSERT FILL CHARS. AFTER A "LINE FEED"  
:: "TERMINAL AVAILABLE" FLAG (BIT<07>=0=YES)  
:: MAX. NUMBER OF ITERATIONS  
:: ESCAPE ON ERROR ADDRESS  
:: CODE FOR BELL  
:: QUESTION MARK  
:: CARRIAGE RETURN  
:: LINE FEED

\*\*\*\*\*  
.SBTTL APT MAILBOX-ETABLE

\*\*\*\*\*  
.EVEN  
\$MAIL: .WORD APT MAILBOX  
\$MSGTY: .WORD AMSGTY :: MESSAGE TYPE CODE  
\$FATAL: .WORD AFATAL :: FATAL ERROR NUMBER  
\$TESTN: .WORD ATESTN :: TEST NUMBER  
\$PASS: .WORD APASS :: PASS COUNT  
\$DEVCT: .WORD ADEVCT :: DEVICE COUNT

738	001206	000000	SUNIT:	:WORD	RUNIT	:: I/O UNIT NUMBER
739	001210	000000	MSGAD:	:WORD	RMSGAD	:: MESSAGE ADDRESS
740	001212	000000	MSGLG:	:WORD	RMSGLG	:: MESSAGE LENGTH
741	001214		SETABLE:			:: APT ENVIRONMENT TABLE
742	001214	000	RENV:	:BYTE	RENV	:: ENVIRONMENT BYTE
743	001215	000	REVM:	:BYTE	REVM	:: ENVIRONMENT MODE BITS
744	001216	000000	SSWREG:	:WORD	RSWREG	:: APT SWITCH REGISTER
745	001220	000000	SUSR:	:WORD	RUSR	:: USER SWITCHES
746	001222	000000	SCPUOP:	:WORD	ACPUOP	:: CPU TYPE, OPTIONS
747			::			BITS 15-11=CPU TYPE
748			::			11/04=01, 11/05=02, 11/20=03, 11/40=04, 11/45=05
749			::			11/70=06, P00=07, 0=10
750			::			BIT 10=REAL TIME CLOCK
751			::			BIT 9=FLOATING POINT PROCESSOR
752			::			BIT 8=MEMORY MANAGEMENT
753	001224	000	SMMS1:	:BYTE	AMMS1	:: HIGH ADDRESS, M.S. BYTE
754	001225	000	SMTYP1:	:BYTE	AMTYP1	:: MEM. TYPE, BLK#1
755			::			MEM. TYPE BYTE -- (HIGH BYTE)
756			::			900 NSEC CORE=001
757			::			300 NSEC BIPOLAR=002
758			::			500 NSEC NOS=003
759	001226	000000	SMADR1:	:WORD	AMADR1	:: HIGH ADDRESS, BLK#1
760			::			MEM. LAST ADDR. =3 BYTES, THIS WORD AND LOW OF "TYPE" ABOVE
761	001230	000	SMMS2:	:BYTE	AMMS2	:: HIGH ADDRESS, M.S. BYTE
762	001231	000	SMTYP2:	:BYTE	AMTYP2	:: MEM. TYPE, BLK#2
763	001232	000000	SMADR2:	:WORD	AMADR2	:: MEM. LAST ADDRESS, BLK#2
764	001234	000	SMMS3:	:BYTE	AMMS3	:: HIGH ADDRESS, M.S. BYTE
765	001235	000	SMTYP3:	:BYTE	AMTYP3	:: MEM. TYPE, BLK#3
766	001236	000000	SMADR3:	:WORD	AMADR3	:: MEM. LAST ADDRESS, BLK#3
767	001240	000	SMMS4:	:BYTE	AMMS4	:: HIGH ADDRESS, M.S. BYTE
768	001241	000	SMTYP4:	:BYTE	AMTYP4	:: MEM. TYPE, BLK#4
769	001242	000000	SMADR4:	:WORD	AMADR4	:: MEM. LAST ADDRESS, BLK#4
770	001244	000000	SVECT1:	:WORD	AVECT1	:: INTERRUPT VECTOR#1, BUS PRIORITY#1
771	001246	000000	SVECT2:	:WORD	AVECT2	:: INTERRUPT VECTOR#2, BUS PRIORITY#2
772	001250	020000	SBASE:	:WORD	ABASE	:: BASE ADDRESS OF EQUIPMENT UNDER TEST
773	001252	000000	SDEVN:	:WORD	ADEVN	:: DEVICE MAP
774	001254	140000	SCDW1:	:WORD	ACDW1	:: CONTROLLER DESCRIPTION WORD#1
775	001256	000004	SCDW2:	:WORD	ACDW2	:: CONTROLLER DESCRIPTION WORD#2
776	001260		SETEND:			
777			.MEXIT			

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.SBTTL ERROR POINTER TABLE

```

; *THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
; *THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
; *LOCATION SITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
; *NOTE1: IF SITEMB IS 0 THE ONLY PERTINENT DATA IS (SERRPC).
; *NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:
    
```

```

; *      EM      ;; POINTS TO THE ERROR MESSAGE
; *      DH      ;; POINTS TO THE DATA HEADER
; *      DT      ;; POINTS TO THE DATA
; *      DF      ;; POINTS TO THE DATA FORMAT
    
```

```

SERRTB:
; ITEM1      EM1,DH1,DT1,DF      ; READ-WRITE ERROR
; ITEM2      EM2,DH2,DT2,DF      ; BASIC ADDRESS TEST.
; ITEM3      EM3,DH3,DT3,DF      ; FAST READ TEST.
; ITEM4      EM4,DH4,DT4,DF      ; SLIDE TEST. ADDR-2 ALTERED.
; ITEM5      EM5,DH5,DT5,DF      ; SLIDE TEST. ADDR+2 ALTERED.
; ITEM6      EM6,DH6,DT6,DF      ; ALTERNATE BIT ERROR.
; ITEM7      EM7,DH7,DT7,DF      ; RAM BYTE TEST ERROR
; ITEM10     EM10,DH10,DT10,DF    ; PROM SPECIAL DATA TEST READ ERROR
; ITEM11     EM11,DH11,DT11,DF    ; PROM TO RAM COPY ERROR.
; ITEM12     EM12,DH12,DT12,DF    ; PROM WRITE TRAP FAILURE.
; ITEM13     EM13,DH13,DT13,DF    ; BUS TIME-OUT (TRAP) ERROR.
; ITEM14     EM14,0,0,0          ; MEM MAP ERROR.
; NON-STANDARD MAP SIGNATURE.
; DH, DT, AND DF = 0
    
```

```

001260
001260 013436 013464 013516
001266 015110
001270 013532 013555 013610
001276 015110
001300 013624 013651 013716
001306 015110
001310 013734 014002 014050
001316 015110
001320 014070 014136 014204
001326 015110
001330 014224 014265 014320
001336 015110
001340 014334 014361 014420
001346 015110
001350 014436 014457 014512
001356 015110
001360 014526 014553 014624
001366 015110
001370 014642 014671 014730
001376 015110
001400 014744 014777 015024
001406 015110
001410 015034 000000 000000
001416 000000
    
```

```

831
832 001420 000000
833 001422 000000
834 001424 000000
835 001426 000000
836 001430 001700
837 001432 000000
838 001434 000000
839
840 001436
841
842
843 001436 012706 001100
844 001442 005026
845 001444 022706 001140
846 001450 001374
847 001452 012706 001100
848
849 001456 012737 007654 000020
850 001464 012737 000340 000022
851 001472 012737 010214 000030
852 001500 012737 000340 000032
853 001506 012737 013120 000034
854 001514 012737 000340 000036
855 001522 012737 012474 000024
856 001530 012737 000340 000026
857 001536 013737 006574 006566
858 001544 013737 001160
859 001550 013737 001162
860 001554 112737 000001 001115
861 001562 012737 001562 001106
862 001570 012737 001570 001110
863
864
865 001576 013746 000004
866 001602 012737 001636 000004
867 001610 012737 177570 001140
868 001616 012737 177570 001142
869 001624 022777 177777 177306
870 001632 001012
871
872 001634 000403
873 001636 012716 001644 645:
874 001642 000002
875 001644 012737 000176 001140 655:
876 001652 012737 000174 001142
877 001660 012637 000004 665:
878
879 001664 005037 001202
880 001670 132737 000200 001215
881 001676 001403
882 001700 012737 001216 001140
883 001706

```

```

.SBTTL INITIAL START-UP.
RAM: 0 ;: RAM START LOC.
RAMEND: 0 ;: RAM LAST LOC
PRM: 0 ;: PROM START LOC
PRMEND: 0 ;: PROM LAST LOC
PRMSZ: 0 ;: PROM SIZE (IN K)
PRMX: 0 ;: 0 NORM, -1 IF SPECIAL 4K SET.
MAINT: 0 ;: 0 NORM, -1 IF ANY DUMP START.

START:
.SBTTL INITIALIZE THE COMMON TAGS
;;CLEAR THE COMMON TAGS (SCHTAG) AREA
MOV #SCHTAG,R6 ;: FIRST LOCATION TO BE CLEARED
CLR (R6)+ ;: CLEAR MEMORY LOCATION
CMP #SMR,R6 ;: DONE?
BNE -6 ;: LOOP BACK IF NO
MOV #STACK,SP ;: SETUP THE STACK POINTER
;;INITIALIZE A FEW VECTORS
MOV #SCOPE,#IOTVEC ;: IOT VECTOR FOR SCOPE ROUTINE
MOV #340,#IOTVEC+2 ;: LEVEL 7
MOV #ERROR,#ERRVEC ;: ERR VECTOR FOR ERROR ROUTINE
MOV #340,#ERRVEC+2 ;: LEVEL 7
MOV #TRAP,#TRAPVEC ;: TRAP VECTOR FOR TRAP CALLS
MOV #340,#TRAPVEC+2 ;: LEVEL 7
MOV #PANON,#PANVEC ;: POWER FAILURE VECTOR
MOV #340,#PANVEC+2 ;: LEVEL 7
MOV #ENDCT,#SEOPCT ;: SETUP END-OF-PROGRAM COUNTER
CLR #TIMES ;: INITIALIZE NUMBER OF ITERATIONS
CLR #ESCAPE ;: CLEAR THE ESCAPE ON ERROR ADDRESS
MOVB #1,#ERRMAX ;: ALLOW ONE ERROR PER TEST
MOV #1,#SLPADR ;: INITIALIZE THE LOOP ADDRESS FOR SCOPE
MOV #1,#SLPER ;: SETUP THE ERROR LOOP ADDRESS
;;SIZE FOR A HARDWARE SWITCH REGISTER. IF NOT FOUND OR IT IS
;;EQUAL TO A "-1" SETUP FOR A SOFTWARE SWITCH REGISTER.
MOV #ERRVEC,-(SP) ;: SAVE ERROR VECTOR
MOV #645,#ERRVEC ;: SET UP ERROR VECTOR
MOV #OSMR,SMR ;: SETUP FOR A HARDWARE SWICH REGISTER
MOV #DISP,DISPLAY ;: AND A HARDWARE DISPLAY REGISTER
CMP #-1,#SMR ;: TRY TO REFERENCE HARDWARE SWR
BNE 665 ;: BRANCH IF NO TIMEOUT TRAP OCCURRED
;: AND THE HARDWARE SWR IS NOT = -1
BR 655 ;: BRANCH IF NO TIMEOUT
;: SETUP FOR TRAP RETURN
645: MOV #655,(SP)
RTI
655: MOV #SWREG,SMR ;: POINT TO SOFTWARE SWR
MOV #DISPREG,DISPLAY
665: MOV (SP)+,#ERRVEC ;: RESTORE ERROR VECTOR

CLR #PASS ;: CLEAR PASS COUNT
BITB #APTSIZE,#ENVM ;: TEST USER SIZE UNDER APT
BEQ 675 ;: YES, USE NON-APT SWITCH
MOV #SSWREG,SMR ;: NO, USE APT SWITCH REGISTER
675:

```

```

894 001706 005737 001434      TST      MAINT      ;MAINT FLAG SET ??
895 001712 001401              BEQ      .+4        ;NO. PROCEED WITH NORMAL START.
896 001714 000207              RTS      PC        ;YES, RETURN TO MAINT ROUTINE.
897
898      .SBTTL  TYPE PROGRAM NAME
899      ;;TYPE THE NAME OF THE PROGRAM IF FIRST PASS
900 001716 005227 177777      INC      #1        ;FIRST TIME?
901 001722 001054              RNE      68$       ;BRANCH IF NO
902 001724 022737 006626 000042  CMP      #SENDAD,2#42 ;ACT-11?
903 001732 001450              BEQ      68$       ;BRANCH IF YES
904 001734 104401 002002      TYPE     67$       ;TYPE ASCIZ STRING
905      .SBTTL  GET VALUE FOR SOFTWARE SWITCH REGISTER
906 001740 005737 000042      TST      2#42     ;ARE WE RUNNING UNDER XXDP/ACT?
907 001744 001012              BNE      70$       ;BRANCH IF YES
908 001746 123727 001214 000001  CMPB    #ENV, #1   ;ARE WE RUNNING UNDER APT?
909 001754 001406              BEQ      70$       ;BRANCH IF YES
910 001756 023727 001140 000176  CMP      SWR, #SWREG ;SOFTWARE SWITCH REG SELECTED?
911 001764 001005              BNE      71$       ;BRANCH IF NO
912 001766 104407              GTSWR                ;GET SOFT-SWR SETTINGS
913 001770 000403              BR       71$
914 001772 112737 000001 001134 70$:  MOVB    #1, SAUTOB ;SET AUTO-MODE INDICATOR
915 002000 002000 000425      71$:  BR       68$
916 002000 000425      ;GET OVER THE ASCIZ
917 002054 000425      ;:69$: .ASCIZ <CRLF>/MD-11-DVMRA-A. LSI-11 UVPRAM-RAM TEST./<CRLF>
918 002054
919
920      .SBTTL  INITIALIZE RAM AND PROM ADDRESSES.
921      ;IF APT, ACT, OR XXDP SKIP DIALOGUE AND USE DEFAULTS.
922      ;RAM = 20000, PROM = 140000, SIZE = 4K.
923
924      TST      2#42     ;ACT OR XXDP ??
925      BNE      1$       ;YES, USE DEFAULTS
926      CMPB    #ENV, #1   ;NO, HOW 'BOUT APT ??
927      BNE      INIT1    ;NO, INITIALIZE VIA OPERATOR
928
929      1$:  MOV      $BASE, RAM ;ACT, XXDP, OR APT, USE DEFAULTS
930      MOV      $BASE, RAMEND ;...1ST RAM LOC = 20000
931      ADD     #776, RAMEND ;...LAST RAM LOC = 020776
932
933      CLR      PRM$      ;CLEAR SPECIAL PROM FLAG
934      MOV      $CDW1, PRM ;...1ST PROM LOC = 140000
935      MOV      $CDW2, PRMSZ ;...SIZE = 4K
936      MOV      $CDW2, R1  ;...CALCULATE LAST LOC.
937      MOV      #3776, PRMEND ;...IN PRMEND
938
939      2$:  DEC      R1
940      BEQ      3$
941      ADD     #4000, PRMEND
942      BR       2$
943
944      3$:  ADD     PRM, PRMEND ;LAST PROM LOC = 1ST + K.-2
945      MOV      #377, MAPSW ;INH TEST 1 MAP OUTPUT.
946      JMP     RSTR†     ;GO START 'EM UP.
    
```

```

;INITIALIZE RAM ADDRESSES. IF 0, USE DEFAULT ADDRESS.
INIT1:
        TYPE      65$      ;;TYPE ASCIZ STRING
        BR        64$      ;;GET OVER THE ASCIZ
;;65$: .ASCIZ <CRLF>/RAM ADDRESS = /
64$:
        RDOCT
        MOV      (SP)+,RAM      ;;POP STACK INTO RAM
        BNE     1$             ;;IS IT 0 ??
        MOV     $BASE,RAM      ;;YES, USE DEFAULT RAM (20000)
        BIT    RAM,#777       ;;256 WORD (1000 BYTE) BOUNDARY ??
        BNE     2$             ;;NO, COMPLAIN
        MOV     RAM,R1        ;;R1=START LOC
        MOV     #776,R2
        ADD    R1,R2          ;;R2=LAST LOC
        MOV     R2,RAMEND    ;;SAVE IT
        JSR    PC,NXM        ;;TEST BOTH FOR NXM
        BR     2$            ;;ONE OR T'OTHER TRAPPED !!!!!
        BR     INIT2        ;;OK, GO INIT FROM SIZE.

2$:     TYPE      M1
        BR      INIT1      ;;SAY "INVALID ADDRESS"...
                               ;;...AND TRY AGAIN.

;NOW GET THE PROM SIZE. IF 0, DEFAULT TO 4K.
;IF X, SPECIAL 4K SET INSTALLED. OTHERWISE ACCEPT 1-4 ONLY.
;ALSO ACCEPT 1024, 2048, 3072, OR 4096, ALTHO ACTUALLY USE
;JUST THE FIRST DIGIT.
INIT2:
        TYPE      65$      ;;TYPE ASCIZ STRING
        BR        64$      ;;GET OVER THE ASCIZ
;;65$: .ASCIZ <CRLF>/PROM SIZE = /
64$:
        ROLIN
        MOV     (SP)+,R0      ;;POP ADDR OF 1ST CHAR.
        MOVB   (R0),PRMSZ    ;;GET THAT CHARACTER.
        CLRB  PRMSZ+1        ;;CLEAR HI BYTE.
        CLR   PRMX           ;;CLEAR SPECIAL PROM FLAG
        TST   PRMSZ          ;;SIZE = 0 (CR ONLY) ??
        BNE   1$             ;;NO IT'S NOT
        MOV   $CDW2,PRMSZ    ;;YES, DEFAULT IS 4K.
        BR   INIT3          ;;GO GET START ADDRESS.

1$:     CMP      #'X,PRMSZ    ;;SIZE = "X" ??
        BNE     2$
        MOV     #-1,PRMX     ;;YES, SET SPECIAL FLAG...
        MOV     #4,PRMSZ    ;;...AND SET SIZE = 4K.
        BR     INIT3        ;;GO GET ADDRESS.

2$:     SUB     #60,PRMSZ    ;;STRIP ASCII BITS
        BMI     3$          ;;SIZE < 0, INVALID.
        SUB     #5,PRMSZ    ;;
        BPL     3$          ;;SIZE > 4, INVALID.
    
```





```

1025 002620 005037 001202 RSTRT: CLR SPASS ;CLEAR PASS COUNTER
1026 002624 012706 001100 AGAIN: MOV #STACK,SP ;INIT THE SP. (LOOP HERE ON EOP)
1027 ;*****
1028 ;*TEST 1 BUILD AND TEST A 32K MEMORY MAP.
1029 ;*****
1030 002630 000240 TST1: NOP
1031 012632 012737 000100 001160 MOV #100,$TIMES ;DO 100 ITERATIONS
1032 012640 112737 000001 001102 MOV #1,$STNM ;INIT TEST NUMBER
1033 002646 012737 000001 001200 MOV #1,$TESTN ;APT TOO
1034 .ENABL L58
1035 002654 012737 003146 001106 MOV #7,$SLPADR ;SET LOOP ADDRESS
1036 002662 013737 001106 001110 MOV $SLPADR,$LPERR ;AND ERROR LOOPER.
1037 ;
1038 ;ENTER AT TST1A FROM "MDUMP" START. DUMP THE MAP AND RETURN.
1039 ;
1040 002670 005037 007020 TST1A: CLR LOC ;START AT LOCATION 0 (0-4K).
1041 012674 012737 000020 007016 MOV #16,$MAPK ;INIT SEGMENT COUNT
1042 002702 012737 007024 007012 MOV $MEMMAP,$RMAP ;...R MAP POINTER...
1043 002710 012737 007026 007014 MOV $MEMMAP+2,$WMAP ;...AND W MAP POINTER.
1044 ;
1045 002716 105737 007022 TSTB MAPSW ;LO BYTE INHIBITS OUTPUT IF AUTO
1046 002722 001004 BNE 15 ;
1047 002724 104401 001171 TYPE ,SCLF ;DOUBLE CRLF.
1048 002730 104401 001171 TYPE ,SCLF ;
1049 012734 017700 004060 15: MOV $LOC,$R0 ;READ... AND IF NO-TRAP...
1050 012740 010077 004054 R01: MOV $R0,$ALOC ;...WRITE, AND IF NO-TRAP...
1051 002744 000261 WRT1: SEC ;...YOU'RE HERE. "LOC" IS R/W.
1052 002746 006177 004040 ROL $RMAP ;SET BIT IN READ MAP...
1053 012752 000261 SEC ;
1054 012754 006177 004034 ROL $WMAP ;...AND IN WRITE MAP.
1055 012760 105737 007022 TSTB MAPSW ;
1056 012764 001032 BNE 25 ;
1057 012766 104401 015120 TYPE ,W ;W = READ/WRITE SEGMENT.
1058 002772 000427 BR 25 ;
1059 ;
1060 002774 000241 RTN1: CLC ;YOU'RE HERE IF "LOC" WAS NEXM.
1061 002776 006177 004010 ROL $RMAP ;CLEAR BIT IN READ MAP...
1062 003002 000241 CLC ;
1063 003004 006177 004004 ROL $WMAP ;...AND WRITE MAP.
1064 003010 105737 007022 ROL $MAPSW ;
1065 003014 001016 BNE 25 ;
1066 003016 104401 015114 TYPE ,DASH ;DASH = NEXM SEGMENT.
1067 003022 000413 BR 25 ;
1068 ;
1069 003024 000261 RTN2: SEC ;YOU'RE HERE IF "LOC" WAS READ ONLY.
1070 003026 006177 003760 ROL $RMAP ;SET BIT IN READ MAP...
1071 003032 000241 CLC ;
1072 003034 006177 003754 ROL $WMAP ;CLEAR IT IN WRITE MAP.
1073 003040 105737 007022 TSTB MAPSW ;
1074 003044 001002 BNE 25 ;
1075 003046 104401 015116 TYPE ,R ;R = READ ONLY SEGMENT.
    
```

```

1076 003052 005337 007016      2S:  DEC  MAPK      ;BUMP SEGMENT COUNT
1077 003056 001016      BNE  3S      ;OR IF 4K (16BITS) NOT DONE YET.
1078 003060 012737 000020 007016  MOV  #16.,MAPK ;INIT COUNT FOR NEXT BANK
1079 003066 062737 000004 007012  ADD  #4,RMAP  ;BUMP MAP POINTERS
1080 003074 062737 000004 007014  ADD  #4,WMAP
1081
1082 003102 105737 007022      TSTB MAPSW
1083 003106 001002      BNE  3S
1084 003110 104401 001171      TYPE ,SCRLF      ;CRLF IF NOT INHIBITED
1085 003114 062737 001000 007020 3S:  ADD  #1000,LOC ;INIT POINTER FOR NEXT SEGMENT
1086 003122 001304      BNE  1S      ;CONTINUE...
1087                                     ;...."LOC" WILL BE 0 AT 32K.
1088 003124 105737 007022      TSTB MAPSW
1089 003130 001006      BNE  7S
1090 003132 104401 001171      TYPE ,SCRLF      ;FINAL CRLF IF NOT INHIBITED.
1091
1092                                     ;NOW, ON INITIAL PASS, COPY MEMMAP TO SAVMAP. ON SUBSEQUENT
1093                                     ;PASSES, COMPARE REFERENCE (SAVMAP) TO CURRENT (MEMMAP)
1094                                     ;AND REPORT ANY DISCREPANCIES.
1095                                     ;IF ENTRY WAS FROM "MDUMP" START, RETURN THERE WITHOUT
1096                                     ;ANY FURTHER ACTION.
1097
1098 003136 005737 001434      TST  MAINT      ;MAINT FLAG SET ??
1099 003142 001401      BEQ  .+4        ;NO
1100 003144 000207      RTS  PC         ;YES, RETURN TO "MDUMP".
1101
1102 003146 005000      7S:  CLR  R0      ;INIT MAP INDEX
1103 003150 105737 007023      TSTB MAPSW+1   ;HI BYTE = 0, ON INIT PASS.
1104 003154 001013      BNE  5S      ;NO, COMPARE THIS MAP WITH REF.
1105 003156 016060 007024 007064 4S:  MOV  MEMMAP(R0),SAVMAP(R0) ;YES, SAVE THE REF MAP.
1106 003164 005720      TST  (R0)+
1107 003166 022700 000034      CMP  #28.,R0  ;DONE ??
1108 003172 001371      BNE  4S      ;NO
1109 003174 012737 177777 007022  MOV  #-1,MAPSW ;YES, SET SWITCH TO INH OUTPUT...
1110 003202 000632      BR   TST1A    ;...AND COMPARE FROM NOW ON.
1111
1112 003204 026060 007024 007064 5S:  CMP  MEMMAP(R0),SAVMAP(R0) ;COMPARE MAPS.
1113 003212 001005      BNE  10S     ;ERROR
1114 003214 005720      6S:  TST  (R0)+   ;CONTINUE AFTER ERROR
1115 003216 022700 000034      CMP  #28.,R0 ;DONE ??
1116 003222 001370      BNE  5S     ;NOT YET
1117 003224 000456      BR   TST2   ;;DONE, GO NEXT TEST.
    
```

```

1118
1119
1120
1121
1122 003226 010001
1123 003230 006201
1124 003232 006201
1125 003234 062701 000061
1126 003240 110137 015064
1127
1128 003244 032700 000002
1129 003250 001401
1130 003252 005740
1131 003254 016037 007024 001122
1132 003256 005720
1133 003264 016037 007024 001126
1134
1135 003272 005001
1136 003274 005737 001122
1137 003300 100007
1138 003302 005737 001126
1139 003306 100010
1140
1141 003310 112761 000127 015067
1142 003316 000407
1143 003320 112761 000055 015067
1144 003326 000403
1145 003330 112761 000122 015067
1146
1147 003336 006137 001122
1148 003342 006137 001126
1149 003346 005201
1150 003350 022701 000020
1151 003354 001347
1152 003356 104014
1153 003360 000715
1154
1155

```

```

;*****
;MAP ERROR. RE-CONSTRUCT AND OUTPUT A MAP FOR THE FAILING
;BANK. CONTINUE AFTER OUTPUT.
105: MOV R0,R1 ;INDEX/2 + 1 = BANK NUMBER
ASR R1
ASR R1
ADD #61,R1 ;R1 = ASCII BANK #.
MOVB R1,EM14A ;SET # IN ERROR MSG STRING.

BIT #BIT1,R0 ;ADJUST INDEX TO PICK 1ST WORD OF PAIR.
BEQ .+4 ;NO ADJ REQ'D.
TST -(R0) ;DEC INDEX
MOV MEMMAP(R0),SBDADR ;FAILING R WORD
TST (R0)+ ;BUMP INDEX
MOV MEMMAP(R0),SBDADR ;FAILING W WORD

115: CLR R1 ;INIT BIT COUNTER
TST SBDADR ;IF R BIT = 0, NEXT.
BPL 12$ ;IF R BIT = 1...
TST SBDADR ;...AND W = 0, READ ONLY.
BPL 13$ ;IF BOTH = 1, READ/WRITE.
;SET APPROPRIATE CHARACTER...

MOVW #W,EM14B(R1)
BR 14$
12$: MOVW #-,EM14B(R1) ;. .IN ERROR MAP...
BR 14$
13$: MOVW #R,EM14B(R1) ;...CHARACTER STRING.

14$: ROL SBDADR ;NOW ROTATE TO NEXT BIT
ROL SBDADR
INC R1 ;BUMP BIT COUNT (INDEX)
CMP #16.,R1 ;CONTINUE FOR 16 BITS.
BNE 115 ;PRINT ERROR "ITEM 14"...
ERROR 14 ;...AND CONTINUE TEST.
BR 65

;*****
.DSABL L58

```

```

1156
1157
1158
1159 003362 000004
1160 003364 012737 000100 001160
1161 003372 005037 003416
1162 003376 004737 003420
1163
1164 003402 012737 177777 003416
1165 003410 004737 003420
1166 003414 000453
1167
1168
1169
1170 003416 000000
1171 003420 012701 000400
1172 003424 013702 001420
1173 003430 013722 003416
1174 003434 005301
1175 003436 001374
1176
1177 003440 012701 000400
1178 003444 012737 003532 001110
1179 003452 013702 001420
1180 003456 023722 003416
1181 003462 001006
1182
1183 003464 005301
1184 003466 001373
1185 003470 013737 001106 001110
1186 003476 000207
1187
1188
1189 003500 013737 003416 001124
1190 003506 016237 177776 001126
1191 003514 010237 001122
1192 003520 062737 177776 001122
1193 003526 104001
1194 003530 000755
1195
1196 003532 062702 177776
1197 003536 013712 003416
1198 003542 000745
1199

;*****
;TEST 2 RAM -- BASIC 1'S AND 0'S TEST.
;*****
TST2: SCOPE
      MOV #100,STIMES ;DO 100 ITERATIONS
      CLR PATRN ;FIRST PATTERN IS ZEROS
      JSR PC,DATTST ;RUN IT...
      MOV #-1,PATRN ;NEXT PATTERN IS ONES.
      JSR PC,DATTST ;DO IT...
      BR TS13 ;SKIP OVER SUBRTN TO NEXT TEST.

;SUBROUTINE FOR 1'S AND 0'S TEST.
PATRN: 0
DATTST: MOV #256,R1 ;WORD COUNT
        MOV RAM,R2 ;FIRST ADDRESS
1$: MOV PATRN,(R2)+ ;FILL THE RAM.
     DEC R1
     BNE 1$ ;CONTINUE TIL DONE.
        MOV #256,R1 ;OK, RESET COUNT.
        MOV #44,$LPERR ;SET FOR SWR9 OPTION IN 'ERROR'
        MOV RAM,R2 ;SET FIRST ADDRESS.
2$: CMP PATRN,(R2)+ ;DATA OK ??
     BNE 4$ ;NO, DATA ERROR.
        DEC R1 ;YES.
        BNE 2$ ;CONTINUE TIL DONE
        MOV $LPADR,$LPERR ;SET FOR SWR9 OPTION IN 'SCOPE'
        RTS PC ;RETURN TO CALLER

;*****
4$: MOV PATRN,$GOODAT ;GOOD DATA.
     MOV -2(R2),$BADAT ;BAD DATA
     MOV R2,$BADADR
     ADD #-2,$BADADR ;BAD ADDRESS
     ERROR 1 ;PRINT ERROR "ITEM 1".
     BR 3$ ;CONTINUE AFTER ERROR. (SWR9=0)
44$: ADD #-2,R2 ;LOOP ON THIS ERROR (SWR9=1)
     MOV PATRN,(R2) ;REFRESH FAILED LOCATION...
     BR 2$ ;...AND TRY AGAIN.
;*****

```

```

1200 ::*****
1201 :#TEST 3 RAM -- BASIC ADDRESS TEST.
1202 :*****
1203 003544 000004          TST3: SCOPE
1204 003546 012737 000100 001160      MOV      #100,STIMES      ;DO 100 ITERATIONS
1205 003554 012701 000400              MOV      #256,R1        ;WORD COUNT
1206 003560 013702 001420              MOV      RAM,R2        ;FIRST ADDRESS
1207 003564 010212          1$: MOV      R2,(R2)        ;FILL WITH OWN ADDRESS.
1208 003566 005722          TST      (R2)+          ;BUMP ADDRESS POINTER
1209 003570 005301          DEC      R1            ;DONE YET ??
1210 003572 001374          BNE     1$            ;NO, CONTINUE FILLING.
1211
1212 003574 012737 003654 001110      MOV      #44$,SLPERR    ;SET FOR SWR9 OPTION IN 'ERROR'
1213 003602 012701 000400              MOV      #256,R1        ;RESET COUNT
1214 003606 013702 001420              MOV      RAM,R2        ;AND ADDRESS.
1215 003612 020212          2$: CMP      R2,(R2)        ;CMP ARE EACH ADDRESS.
1216 003614 001007          BNE     4$            ;ADDRESS ERROR
1217 003616 005722          3$: TST      (R2)+          ;BUMP ADDRESS
1218 003620 005301          DEC      R1            ;AND CONTINUE TIL DONE
1219 003622 001373          BNE     2$
1220
1221 003624 013737 001106 001110      MOV      $LPADR,$LPERR ;SET FOR SWR9 OPTION IN 'SCOPE'
1222 003632 000412          BR      TST4          ;;DONE, GO TO NEXT TEST.
1223
1224 ::*****
1225 003634 010237 001122          4$: MOV      R2,$B0ADR    ;BAD ADDRESS
1226 003640 010237 001124          MOV      R2,$G0DAT    ;GOOD DATA
1227 003644 011237 001126          MOV      (R2),$B0DAT  ;BAD DATA
1228 003650 104002          ERROR   2            ;PRINT ERROR "ITEM 2".
1229 003652 000761          BR      3$            ;CONTINUE AFTER ERROR. (SWR9=0)
1230
1231 003654 010212          44$: MOV      R2,(R2)    ;OR
1232 003656 000755          BR      2$            ;RETRY ON THIS ERROR (SWR9=1)
1233 ;;*****
    
```

```

1234
1235
1236
1237 003660 000004
1238 003662 012737 000100 001160
1239 003670 013701 001420
1240 003674 012702 015122
1241 003700 013703 001422
1242 003704 062703 000002
1243
1244 003710 012122
1245
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1251
1252
1253 004710 013701 001420
1254 004714 012702 015122
1255 004720 022122
1256 004722 001003
1257
1258 004724 020103
1259 004726 001374
1260 004730 000415
1261
1262
1263 004732 016137 177776 001122
1264 004740 016237 177776 001126
1265 004746 010137 001120
1266 004752 062737 177776 001120
1267 004760 104003
1268 004762 000760
1269
1270
    
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```

*****
:TEST 4 RAM -- FAST READ TEST.
*****
TST4: SCOPE
      MOV #100,S1TIMES ;:DO 100 ITERATIONS
      MOV RAM,R1 ;:RAM START LOC.
      MOV #IBUF,R2 ;:INTERNAL BUFFER ADDRESS
      MOV RAMEND,R3 ;:END OF RAM +2.
      ADD #2,R3
      MOV (R1)+,(R2)+ ;:FILL THE BUFFER WITH 256
                           ;:CONSECUTIVE READS.

:THE MOV INSTR ABOVE IS REPEATED 255 TIMES.
:LISTING TURNED OFF FOR ALL THAT.

:NOW THE INTERNAL BUFFER IS A RAM IMAGE.

      MOV RAM,R1 ;:RESET POINTERS...
      MOV #IBUF,R2
      .S: CMP (R1)+,(R2)+ ;:...AND COMPARE EACH ADDRESS
          BNE 2$ ;:ADDRESS ERROR.

      3$: CMP R1,R3 ;:DONE YET ??
          BNE 1$ ;:NOPE, CONTINUE CHECKING.
          BR TST5 ;:YES, GO TO NEXT TEST.

*****
2$: MOV -2(R1),SBOADR ;:RAM WORD
      MOV -2(R2),SBODAT ;:BUFFER WORD
      MOV R1,$GOADR
      ADD #-2,$GOADR ;:RAM ADDRESS (=GOOD DATA).
      ERROR 3 ;:PRINT ERROR "ITEM 3".
      BR 3$ ;:CONTINUE AFTER ERROR (SWR9=0)
           ;:...OR RESTART AT 'TST3+2' (SWR9=1)
*****
    
```

# E03

LSI-11 UVPROM-RAM TEST. MD-11-DVMRA-A. MACY11 27(663) 24-MAR-77 13:51 PAGE 29  
 DVMRAA.P11 TS RAM -- SLIDING 0 BIT TEST.

```

1271
1272
1273
1274 004764 000004
1275 004766 012737 000100 001160
1276 004774 012737 177777 005060
1277 005002 012737 177776 005062
1278 005010 004737 005064
1279 005014 013737 001106 001110
1280
1281
1282
1283
1284 005022 000004
1285 0 24 012737 000100 001160
1286 0 32 005037 005060
1287 0 36 012737 000001 005062
1288 0 44 004737 005064
1289 0 50 013737 001106 001110
1290 0 56 000544
1291
1292
1293
1294 005060 000000
1295 0 62 000000
1296 0 64 013702 001420
1297 0 70 013703 001422
1298 0 74 062703 000002
1299 0 80 013722 005060
1300 005104 020203
1301 005106 001374
1302
1303 005110 012737 005360 001110
1304 005116 013702 001420
1305 005122 062703 177776
1306 005126 013701 005062
1307 005132 010112
1308 005134 020112
1309 005136 001036
1310
1311 005140 020237 001420
1312 005144 001404
1313 005146 023762 005060 177776
1314 005154 001037
1315
1316 005156 020203
1317 005160 001424
1318
1319 005162 023762 005060 000002
1320 005170 001052
1321
1322 005172 005737 005062
1323 005176 100403
1324 005200 006101

```

```

*****
:TEST 5 RAM -- SLIDING 0 BIT TEST.
*****
TST5: SCOPE
MOV #100,STIMES ;DO 100 ITERATIONS
MOV #-1,FILLR ;BACKGROUND = 1'S
MOV #-2,SLIDR ;SLIDE A 0 BIT.
JSR PC,SLIDE ;GO DO IT.
MOV SLPADR,SLPERR ;SET FOR SWR9 OPTION IN 'SCOPE'

*****
:TEST 6 RAM -- SLIDING 1 BIT TEST.
*****
TST6: SCOPE
MOV #100,STIMES ;DO 100 ITERATIONS
CLR FILLR ;BACKGROUND = 0'S
MOV #1,SLIDR ;SLIDE A 1 BIT.
JSR PC,SLIDE ;GO DO IT.
MOV SLPADR,SLPERR ;SET FOR SWR9 OPTION IN 'SCOPE'
BR TST7 ;DONE, SKIP OVER SUBRTNS TO NEXT TEST.

;SUBROUTINE FOR SLIDING BIT TESTS.
FILLR: 0 ;PATTERN FOR FILLER.
SLIDR: 0 ;BIT TO SLIDE.
SLIDE: MOV RAM,R2 ;FIRST LOC.
MOV RAMEND,R3
ADD #2,R3 ;LAST LOC +2.
1$: MOV FILLR,(R2)+ ;FILL WITH BACKGROUND
CMP R2,R3 ;DONE YET ??
BNE 1$ ;NO.

MOV #40$,SLPERR ;SET FOR SWR9 OPTION IN 'ERROR'
MOV RAM,R2 ;RESET 1ST LOC.
ADD #-2,R3 ;R3 = LAST LOC.
MOV SLIDR,R1 ;R1 = SLIDING BIT WORD
2$: MOV R1,(R2) ;TEST 1ST (NEXT) LOC.
CMP R1,(R2) ;IS PATTERN RIGHT ??
BNE 10$ ;NO, PATTERN ERROR.

11$: CMP R2,RAM ;IS THIS THE 1ST LOC ??
BEQ 21$ ;YES, DONT CHECK LOC-2.
CMP FILLR,-2(R2) ;IS LOC-2 UNDISTURBED ??
BNE 20$ ;NO, ERROR

21$: CMP R2,R3 ;IS THIS LAST LOC ??
BEQ 6$ ;YES, YOU'RE ALL DONE.

CMP FILLR,2(R2) ;IS LOC+2 UNDISTURBED ??
BNE 30$ ;NO, ERROR.

31$: TST SLIDR ;CLEAR 'C', SLIDING 0 OR 1 ??
BMI 4$ ;0, SKIP NEXT 3.
ROL R1 ;SLIDE THE 1 LEFT 1.

```

F03

```

1325 005202 103353          BCC 25          ; LOOP FOR 16 BITS.
1326 005204 000403          BR 55          ; OK, NOW SET FOR NEXT LOC.
1327 005206 000261          45: SEC          ; SET 'C'.
1328 005210 006101          ROL R1        ; SLIDE THE 0 LEFT 1.
1329 005212 103747          BCS 25        ; LOOP FOR 16 BITS.
1330
1331 005214 013712 005060    55: MOV FILLR,(R2) ; RESET BACKGROUND PATTERN.
1332 005220 062702 000002   ADD #2,R2      ; BUMP ADDRESS POINTER.
1333 005224 013701 005062   MOV SLIDR,R1  ; RESET SLIDE WORD.
1334 005230 000740          BR 25         ; ...AND DO IT ALL AGAIN.
1335
1336 005232 000207          65: RTS PC      ; EXIT TO CALLER.
1337
1338
1339
1340
1341
1342 005234 010137 001124    105: MOV R1,$GDADR ; GOOD DATA
1343 005240 010237 001122   MOV R2,$BDADR ; FAILING ADDRESS
1344 005244 011237 001126   MOV (R2),$BDAT ; BAD DATA
1345 005250 104001          ERROR 1       ; PRINT ERROR "ITEM 1".
1346 005252 000732          BR 115       ; CONTINUE AFTER ERROR. (SWR9=0)
1347
1348
1349
1350
1351 005254 010237 001120    205: MOV R2,$GDADR ; BASE LOC
1352 005260 011237 001124   MOV (R2),$GDAT ; ...CONTENTS = SLIDE PATTERN
1353 005264 010237 001122   MOV R2,$BDADR
1354 005270 062737 177776 001122  ADD #2,$BDADR ; FAILING ADDR = LOC-2
1355 005276 016237 177776 001126  MOV -2(R2),$BDAT ; ...CONTENTS = BAD DATA
1356 005304 104004          ERROR 4       ; PRINT ERROR "ITEM 4".
1357 005306 013762 005060 177776  MOV FILLR,-2(R2) ; REFRESH FAILED LOCATION
1358 005314 000720          BR 215       ; CONTINUE AFTER ERROR. (SWR9=0)
1359
1360
1361
1362
1363 005316 010237 001120    305: MOV R2,$GDADR ; SAME AS ABOVE..
1364 005322 011237 001124   MOV (R2),$GDAT
1365 005326 010237 001122   MOV R2,$BDADR
1366 005332 062737 000002 001122  ADD #2,$BDADR ; FAILING ADDR = LOC+2
1367 005340 016237 000002 001126  MOV 2(R2),$BDAT
1368 005346 104005          ERROR 5       ; PRINT ERROR "ITEM 5".
1369 005350 013762 005060 000002  MOV FILLR,2(R2) ; REFRESH FAILED LOCATION
1370 005356 000705          BR 315       ; CONTINUE AFTER ERROR. (SWR9=0)
1371
1372
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1378
1379 005360 013777 005060 173534 405: MOV FILLR,$BDADR ; ...OR... REFRESH FAILED LOCATION...
1380 005366 000661          BR 25        ; ...AND RETRY (SWR9=1)
1381
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1371
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1373
1374 005370 000004
1375 005372 012737 000100 001160
1376 005400 013702 001420
1377 005404 013703 001422
1378 005410 062703 000002
1379 005414 012701 052525
1380
1381 005420 010122 15:
1382 005422 005101 COM
1383 005424 020203 CMP
1384 005426 001374 BNE
1385
1386 005430 012737 005532 001110
1387 005436 013702 001420
1388 005442 020122 25:
1389 005444 001016 BNE
1390
1391 005446 005101 45:
1392 005450 020203 COM
1393 005452 001373 CMP
1394 BNE
1395
1396
1397
1398 005454 013702 001420
1399 005460 022701 052525
1400 005464 001002
1401 005466 005101
1402 005470 000753 BR
1403
1404 005472 013737 001106 001110 55:
1405 005500 000416 BR
1406
1407
1408 005502 016237 177776 001126 35:
1409 005510 010237 001120 MOV
1410 005514 062737 177776 001120 MOV
1411 005522 010137 001124 ADD
1412 005526 104006 MOV
1413 005530 000746 ERROR
1414 BR
1415 005532 010142 305:
1416 005534 000742 MOV
1417 BR

```

```

*****
:TEST 7 RAM -- ALTERNATE BIT PATTERN TEST.
*****
TST7: SCOPE
MOV #100,STIMES ;DO 100 ITERATIONS
MOV RAM,R2 ;1ST LOC
MOV RAMEND,R3
ADD #2,R3 ;LAST LOC +2
MOV #52525,R1 ;INITIAL R1 = 052525

15: MOV R1,(R2)+ ;STORE A WORD.
COM R1 ;COMPLIMENT IT
CMP R2,R3 ;DONE ALL ??
BNE 15 ;NO, STORE COMP IN NEXT LOC.

MOV #305,SLPERR ;SET FOR SWR9 OPTION IN 'ERROR'
MOV RAM,R2 ;NOW COMPARE THEM.
CMP R1,(R2)+ ;PATTERN RIGHT ??
BNE 25 ;NO, PATTERN ERROR

45: COM R1 ;COMP PATTERN
CMP R2,R3 ;COMPARED ALL ??
BNE 25 ;NO, COMPARE NEXT

: AT THIS POINT 052525,125252,052525,125252, ETC HAS BEEN DONE.
: NOW USE 125252,052525,125252, ETC.

MOV RAM,R2 ;RESET START LOC.
CMP #52525,R1 ;IS THIS 1ST PASS ??
BNE 55 ;NO, YOU'RE ALL DONE
COM R1 ;YES, SET UP FOR 2ND PASS.
BR 15 ;...AND DO IT.

55: MOV SLPADR,SLPERR ;SET FOR SWR9 OPTION IN 'SCOPE'
BR TST10 ;...AND GO TO NEXT TEST.

*****
35: MOV -2(R2),SBO DAT ;BAD DATA
MOV R2,$GDADR
ADD #2,$GDADR ;FAILING ADDRESS.
MOV R1,$GDOAT ;GOOD DATA
ERROR 6 ;PRINT ERROR "ITEM 6".
BR 45 ;CONTINUE AFTER ERROR. (SWR9=0)
OR
305: MOV R1,-(R2) ;REFRESH FAILED LOCATION...
BR 25 ;...AND RETRY (SWR9=1)
*****

```

```

1418
1419
1420
1421 005536 000004
1422 005540 012737 000100 001160
1423 005546 005037 005640
1424 005552 005001
1425 005554 012737 000377 005634
1426 005562 004737 005642
1427
1428 005566 012701 000001
1429 005572 005137 005634
1430 005576 004737 005642
1431
1432 005602 012737 177777 005640
1433 005610 005001
1434 005612 004737 005642
1435
1436 005616 012701 000001
1437 005622 005137 005634
1438 005626 004737 005642
1439
1440 005632 000475
1441
1442
1443
1444 005634 000000
1445 005636 000000
1446 005640 000000
1447 005642 013702 001420
1448 005646 012703 000400
1449 005652 013722 005640
1450 005656 005303
1451 005660 001374
1452
1453 005662 012737 006016 001110
1454 005670 013737 001420 005636
1455 005676 063701 005636
1456 005702 012703 000400
1457 005706 005737 005640
1458 005712 001003
1459 005714 112711 177777
1460 005720 000401
1461 005722 105011
1462
1463 005724 000240
1464 005726 023777 005634 177702
1465 005734 001013
1466 005736 062737 000002 005636
1467 005744 062701 000002
1468 005750 005303
1469 005752 001355
1470 005754 013737 001106 001110
1471 005762 000207

```

```

*****
:TEST 10 RAM -- WRITE BYTE TEST.
*****
TST10: SCOPE
MOV #100,STIMES ;DO 100 ITERATIONS
CLR BKG ;BACKGROUND PATTERN = 0.
CLR R1 ;BYTE INDEX = 0.
MOV #377, PTRN ;TEST PATTERN = 0,-1
JSR PC, BYTTST ;FILL WITH ZEROS, WRITE LO...
;... BYTE -1, AND TEST WORD.
MOV #1, R1 ;BYTE INDEX = 1.
COM PTRN ;TEST PATTERN = -1, 0
JSR PC, BYTTST ;FILL WITH ZEROS, WRITE HI...
;... BYTE -1, AND TEST WORD.
MOV #-1, BKG ;BACKGROUND PATTERN = -1.
CLR R1 ;BYTE INDEX = 0.
JSR PC, BYTTST ;FILL WITH 1'S, WRITE LO...
;... BYTE 0, AND TEST WORD.
MOV #1, R1 ;BYTE INDEX = 1
COM PTRN ;TEST PATTERN = 0,-1
JSR PC, BYTTST ;FILL WITH 1'S, WRITE HI...
;... BYTE 0, AND TEST WORD.
BR TST11 ;DONE, GO TO NEXT TEST.

;SUBROUTINE FOR RAM BYTE TEST.
PTRN: 0 ;0 -1 OR -1 0
WORDX: 0 ;WORD ADDRESS.
BKG: 0 ;BACKGROUND PATTERN
BYTTST: MOV RAM, R2 ;INIT RAM ADDRESS
;... AND WORD COUNT.
1$: MOV #400, R3 ;FILL RAM WITH BACKGROUND.
MOV BKG, (R2)+
DEC R3
BNE 1$

2$: MOV #11$, SLPERR ;SET FOR SWR9 OPTION IN 'ERROR'
MOV RAM, WORDX ;SET WORD ADDRESS...
;... AND BYTE ADDRESS (R1).
4$: MOV #400, R3 ;BACKGROUND = 0 ??
TST BKG ;NO, WRITE A 0 BYTE.
BNE 2$ ;YES, WRITE A -1 BYTE...
;... AND SKIP NEXT INSTR.
MOVB #-1, (R1) ;WRITE A 0 BYTE
BR 3$

2$: CLRB (R1)

3$: NOP
CMP PTRN, 2WORDX ;PATTERN RIGHT IN RAM ??
BNE 10$ ;NO, ERROR.
5$: ADD #2, WORDX ;YES, BUMP ADDRESS POINTERS.
ADD #2, R1
DEC R3
BNE 4$
MOV SLPAOR, SLPERR ;CONTINUE
PC ;SET FOR SWR9 OPTION IN 'SCOPE'
;... AND EXIT.

```

```

1472
1473
1474 005764 013737 005636 001120 10S: MOV WORDX, $GDADR ; FAILING WORD ADDRESS
1475 007772 017737 177640 001126 MOV 2WORDX, $BODAT ; BAD DATA WORD.
1476 007000 010137 001122 MOV B1, $B0ADR ; BYTE WRITTEN ADDRESS
1477 006004 013737 005634 001124 MOV PTRN, $GDAT ; GOOD DATA PATTERN
1478 006012 104007 ERROR 7 ; PRINT ERROR "ITEM 7".
1479 006014 000750 BR 5S ; CONTINUE AFTER ERROR (SWR9=0)
1480 ; OR ...
1481 006016 013777 005640 177612 11S: MOV BKX, 2WORDX ; REFRESH FAILED LOC...
1482 006024 000730 BR 4S ; ...AND RETRY (SWR9=1)
1483 ;*****

```

```

1484
1485
1486
1487 006026 000004
1488 006030 012737 000100 001160
1489 006036 013701 001424
1490 006042 013702 001420
1491 006046 012703 000400
1492 006052 010304
1493 006054 012737 006206 001110
1494
1495 006062 020102 1$: CMP R1,R2 ;IF RAM IS WITHIN PROM SPACE...
1496 006064 001002 BNE 5$
1497 006066 062701 001000 ADD #512.,R1 ;...SKIP 256 PROM LOCATIONS.
1498
1499 006072 012122 5$: MOV (R1)+,(R2)+ ;COPY PROM TO RAM
1500 006074 005303 DEC R3
1501 006076 001371 BNE 1$
1502
1503 006100 162701 001000 SUB #1000,R1 ;RESET PROM ADDRESS
1504 006104 013702 001420 MOV RAM,R2 ;AND RAM ADDRESS
1505 006110 021112 2$: CMP (R1),(R2) ;COMPARE PROM-RAM
1506 006112 001023 BNE 10$ ;ERROR !!!
1507 006114 005304 11$: DEC R4 ;256 WORDS DONE ??
1508 006116 001403 BEQ 3$ ;YES
1509 006120 005721 TST (R1)+ ;NO, BUMP POINTERS...
1510 006122 005722 TST (R2)+
1511 006124 000771 BR 2$ ;...AND CONTINUE
1512
1513 006126 020137 001426 3$: CMP R1,PROMEND ;END OF PROM ??
1514 006132 001407 BEQ 4$ ;YES
1515 006134 005721 TST (R1)+ ;NO, BUMP PROM ADDRESS
1516 006136 013702 001420 MOV RAM,R2 ;INIT RAM ADDRESS...
1517 006142 012703 000400 MOV #400,R3 ;...AND COUNTERS
1518 006146 010304 MOV R3,R4
1519 006150 000744 BR 1$ ;AND DO THE NEXT CHUNK.
1520
1521 006152 013737 001106 001110 4$: MOV SLPADR,SLPERR ;SET FOR SWR9 OPTION IN 'SCOPE'
1522 006160 000414 BR TST12 ;...AND GO TO NEXT TEST.
1523
1524
1525 006162 010137 001120 10$: MOV R1,$GDADR ;PROM ADDRESS
1526 006166 011137 001124 MOV (R1),$GDADR ;PROM DATA
1527 006172 010237 001122 MOV R2,$BDADR ;RAM ADDRESS
1528 006176 011237 001126 MOV (R2),$BDADR ;RAM DATA
1529 006202 104011 ERROR 11 ;PRINT ERROR "ITEM 11".
1530 006204 000743 BR 11$ ;CONTINUE AFTER ERROR (SWR9=0)
1531
1532 006206 011112 12$: MOV (R1),(R2) ;...OR...
1533 006210 000737 BR 2$ ;REFRESH FAILED RAM LOC...
1534 ; ;...AND RETRY (SWR9=1)
; ;*****

```

```

1535
1536
1537
1538 006212 000004
1539 006214 012737 000100 001160
1540
1541 006222 012737 006246 001110
1542 006230 013701 001424
1543
1544 006234 020137 001420
1545 006240 001002
1546 006242 062701 001000
1547
1548 006246 011102
1549 006250 010211
1550
1551
1552
1553
1554 006252 010137 001122
1555 006256 010237 001124
1556 006262 011137 001126
1557 006266 104012
1558 006270 000400
1559
1560
1561
1562
1563
1564 006272 020137 001426
1565 006276 001402
1566 006300 005721
1567 006302 000754
1568
1569 006304 013737 001106 001110
1570 006312 000400
1571

```

```

*****
:TEST 12 PROM -- WRITE-TRAP TEST.
*****
TST12: SCOPE
MOV #100,STIMES ;;DO 100 ITERATIONS
ENABL L5B
MOV #15,SLPERR ;SET FOR SWR9 OPTION IN 'ERROR'
MOV PRM,R1 ;INIT PROM ADDRESS.
SS: CMP R1,PRM ;IF RAM IS WITHIN PROM SPACE...
BNE IS ;...SKIP 256 PROM LOCATIONS.
ADD #512.,R1
IS: MOV (R1),R2 ;SAVE PROM WORD
MOV R2,(R1) ;...AND TRY TO RE-WRITE IT.
;IT SHOULD TRAP AND RETURN TO WTRTN
*****
:NO-TRAP IS AN ERROR CONDITION.
WRT12: MOV R1,$BDADR ;ERROR, FAILING ADDRESS
MOV R2,$CDOAT ;DATA WAS ...
MOV (R1),$BDOAT ;DATA IS ... (SHOULD BE SAME.)
ERROR 12 ;PRINT ERROR "ITEM 12".
BR WTRTN ;CONTINUE AFTER ERROR (SWR9=0)
;...OR RETRY AT IS (SWR9=1)
*****
:YOU'RE HERE IF THE WRITE ATTEMPT TRAPPED.
WTRTN: CMP R1,PRMEND ;END OF PROM ??
BEQ 45 ;YES
TST (R1)+ ;NO, BUMP POINTER
BR 55 ;...AND DO THE NEXT ADDRESS.
45: MOV $LPADR,SLPERR ;SET FOR SWR9 OPTION IN 'SCOPE'
BR TST13 ;DONE, NEXT TEST.
.DSABL L5B

```

```

1572
1573
1574
1575 006314 000004
1576 006316 012737 000100 001160
1577
1578
1579
1580
1581 006324 005737 001432
1582 006330 001010
1583 006332 005037 001160
1584 006336 000137 006540
1585
1586
1587
1588
1589
1590
1591
1592
1593
1594
1595
1596
1597
1598
1599
1600
1601
1602
1603 006342 010000
1604 006344 020000
1605 006346 040000
1606 006350 100000
1607
1608 006352 000240
1609 006354 000240
1610 006356 012737 006352 001106
1611 006364 012737 006420 001110
1612 006372 013701 006342
1613 006376 013702 001424
1614
1615 006402 020237 001420
1616 006406 001004
1617 006410 062702 001000
1618 006414 062701 000400
1619
1620 006420 020112
1621 006422 001036
1622 006424 005201
1623 006426 005722
1624
1625 006430 022701 012000
    
```

```

*****
;TEST 13 PROM DATA TEST -- ENGINEERING USE ONLY.
*****
1575: SCOPE
1576: MOV #100,STIMES ;;DO 100 ITERATIONS

;IF SPECIAL TEST PROM INSTALLED, DO THIS TEST.
;SKIP TO END-PASS OTHERWISE.

1581: TST PRMX ;SPECIAL TEST PROM ??
1582: BNE XTEST ;YES, EXECUTE THIS TEST.
1583: CLR STIMES ;NO, CLEAR ITER COUNT...
1584: JMP SEOP ;NO, END PASS.

;FOR THIS TEST, A SPECIAL SET OF PROM CHIPS (8) MUST BE
;INSTALLED IN THE PROM BOARD. THESE CHIPS (2/K) ARE BLASTED
;WITH THE FOLLOWING DATA PATTERN:

1590: 1ST K 010000 THRU 011777
1591: 2ND K 022000 023777
1592: 3RD K 044000 045777
1593: 4TH K 106000 107777

;NOTE THAT THE BIT PATTERN EMPLOYS TWO FIELDS AS FOLLOWS:
;BITS 00-11 FORM A BINARY COUNTER, RANGE 0-7777 (4095.).
;BITS 12-15 FORM A RING COUNTER, WHERE;
;BIT 12=1 IN 1ST K.
;BIT 13=1 IN 2ND K.
;BIT 14=1 IN 3RD K.
;BIT 15=1 IN 4TH K.

1603: K1: BIT12 ;BIT12=1 IN 1ST K.
1604: K2: BIT13 ;BIT13=1 IN 2ND K.
1605: K3: BIT14 ;BIT14=1 IN 3RD K.
1606: K4: BIT15 ;BIT15=1 IN 4TH K.

1608: XTEST: NOP
1609: NOP
1610: MOV #XTEST,$LPADR ;SET ITERATION ADDRESS
1611: MOV #55,$LPERR ;SET FOR SWR9 OPTION IN 'ERROR'
1612: MOV K1,R1 ;INIT PATTERN FOR 1ST K.
1613: MOV PRM,R2 ;INIT ADDRESS.

1615: 65: CMP R2,RAM ;IF RAM IS WITHIN PROM SPACE...
1616: BNE 55
1617: ADD #512.,R2 ;...SKIP 256 PROM LOCATIONS.
1618: ADD #256.,R1 ;...ADJUST PATTERN ACCORDINGLY.

1620: 55: CMP R1,(R2) ;COMPARE THIS(NEXT) LOCATION.
1621: BNE 105 ;ERROR
1622: 115: INC R1 ;BUMP PATTERN WORD...
1623: TST (R2)+ ;...AND ADDRESS.

1625: CMP #12000,R1 ;1ST K DONE ??
    
```

```

1626 006434 001412          BEQ      1$          ; YES
1627 006436 022701 024000    CMP      #24000,R1   ; 2ND K DONE ??
1628 006442 001412          BEQ      2$          ; YES
1629 006444 022701 046000    CMP      #46000,R1   ; 3RD K DONE ??
1630 006450 001412          BEQ      3$          ; YES
1631 006452 022701 110000    CMP      #110000,R1  ; 4TH K DONE ??
1632 006456 001412          BEQ      4$          ; YES
1633 006460 000757          BR       5$          ; CO INUE IN CURRENT SECTION.
1634
1635 006462 063701 006342    1$:     ADD      K1,R1 ; INIT PATTERN FOR 2ND K.
1636 006466 000745          BR       6$
1637 006470 063701 006344    2$:     ADD      K2,R1 ; INIT PATTERN FOR 3RD K.
1638 006474 000742          BR       6$
1639 006476 063701 006346    3$:     ADD      K3,R1 ; INIT PATTERN FOR 4TH K.
1640 006502 000737          BR       6$
1641
1642 006504 000240    4$:     NOP
1643 006506 000240          NOP
1644 006510 013737 001106 001110  MOV      SLPADR,SLPERR ; SET FOR SWR9 OPTION IN 'SCOPE'
1645 006516 000410          BR       SEOP        ;;DONE, END-OF-PASS
1646
1647 ;*****
1648 006520 010237 001122    10$:   MOV      R2,$BADR    ; FAILING ADDRESS
1649 006524 010137 001124    MOV      R1,$GOODAT ; GOOD DATA
1650 006530 011237 001126    MOV      (R2),$BADAT ; BAD DATA
1651 006534 104010          ERROR    10          ; PRINT ERROR "ITEM 10".
1652 006536 000732          BR       11$        ; ...AND GO ON (SWR9=0).
1653 ; ...OR RETRY AT 5$ (SWR9=1)
1654 ;*****

```

.SBTTL END OF PASS ROUTINE

\*\*\*\*\*  
; INCREMENT THE PASS NUMBER (\$PASS)  
; \*TYPE "END PASS #XXXXX" (WHERE XXXXX IS A DECIMAL NUMBER)  
; \*IF THERES A MONITOR GO TO IT  
; \*IF THERE ISN'T JUMP TO AGAIN

SEOP:

SCOPE  
CLR \$SYSTEM ; ZERO THE TEST NUMBER  
CLR \$TIMES ; ZERO THE NUMBER OF ITERATIONS  
INC \$ ; INCREMENT THE PASS NUMBER  
BIC \$1, \$WORD, \$PASS ; DON'T ALLOW A NEG. NUMBER  
DEC (PC)+ ; LOOP?

SEOPCT:

.WORD 1  
BGT \$DOAGN ; YES  
MOV (PC)+, 2(PC)+ ; RESTORE COUNTER

SENDCT:

.WORD 1

SEOPCT  
TYPE \$SENDMG ; TYPE "END PASS #"  
MOV \$PASS, -(SP) ; SAVE \$PASS FOR TYPEOUT  
TYPOS ; GO TYPE--DECIMAL ASCII WITH SIGN  
TYPE \$SENUL ; TYPE A NULL CHARACTER

\$GET42:

MOV \$M42, R0 ; GET MONITOR ADDRESS  
BEQ \$DOAGN ; BRANCH IF NO MONITOR  
RESET ; CLEAR THE WORLD

SENDAD:

JSR PC, (R0) ; GO TO MONITOR  
NOP ; SAVE ROOM  
NOP ; FOR  
NOP ; ACT11

\$DOAGN:

JMP 2(PC)+ ; RETURN

\$RTNAD:

.WORD AGAIN

\$ENULL:

.BYTE -1, -1, 0 ; NULL CHARACTER STRING

\$SENDMG:

.ASCIZ <15><12>/END PASS #/

1655  
1656  
1657  
1658  
1659  
1660  
1661  
1662  
1663 006540  
1664 00 540 000004  
1665 00 542 00 037 001102  
1666 00 546 00 037 001160  
1667 00 0 00 237 001202  
1668 00 0 00 6 00 737 100000 001202  
1669 00 0 00 4 00 727  
1670 00 0 00 6 00 J01  
1671 00 0 00 370 003022  
1672 00 0 00 572 012737  
1673 00 0 00 574 000001  
1674 00 0 00 576 00 566  
1675 00 0 00 600 104401 006645  
1676 00 0 00 604 013746 001202  
1677 00 0 00 610 104405  
1678 00 0 00 612 104401 006642  
1679 00 0 00 616 013700 000042  
1680 00 0 00 62 001405  
1681 00 0 00 624 000005  
1682 00 0 00 626 004710  
1683 00 0 00 630 001240  
1684 00 0 00 634 00 240  
1685 00 0 00 638 000240  
1686 00 0 00 636  
1687 00 0 00 636 000137  
1688 00 0 00 640 002624  
1689 00 0 00 642 377 377 000  
1690 00 0 00 645 015 042412 042116  
1691 006652 050040 051501 020123  
1692 006660 000043



```

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1700
1701
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1703 006662 005711
1704 016664 005712
1705 016666 062716 000002
1706 006672 000207
1707 006674 000207
1708
1709
1710
1711
1712
1713
1714
1715 006676 022716 002740
1716 006702 001003
1717 006704 012716 002774
1718 006710 000002
1719
1720 006712 022716 002744
1721 006716 001003
1722 006720 012716 003024
1723 006724 000002
1724
1725 006726 022716 006252
1726 006732 001003
1727 006734 012716 006272
1728 006740 000002
1729
1730 006742 022716 006664
1731 006746 001403
1732 016750 022716 006666
1733 006754 001003
1734 006756 012716 006674
1735 006762 000002
1736
1737 006764
1738 006764 012637 001122
1739 006770 012637 001126
1740 006774 012737 007004 001110
1741 007002 104013
1742
1743
1744 007004 000000
1745 007006 000137 001436
    
```

```

.SBTTL COMMON SUBROUTINES AND TABLES
*****
THIS ROUTINE WILL TEST FOR A NEXM IN THE RANGE (R1) THRU (R2).

CALL:  MOV ADR1,R1
      MOV ADR2,R2
      JSR PC,NXM
      RETURN IF BUSERR TRAP
      RETURN IF NO-TRAP

NXM:   TST (R1)          ;TEST THE ADDRESSES.
      TST (R2)          ;...IF EITHER TRAP, RETURN TO NXM1.
      ADD #2,(SP)       ;IF NOT, BUMP RETURN PC...
      RTS PC            ;...AND RETURN TO CALL+6.
NXM1:  RTS PC           ;...ONE OR THE OTHER TRAPPED...
      ;...RETURN TO CALL+4.

*****
BUS TIME-OUT PROCESSOR.  BUS ERRORS ARE EXPECTED DURING
INITIALIZATION, AND IN TESTS 1 AND 12.
IF ANY OTHER, REPORT BUS-ERROR, AND HALT.

TIMEOUT:  CMP #ADR1,(SP) ;TST1 READ TRAP ??
          BNE 15          ;NO
          MOV #RTN1,(SP) ;YES, ADJUST RETURN PC
          RTI            ;AND RETURN THERE.

15:       CMP #WRT1,(SP) ;TST1 WRITE TRAP ??
          BNE 25          ;NO
          MOV #RTN2,(SP) ;YES...
          RTI            ;...RETURN THERE

25:       CMP #WRT12,(SP);TST12 WRITE TRAP ??
          BNE 35          ;NO
          MOV #WTRTN,(SP);YES...
          RTI            ;...RETURN THERE.

35:       CMP #NXM+2,(SP);NEXM ROUTINE TRAP 1 ??
          BEQ 45          ;YES
          CMP #NXM+4,(SP);NO, NEXM TRAP 2 ??
          BNE 55          ;NO
          MOV #NXM1,(SP) ;RETURN TO NEXM ROUTINE.

45:       RTI

55:       MOV (SP)+,$B0ADR ;POP STACK INTO $B0ADR
          MOV (SP)+,$B0DAT ;POP STACK INTO $B0DAT
          MOV #STOP,$LPERR ;OVERRIDE SWR9 OPTION.
          ERROR 13        ;REPORT BUS ERROR...
          ;...$B0ADR = TRAP PC
          ;...$B0DAT = TRAP PS

STOP:    HALT
          JMP START      ;...RESTART ON "CONTINUE".
    
```

1746  
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007012 007024  
007014 007026  
007016 000020  
007020 000000  
007022 000 000  
007024 000020  
007064 000020  
  
001456  
012737 177777 001434  
012706 001100  
004737 001456  
005037 007022  
004737 002670  
005037 001434  
000000  
000137 007124

\*\*\*\*\*  
MEMORY MAP FOR TEST 1.  
THE MAP CONSISTS OF 2 WORDS FOR EACH 4K BANK THRU 32K.  
WORD 1 IS THE READ MAP WHERE EACH BIT REPRESENTS 1 256 WORD  
SEGMENT OF THE BANK. EACH BIT IS SET 0 IF THAT SEGMENT IS  
"NEXT", SET 1 OTHERWISE.

WORD 2 IS THE WRITE MAP FOR THE SAME BANK. EACH BIT IS  
SET 0 IF THAT SEGMENT IS "READ-ONLY" OR "NEXT", SET 1 OTHERWISE.

BIT# TO ADDRESS CORRESPONDENCE IS AS FOLLOWS:

BIT #	RANGE
15	0 - 776
14	1000 - 1776
13	2000 - 2776
12	3000 - 3776
11	4000 - 4776
10	5000 - 5776
09	6000 - 6776
08	7000 - 7776
07	10000 - 10776
06	11000 - 11776
05	12000 - 12776
04	13000 - 13776
03	14000 - 14776
02	15000 - 15776
01	16000 - 16776
00	17000 - 17776

RMAP: .WORD MEMMAP ;POINTS TO 1ST WORD IN PAIR.  
WMAP: .WORD MEMMAP+2 ;POINTS TO 2ND WORD IN PAIR.  
MAPK: .WORD 16. ;BIT (SEGMENT) COUNTER.  
LOC: .WORD 0 ;POINTS TO LOC TO TEST.  
MAPSW: .BYTE 0,0 ;LO BYTE IS TYPE/INHIB SWITCH  
;HI BYTE IS COPY/COMPARE SWITCH  
MEMMAP: .BLKW 16. ;THIS IS THE WORKING (SCRATCH) MAP.  
;2 WORDS/4K BANK AS DESCRIBED ABOVE.  
SAVMAP: .BLKW 16. ;INITIAL COPY OF "MEMMAP" IS SAVED  
;HERE AS THE REFERENCE MAP.

\*\*\*\*\*  
SUBROUTINE TO DUMP A 32K MAP AND HALT. START AT LOC 220.

STARTM=START+20  
MDUMP: MOV @-1,MAINT ;SET MAINT FLAG.  
MOV @STACK,SP ;INIT THE STACK  
JSR PC,STARTM ;INIT VECTORS, AND RETURN.  
CLR MAPSW ;ENABLE MAP OUTPUT.  
JSR PC,TST1A ;...DO IT...  
CLR MAINT ;...RETURN...  
HALT ;...AND HALT.  
JMP MDUMP

```

1800 .....
1801 : THIS ROUTINE WILL OUTPUT ANY GIVEN MEM RANGE TO THE
1802 : CONSOLE TTY.
1803
1804 : START AT LOC 210 TO DUMP OCTAL,
1805 : AND LOC 214 TO DUMP BINARY.
1806
1807 : OPERATOR IS REQUIRED TO INPUT FIRST ADDRESS,
1808 : AND NUMBER OF WORDS TO DUMP.
1809
1810 : AN ODD ADDRESS INPUT WILL BE DECREMENTED BY 1, BEFORE USE.
1811 : WORD COUNT INPUT MUST BE NON-ZERO.
1812 : RESTART DUMPER AT END.
1813
1814 007164 000000 000000 000000 000000
1815 007166 000000 000000 000000 000000
1816 007170 000000 000000 000000 000000
1817 007172 000000 000000 000000 000000
1818
1819 007174 012737 177777 001434 00DUMP: MOV      0-1,MAINT      ;SET MAINT FLAG
1820 007202 012706 001100 001434 00DUMP: MOV      #STACK,SP      ;INIT THE STACK
1821 007206 004737 001456 001434 00DUMP: JSR      PC,STARTM     ;INIT VECTORS, AND RETURN
1822 007212 004737 001434 001434 00DUMP: CLR      MAINT
1823 007216 104401 007224 007224 00DUMP: TYPE     65$          ;:TYPE ASCIZ STRING
1824 007222 000417 007224 007224 00DUMP: BR       64$          ;:GET OVER THE ASCIZ
1825
1826 007262 000417 007224 007224 00DUMP: .ASCIZ  <CRLF><CRLF>/OCTAL MEMORY DUMP ROUTINE./<CRLF>
1827 007262 005037 007164 007172 00DUMP: CLR      MODE          ;: 0 = OCTAL DUMP
1828 007266 012737 000004 007172 00DUMP: MOV      #4,LINE       ;:WITH 4 WORDS PER LINE.
1829 007274 000442 007172 007172 00DUMP: BR       DSTRT
1830
1831 007276 012737 177777 001434 00DUMP: MOV      0-1,MAINT      ;SET MAINT FLAG
1832 007304 012706 001100 001434 00DUMP: MOV      #STACK,SP      ;INIT THE STACK
1833 007310 004737 001456 001434 00DUMP: JSR      PC,STARTM     ;INIT VECTORS, AND RETURN
1834 007314 005037 001434 001434 00DUMP: CLR      MAINT
1835 007320 104401 007326 007326 00DUMP: TYPE     65$          ;:TYPE ASCIZ STRING
1836 007324 000420 007326 007326 00DUMP: BR       64$          ;:GET OVER THE ASCIZ
1837
1838 007366 000420 007326 007326 00DUMP: .ASCIZ  <CRLF><CRLF>/BINARY MEMORY DUMP ROUTINE./<CRLF>
1839 007366 012737 177777 007164 00DUMP: MOV      0-1,MODE       ;:-1 = BINARY DUMP
1840 007374 012737 000002 007172 00DUMP: MOV      #2,LINE       ;:WITH 2 WORDS PER LINE.
1841
1842 007402 000411 007410 007410 00DUMP: CLR      MAINT
1843 007402 104401 007410 007410 00DUMP: TYPE     65$          ;:TYPE ASCIZ STRING
1844 007406 000411 007410 007410 00DUMP: BR       64$          ;:GET OVER THE ASCIZ
1845
1846 007432 000411 007410 007410 00DUMP: .ASCIZ  <CRLF>/FIRST ADDRESS = /
1847 007432 104413 007166 007166 00DUMP: RDOCT
1848 007434 012637 007166 007166 00DUMP: MOV      (SP)+,ADDRS    ;:RECEIVE HIS RESPONSE.
1849 007440 042737 000001 007166 00DUMP: BIC      #BIT0,ADDRS   ;:POP STACK INTO ADDRS
1850
1851 007446 000414 007454 007454 00DUMP:
1852 007446 104401 007454 007454 00DUMP: TYPE     67$          ;:TYPE ASCIZ STRING
1853 007452 000414 007454 007454 00DUMP: BR       66$          ;:GET OVER THE ASCIZ
    
```

```

1854      ;:67S: .ASCIZ <CRLF>/WORD COUNT (OCTAL) = /
1855      66S:
1856      RDOCT
1857      MOV (SP)+,WORDS
1858      BEQ 2S
1859
1860      MOV ADDR5,R1
1861      MOV WORDS,R2
1862      RSL R2
1863      ADD #2,R2
1864      ADD R1,R2
1865      JSR PC,NXM
1866      BR 10S
1867      BR DMPGO
1868
1869      10S: TYPE M3
1870      BR DSTRT
1871
1872      DMPGO: TYPE $CRLF
1873      MOV ADDR5,-(SP)
1874      TYP0S
1875      .BYTE 6
1876      .BYTE 0
1877      MOV LINE,R1
1878
1879      3S: TYPE SPACE3
1880      MOV ADDR5,-(SP)
1881      TST MODE
1882      BEQ 1S
1883      TYPBN
1884      BR 2S
1885
1886      1S: TYPE OCTAL DATA.
1887      2S: ADD #2,ADDR5
1888      DEC WORDS
1889      BEQ 4S
1890
1891      DEC R1
1892      BEQ DMPGO
1893      BR 3S
1894
1895      4S: TYPE , $CRLF
1896      NOP
1897      BR DSTRT
1898
1899      040 040 040 SPACE3: .BYTE 40,40,40,0
1900      000
    
```

```

;GET THE NUMBER
;POP STACK INTO WORDS
;CANT USE 0, ASK FOR ANOTHER.

;FIRST ADDRESS IN R1.
;WORD COUNT...
;...TIMES 2 = BYTE COUNT.
;ADJUST
;PLUS (R1) = LAST ADDR IN R2.
;SEE IF THE RANGE IS VALID.
;ITS NOT, RANGE IS NEXM.
;IT IS, START DUMPING.

;INVALID DUMP RANGE.
;TRY AGAIN.

;PUSH ADDR5 ON STACK
;TYPE THE ADDRESS.

;SUPPRESS LEAD ZEROS.
;SET WORDS/LINE COUNT

;3 SPACES BETWEEN WORDS
;PUSH ADDR5 ON STACK

;TYPE BINARY DATA.

;TYPE OCTAL DATA.
;BUMP ADDRESS POINTER.
;AND WORD COUNT
;QUIT WHEN WC = 0.

;BUMP WORDS/LINE COUNT
;START A NEW LINE
;NEXT WORD, THIS LINE

;RESTART ON 'CONT'

;3 SPACES
    
```

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007654  
007654 104410  
007656 032777 040000 171254  
007664 001131  
007666 000416  
007670 013746 000004  
007674 012737 007714 000004  
007702 005737 177060  
007706 012637 000004  
007712 001500  
007714 021226  
007716 012637 000004  
007722 000440  
007724  
007724 032777 000400 171206  
007732 001421  
007734 005046  
007736 117716 171176  
007742 001414  
007744 022716 000013  
007750 002411  
007752 011637 001102  
007756 005316  
007760 006316  
007762 062716 010166  
007766 013637 001106  
007772 000466  
007774 005726  
007776 105737 001103  
010002 001421  
010004 123737 001115 001103  
010012 101015  
010014 032777 001000 171116  
010022 001404  
010024 013737 001110 001106  
010032 000446  
010034 105037 001103

```

.SBTTL SYSMAC UTILITIES FOLLOW:
.SBTTL SCOPE HANDLER ROUTINE

*****
#THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
#AND LOAD THE TEST NUMBER(STSTNM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
#AND LOAD THE ERROR FLAG (SERFLG) INTO DISPLAY<15:08>
#THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
#SM14=1 LOOP ON TEST
#SM11=1 INHIBIT ITERATIONS
#SM09=1 LOOP ON ERROR
#SM08=1 LOOP ON TEST IN SWR<7:0>
#CALL
;* SCOPE ;;SCOPE=IOT

$SCOPE:
CKSWR
1$: BIT #BIT14,2SWR ;;TEST FOR CHANGE IN SOFT-SWR
BNE $OVER ;;LOOP ON PRESENT TEST?
;;YES IF SM14=1
:####START OF CODE FOR THE XOR TESTER####
$XTSTR: BR 6$ ;;IF RUNNING ON THE "XOR" TESTER CHANGE
;;THIS INSTRUCTION TO A "NOP" (NOP=240)
;;SAVE THE CONTENTS OF THE ERROR VECTOR
MOV 2$ERRVEC, -(SP) ;;SET FOR TIMEOUT
MOV 5$ 2$ERRVEC ;;TIME OUT ON XOR?
TST 2$177060 ;;RESTORE THE ERROR VECTOR
MOV (SP)+, 2$ERRVEC ;;GO TO THE NEXT TEST
BR $SVLAD ;;CLEAR THE STACK AFTER A TIME OUT
5$: CMP (SP)+, (SP)+ ;;RESTORE THE ERROR VECTOR
MOV (SP)+, 2$ERRVEC ;;LOOP ON THE PRESENT TEST
BR 7$ TESTER####
6$:;####END OF CODE FOR THE XOR TESTER####
BIT #BIT08,2SWR ;;LOOP ON SPEC. TEST?
BEQ 2$ BR IF NO
CLR -(SP) ;;CLEAR A TEMP. LOCATION
MOVB 2$SWR, (SP) ;;PICKUP THE DESIRED TEST NUMBER
BEQ 8$ BRANCH IF BAD TEST NUMBER IN SWR
CMP #13, (SP) ;;CHECK THE NUMBER IN THE SWR
BLT 8$ BRANCH IF TEST NUMBER IS OUT OF RANGE
MOV (SP), STSTNM ;;UPDATE THE TEST NUMBER
DEC (SP) ;;BACKUP BY ONE
ASL (SP) ;;SCALE THE TEST NUMBER AS AN INDEX
ADD #$$SW08TBL, (SP) ;;FORM THE ADDRESS OF TEST POINTER
MOV 2$(SP)+, $LPADR ;;SET LOOP ADDRESS TO DESIRED TEST
BR $OVER ;;GO LOOP ON THE TEST
8$: TST (SP)+ ;;CLEAN THE BAD TEST NUMBER OFF OF THE STACK
2$: TSTB $SERFLG ;;HAS AN ERROR OCCURRED?
BEQ 3$ BR IF NO
CMPB $SERMAX, $SERFLG ;;MAX. ERRORS FOR THIS TEST OCCURRED?
BHI 3$ BR IF NO
BIT #BIT09,2SWR ;;LOOP ON ERROR?
BEQ 4$ BR IF NO
MOV $LPERR, $LPADR ;;SET LOOP ADDRESS TO LAST SCOPE
BR $OVER
4$: CLRB $SERFLG ;;ZERO THE ERROR FLAG
    
```

1955	010040	005037	001160		CLR	\$TIMES	:: CLEAR THE NUMBER OF ITERATIONS TO MAKE
1956	010044	000415			BR	IS	:: ESCAPE TO THE NEXT TEST
1957	010046	032777	004000	171064	3S: BIT	#BIT11,2SWR	:: INHIBIT ITERATIONS?
1958	010054	001011			BNE	IS	:: BR IF YES
1959	010056	005737	001202		TST	\$PASS	:: IF FIRST PASS OF PROGRAM
1960	010062	001406			BEQ	IS	:: INHIBIT ITERATIONS
1961	010064	005237	001104		INC	\$ICNT	:: INCREMENT ITERATION COUNT
1962	010070	023737	001160	001104	CHP	\$TIMES,\$ICNT	:: CHECK THE NUMBER OF ITERATIONS MADE
1963	010076	002024			BGE	\$OVER	:: BR IF MORE ITERATION REQUIRED
1964	010100	012737	000001	001104	1S: MOV	#1,\$ICNT	:: REINITIALIZE THE ITERATION COUNTER
1965	010106	013737	010164	001160	MOV	\$MXCNT,\$TIMES	:: SET NUMBER OF ITERATIONS TO DO
1966	010114	105237	001102		SSVLAD: INCB	\$STNH	:: COUNT TEST NUMBERS
1967	010120	113737	001102	001200	MOVB	\$STNH,\$TESTN	:: SET TEST NUMBER IN APT MAILBOX
1968	010126	01.637	001106		MOV	(SP),\$LPADR	:: SAVE SCOPE LOOP ADDRESS
1969	010132	011637	001110		MOV	(SP),\$LPERR	:: SAVE ERROR LOOP ADDRESS
1970	010136	005037	001162		CLR	\$ESCAPE	:: CLEAR THE ESCAPE FROM ERROR ADDRESS
1971	010142	112737	000001	001115	MOVB	#1,\$ERMAX	:: ONLY ALLOW ONE(1) ERROR ON NEXT TEST
1972	010150	013777	001102	170764	SOVER: MOV	\$STNH,\$DISPLAY	:: DISPLAY TEST NUMBER
1973	010156	013716	001106		MOV	\$LPADR,(SP)	:: FUDGE RETURN ADDRESS
1974	010162	000002			RTI		:: FIXES PS
1975	010164	003720			\$MXCNT: 2000.		:: MAX. NUMBER OF ITERATIONS
1976	010166				\$SWOBTBL:		
1977	010166	002632			.WORD	TST1+2	:: STARTING ADDRESS OF TEST 1
1978	010170	003364			.WORD	TST2+2	:: STARTING ADDRESS OF TEST 2
1979	010172	003546			.WORD	TST3+2	:: STARTING ADDRESS OF TEST 3
1980	010174	003662			.WORD	TST4+2	:: STARTING ADDRESS OF TEST 4
1981	010176	004766			.WORD	TST5+2	:: STARTING ADDRESS OF TEST 5
1982	010200	005024			.WORD	TST6+2	:: STARTING ADDRESS OF TEST 6
1983	010202	005372			.WORD	TST7+2	:: STARTING ADDRESS OF TEST 7
1984	010204	005540			.WORD	TST10+2	:: STARTING ADDRESS OF TEST 10
1985	010206	006030			.WORD	TST11+2	:: STARTING ADDRESS OF TEST 11
1986	010210	006214			.WORD	TST12+2	:: STARTING ADDRESS OF TEST 12
1987	010212	006316			.WORD	TST13+2	:: STARTING ADDRESS OF TEST 13

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010214  
010214 104410  
010216 105237 001103  
010222 001775  
010224 013777 001102 170710  
010232 032777 002000 170700  
010240 001402  
010242 104401 001164  
010246 005237 001112  
010252 011637 001116  
010256 162737 000002 001116  
010264 117737 170626 001114  
010272 032777 020000 170640  
010300 001004  
010302 004737 010414  
010306 104401 001171  
010312  
010312 122737 000001 001214  
010320 001007  
010322 113737 001114 010334  
010330 004737 012670  
010334 000  
010335 000  
010336 000777  
010340 005777 170574  
010344 100002  
010346 000000  
010350 104410  
010352 032777 001000 170560  
010360 001402  
010362 013716 001110  
010366 005737 001162  
010372 001402  
010374 013716 001162  
010400  
010400 022737 006626 000042  
010406 001001  
010410 000000  
010412  
010412 000002

.SBTTL ERROR HANDLER ROUTINE

\*\*\*\*\*  
\*THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT,  
\*SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL,  
\*AND GO TO STARTYP ON ERROR.  
\*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:  
\*SW15=1 HALT ON ERROR  
\*SW13=1 INHIBIT ERROR TYPEOUTS  
\*SW10=1 BELL ON ERROR  
\*SW09=1 LOOP ON ERROR  
\*CALL  
\* ERROR N ;;ERROR=EMT AND N=ERROR ITEM NUMBER

ERROR:

75: CKSWR ;; TEST FOR CHANGE IN SOFT-SWR  
INCB SERFLG ;; SET THE ERROR FLAG  
BEQ 75 ;; DON'T LET THE FLAG GO TO ZERO  
MOV STSTNM,20ISPLAY ;; DISPLAY TEST NUMBER AND ERROR FLAG  
BIT #BIT10,2SWR ;; BELL ON ERROR?  
BEQ 15 ;; NO - SKIP  
TYPE SBELL ;; RING BELL  
15: INC SERITL ;; COUNT THE NUMBER OF ERRORS  
MOV (SP),SERAPC ;; GET ADDRESS OF ERROR INSTRUCTION  
SUB #2,SERAPC  
MOVB #SERAPC,SITEMB ;; STRIP AND SAVE THE ERROR ITEM CODE  
BIT #BIT13,2SWR ;; SKIP TYPEOUT IF SET  
BNE 20S ;; SKIP TYPEOUTS  
JSR PC,SERTYP ;; GO TO USER ERROR ROUTINE  
TYPE ,SCLF  
20S: CMPB #APTEMV,SENV ;; RUNNING IN APT MODE  
BNE 25 ;; NO SKIP APT ERROR REPORT  
MOVB SITEMB,21S ;; SET ITEM NUMBER AS ERROR NUMBER  
JSR PC,SATY4 ;; REPORT FATAL ERROR TO APT  
21S: .BYTE 0  
.BYTE 0  
22S: BR 22S ;; APT ERROR LOOP  
TST 2SWR ;; HALT ON ERROR  
25: BPL 35 ;; SKIP IF CONTINUE  
HALT ;; HALT ON ERROR!  
CKSWR ;; TEST FOR CHANGE IN SOFT-SWR  
35: BIT #BIT09,2SWR ;; LOOP ON ERROR SWITCH SET?  
BEQ 45 ;; BR IF NO  
MOV \$LPERR,(SP) ;; FUDGE RETURN FOR LOOPING  
45: TST \$ESCAPE ;; CHECK FOR AN ESCAPE ADDRESS  
BEQ 55 ;; BR IF NONE  
MOV \$ESCAPE,(SP) ;; FUDGE RETURN ADDRESS FOR ESCAPE  
55: CMP #SENDAD,2#42 ;; ACT-11 AUTO-ACCEPT?  
BNE 65 ;; BRANCH IF NO  
HALT ;; YES  
65: RTI ;; RETURN

```

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2050 010414 104401 001171
2051 010420 010046
2052 010422 005300
2053 010424 153700 001114
2054 010430 001004
2055
2056 010432 013746 001116
2057
2058 010436 104402
2059 010440 000426
2060 010442 005300
2061 010444 006300
2062 010446 006300
2063 010450 006300
2064 010452 062700 001260
2065 010456 012037 010466
2066 010462 001404
2067 010464 104401
2068 010466 000000
2069 010470 104401 001171
2070 010474 012037 010504
2071 010500 001404
2072 010502 104401
2073 010504 000000
2074 010506 104401 001171
2075 010512 011000
2076 010514 001004
2077 010516 012600
2078 010520 104401 001171
2079 010524 000207
2080 010526
2081 010528 013046
2082 010530 104402
2083 010532 005710
2084 010534 001770
2085 010536 104401 010544
2086 010542 000771
2087 010544 020040 000
2088 010550
    
```

.SBTTL ERROR MESSAGE TYPEOUT ROUTINE

```

*****
*THIS ROUTINE USES THE "ITEM CONTROL BYTE" ($ITEMB) TO DETERMINE WHICH
*ERROR IS TO BE REPORTED. IT THEN OBTAINS, FROM THE "ERROR TABLE" ($ERRTB),
*AND REPORTS THE APPROPRIATE INFORMATION CONCERNING THE ERROR.
    
```

\$ERRTYP:

```

        TYPE      $CRLF          ;; "CARRIAGE RETURN" & "LINE FEED"
        MOV        RO, -(SP)      ;; SAVE RO
        CLR        RO             ;; PICKUP THE ITEM INDEX
        BISB       @#$ITEMB, RO
        BNE        IS
        MOV        $ERRPC, -(SP)  ;; IF ITEM NUMBER IS ZERO, JUST
        ;; TYPE THE PC OF THE ERROR
        ;; SAVE $ERRPC FOR TYPEOUT
        ;; ERROR ADDRESS
        TYPOC      ;; GO TYPE--OCTAL ASCII(ALL DIGITS)
        BR         6$            ;; GET OUT
        IS:        DEC            ;; ADJUST THE INDEX SO THAT IT WILL
        ;; ASL            ;; WORK FOR THE ERROR TABLE
        ASL        RO
        ASL        RO
        ASL        RO
        ADD        @#$ERRTB, RO   ;; FORM TABLE POINTER
        MOV        (RO)+, 2$     ;; PICKUP "ERROR MESSAGE" POINTER
        BEQ        3$           ;; SKIP TYPEOUT IF NO POINTER
        TYPE       "ERROR MESSAGE"
        ;; "ERROR MESSAGE" POINTER GOES HERE
        2$:        .WORD        0
        TYPE       $CRLF        ;; "CARRIAGE RETURN" & "LINE FEED"
        3$:        MOV        (RO)+, 4$
        BEQ        5$           ;; PICKUP "DATA HEADER" POINTER
        ;; SKIP TYPEOUT IF 0
        TYPE       "DATA HEADER"
        4$:        .WORD        0
        TYPE       $CRLF        ;; "CARRIAGE RETURN" & "LINE FEED"
        5$:        MOV        (RO), RO
        BEQ        7$           ;; PICKUP "DATA TABLE" POINTER
        GO         TYPE THE DATA
        6$:        MOV        (SP)+, RO
        RESTORE   RO
        TYPE       $CRLF        ;; "CARRIAGE RETURN" & "LINE FEED"
        7$:        RTS        PC
        ;; RETURN
        MOV        @2(RO)+, -(SP) ;; SAVE @2(RO)+ FOR TYPEOUT
        TYPOC      ;; GO TYPE--OCTAL ASCII(ALL DIGITS)
        TST        (RO)         ;; IS THERE ANOTHER NUMBER?
        BR         6$          ;; BR IF NO
        TYPE       TWO(2) SPACES
        8$:        BR         7$
        ;; LOOP
        .ASCIZ    "/ /"
        .EVEN
    
```



.SBTTL TYPE ROUTINE

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010550 105737 001157  
010554 100002  
010556 000000  
010560 000430  
010562 010046  
010564 017600 000002  
010570 122737 000001 001214  
010576 001011  
010600 132737 000100 001215  
010606 001405  
010610 010037 010620  
010614 004737 012660  
010620 000000  
010622 132737 000040 001215  
010630 001003  
010632 112046  
010634 001005  
010636 005726  
010640 012600  
010642 062716 000002  
010646 000002  
010650 122716 000011  
010654 001430  
010656 122716 000200  
010664 001006  
010666 005726  
010670 104401  
010672 001171  
010672 105037 011026  
010676 000755  
010700 004737 010762  
010704 123726 001156  
010710 001350  
010712 013746 001154  
010716 105366 000001  
010722 002770

```
*****
#ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
#THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
#NOTE1: $NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
#NOTE2: $FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
#NOTE3: $FILLC CONTAINS THE CHARACTER TO FILL AFTER.
#
#CALL:
#1) USING A TRAP INSTRUCTION
#      TYPE      ,MESADR      ;;MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
#OR
#      TYPE
#      MESADR
#
STYPE:  TSTB      $TPFLG      ;; IS THERE A TERMINAL?
        BPL      1$          ;; BR IF YES
        HALT     0$          ;; HALT HERE IF NO TERMINAL
        BR       3$          ;; LEAVE
1$:     MOV      RO, -(SP)    ;; SAVE RO
        MOV      22(SP), RO  ;; GET ADDRESS OF ASCIZ STRING
        CMPB    #APTENV, $ENV ;; RUNNING IN APT MODE
        BNE     62$         ;; NO, GO CHECK FOR APT CONSOLE
        BITB    #APTSPOOL, $ENVM ;; SPOOL MESSAGE TO APT
        BEQ     62$         ;; NO, GO CHECK FOR CONSOLE
        MOV     RO, 61$      ;; SETUP MESSAGE ADDRESS FOR APT
        JSR    PC, $ATY3    ;; SPOOL MESSAGE TO APT
        .WORD   0           ;; MESSAGE ADDRESS
        BITB    #APTCSUP, $ENVM ;; APT CONSOLE SUPPRESSED
        BNE     60$         ;; YES, SKIP TYPE OUT
        MOVB   (RO)+, -(SP)  ;; PUSH CHARACTER TO BE TYPED ONTO STACK
        BNE     4$          ;; BR IF IT ISN'T THE TERMINATOR
        TST    (SP)+        ;; IF TERMINATOR POP IT OFF THE STACK
        MOV     (SP)+, RO    ;; RESTORE RO
        RDB    #2, (SP)     ;; ADJUST RETURN PC
        RTI                    ;; RETURN
4$:     CMPB    #HT, (SP)    ;; BRANCH IF <HT>
        BEQ     8$          ;; BRANCH IF NOT <CRLF>
        CMPB   #CRLF, (SP)
        BNE     5$          ;; POP <CR><LF> EQUIV
        TST    (SP)+        ;; TYPE A CR AND LF
        TYPE   $CHARCNT    ;; CLEAR CHARACTER COUNT
        BR     2$          ;; GET NEXT CHARACTER
5$:     JSR    PC, $TYPEC    ;; GO TYPE THIS CHARACTER
        CMPB   $FILLC, (SP)+ ;; IS IT TIME FOR FILLER CHARS.?
        BNE     2$         ;; IF NO GO GET NEXT CHAR.
        MOV     $NULL, -(SP) ;; GET # OF FILLER CHARS. NEEDED
        AND    1(SP)       ;; AND THE NULL CHAR.
        DECB   1(SP)       ;; DOES A NULL NEED TO BE TYPED?
        BLT    6$          ;; BR IF NO--GO POP THE NULL OFF OF STACK
6$:     JSR    PC, $TYPEC    ;; GO TYPE THIS CHARACTER
        CMPB   $FILLC, (SP)+ ;; IS IT TIME FOR FILLER CHARS.?
        BNE     2$         ;; IF NO GO GET NEXT CHAR.
        MOV     $NULL, -(SP) ;; GET # OF FILLER CHARS. NEEDED
        AND    1(SP)       ;; AND THE NULL CHAR.
        DECB   1(SP)       ;; DOES A NULL NEED TO BE TYPED?
        BLT    6$          ;; BR IF NO--GO POP THE NULL OFF OF STACK
```

```

2143 010724 004737 010762      JSR      PC,$TYPEC      ;; GO TYPE A NULL
2144 010730 105337 011026      DECB    $CHARCNT      ;; DO NOT COUNT AS A COUNT
2145 010734 000770              BR       7$           ;; LOOP
2146
2147      ;HORIZONTAL TAB PROCESSOR
2148
2149 010736 112716 000040      8$:     MOVB    #' (SP)      ;; REPLACE TAB WITH SPACE
2150 010742 004737 010762      9$:     JSR     PC,$TYPEC      ;; TYPE A SPACE
2151 010746 132737 000007 011026  BITB    #7,$CHARCNT      ;; BRANCH IF NOT AT
2152 010754 011372              BNE     9$           ;; TAB STOP
2153 010756 0 5726              TST     (SP)+         ;; POP SPACE OFF STACK
2154 010760 010724              BR      2$           ;; GET NEXT CHARACTER
2155 010762 105777 170162      $TYPEC: TSTB    2$TPS        ;; WAIT UNTIL PRINTER IS READY
2156 010766 100375              BPL     $TYPEC
2157 010770 116677 170154      MOVB    2(SP),2$TPB      ;; LOAD CHAR TO BE TYPED INTO DATA REG.
2158 010776 122766 100015 000002  CMPB    #CR,2(SP)        ;; IS CHARACTER A CARRIAGE RETURN?
2159 011004 001003              BNE     1$           ;; BRANCH IF NO
2160 011006 105037 011076      CLRB    $CHARCNT        ;; YES--CLEAR CHARACTER COUNT
2161 011012 000406              BR      $TYPEX
2162 011014 122766 000012 000002  1$:     CMPB    #LF,2(SP)   ;; IS CHARACTER A LINE FEED?
2163 011022 001402              BEQ    $TYPEX          ;; BRANCH IF YES
2164 011024 105227              INCB   (PC)+           ;; COUNT THE CHARACTER
2165 011026 000000      $CHARCNT: .WORD 0      ;; CHARACTER COUNT STORAGE
2166 011030 000207      $TYPEX: RTS           PC
2167

```

```

2168 .SBTTL CONVERT BINARY TO DECIMAL AND TYPE ROUTINE
2169
2170 *****
2171 *THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 5-DIGIT
2172 *SIGNED DECIMAL (ASCII) NUMBER AND TYPE IT. DEPENDING ON WHETHER THE
2173 *NUMBER IS POSITIVE OR NEGATIVE A SPACE OR A MINUS SIGN WILL BE TYPED
2174 *BEFORE THE FIRST DIGIT OF THE NUMBER. LEADING ZEROS WILL ALWAYS BE
2175 *REPLACED WITH SPACES.
2176 *CALL:
2177 *      MOV      NUM,-(SP)      ;;PUT THE BINARY NUMBER ON THE STACK
2178 *      TYPDS                    ;;GO TO THE ROUTINE
2179
2180 STYPDS:
2181 MOV      R0,-(SP)      ;;PUSH R0 ON STACK
2182 MOV      R1,-(SP)      ;;PUSH R1 ON STACK
2183 MOV      R2,-(SP)      ;;PUSH R2 ON STACK
2184 MOV      R3,-(SP)      ;;PUSH R3 ON STACK
2185 MOV      R5,-(SP)      ;;PUSH R5 ON STACK
2186 MOV      #20200,-(SP)    ;;SET BLANK SWITCH AND SIGN
2187 MOV      20(SP),R5     ;;GET THE INPUT NUMBER
2188 BPL      1$           ;;BR IF INPUT IS POS.
2189 NEG      R5           ;;MAKE THE BINARY NUMBER POS.
2190 MOV      #'-,1(SP)     ;;MAKE THE ASCII NUMBER NEG.
2191 CLRB    R0           ;;ZERO THE CONSTANTS INDEX
2192 MOV      #SOBLK,R3     ;;SETUP THE OUTPUT POINTER
2193 MOV      #' ,(R3)+     ;;SET THE FIRST CHARACTER TO A BLANK
2194 CLRB    R2           ;;CLEAR THE BCD NUMBER
2195 MOV      #OTBL(R0),R1  ;;GET THE CONSTANT
2196 SUB     R1,R5        ;;FORM THIS BCD DIGIT
2197 BLT     4$           ;;BR IF DONE
2198 INC     R2           ;;INCREASE THE BCD DIGIT BY 1
2199 BR      3$
2200 ADD     R1,R5        ;;ADD BACK THE CONSTANT
2201 TST     R2           ;;CHECK IF BCD DIGIT=0
2202 BNE     5$           ;;FALL THROUGH IF 0
2203 TSTB   (SP)         ;;STILL DOING LEADING 0'S?
2204 BMI     7$           ;;BR IF YES
2205 ASLB   (SP)         ;;MSD?
2206 BCC     6$           ;;BR IF NO
2207 MOV      1(SP),-1(R3)  ;;YES--SET THE SIGN
2208 BIS     #'0,R2       ;;MAKE THE BCD DIGIT ASCII
2209 BIS     #' ,R2       ;;MAKE IT A SPACE IF NOT ALREADY A DIGIT
2210 MOV      R2,(R3)+    ;;PUT THIS CHARACTER IN THE OUTPUT BUFFER
2211 TST     (R0)+        ;;JUST INCREMENTING
2212 CMP     R0,#10      ;;CHECK THE TABLE INDEX
2213 BLT     2$           ;;GO DO THE NEXT DIGIT
2214 BGT     8$           ;;GO TO EXIT
2215 MOV     R5,R2       ;;GET THE LSD
2216 BR      6$
2217 TSTB   (SP)+        ;;GO CHANGE TO ASCII
2218 BPL     9$           ;;WAS THE LSD THE FIRST NON-ZERO?
2219 MOV      -1(SP),-2(R3) ;;BR IF NO
2220 CLRB   (R3)         ;;YES--SET THE SIGN FOR TYPING
2221 MOV     (SP)+,R5    ;;SET THE TERMINATOR
                       ;;POP STACK INTO R5
    
```

```

2222 011210 012603      MOV      (SP)+,R3      ;; POP STACK INTO R3
2223 011212 012602      MOV      (SP)+,R2      ;; POP STACK INTO R2
2224 011214 012601      MOV      (SP)+,R1      ;; POP STACK INTO R1
2225 011216 012600      MOV      (SP)+,R0      ;; POP STACK INTO R0
2226 011220 104401      TYPE     $DBLK        ;; NOW TYPE THE NUMBER
2227 011224 016666      MOV      2(SP),4(SP)  ;; ADJUST THE STACK
2228 011232 012616      MOV      (SP)+,(SP)
2229 011234 000002      RTI
2230 011236 023420      SOTBL:  10000.
2231 011240 001750      1000.
2232 011242 000144      100.
2233 011244 000012      10.
2234 011246 000004      SDBLK:  .BLKW  4
  
```

```

2235
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2240
2241
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2247
2248
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2250
2251
2252
2253
2254
2255
2256
2257
2258
2259
2260 011256 017646 000000
2261 011262 116637 000001 011501
2262 011270 112637 011503
2263 011274 062716 000002
2264 011300 000406
2265 011302 112737 000001 011501
2266 011310 112737 000006 011503
2267 011316 112737 000005 011500
2268 011324 010346
2269 011326 010446
2270 011330 010546
2271 011332 113704 011503
2272 011336 005404
2273 011340 062704 000006
2274 011344 110437 011502
2275 011350 113704 011501
2276 011354 016505 000012
2277 011360 005003
2278 011362 006105 1S:
2279 011364 000404 BR
2280 011366 006105 2S:
2281 011370 006105
2282 011372 006105
2283 011374 010503
2284 011376 006103 3S:
2285 011400 105337 011502
2286 011404 100316
2287 011406 042703 177770
2288 011412 001002
    
```

```

.SBTTL BINARY TO OCTAL (ASCII) AND TYPE

*****
*THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT
*OCTAL (ASCII) NUMBER AND TYPE IT.
*STYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE
*CALL:
*   MOV     NUM,-(SP)           ;; NUMBER TO BE TYPED
*   TYPOS   ;; CALL FOR TYPEOUT
*   .BYTE  N                   ;; N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE
*   .BYTE  M                   ;; M=1 OR 0
*                                   ;; 1=TYPE LEADING ZEROS
*                                   ;; 0=SUPPRESS LEADING ZEROS
*
*STYPON---ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST
*STYPOS OR STYPOC
*CALL:
*   MOV     NUM,-(SP)           ;; NUMBER TO BE TYPED
*   TYPON   ;; CALL FOR TYPEOUT
*
*STYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER
*CALL:
*   MOV     NUM,-(SP)           ;; NUMBER TO BE TYPED
*   TYPOC   ;; CALL FOR TYPEOUT
*
*STYPOS: MOV     2(SP),-(SP)      ;; PICKUP THE MODE
*         MOVB  1(SP),SOFILL     ;; LOAD ZERO FILL SWITCH
*         MOVB  (SP)+,SOMODE+1   ;; NUMBER OF DIGITS TO TYPE
*         ADD   2,SP             ;; ADJUST RETURN ADDRESS
*         BR    STYPON
*
*STYPOC: MOVB  01,SOFILL         ;; SET THE ZERO FILL SWITCH
*         MOVB  06,SOMODE+1     ;; SET FOR SIX(6) DIGITS
*         MOVB  05,SOCNT        ;; SET THE ITERATION COUNT
*         MOV   R3,-(SP)        ;; SAVE R3
*         MOV   R4,-(SP)        ;; SAVE R4
*         MOV   R5,-(SP)        ;; SAVE R5
*         MOVB  SOMODE+1,R4     ;; GET THE NUMBER OF DIGITS TO TYPE
*         NEG   R4              ;;
*         ADD   06,R4           ;; SUBTRACT IT FOR MAX. ALLOWED
*         MOVB  R4,SOMODE       ;; SAVE IT FOR USE
*         MOVB  SOFILL,R4       ;; GET THE ZERO FILL SWITCH
*         MOV   12(SP),R5       ;; PICKUP THE INPUT NUMBER
*         CLR   R3              ;; CLEAR THE OUTPUT WORD
*         ROL   R5              ;; ROTATE MSB INTO "C"
*         BR    3S              ;; GO DO MSB
*
*         ROL   R5              ;; FORM THIS DIGIT
*
*         ROL   R5
*         ROL   R5
*         MOV   R5,R3
*         ROL   R3              ;; GET LSB OF THIS DIGIT
*         DECB  SOMODE          ;; TYPE THIS DIGIT?
*         BPL   7S              ;; BR IF NO
*         BIC   #177770,R3     ;; GET RID OF JUNK
*         BNE   4S              ;; TEST FOR 0
    
```

```

2300 011414 005704
2301 011416 001473
2302 011417 000000
2303 011418 000000
2304 011419 000000
2305 011420 000000
2306 011421 000000
2307 011422 000000
2308 011423 000000
2309 011424 000000
2310 011425 000000
2311 011426 000000

```

```

000060
000040
110337 011476
104401 011476
105337 011500
003347
002402
005204
000744
012605
012604
012603
016666 000002 000004
012616
000002
000
000
000
000
000000

```

```

TST R7
BEQ R7,R7
45: INC R7
BIS #0,R3
55: BIS #0,R3
MOV R3,R3
TYPE R3
75: DECB $OCNT
BGT R3,R3
BLT R3,R3
INC R3
BR R3
65: MOV (SP)+,R5
MOV (SP)+,R4
MOV (SP)+,R3
MOV 2(SP),4(SP)
MOV (SP)+,(SP)
RTI
85: .BYTE 0
.SOCNT: .BYTE 0000
.SOFILL: .BYTE 0000
.SOMODE: .WORD 0

```

```

::: SUPPRESS THIS 0?
::: BR IF YES
::: DON'T SUPPRESS ANYMORE 0'S
::: MAKE THIS DIGIT ASCII
::: MAKE ASCII IF NOT ALREADY
::: SAVE FOR TYPING
::: GO TYPE THIS DIGIT
::: COUNT BY 1
::: BR IF MORE TO DO
::: BR IF DONE
::: INSURE LAST DIGIT ISN'T A BLANK
::: GO DO THE LAST DIGIT
::: RESTORE R5
::: RESTORE R4
::: RESTORE R3
::: SET THE STACK FOR RETURNING
::: RETURN
::: STORAGE FOR ASCII DIGIT
::: TERMINATOR FOR TYPE ROUTINE
::: OCTAL DIGIT COUNTER
::: ZERO FILL SWITCH
::: NUMBER OF DIGITS TO TYPE

```

```

2312 .SBTTL BINARY TO ASCII AND TYPE ROUTINE
2313
2314 ::*****
2315 ::THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 16-BIT
2316 ::BINARY-ASCII NUMBER AND TYPE IT.
2317 ::CALL:
2318 ::      MOV      NUMBER,-(SP)      ::NUMBER TO BE TYPED
2319 ::      TYPBN
2320
2321 STYPBN: MOV      R1,-(SP)          ::SAVE R1 ON THE STACK
2322          MOV      6(SP),R1        ::GET THE INPUT NUMBER
2323          SEC
2324          MOV      000006         ::SET "C" SO CAN KEEP TRACK OF THE NUMBER OF BITS
2325          MOV      011556 1$:     ::SET CHARACTER TO AN ASCII "0".
2326          ROL      R1             ::GET THIS BIT
2327          BEQ      2$             ::DONE?
2328          ADCB     $BIN           ::NO--SET THE CHARACTER EQUAL TO THIS BIT
2329          TYPE     , $BIN        ::GO TYPE THIS BIT
2330          CLC
2331          BR       1$             ::CLEAR "C" SO CAN KEEP TRACK OF BITS
2332          MOV      (SP)+,R1       ::GO DO THE NEXT BIT
2333          MOV      2(SP),4(SP)    ::POP THE STACK INTO R1
2334          MOV      (SP)+,(SP)    ::ADJUST THE STACK
2335          RTI
2336          .BYTE   0,0           ::RETURN TO USER
2337                               ::STORAGE FOR ASCII CHAR. AND TERMINATOR

```

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2389

.SBTTL TTY INPUT ROUTINE  
:\*\*\*\*\*  
.ENABL LSB  
:\*\*\*\*\*  
:SOFTWARE SWITCH REGISTER CHANGE ROUTINE  
:ROUTINE IS ENTERED FROM THE TRAP HANDLER, AND WILL  
:SERVICE THE TEST FOR CHANGE IN SOFTWARE SWITCH REGISTER TRAP CALL  
:WHEN OPERATING IN TTY FLAG MODE.  
SCKSWR: CMP #SWREG, SWR  
BNE 155  
TSTB #STKS  
BPL 155  
MOVB #STKB, -(SP)  
BIC #177, (SP)  
CMP #7, (SP)+  
BNE 155  
CMPB SAUTOB, #1  
BEQ 155  
SGTSWR: TYPE , SCNTLG  
TYPE , SWSWR  
MOV SWREG, -(SP)  
TYPOC  
TYPE , SWNEW  
19S: CLR -(SP)  
CLR -(SP)  
7S: TSTB #STKS  
BPL 7S  
MOVB #STKB, -(SP)  
BIC #177, (SP)  
9S: CMP (SP), #25  
BNE 10S  
TYPE , SCNTLU  
20S: ADD #6, SP  
BR 19S  
10S: CMP (SP), #15  
BNE 16S  
TST 4(SP)  
BEQ 11S  
MOV 2(SP), #SWR  
11S: ADD #6, SP  
14S: TYPE , SCALF  
CMPB \$INTAG, #1  
BNE 15S  
MOV #100, #STKS  
15S: RTI

IS THE SOFT-SWR SELECTED?  
BRANCH IF NO  
CHAR THERE?  
IF NO, DON'T WAIT AROUND  
SAVE THE CHAR  
STRIP-OFF THE ASCII  
IS IT A CONTROL G?  
NO, RETURN TO USER  
ARE WE RUNNING IN AUTO-MODE?  
BRANCH IF YES  
ECHO THE CONTROL-G (1G)  
TYPE CURRENT CONTENTS  
SAVE SWREG FOR TYPEOUT  
GO TYPE--OCTAL ASCII(ALL DIGITS)  
PROMPT FOR NEW SWR  
CLEAR COUNTER  
THE NEW SWR  
CHAR THERE?  
IF NOT TRY AGAIN  
PICK UP CHAR  
MAKE IT 7-BIT ASCII  
IS IT A CONTROL-U?  
BRANCH IF NOT  
YES, ECHO CONTROL-U (1U)  
IGNORE PREVIOUS INPUT  
LET'S TRY IT AGAIN  
IS IT A <CR>?  
BRANCH IF NO  
YES, IS IT THE FIRST CHAR?  
BRANCH IF YES  
SAVE NEW SWR  
CLEAR UP STACK  
ECHO <CR> AND <LF>  
RE-ENABLE TTY KBD INTERRUPTS?  
BRANCH IF NOT  
RE-ENABLE TTY KBD INTERRUPTS  
RETURN



```

2390 011762 004737 010762
2391 011766 021627 000060
2392 011772 022420
2393 011774 021627 000067
2394 012000 003015
2395 012002 042726 000060
2396 012006 005766 000002
2397 012012 001403
2398 012014 006316
2399 012016 006316
2400 012020 006316
2401 012022 005266 000002
2402 012026 005616 177776
2403 012032 000707
2404 012034 104401 001170
2405 012040 000720

```

```

16S: JSR PC,STYPEC
      CMP (SP),#60
      BLT 18S
      CMP (SP),#67
      BGT 18S
      BIC #60,(SP)+
      TST 2(SP)
      BEQ 17S
      ASL (SP)
      ASL (SP)
      ASL (SP)
17S: INC 2(SP)
      BIS -2(SP),(SP)
      BR 7S
18S: TYPE $QUES
      BR 20S
.DSABL L38

```

```

:: ECHO CHAR
:: CHAR < 0?
:: BRANCH IF YES
:: CHAR > ?
:: BRANCH IF YES
:: STRIP-OFF ASCII
:: IS THIS THE FIRST CHAR
:: BRANCH IF YES
:: NO, SHIFT PRESENT
:: CHAR OVER TO MAKE
:: ROOM FOR NEW ONE.
:: KEEP COUNT OF CHAR
:: SET IN NEW CHAR
:: GET THE NEXT ONE
:: TYPE ?(CR)<LF>
:: SIMULATE CONTROL-U

```

```

*****
*THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY

```

```

*CALL:
*      R0CHR
*      RETURN HERE
*
*      INPUT A SINGLE CHARACTER FROM THE TTY
*      CHARACTER IS ON THE STACK
*      WITH PARITY BIT STRIPPED OFF

```

```

2406 012046 011646
2407 012048 016666 000004 000002
2408 012052 105777 167066
2409 012056 100375
2410 012060 117766 167062 000004
2411 012066 042766 177600 000004
2412 012074 026627 000004 000023
2413 012102 001013
2414 012104 105777 167034
2415 012110 100375
2416 012112 117746 167030
2417 012116 042716 177600
2418 012122 022627 000021
2419 012126 001366
2420 012130 000750
2421 012136 026627 000004 000140
2422 012140 002407
2423 012142 000527 000004 000175
2424 012150 000003
2425 012152 042766 000040 000004
2426 012160 000002

```

```

SR0CHR: MOV (SP),-(SP)
        MOV 4(SP),2(SP)
1S:     TSTB 2$TKS
        BPL 1S
        MOVB 2$TKB,4(SP)
        BIC #177,4(SP)
        CMP 4(SP),#23
        BNE 3S
2S:     TSTB 2$TKS
        BPL 2S
        MOVB 2$TKB,-(SP)
        BIC #177,(SP)
        CMP (SP)+,#21
        BNE 2S
        BR 1S
3S:     CMP 4(SP),#140
        BLT 4S
        CMP 4(SP),#175
        BGT 4S
        BIC #40,4(SP)
4S:     RTI

```

```

:: PUSH DOWN THE PC
:: SAVE THE PS
:: WAIT FOR
:: A CHARACTER
:: READ THE TTY
:: GET RID OF JUNK IF ANY
:: IS IT A CONTROL-5?
:: BRANCH IF NO
:: WAIT FOR A CHARACTER
:: LOOP UNTIL ITS THERE
:: GET CHARACTER
:: MAKE IT 7-BIT ASCII
:: IS IT A CONTROL-Q?
:: IF NOT DISCARD IT
:: YES, RESUME
:: IS IT UPPER CASE?
:: BRANCH IF YES
:: IS IT A SPECIAL CHAR?
:: BRANCH IF YES
:: MAKE IT UPPER CASE
:: GO BACK TO USER

```

```

*****
*THIS ROUTINE WILL INPUT A STRING FROM THE TTY

```

```

*CALL:
*      R0LIN
*      RETURN HERE
*
*      INPUT A STRING FROM THE TTY
*      ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK
*      TERMINATOR WILL BE A BYTE OF ALL 0'S

```

012316  
012317  
012318  
012319  
012320  
012321  
012322  
012323  
012324  
012325  
012326  
012327  
012328  
012329  
012330  
012331  
012332  
012333  
012334  
012335  
012336  
012337  
012338  
012339  
012340

010346  
012703 012270  
022703 012300  
101405  
104411  
112613  
122713 000177  
001003  
104401 001170  
000763  
111337 012266  
104401 012266  
122723 000015  
001356  
10163 177777  
104401 001172  
012603  
011646  
011646 000004 000002  
012270 000004  
000002  
000  
000  
000110  
052536 000015 000  
136 070012  
005015 050013 020122  
050075 010  
040 047040 053505  
036440 000040

SPDLIN: MOV R3 -(SP)  
15: MOV #STTYIN,R3  
25: CMP #STTYIN+8.,R3  
BLOS 45  
ROCHR  
NOVB (SP)+,R3  
105: CMPB #177,R3  
BNE 35  
45: TYPE 15QUES  
BR 15  
35: NOVB (R3),95  
TYPE 95  
CMPB #15,(R3)+  
BNE 25  
CLRB -1(R3)  
TYPE 15LF  
MOV (SP)+,R3  
MOV (SP)-,(SP)  
MOV 4(SP),2(SP)  
MOV #STTYIN,4(SP)  
RTI  
95: .BYTE 0  
.BYTE 0  
STTYIN: .BLKB 8.  
\$CNTLU: .ASCIZ /?U<15><12>  
\$NTLG: .ASCIZ /?G<15><12>  
\$MSMR: .ASCIZ <15><12>/SMR = /  
\$MNEW: .ASCIZ / NEW = /

;; SAVE R3  
;; GET ADDRESS  
;; BUFFER FULL?  
;; BR IF YES  
;; GO READ ONE CHARACTER FROM THE TTY  
;; GET CHARACTER  
;; IS IT A RUBOUT  
;; SKIP IF NOT  
;; TYPE A '?'  
;; CLEAR THE BUFFER AND LOOP  
;; ECHO THE CHARACTER  
;; CHECK FOR RETURN  
;; LOOP IF NOT RETURN  
;; CLEAR RETURN (THE 15)  
;; TYPE A LINE FEED  
;; RESTORE R3  
;; ADJUST THE STACK AND PUT ADDRESS OF THE  
;; FIRST ASCII CHARACTER ON IT  
;; RETURN  
;; STORAGE FOR ASCII CHAR. TO TYPE  
;; TERMINATOR  
;; RESERVE 8 BYTES FOR TTY INPUT  
;; CONTROL "U"  
;; CONTROL "G"



.SBTTL POWER DOWN AND UP ROUTINES

\*\*\*\*\*

POWER DOWN ROUTINE

```

012737 012634 000024 SPWRON: MOV $SILLUP, @SPWRVEC ;; SET FOR FAST UP
012737 000340 000026 MOV @340, @SPWRVEC+2 ;; PRIO:7
010046 MOV @R0, -(SP) ;; PUSH R0 ON STACK
010146 MOV @R1, -(SP) ;; PUSH R1 ON STACK
010246 MOV @R2, -(SP) ;; PUSH R2 ON STACK
010346 MOV @R3, -(SP) ;; PUSH R3 ON STACK
010446 MOV @R4, -(SP) ;; PUSH R4 ON STACK
010546 MOV @R5, -(SP) ;; PUSH R5 ON STACK
010637 166410 MOV @R6, -(SP) ;; PUSH R6 ON STACK
010637 012640 MOV SP, @R6 ;; SET SP
012737 012546 000024 MOV @SPWRUP, @SPWRVEC ;; SET UP VECTOR
000000 HALT
012546 000776 BR .-2 ;; HANG UP

```

\*\*\*\*\*

POWER UP ROUTINE

```

012737 012634 000024 SPWRUP: MOV $SILLUP, @SPWRVEC ;; SET FOR FAST DOWN
013706 012640 000024 MOV $SAVR6, SP ;; GET SP
012640 CLR @SAVR6 ;; WAIT LOOP FOR THE TTY
012640 15: INC @SAVR6 ;; WAIT FOR THE INC
01375 BNE 15 OF MONO
012677 166342 MOV (SP)+, @SWR ;; POP STACK INTO @SWR
MOV (SP)+, @R5 ;; POP STACK INTO R5
MOV (SP)+, @R4 ;; POP STACK INTO R4
MOV (SP)+, @R3 ;; POP STACK INTO R3
MOV (SP)+, @R2 ;; POP STACK INTO R2
MOV (SP)+, @R1 ;; POP STACK INTO R1
MOV (SP)+, @R0 ;; POP STACK INTO R0
012600 MOV @SPWRON, @SPWRVEC ;; SET UP THE POWER DOWN VECTOR
012737 012474 000024 MOV @340, @SPWRVEC+2 ;; PRIO:7
000340 000026 TYPE .WORD SPOWER ;; REPORT THE POWER FAILURE
SPWRNG: .WORD SPOWER ;; POWER FAIL MESSAGE POINTER
SILLUP: HALT ;; THE POWER UP SEQUENCE WAS STARTED
BR .-2 ;; BEFORE THE POWER DOWN WAS COMPLETE
$SAVR6: 0 ;; PUT THE SP HERE
012642 005015 047520 042527 SPOWER: .ASCIZ <15><12>"POWER"
012650 000122 .EVEN

```

012737 012634 000024 SPWRON: MOV \$SILLUP, @SPWRVEC  
012737 000340 000026 MOV @340, @SPWRVEC+2  
010046 MOV @R0, -(SP)  
010146 MOV @R1, -(SP)  
010246 MOV @R2, -(SP)  
010346 MOV @R3, -(SP)  
010446 MOV @R4, -(SP)  
010546 MOV @R5, -(SP)  
010637 166410 MOV @R6, -(SP)  
010637 012640 MOV SP, @R6  
012737 012546 000024 MOV @SPWRUP, @SPWRVEC  
000000 HALT  
012546 000776 BR .-2  
012737 012634 000024 SPWRUP: MOV \$SILLUP, @SPWRVEC  
013706 012640 000024 MOV \$SAVR6, SP  
012640 CLR @SAVR6  
012640 15: INC @SAVR6  
01375 BNE 15  
012677 166342 MOV (SP)+, @SWR  
MOV (SP)+, @R5  
MOV (SP)+, @R4  
MOV (SP)+, @R3  
MOV (SP)+, @R2  
MOV (SP)+, @R1  
MOV (SP)+, @R0  
012600 MOV @SPWRON, @SPWRVEC  
012737 012474 000024 MOV @340, @SPWRVEC+2  
000340 000026 TYPE .WORD SPOWER  
SPWRNG: .WORD SPOWER  
SILLUP: HALT  
BR .-2  
\$SAVR6: 0  
012642 005015 047520 042527 SPOWER: .ASCIZ <15><12>"POWER"  
012650 000122 .EVEN

.SBTTL APT COMMUNICATIONS ROUTINE

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012652 112737 000001 013116  
012660 112737 000001 013114  
012666 000403  
012670 112737 000001 013116  
012676  
012678 010046  
012700 010146  
012702 105737 013114  
012706 001450  
012710 122737 000001 001214  
012716 001031  
012720 132737 000100 001215  
012726 001425  
012730 017600 000004  
012734 062766 000002 000004  
012742 005737 001174  
012746 001375  
012750 010037 001210  
012754 105720  
012756 001376  
012760 163700 001210  
012764 006200  
012766 010037 001212  
012772 012737 000004 001174  
013000 000413  
013002 017637 000004 013026  
013010 062766 000002 000004  
013016 013746 177776  
013022 004737 010550  
013036 000000  
013030 105737 013116  
013034 001416  
013036 005737 001214  
013042 001413  
013044 005737 001174  
013050 001375  
013052 017637 000004 001176  
013060 062766 000002 000004  
013066 005237 001174  
013072 105037 013116  
013076 105037 013115  
013102 105037 013114  
013106 012601  
013110 012600  
013112 000207  
013114 000  
013115 000  
013116 000  
013120  
000200

```
*****
SATY1:  MOVB  #1,SFFLG      ;;TO REPORT FATAL ERROR
SATY3:  MOVB  #1,SMFLG     ;;TO TYPE A MESSAGE
        BR    SATYC
SATY4:  MOVB  #1,SFFLG     ;;TO ONLY REPORT FATAL ERROR
SATYC:
        MOV   RO,-(SP)      ;;PUSH RO ON STACK
        MOV   RI,-(SP)      ;;PUSH RI ON STACK
        TSTB  SMFLG        ;;SHOULD TYPE A MESSAGE?
        BEQ   SS           ;;IF NOT: BR
        CMB   #APTENV,SENV  ;;OPERATING UNDER APT?
        BNE   SS           ;;IF NOT: BR
        BITB  #APTPOOL,SENV ;;SHOULD SPOOL MESSAGES?
        BEQ   SS           ;;IF NOT: BR
        MOV   #4(SP),RO     ;;GET MESSAGE ADDR.
        ADD   #2,4(SP)      ;;BUMP RETURN ADDR.
        TST   $MSGTYPE     ;;SEE IF DONE W/ LAST XMISSION?
        BNE   IS          ;;IF NOT: WAIT
        MOV   RO,$MSGADR    ;;PUT ADDR IN MAILBOX
        TSTB  (RO)+        ;;FIND END OF MESSAGE
        BNE   2S
        SUB   $MSGADR,RO   ;;SUB START OF MESSAGE
        ASR   RO           ;;GET MESSAGE LNTH IN WORDS
        MOV   RO,$MSGLEN    ;;PUT LENGTH IN MAILBOX
        MOV   #4,$MSGTYPE  ;;TELL APT TO TAKE MSG.
        BR    SS
        MOV   #4(SP),4S    ;;PUT MSG ADDR IN JSR LINKAGE
        ADD   #2,4(SP)     ;;BUMP RETURN ADDRESS
        MOV   177776,-(SP) ;;PUSH 177776 ON STACK
        JSR   PC,$TYPE     ;;CALL TYPE MACRO
        .WORD 0
        TSTB  $FFLG        ;;SHOULD REPORT FATAL ERROR?
        BEQ   12S         ;;IF NOT: BR
        TST   $ENV         ;;RUNNING UNDER APT?
        BEQ   12S         ;;IF NOT: BR
        TST   $MSGTYPE     ;;FINISHED LAST MESSAGE?
        BNE   11S         ;;IF NOT: WAIT
        MOV   #4(SP),$FATAL ;;GET ERROR #
        ADD   #2,4(SP)     ;;BUMP RETURN ADDR.
        INC   $MSGTYPE     ;;TELL APT TO TAKE ERROR
        CLRB  $FFLG        ;;CLEAR FATAL FLAG
        CLRB  $LFLG        ;;CLEAR LOG FLAG
        CLRB  $MFLG        ;;CLEAR MESSAGE FLAG
        MOV   (SP)+,R1     ;;POP STACK INTO R1
        MOV   (SP)+,R0     ;;POP STACK INTO R0
        RTS   PC          ;;RETURN
$MFLG:  .BYTE 0           ;;MESSG. FLAG
$LFLG:  .BYTE 0           ;;LOG FLAG
$FFLG:  .BYTE 0           ;;FATAL FLAG
        .EVEN
APTSIZE=200
```

J05

LSI-11 UVPR0M-RAM TEST. MD-11-DVMRA-A. MACY11 27(663) 24-MAR-77 13:51 PAGE 60  
DVMRAA.P11 APT COMMUNICATIONS ROUTINE

2625  
2626  
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000001  
000100  
000040

APTENV=001  
APTSP00L=100  
APTC SUP=040

013120  
013121  
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013201  
013202

.SBTTL TRAP DECODER

```

*****
; THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
; AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
; OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL
; GO TO THAT ROUTINE.
    
```

```

013120 010046
013121 011500 000002
013122 011540
013123 111000
013124 006300
013125 013154
013126 000200
    
```

```

$TRAP:  MOV    RO, -(SP)           ;; SAVE RO
        MOV    2(SP), RO         ;; GET TRAP ADDRESS
        TST    -(RO)            ;; BACKUP BY 2
        MOVB   (RO), RO         ;; GET RIGHT BYTE OF TRAP
        ASL    RO                ;; POSITION FOR INDEXING
        MOV    $TRAPD(RO), RO    ;; INDEX TO TABLE
        RTS    RO                ;; GO TO ROUTINE
    
```

;; THIS IS USE TO HANDLE THE "GETPRI" MACRO

```

013142 011646
013143 016666 000004 000002
013152 000002
    
```

```

$TRAP2: MOV    (SP), -(SP)       ;; MOVE THE PC DOWN
        MOV    4(SP), 2(SP)     ;; MOVE THE PSW DOWN
        RTI                    ;; RESTORE THE PSW
    
```

.SBTTL TRAP TABLE

```

; THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
; BY THE "TRAP" INSTRUCTION.
    
```

```

; ROUTINE
;
$TRAPD:  WORD    $TRAP2
        $TYPE   ;; CALL=TYPE      TRAP+1(104401)  TTY TYPEOUT ROUTINE
        $TYPOC  ;; CALL=TYPOC    TRAP+2(104402)  TYPE OCTAL NUMBER (WITH LEADING ZEROS)
        $TYPOS  ;; CALL=TYPOS    TRAP+3(104403)  TYPE OCTAL NUMBER (NO LEADING ZEROS)
        $TYPON  ;; CALL=TYPON    TRAP+4(104404)  TYPE OCTAL NUMBER (AS PER LAST CALL)
        $TYPOS  ;; CALL=TYPOS    TRAP+5(104405)  TYPE DECIMAL NUMBER (WITH SIGN)
        $TYPEN  ;; CALL=TYPEN    TRAP+6(104406)  TYPE BINARY (ASCII) NUMBER
;
        $GTSWR  ;; CALL=GTSWR    TRAP+7(104407)  GET SOFT-SWR SETTING
;
        $CKSWR  ;; CALL=CKSWR    TRAP+10(104410)  TEST FOR CHANGE IN SOFT-SWR
        $PDCHR  ;; CALL=PDCHR    TRAP+11(104411)  TTY TYPEIN CHARACTER ROUTINE
        $ROLIN  ;; CALL=ROLIN    TRAP+12(104412)  TTY TYPEIN STRING ROUTINE
        $RDOCT  ;; CALL=RDOCT    TRAP+13(104413)  READ AN OCTAL NUMBER FROM TTY
    
```

```

2672 013204 047111 040526 044514
2673 013212 020104 040522 020115
2674 013 02101 051104 051505
2675 013 020123 040522 043516
2676 013 02101 051104 051505
2677 013 020123 040522 043516
2678 013 02101 051104 051505
2679 013 020123 040522 043516
2680 013 02101 051104 051505
2681 013 020123 040522 043516
2682 013 02101 051104 051505
2683 013 020123 040522 043516
2684 013 02101 051104 051505
2685 013 020123 040522 043516
2686 013 02101 051104 051505
2687 013 020123 040522 043516
2688 013 02101 051104 051505
2689 013 020123 040522 043516
2690 013 02101 051104 051505
2691 013 020123 040522 043516
2692 013 02101 051104 051505
2693 013 020123 040522 043516
2694 013 02101 051104 051505
2695 013 020123 040522 043516
2696 013 02101 051104 051505
2697 013 020123 040522 043516
2698 013 02101 051104 051505
2699 013 020123 040522 043516
2700 013 02101 051104 051505
2701 013 020123 040522 043516
2702 013 02101 051104 051505
2703 013 020123 040522 043516
2704 013 02101 051104 051505
2705 013 020123 040522 043516
2706 013 02101 051104 051505
2707 013 020123 040522 043516
2708 013 02101 051104 051505
2709 013 020123 040522 043516
2710 013514 000104
2711
2712 013516 001200 001116 001122
2713 013524 001124 001126 000000
2714 013532 040522 020115 042101
2715 013540 051104 051505 020123
2716 013546 051105 047522 027122
2717 013 000
2718 013 124 051505 021524
2719 013562 042411 051122 041520
2720 013570 040411 042104 004522
2721 013576 047507 042117 041011
2722 013604 042101 000
2723
2724 013610 001200 001116 001122
2725 013616 001124 001126 000000

```

```

.SBTTL ERROR MESSAGES AND MISCELLANEOUS.
*****
M1: .ASCII /INVALID RAM ADDRESS RANGE./<CR LF>

.ASCIIZ /NEXT OR NOT ON 256 WORD BOUNDARY./

M2: .ASCII /INVALID PROM ADDRESS RANGE./<CR LF>

.ASCIIZ /NEXT, INCORRECT SIZE, OR NOT ON 1K BOUNDARY./

M3: .ASCIIZ /INVALID DUMP RANGE./

EM1: .ASCIIZ /RAM READ-WRITE ERROR./

DH1: .ASCIIZ /TEST# ERRPC ADDR GOOD BAD/

DT1: .EVEN
.WORD $TESTN, $ERRPC, $SBOADR, $SGDDAT, $SBDAT, 0

EM2: .ASCIIZ /RAM ADDRESS ERROR./

DH2: .ASCIIZ /TEST# ERRPC ADDR GOOD BAD/

DT2: .EVEN
.WORD $TESTN, $ERRPC, $SBOADR, $SGDDAT, $SBDAT, 0

```



2726	013624	040523	020115	040506	EM3:	.ASCIZ	/RAM FAST READ ERROR./
2727	013624	052122	051040	040506			
2728	013640	020122	051104	047522			
2729	013646	027122	000				
2730	013651	124	051505	021524	DH3:	.ASCIZ	/TEST# ERRPC ADDR GOOD RAMDAT BUFDAT/
2731	013656	042411	051122	041520			
2732	013656	040411	042104	004522			
2733	013656	047507	042117	051011			
2734	013700	046501	040504	004524			
2735	013706	052502	042106	052101			
2736	013714	000					
2737		013716					
2738	013716	001200	001116	001120	DT3:	.EVEN	STESTN, SERRPC, SGDADR, SGADR, SBDADR, SBODAT, 0
2739	013724	001120	001122	001126			
2740	013732	000000					
2741	013734	040522	020115	046123	EM4:	.ASCIZ	/RAM SLIDING BIT. ADDR-2 WAS ALTERED./
2742	013742	042111	047111	020107			
2743	013750	044502	027124	020040			
2744	013756	042101	051104	031055			
2745	013764	053440	051501	040440			
2746	013772	052114	051105	042105			
2747	014000	000056					
2748	014002	042524	052123	004443	DH4:	.ASCIZ	/TEST# ERRPC ADDR PATRN ADR-2 GOOD BAD/
2749	014010	051105	023122	004503			
2750	014016	042101	051104	020111			
2751	014024	052101	047122	040411			
2752	014032	051104	031055	043411			
2753	014040	047517	004504	040502			
2754	014046	000104					
2755							
2756	014050	001200	001116	001120	DT4:	.EVEN	STESTN, SERRPC, SGDADR, SGDDAT, SBDADR, FILLR, SBODAT, 0
2757	014056	001124	001122	005060			
2758	014064	001126	000000				
2759	014070	040522	020115	046123	EM5:	.ASCIZ	/RAM SLIDING BIT. ADDR+2 WAS ALTERED./
2760	014076	042111	047111	020107			
2761	014104	044502	027124	020040			
2762	014112	042101	051104	031053			
2763	014120	053440	051501	040440			
2764	014126	052114	051105	042105			
2765	014134	001056					
2766	014136	042524	052123	004443	DH5:	.ASCIZ	/TEST# ERRPC ADDR PATRN ADR+2 GOOD BAD/
2767	014144	051105	023122	004503			
2768	014152	042101	051104	050011			
2769	014160	052101	047122	040411			
2770	014166	051104	031053	043411			
2771	014174	047517	004504	040502			
2772	014202	000104					
2773							
2774	014204	001200	001116	001120	DT5:	.EVEN	STESTN, SERRPC, SGDADR, SGDDAT, SBDADR, FILLR, SBODAT, 0
2775	014212	001124	001122	005060			
2776	014220	001126	000000				
2777	014224	040522	020115	046101	EM6:	.ASCIZ	/RAM ALTERNATE BIT PATTERN ERROR./
2778	014232	042524	047122	052101			
2779	014240	020105	044502	020124			

2780	014246	040520	052124	051105								
2781	014254	020116	051105	047522								
2783	014272	027116	051505	021524	DM6:	.ASCIZ	/TEST#	ERRPC	ADDR	GOOD	BAD/	
2784	014272	042411	051122	041520								
2785	014370	040411	042104	004522								
2786	014306	047507	042117	041011								
2787	014314	042101	000									
2789	014320	014300	001116	001120	DT6:	.EVEN						
2790	014306	001120	001116	000000		.WORD	STESTN,	SERRPC,	SGDADR,	SGDDAT,	SBDADR,0	
2791	014306	000000	001116	054502	EM7:	.ASCIZ	/RAM	BYTE	TEST	ERROR./		
2792	014306	000000	051105	051505								
2793	014306	000000	051105	047522								
2794	014306	027116	051505	021524	DM7:	.ASCIZ	/TEST#	ERRPC	ADDR	GOOD	BAD	BYTE/
2795	014306	027116	051122	041520								
2796	014306	042411	042104	004522								
2797	014306	047507	042117	041011								
2798	014402	047507	042117	041011								
2799	014410	042101	041011	052131								
2800	014416	000105										
2801	014420	001200	001116	001120	DT7:	.EVEN						
2802	014430	001120	001126	001122		.WORD	STESTN,	SERRPC,	SGDADR,	SGDDAT,	SBDADR,0	
2803	014430	000000										
2804	014430	051120	046517	051040	EM10:	.ASCIZ	/PROM	READ	ERROR./			
2805	014430	047522	027116	051105								
2806	014430	047522	051505	021524	DM10:	.ASCIZ	/TEST#	ERRPC	ADDR	GOOD	BAD/	
2807	014430	042411	051122	041520								
2808	014430	042101	042104	004522								
2809	014430	047507	042117	041011								
2810	014430	042101	000									
2811	014512	014512										
2814	014512	001200	001116	001122	DT10:	.EVEN						
2815	014512	001120	001126	001070		.WORD	STESTN,	SERRPC,	SBDADR,	SGDDAT,	SBDADR,0	
2816	014512	051120	046517	051055	EM11:	.ASCIZ	/PROM-RAM	COPY	ERROR./			
2817	014512	047507	041440	050117								
2818	014512	047507	051105	047522								
2819	014512	027116	000									
2820	014512	042411	051505	021524	DM11:	.ASCIZ	/TEST#	ERRPC	PRMADR	PRMDAT	RAMADR	RAMDAT/
2821	014512	042411	051122	041520								
2822	014512	042101	046522	042101								
2823	014574	004504	051120	042115								
2824	014602	052101	051011	046501								
2825	014610	042101	004504	040522								
2826	014616	042115	052101	000								
2827	014624	014624										
2828	014624	001200	001116	001120	DT11:	.EVEN						
2829	014624	001120	001122	001126		.WORD	STESTN,	SERRPC,	SGDADR,	SGDDAT,	SBDADR,0	
2830	014624	000000										
2831	014642	051120	046517	053440	EM12:	.ASCIZ	/PROM	WRITE-TRAP	ERROR./			
2832	014650	044522	042524	052055								
2833	014656	040522	020120	051105								

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2863 014664 047522 027122 021508
2864 014671 014711 021508 021524
2865 014678 014711 021508 021524
2866 014704 014711 021508 021524
2867 014712 014711 021508 021524
2868 014720 014711 021508 021524
2869 014728 014711 021508 021524
2870 014736 014711 021508 021524
2871 014744 014711 021508 021524
2872 014752 014711 021508 021524
2873 014760 014711 021508 021524
2874 014768 014711 021508 021524
2875 014776 014711 021508 021524
2876 014784 014711 021508 021524
2877 014792 014711 021508 021524

```

```

DH12: .ASCIZ /TEST# ERRPC ADDR DATWAS DATIS/
      .EVEN
DT12: .WORD $TESTN, SERRPC, SBDAOR, $GDOAT, $BDOAT, 0
EM13: .ASCIZ /BUS TIME-OUT (TRAP) ERROR./
      .EVEN
DH13: .ASCIZ /TEST# TRAPPC TRAPPS/
      .EVEN
DT13: .WORD $TESTN, SBDAOR, $BDOAT, 0
EM14: .ASCII /MEMORY MAP ERROR./<15><12>/BANK /
EM14A: .ASCII /0 /
EM14B: .ASCIZ /-----/
      .EVEN
DF:   .WORD 0,0 ;OCTAL FORMAT FOR ALL.
DASH: .ASCIZ /-/
R:    .ASCIZ /R/
W:    .ASCIZ /W/
; DATA BUFFER FOR RAM FAST ADDRESS TEST.
IBUF: .BLKW 256.
; *****
      NOP ;END. HERE THRU 17776 IS FREE MEMORY.
      .END

```

ABASE = 020000	502#	731	772					
ACOM1 = 140000	503#	731	774					
ACOM2 = 000004	504#	731	775					
ACPUOP = 000000	731	746						
ADORS = 007166	1815#	1848#	1849#	1860	1873	1880	1887#	
ROOM0 = 000000	731							
ROOM1 = 000000	731							
ROOM10 = 000000	731							
ROOM11 = 000000	731							
ROOM12 = 000000	731							
ROOM13 = 000000	731							
ROOM14 = 000000	731							
ROOM15 = 000000	731							
ROOM2 = 000000	731							
ROOM3 = 000000	731							
ROOM4 = 000000	731							
ROOM5 = 000000	731							
ROOM6 = 000000	731							
ROOM7 = 000000	731							
ROOM8 = 000000	731							
ROOM9 = 000000	731							
RDEVCT = 000000	731	737						
RDEVH = 000000	731	773						
RENV = 000000	731	742						
RENVH = 000000	731	743						
RFATAL = 000000	731	734						
AGAIN = 002624	1026#	1688						
AMADR1 = 000000	731	759						
AMADR2 = 000000	731	763						
AMADR3 = 000000	731	766						
AMADR4 = 000000	731	769						
AMARS1 = 000000	731	753						
AMARS2 = 000000	731	761						
AMARS3 = 000000	731	764						
AMARS4 = 000000	731	767						
AMSGAD = 000000	731	739						
AMSGLG = 000000	731	740						
AMSGTY = 000000	731	733						
AMTYP1 = 000000	731	754						
AMTYP2 = 000000	731	762						
AMTYP3 = 000000	731	765						
AMTYP4 = 000000	731	768						
APASS = 000000	731	736						
APR10A = 000000	731							
APTCSU = 000740	2119	2627#						
APTEHV = 000001	2019	2112	2583	2625#				
APTSIZ = 000200	880	2624#						
APTSPO = 000100	2114	2585	2626#					
ASWREG = 000000	731	744						
ATESTN = 000000	731	735						
AUNIT = 000000	731	738						
ALSMR = 000000	731	745						
AVECT1 = 000000	731	770						
AVECT2 = 000000	731	771						





DDUMP 007174	536	1619#																
PATRN 003416	1161#	1164#	1170#	1173	1180	1189	1197											
PC =x000007	569#	87#	951#	1018#	1100#	1162#	1165#	1186#	1278#	1288#	1336#	1426#	1430#					
	1434#	1433#	1471#	1669#	1672#	1682#	1687	1706#	1707#	1794#	1796#	1821#	1833#					
	1865#	2016#	2022#	2079#	2117#	2136#	2143#	2150#	2164#	2166#	2390#	2602#	2619#					
PIR0 = 177772	555#																	
PIR0VE= 000240	649#																	
PRM 001424	834#	923#	931	1002#	1004#	1005	1008	1489	1542	1613								
PRMEND 001426	835#	926#	929#	931#	1017#	1513	1564											
PRMSZ 001430	836#	924#	970#	971#	973	975#	978	981#	984#	986#	988#	1010						
PRPX 001432	837#	922#	972#	980#	1581													
PR0 = 000000	572#																	
PR1 = 000040	573#																	
PR2 = 000100	574#																	
PR3 = 000140	575#																	
PR4 = 000200	576#																	
PR5 = 000240	577#																	
PR6 = 000300	578#																	
PR7 = 000340	579#																	
PS = 177776	552#	553																
PSM = 177776	553#																	
PTRN 005634	1425#	1429#	1437#	1444#	1464	1477												
PURVEC= 000024	644#	855#	856#	2532#	2533#	2542#	2548#	2560#	2561#									
R 015116	1075	2868#																
RAM 001420	832#	918#	942#	944#	945	947	1172	1179	1206	1214	1239	1253	1296					
	1304	1311	1376	1387	1398	1447	1454	1490	1504	1516	1544	1615						
RAMEND 001422	833#	919#	920#	950#	1241	1297	1377											
ROCHR = 104411	2449	2669#																
RDLIN = 104412	968	2494	2670#															
RDOCT = 104413	941	1001	1847	1856	2671#													
RD1 002740	1050#	1715																
RESVEC= 000010	639#																	
RMAP 007012	1042#	1052#	1061#	1070#	1079#	1777#												
RSTRT 002620	534	933	1021	1025#														
RTN1 002774	1060#	1717																
RTN2 003024	1069#	1722																
RO =%000000	560#	969#	970	1049#	1050	1102#	1105#	1106	1107	1112	1114	1115	1122					
	1128	1130	1131	1132	1133	1679#	1682	2051	2052#	2053#	2060#	2061#	2062#					
	2063#	2064#	2065	2070	2075#	2077#	2081	2083	2110	2111#	2116	2121	2124#					
	2181	2191#	2195	2211	2212	2225#	2491	2495#	2496	2499	2519#	2522#	2534					
	2559#	2579	2587#	2591	2592	2594#	2595#	2596	2618#	2636	2637#	2638	2639#					
	2640#	2641#	2642#															
R1 =%000001	561#	925#	927#	947#	949	1008#	1016	1122#	1123#	1124#	1125#	1126	1135#					
	1141#	1143#	1145#	1149#	1150	1171#	1174#	1177#	1183#	1205#	1209#	1213#	1218#					
	1239#	1244	1250	1253#	1255	1258	1263	1265	1306#	1307	1308	1324#	1328#					
	1333#	1342	1379#	1381	1382#	138	1391#	1399	1401#	1411	1415	1424#	1428#					
	1433#	1436#	1455#	1459#	1461#	1467#	1476	1489#	1495	1497#	1499	1503#	1505					
	1509	1513	1515	1525	1526	1532	1542#	1544	1546#	1548	1549#	1555	1557					
	1564	1566	1612#	1618#	1620	1622#	1625	1627	1629	1631	1635#	1637#	1639#					
	1649	1703	1860#	1864	1877#	1891#	2182	2195#	2196	2200	2224#	2321	2322#					
	2325#	2331#	2492	2497#	2505#	2507#	2509#	2512#	2515	2518#	2535	2558#	2580					
	2617#																	
R2 =%000002	562#	948#	949#	950	1009#	1013#	1016#	1017	1172#	1173#	1179#	1180	1190					
	1191	1196#	1197#	1206#	1207#	1208	1214#	1215	1217	1225	1226	1227	1231#					









SGDOAT	001124	704#	1189#	1226#	1342#	1350#	1360#	1411#	1477#	1526#	1556#	1649#	2712	2724
		2756	2774	2789	2802	2814	2828	2842						
SGETY2	006616	1679#												
SGTSMR	011630	2358#	2666											
SHO	000003	498	499											
SHIBTS	001000	678#												
SHIOCT	012472	2516#	2527#											
SICNT	001104	695#	1961#	1962	1964#	1975								
SILLUP	012634	2532	2548	2565#										
SINTAG	001135	709#	2386	2475										
SITEMB	001114	699#	2013#	2021	2042	2053								
SLF	001172	726#	2042	2168	2460	2469	2528							
SLFLG	013115	2615#	2621#											
SLPROR	001106	696#	861#	1035#	1036	1185	1221	1279	1289	1404	1470	1521	1569	1610#
		1644	1943#	1952#	1968#	1973	1975							
SLPERR	001110	697#	862#	1036#	1178#	1185#	1212#	1221#	1279#	1289#	1303#	1386#	1404#	1453#
		1470#	1493#	1521#	1541#	1569#	1611#	1644#	1740#	1952	1969#	1975	2032	
SMOAR1	001226	759#												
SR2	001232	763#												
SR3	001236	766#												
SR4	001242	769#												
SR5	001174	679	683	732#	879	898	1967	2019	2112					
SR6	001224	753#												
SR7	001230	761#												
SR8	001234	764#												
SR9	001240	767#												
SR10	001002	679#												
SR11	013114	2575#	2581	2616#	2620#									
SR12	012323	2361	2473#											
SR13	001210	739#	2591#	2594										
SR14	001212	740#	2596#											
SR15	001174	733#	2589	2597#	2609	2613#								
SR16	012312	2358	2471#											
SR17	001225	754#												
SR18	001231	762#												
SR19	001235	765#												
SR20	001241	768#												
SR21	010164	1965	1975#											
SR22	001154	717#	2139	2168										
SR23	000001	1027#	1156#	1200#	1234#	1271#	1281#	1371#	1418#	1484#	1535#	1572#		
SR24	011500	2267#	2296#	2309#										
SR25	011502	2262#	2266#	2271	2274#	2285#	2311#							
SR26	010150	1919	1944	1953	1963	1972#								
SR27	001202	736#	879#	1025#	1667#	1668#	1676	1689	1959	1976				
SR28	001006	681#												
SR29	012642	2563	2568#											
SR30	012474	855	2532#	2560										
SR31	012630	2563#												
SR32	012546	2542	2548#											
SR33	001170	724#	991	2042	2168	2404	2453	2469	2525	2528				
SR34	012042	2417#	2669											
SR35	*****	2672												
SR36	012162	2445#	2670											
SR37	012334	2489#	2671											

U







.SFRT	487#	2042
.S -E	487#	2528
.S UC	487#	2475
.S RD	487#	2336
.S DP	487#	1902
.STK-P	487#	2628
.STYPB	487#	2312
.STYPO	487#	2168
.STYPE	487#	2089
.STYPO	487#	2235







.BLK8	2859														
.BLK9	1785	2234	2873												
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	764	765	767	768	1689	1781	1875	1876	1899	2023	2024	2307	2308	2309	
.DSOAL	2310	2335	2466	2620	2621	2622									
.END	1155	1571	2406												
.ENDC	487	1034	1540	2339											
	2877														
	493	512	514	515	516	535	544	650	654	658	660	665	667	674	
	687	691	693	721	722	723	724	731	753	761	764	767	770	771	
	772	773	774	775	778	840	847	851	853	855	857	858	859	861	
	863	864	892	894	900	906	908	967	1000	1028	1029	1030	1031	1032	
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	1275	1276	1282	1283	1294	1295	1296	1339	1371	1372	1373	1374	1375	1376	
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	1687	1689	1690	1693	1695	1711	1747	1838	1865	1838	1846	1855	1905	1908	
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	628	629	630	631	632	633	634								
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	673	686	690	692	721	722	723	730	753	761	764	767	770		
	771	772	773	774	775	776	777	847	849	851	853	855	857		
	858	859	861	879	891	892	893	907	939	966	999	1027	1029		
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.IIF	408	493	498	499	509	510	511	512	515	516	522	727	731	848	851
	857	858	859	861	862	892	1659	1665	1666	1677	1689	1693	1908	1909	1910
	1911	1912	1913	1917	1936	1955	1956	1972	1975	1976	1994	1995	1996	1997	1998
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	1	487	515	522	650	721	728	731	840	863	892	895	908	940	967
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	1375	1418	1422	1484	1488	1535	1539	1572	1576	1665	1681	1826	1838	1846	1855
	1912	1976	1978	1979	1980	1981	1982	1983	1984	1985	1986	1987	1988	2037	2438
.MACRO	2651	2659	2660	2661	2662	2663	2664	2665	2666	2667	2668	2669	2670	2671	2672
.MCALL	516	684	879	2651											
.MEXIT	487	650	728	863	895										
.MLIST	1	487	515	522	650	721	728	731	840	863	892	895	908	940	967
	1000	1027	1031	1156	1160	1200	1204	1234	1238	1250	1271	1275	1281	1295	1371
	1375	1418	1422	1484	1488	1535	1539	1572	1576	1665	1681	1826	1838	1846	1855
	1912	1976	1978	1979	1980	1981	1982	1983	1984	1985	1986	1987	1988	2037	2438
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	516	684	778	831	884	934	1025	1076	1118	1156	1200	1234	1271	1371	1418
	1484	1535	1572	1655	1693	1746	1800	1901	1988	2042	2089	2168	2235	2312	2336
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.REPT	1														
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	505	516	540	651	662	684	728	778	831	841	888	895	909	1027	1156
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.TITLE	2089	2168	2235	2312	2336	2475	2528	2571	2628	2651	2672				
.WORD	488														
	522	523	524	527	530	659	678	679	680	681	682	683	692	695	696
	697	698	701	702	703	704	705	706	707	710	711	712	733	734	735
	736	737	738	739	740	744	745	746	759	763	766	769	770	771	772
	773	774	775	1670	1673	1688	1777	1778	1779	1780	1977	1978	1979	1980	1981
	1982	1983	1984	1985	1986	1987	2068	2073	2118	2165	2311	2524	2527	2563	2603
	2658	2712	2724	2738	2756	2774	2789	2802	2814	2828	2842	2854	2866		

ERRORS DETECTED: 0

#DVMPAA.BIC,DVMPAA.SEQ/SOL/NL:TOC=DVMPAA.P11  
RUN-TIME: 26 10 1 SECONDS  
CORE USED: 26K