

IBV11-A

IBV11-A DIAGNOSTIC
MD-11-DVIBA-A

EP-DVIBA-A-DL-A
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FICHE 1 OF 1

JUN 1977
digital
MADE IN USA



IBV11-A
MD-11-DVIBA-A

HDR1DVIBARSEQ

00010000

770526

801
PDP10 411

IDENTIFICATION

SEQ 0001

Product Code: MAINDEC-11-DVIBA-A-D

Product Name: IBV11-A Diagnostic

Date Created: JAN 1977

Maintainer: Diagnostic Engineering

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1.0 ABSTRACT

This program allows the user to check-out or debug the IBV11-A, LSI/IB interface option. In order to check-out a greater portion of logic on this option, a second IBV11-A is needed. See section 2.1. When a second IBV11-A can be obtained in order to run this diagnostic, the user must inform this diagnostic to exercise the logic on one IBV11-A that requires a KGM (Known Good Module). Please note that the second IBV11-A should be known good. No attempt is made to checkout the KGM and no conclusion that if good passes are made through this diagnostic that the KGM is also good. Signals "SRQ", "eri", "BIAKI", "DA11" and "ER1IH8" are not tested on the IBV11-A if a KGM is not used.

If the user is unfamiliar with an LSI-11 he should review sections 8.4 and 8.5. A software switch register is included with this program.

Every effort was made to make this program conform to LSI-11 programming restrictions. However, the user should read sections 7.1 and 7.2.

2.0 REQUIREMENTS

2.1 Equipment

1. PDP-11 Family Computer with 4K of memory (or more) and console I/O facilities (i.e., TTY).
2. IBV11-A under test.
3. (Optional) Second IBV11-A "KGM" (known good module). The "KGM" must be electrically second on the LSI-11-BUS. It must have an instrumentation Bus Cable between it and the first IBV-11. Its base address should be 760160 and vector address of 660 (see section 3.2 if different).

NOTE

While it is generally recommended that a "KGM" is used, if one is available, deposit a "000001" into location "SCDW1". No test will be performed that requires the "KGM" if SCDW1 is zero.

2.2 Storage

This program occupies and uses the lower 4K of memory.

3.0 LOADING PROCEDURE

3.1 Method

Standard procedure for normal binary tapes should be followed.

1. Absolute loader must be in memory.
2. Place binary tape in reader.
3. Type address #7500 (* determine by location of loader).
4. Type "G" (program will be loaded into memory).

The program can also be loaded by XXDP, ACT or APT.

3.2 Non-Standard Address, Vector, or Use of Software Switch Register

This program is set to test a IBV11-A with a standard address and vector. If any of these are different on the IBV11-A you are testing, change the corresponding location in memory before starting this test.

TAG	ADDRESS	CURRENT CONTENTS	COMMENTS
\$BASE:	1250	160150	::BASE ADDRESS OF EQUIPMENT UNDER TEST
\$VECT1:	1244	000640	::INTERRUPT VECTOR #1
\$WREG:	176	000000	::MANUAL SWR.
IBS2:	1366	160160	::ADDRESS OF SECOND IBV11-A.
VECTA2:	1372	660	::VECTOR ADDRESS OF SECOND IBV11-A.
\$CDW1:	1254	000000	::DEVICE DESCRIPTOR WORD #1 (if = 000001 to use "KGM" in testing 1st IBV-11) (Default = 00000 to disable use of KGM in tests.)

4.0 STARTING PROCEDURE

4.1 Control Switch Setting

Before starting the diagnostic, set all switch register bits as desired. See section 5.1.

4.2 Starting Addresses

200 Start of Logic Tests

4.3 Program and/or Operator Action

1. Load program into memory.
2. Enter keyboard "ODT".
3. Alter location "SWREG" to reflect desired options of a switch register - See section 5.1.
4. Type starting address, followed by "G" to start program.

5.0 OPERATING PROCEDURE

5.1 Switch Register Function

<u>SWR BIT</u>	<u>OCTAL</u>	<u>FUNCTION WHEN SET</u>
15	100000	HALT ON ERROR
14	040000	LOOP ON TEST
13	020000	INHIBIT ERROR TYPEOUT
11	004000	INHIBIT ITERATIONS (SHORT PASS)
10	002000	BELL ON ERROR
09	001000	LOOP ON ERROR
08	000400	LOOP ON TEST IN SWR <7:0>

NOTE

The Switch Register may be changed at any time while the diagnostic is running by typing "tG".

5.2 Scope Loops

If an error occurs and the user wishes to scope the error, "SWREG" should be altered to "100000" at the start of the test to halt on error, then when the program halts on error and the CPU enters "ODT", "SSWREG" should be altered to "060000" to loop on current test and inhibit error typeout, then type "P" to continue program execution.

5.3 Program and/or Operator Action

1. When the program is initially started it will type:

MD-11-DVIBA-A

SWR=000000 NEW=

2. Program now waits for the operator to enter a switch register setting (see section 8.6). If the program is restarted, only "MD-11-DVIBA-A" is typed. To change the switch register setting, see section 8.6.
3. Program executes first pass of logic tests, subtest iterations inhibited.
4. Program reports any errors it detects.
5. Program reports "END PASS 1".
6. Program executes second pass of logic tests, only this time it will loop on each test 2000 times.
7. Program the reports "END PASS 2".
8. Program will continue executing steps 6 and 7 until stoped.

6.0 ERRORS

6.1 Error Printout

Printout varies with the error detected. The error PC typed out is the actual location of the error call.

6.2 Non-Standard Error Halt

Bus errors will cause a Halt in the routine "IOTRD". The address that caused this trap will be in "TRTO".

7.0 RESTRICTIONS

7.1 Starting Restriction

If a free-running clock, such as 60Hz from the power supply, is attached to the "BEVNT" bus line on both REV level C/D and E systems, an interrupt to location 100 will occur when using the "G" and "L" commands prior to executing the first instruction. Therefore this program can not disable the BEVNT bus line by inhibiting interrupts.

User systems requiring a free-running clock attached to the BEVNT bus line can temporarily avoid this situation by setting the PSW(RS) to 200, loading the PC with the starting address instead of using the "G" command, and then using the "P" command. Before using the "L" command, the PSW(RS) can be set to 200, thereby inhibiting interrupts, to avoid receiving the event interrupt after loading the ABS loader.

7.2 Possible Program "BOMBS"

The first two tests of this program check to see if the IBV11-A responds to the address the program thinks its at. If the IBV11-A does not respond, a bus error occurs.

For more information on the next subject, see JAN. 1976 LSI-11 engineering bulletin issued by the Digital Components Group.

Bus errors may alter the preset contents of location 4 before the trap is executed, thereby transferring program control to area in the program that was not set up to handle the trap. If this happens, the program will "BOMB" and possibly rewrite parts of itself.

8.0 MISCELLANEOUS

8.1 Power Fail

After a power failure occurs, the program execution will continue at the point where the power occurred. The program will type "POWER".

8.2 XXDP, ACT, APT

The program is chainable under XXDP, ACT, or APT. Although "APT HOOKS" have been installed, they have not been tested.

8.3 Execution Time

0.1 Minutes (6 sec) Iteration Inhibited - No Errors
0.5 Minutes (30 sec) With Iterations - No Errors

B.4 LSI-11 "ODT" Commands

<u>FORMAT</u>	<u>DESCRIPTION</u>
<CR> RETURN	Close opened location and accept next command.
<LF> LINE FEED	Close current location; open next sequential location.
↑(UPARROW)	Open previous location.
< (LEFT ARROW)	Take contents of opened location, indexed by contents of PC, and open that location.
Ⓜ	Take contents of opened location as absolute address and open that location.
R/	Open the word at location R.
/	Reopen the last location.
SN/ or RN/	Open general register N(0-7) or S(PS register).
R:G or RG	GOTO location R and start program.
NL	Execute Bootstrap loader using N as device CSR. Console device is 177560.
:P or P	Proceed with program execution.
RUBOUT	Erases previous numeric character. Response is a backslash (\).

B.5 Entering LSI-11 "ODT"

The halt or ODT microcode state of the KD11F (LSI-11 module) can be entered in five different ways (others are a subset of these) from the run state:

1. Execution of a LSI-11 halt instruction.
2. A double bus error.
3. As a power up option.
4. ASCII break with DLV11 framing error asserting the B halt line (enabled by jumper of DLV11).

Upon entering the halt state, the KD11F responds through the set of command listed in section B.4.

8.6 Use of Program Software SWR

The software switch register may be changed by typing \uparrow G (control and letter G keys typed simultaneously). When \uparrow G is typed, the program responds by typing "SWR=XXXXXX" where XXXXXX equals the former contents of the switch register.

If you wish to keep the current value, type <CR>. If you wish to change the value, type the new value followed by a <CR>.

It is important to note that the diagnostic is not running after the \uparrow G until a <CR> is typed.

8.7 Trap Catcher

The Trap Catcher in this diagnostic employs a new concept. This concept will enable the user of this diagnostic to gain more knowledge of the events that lead the program to this area.

The Trap Catch consists of PC+2 and JSR PC, RD. (i.e., Location 300 would contain 302 and location 302 would contain 4700).

When a device interrupts to the Trap Catcher, it would pick up the PC+2 of the trap as an address of the interrupt service routine.

The program would then pick up "4700" as the new PSW. Bit 7 of the new PSW having been 1, would cause further interrupts from happening. When the CPU attempts to execute "4700" (JSR PC, RD), a Bus-time-out trap will occur to location 4. Location 4 contains a pointer to "IOTRD", a routine that will report the trap as an error.

To guard against "Real" Bus errors routing us through location 4 to "IOTRD", we check to see if the trap that brought us to location 4 really came from the Trap Catcher area. If not we'll halt and leave the Trap Address in "TRTO".

More about the interrupt error can be found in the description of the error in the program listing in the routine "IOTRD".

22	OPERATIONAL SWITCH SETTINGS
34	TRAP CATCHER
53	BASIC DEFINITIONS
169	ACT11 HOOKS
182	APT PARAMETER BLOCK
205	COMMON TAGS
249	APT MAILBOX-ETABLE
298	ERROR POINTER TABLE
369	REG ADDRESS AND COMMON TAGS
401	PROGRAM START
405	INITIALIZE THE COMMON TAGS
505	TYPE PROGRAM NAME
510	GET VALUE FOR SOFTWARE SWITCH REGISTER
526	T1 #TEST THE ADDRESSABILITY OF THE IBS, IBD REGISTERS
581	T2 #TEST THAT BASE ADDRESSES +4, +6 RESPOND WHEN ADDRESSED
634	T3 #TEST THAT IBS IS CLEAR AT INIT OF TESTING
656	T4 #TEST THAT IBD IS CLEAR AT INIT OF TESTING
679	T5 #TEST THAT BASE ADDRESSES +4, +6 RETURN ZERO WHEN READ
718	T6 #TEST THAT WE CAN SET TCS, TCS SETS CMD
755	T7 #TEST THAT EOP WILL SET
792	T10 #TEST THAT RE WILL SET + CLEAR
829	T11 #TEST THAT IBC WILL SET AND CLEAR
872	T12 #TEST THAT TON (BIT05) AND TKR SET AND CLEAR
910	T13 #MAKE SURE WE CAN SET AND CLEAR BIT06 (IE)
954	T14 #TEST THAT BIT 7 (ACC) CAN BE SET AND CLEARED
993	T15 #TEST THAT IBD BIT 0 CAN BE SET + CLEARED
1033	T16 #TEST THAT IBD BIT 1 CAN BE SET + CLEARED
1073	T17 #TEST THAT IBD BIT 2 CAN BE SET + CLEARED
1113	T20 #TEST THAT IBD BIT 3 CAN BE SET + CLEARED
1153	T21 #TEST THAT IBD BIT 4 CAN BE SET + CLEARED
1193	T22 #TEST THAT IBD BIT 5 CAN BE SET + CLEARED
1233	T23 #TEST THAT IBD BIT 6 CAN BE SET + CLEARED
1273	T24 #TEST THAT IBD BIT 7 CAN BE SET + CLEARED
1313	T25 #TEST THAT NO DATA GETS XFERRERD, IF NOT ENABLED
1337	T26 #TEST IBD BITS DAC, AND DAV
1402	T27 #TEST THAT REN SETS WHEN REM SETS, ALSO TEST CLEAR
1443	T30 #TEST THAT IFC SETS WHEN IBS SETS, ALSO TEST CLEAR
1487	T31 #TEST THAT ATN SETS WHEN TCS SETS, ALSO TEST CLEAR
1528	T32 #TEST THAT EOI SETS WHEN EOP SETS, ALSO TEST CLEAR
1567	T33 #TEST THAT RFD SET WHEN CSR CLEAR, CLEAR WHEN ACC SET
1601	T34 #TEST THAT WE CAN GENERATE AN ER2
1624	T35 #TEST THAT BUS INIT CLEARS ACC, TON, LON, REM, EIP, TCS
1646	T36 #TEST IBC CLEARS ACC, TON, LON, REM AND EOP
1670	T37 #TEST THAT BUS INIT INDIRECTLY CLEARS IBD
1693	
1694	INTERRUPT TESTS
1695	
1697	T40 #TEST THAT CMD CAN GENERATE AN INTERRUPT B
1731	T41 #TEST THAT TKR AND LNR CAN GENERATE INTERRUPTS
1796	T42 #TEST THAT ER2 CAN GENERATE AN INTERRUPT
1838	
1839	SECOND MODULE TESTS
1840	
1842	T43 #TEST THAT MODULE PASSES "BIAKI"

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DVIBA.P11 TABLE OF CONTENTS

SEQ 0015

1893	T44	*TEST THAT SRQ CAN GENERATE AN INTERRUPT
1944	T45	*TEST THAT ERROR1 IS GENERATED IF ATN IS ON THE IB BUS
1975	T46	*TEST THAT ERROR 1 IS GENERATED IF IFC IS PUT ON IB BUS BY SECOUND MODULE
2008	T47	*TEST THAT ERROR 1 IS GENERATED IF REN IS ON IB BUS
2038	T50	*TEST THAT AN ERROR 1 CAN GENERATE AN INTERRUPT
2088	T51	*TEST THAT DATA CAN BE XFERRERD BETWEEN THE MODULE UNDER TEST AND THE KGM
2132	T52	*TEMP END OF TESTS
2140		SYSMAC ROUTINES:
2142		END OF PASS ROUTINE
2197		ERROR HANDLER ROUTINE
2247		ERROR MESSAGE TYPEOUT ROUTINE
2294		SCOPE HANDLER ROUTINE
2360		TTY INPUT ROUTINE
2499		BINARY TO OCTAL (ASCII) AND TYPE
2576		CONVERT BINARY TO DECIMAL AND TYPE ROUTINE
2643		TYPE ROUTINE
2722		APT COMMUNICATIONS ROUTINE
2779		POWER DOWN AND UP ROUTINES
2903		TRAP DECODER
2926		TRAP TABLE
2946		MESSAGES AND TABLES

1
2
3
4
5
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7
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9
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11
12
13
14
15
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54

167400

000001

000000

000004	012066	000200		
000174	000000			
000176	000000			
000100	000104	000200	000002	
000200	000137	001422		

```
.NLIST MC,MD,CND
.LIST ME
.ENABL ABS
.ENABL AMA
.MCALL .HEADER .SETUP .SETTRAP .TRMTRP .STRAP .SRDOCT
.MCALL .STYPBIN, TYPOCS, .SPOWER, .SCATCH, .STYPOCT, .EQUAT
.MCALL .SCMTAG, .SWRHI, .SEOP, .SERROR, .SERRTYP
.MCALL .STYPDEC, .SSCOPE, .SREAD, .STYPE
.MCALL .SACT11, .SAPTHDR, .SAPTYPE
$SWR=167400
```

```
.TITLE MAINDEC-11-DVIBA-A
*COPYRIGHT (C) 1976
*DIGITAL EQUIPMENT CORP.
*MAYNARD, MASS. 01754
*
*PROGRAM BY EDWARD C. BADGER
*
*THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
*PACKAGE (MAINDEC-11-DZQAC-C2), SEPT 14, 1976.
*
```

```
$TN=1
; THIS VERSION LAST EDITED - OCT. 6, 1976
```

```
.SBTTL OPERATIONAL SWITCH SETTINGS
*
* SWITCH USE
*-----
* 15 HALT ON ERROR
* 14 LOOP ON TEST
* 13 INHIBIT ERROR TYPEOUTS
* 11 INHIBIT ITERATIONS
* 10 BELL ON ERROR
* 9 LOOP ON ERROR
* 8 LOOP ON TEST IN SWR<7:0>
```

```
.SBTTL TRAP CATCHER
.=0
*ALL UNUSED LOCATIONS FROM 4-776 CONTAIN A ".+2"
*AND "JSR PC,R0" SEQUENCE TO CATCH ILLEGAL INTERRUPTS,
*AND INTERRUPTS TO THE WRONG VECTOR.
*LOCATION 0 CONTAINS A 0 TO CATCH IMPROPERLY LOADED
*VECTORS
.=4
.WORD IOTRD,200 ;HANDLE BUSS ERROR.
.=174
DISPREG: .WORD 0 ;;SOFTWARE DISPLAY REGISTER.
SWREG: .WORD 0 ;;SOFTWARE SWITCH REGISTER.
.=100
.WORD 104,200,2 ;IF "B EVENT" ON Q-BUS IS
;CONNECTED, WE NEED A WAY OF
;IGNORING ITS INTERRUPTS.
.=200
JMP START
```

```

55      .SBTTL  BASIC DEFINITIONS
56
57
58      ;*INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***
59      001100  STACK= 1100
60      .EQUIV  EMT,ERROR      ;;BASIC DEFINITION OF ERROR CALL
61      .EQUIV  IOT,SCOPE     ;;BASIC DEFINITION OF SCOPE CALL
62
63      ;*MISCELLANEOUS DEFINITIONS
64      000011  HT= 11          ;;CODE FOR HORIZONTAL TAB
65      000012  LF= 12          ;;CODE FOR LINE FEED
66      000015  CR= 15          ;;CODE FOR CARRIAGE RETURN
67      000200  CRLF= 200       ;;CODE FOR CARRIAGE RETURN-LINE FEED
68      177776  PS= 177776     ;;PROCESSOR STATUS WORD
69      .EQUIV  PS,PSW
70      177774  STKLM= 177774   ;;STACK LIMIT REGISTER
71      177772  PIRQ= 177772   ;;PROGRAM INTERRUPT REQUEST REGISTER
72      177570  DSWR= 177570   ;;HARDWARE SWITCH REGISTER
73      177570  DDISP= 177570  ;;HARDWARE DISPLAY REGISTER
74
75      ;*GENERAL PURPOSE REGISTER DEFINITIONS
76      000000  R0= %0          ;;GENERAL REGISTER
77      000001  R1= %1          ;;GENERAL REGISTER
78      000002  R2= %2          ;;GENERAL REGISTER
79      000003  R3= %3          ;;GENERAL REGISTER
80      000004  R4= %4          ;;GENERAL REGISTER
81      000005  R5= %5          ;;GENERAL REGISTER
82      000006  R6= %6          ;;GENERAL REGISTER
83      000007  R7= %7          ;;GENERAL REGISTER
84      000006  SP= %6          ;;STACK POINTER
85      000007  PC= %7          ;;PROGRAM COUNTER
86
87      ;*PRIORITY LEVEL DEFINITIONS
88      000000  PR0= 0          ;;PRIORITY LEVEL 0
89      000040  PR1= 40         ;;PRIORITY LEVEL 1
90      000100  PR2= 100        ;;PRIORITY LEVEL 2
91      000140  PR3= 140        ;;PRIORITY LEVEL 3
92      000200  PR4= 200        ;;PRIORITY LEVEL 4
93      000240  PR5= 240        ;;PRIORITY LEVEL 5
94      000300  PR6= 300        ;;PRIORITY LEVEL 6
95      000340  PR7= 340        ;;PRIORITY LEVEL 7
96
97      ;*"SWITCH REGISTER" SWITCH DEFINITIONS
98      100000  SW15= 100000
99      040000  SW14= 40000
100     020000  SW13= 20000
101     010000  SW12= 10000
102     004000  SW11= 4000
103     002000  SW10= 2000
104     001000  SW09= 1000
105     000400  SW08= 400
106     000200  SW07= 200
107     000100  SW06= 100
108     000040  SW05= 40

```

109 000020
 110 000010
 111 000004
 112 000002
 113 000001

SW04= 20
 SW03= 10
 SW02= 4
 SW01= 2
 SW00= 1
 .EQUIV SW09,SW9
 .EQUIV SW08,SW8
 .EQUIV SW07,SW7
 .EQUIV SW06,SW6
 .EQUIV SW05,SW5
 .EQUIV SW04,SW4
 .EQUIV SW03,SW3
 .EQUIV SW02,SW2
 .EQUIV SW01,SW1
 .EQUIV SW00,SW0

114
 115
 116
 117
 118
 119
 120
 121
 122
 123
 124

:#DATA BIT DEFINITIONS (BIT00 TO BIT15)

125
 126 100000
 127 040000
 128 020000
 129 010000
 130 004000
 131 002000
 132 001000
 133 000400
 134 000200
 135 000100
 136 000040
 137 000020
 138 000010
 139 000004
 140 000002
 141 000001

BIT15= 100000
 BIT14= 40000
 BIT13= 20000
 BIT12= 10000
 BIT11= 4000
 BIT10= 2000
 BIT09= 1000
 BIT08= 400
 BIT07= 200
 BIT06= 100
 BIT05= 40
 BIT04= 20
 BIT03= 10
 BIT02= 4
 BIT01= 2
 BIT00= 1
 .EQUIV BIT09,BIT9
 .EQUIV BIT08,BIT8
 .EQUIV BIT07,BIT7
 .EQUIV BIT06,BIT6
 .EQUIV BIT05,BIT5
 .EQUIV BIT04,BIT4
 .EQUIV BIT03,BIT3
 .EQUIV BIT02,BIT2
 .EQUIV BIT01,BIT1
 .EQUIV BIT00,BIT0

142
 143
 144
 145
 146
 147
 148
 149
 150
 151
 152

:#BASIC "CPU" TRAP VECTOR ADDRESSES

153
 154 000004
 155 000010
 156 000014
 157 000014
 158 000014
 159 000020
 160 000024
 161 000030
 162 000034

ERRVEC= 4 ;: TIME OUT AND OTHER ERRORS
 RESVEC= 10 ;: RESERVED AND ILLEGAL INSTRUCTIONS
 TBITVEC= 14 ;: "T" BIT
 TRTVEC= 14 ;: TRACE TRAP
 BPTVEC= 14 ;: BREAKPOINT TRAP (BPT)
 IOTVEC= 20 ;: INPUT/OUTPUT TRAP (IOT) **SCOPE**
 PWRVEC= 24 ;: POWER FAIL
 EMTVEC= 30 ;: EMULATOR TRAP (EMT) **ERROR**
 TRAPVEC= 34 ;: "TRAP" TRAP

163 000060
164 000064
165 000240
166
167 160150
168 000640
169 000200
170 000001
171
172
173
174
175
176 000204
177 000046
178 000046 007052
179 000052 000052
180 000052 000000
181 000052 000204
182
183 001000
184
185
186
187
188
189
190 001000
191 000024 000024
192 000024 000200
193 000044 000044
194 000044 001000
195 001000
196
197
198
199
200 001000
201 001000 000000
202 001002 001174
203 001004 000074
204 001006 000170
205 001010 000170
206 001012 000031
207

TKVEC= 60 ;:TTY KEYBOARD VECTOR
TPVEC= 64 ;:TTY PRINTER VECTOR
PIRQVEC=240 ;:PROGRAM INTERRUPT REQUEST VECTOR

ABASE= 160150
AVECT1= 640
APRIOR= 200
\$TN=1

.SBTTL ACT11 HOOKS

;;*****

;;HOOKS REQUIRED BY ACT11

;;\$SVPC=. ;SAVE PC
;SENDAD ;:1)SET LOC.46 TO ADDRESS OF SENDAD IN .SEOP
;WORD 0 ;:2)SET LOC.52 TO ZERO
;=\$SVPC ;: RESTORE PC
;=1000

.SBTTL APT PARAMETER BLOCK

;;*****

;;SET LOCATIONS 24 AND 44 AS REQUIRED FOR APT

;;*****

;;\$X=. ;:SAVE CURRENT LOCATION
;=24 ;:SET POWER FAIL TO POINT TO START OF PROGRAM
200 ;:FOR APT START UP
;=44 ;:POINT TO APT INDIRECT ADDRESS PNTR.
\$APTHDR ;:POINT TO APT HEADER BLOCK
;=\$X ;:RESET LOCATION COUNTER

;;*****

;;SETUP APT PARAMETER BLOCK AS DEFINED IN THE APT-PDP11 DIAGNOSTIC
;;INTERFACE SPEC.

\$APTHD:
\$HIPTS: .WORD 0 ;:TWO HIGH BITS OF 18 BIT MAILBOX ADDR.
\$MADR: .WORD \$MAIL ;:ADDRESS OF APT MAILBOX (BITS 0-15)
\$STMT: .WORD 60. ;:RUN TIM OF LONGEST TEST
\$PASTM: .WORD 120. ;:RUN TIME IN SECS. OF 1ST PASS ON 1 UNIT (QUICK VERIFY)
\$UNITM: .WORD 120. ;:ADDITIONAL RUN TIME (SECS) OF A PASS FOR EACH ADDITIONAL UNIT
;SETEND-\$MAIL/2 ;:LENGTH MAILBOX-ETABLE(WORDS)

208
209
210
211
212
213
214
215 001100
216 001100 000000
217 001102 000
218 001103 000
219 001104 000000
220 001106 000000
221 001110 000000
222 001112 000000
223 001114 000
224 001115 001
225 001116 000000
226 001120 000000
227 001122 000000
228 001124 000000
229 001126 000000
230 001130 000000
231 001132 000000
232 001134 000
233 001135 000
234 001136 000000
235 001140 177570
236 001142 177570
237 001144 177560
238 001146 177562
239 001150 177564
240 001152 177566
241 001154 000
242 001155 002
243 001156 012
244 001157 000
245 001160 000000
246 001162 000000
247 001164 177607 000377
248 001170 077
249 001171 015
250 001172 000012
251
252
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257 001174
258 001174 000000
259 001176 000000
260 001200 000000
261 001204 000000

.SBTTL COMMON TAGS

```

*****
;THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
;USED IN THE PROGRAM.

```

```

SCMTAG:      .=1100                ;; START OF COMMON TAGS
              .WORD                0
STSTNM:      .BYTE                000  ;; CONTAINS THE TEST NUMBER
SERFLG:      .BYTE                000  ;; CONTAINS ERROR FLAG
SICNT:       .WORD                000  ;; CONTAINS SUBTEST ITERATION COUNT
SLPADR:      .WORD                000  ;; CONTAINS SCOPE LOOP ADDRESS
SLPERR:      .WORD                000  ;; CONTAINS SCOPE RETURN FOR ERRORS
SERTTL:      .WORD                000  ;; CONTAINS TOTAL ERRORS DETECTED
SITEMB:      .BYTE                000  ;; CONTAINS ITEM CONTROL BYTE
SERMAX:      .BYTE                001  ;; CONTAINS MAX. ERRORS PER TEST
SERRPC:      .WORD                000  ;; CONTAINS PC OF LAST ERROR INSTRUCTION
SGDADR:      .WORD                000  ;; CONTAINS ADDRESS OF 'GOOD' DATA
SBDADR:      .WORD                000  ;; CONTAINS ADDRESS OF 'BAD' DATA
SGDDAT:      .WORD                000  ;; CONTAINS 'GOOD' DATA
SBDDAT:      .WORD                000  ;; CONTAINS 'BAD' DATA
              .WORD                000  ;; RESERVED--NOT TO BE USED
SAUTOB:      .BYTE                000  ;; AUTOMATIC MODE INDICATOR
SINTAG:      .BYTE                000  ;; INTERRUPT MODE INDICATOR
              .WORD                0
SWR:         .WORD                DSWR  ;; ADDRESS OF SWITCH REGISTER
DISPLAY:     .WORD                DDISP ;; ADDRESS OF DISPLAY REGISTER
STKS:        177560                ;; TTY KBD STATUS
STKB:        177562                ;; TTY KBD BUFFER
STPS:        177564                ;; TTY PRINTER STATUS REG. ADDRESS
STPB:        177566                ;; TTY PRINTER BUFFER REG. ADDRESS
SNUL:        .BYTE                0    ;; CONTAINS NULL CHARACTER FOR FILLS
SFILLS:      .BYTE                2    ;; CONTAINS # OF FILLER CHARACTERS REQUIRED
SFILLC:      .BYTE                12   ;; INSERT FILL CHARS. AFTER A "LINE FEED"
STPFLG:      .BYTE                0    ;; "TERMINAL AVAILABLE" FLAG (BIT<07>=0=YES)
STIMES:      0                    ;; MAX. NUMBER OF ITERATIONS
SESCAPE:     0                    ;; ESCAPE ON ERROR ADDRESS
SBELL:       .ASCIZ <207><377><377> ;; CODE FOR BELL
SQUES:       .ASCII /?/           ;; QUESTION MARK
SCRLF:       .ASCII <15>          ;; CARRIAGE RETURN
SLF:         .ASCIZ <12>          ;; LINE FEED

```

.SBTTL APT MAILBOX-ETABLE

```

*****
;EVEN
SMAIL:       .WORD                0    ;; APT MAILBOX
MSGTY:       .WORD                0    ;; MESSAGE TYPE CODE
SFATAL:      .WORD                0    ;; FATAL ERROR NUMBER
STESTN:      .WORD                0    ;; TEST NUMBER
SPASS:       .WORD                0    ;; PASS COUNT
SDEVCT:      .WORD                0    ;; DEVICE COUNT

```

262	001206	000000	SUNIT:	.WORD	AUNIT	:: I/O UNIT NUMBER
263	001210	000000	SMSGAD:	.WORD	AMSGAD	:: MESSAGE ADDRESS
264	001212	000000	SMSGLG:	.WORD	AMSGLG	:: MESSAGE LENGTH
265	001214		SETABLE:			:: APT ENVIRONMENT TABLE
266	001214	000	SENV:	.BYTE	AENV	:: ENVIRONMENT BYTE
267	001215	000	SENVN:	.BYTE	AENVN	:: ENVIRONMENT MODE BITS
268	001216	000000	SSWREG:	.WORD	ASWREG	:: APT SWITCH REGISTER
269	001220	000000	SUSWR:	.WORD	AUSWR	:: USER SWITCHES
270	001222	000000	SCPUOP:	.WORD	ACPUOP	:: CPU TYPE, OPTIONS
271			.*			BITS 15-11=CPU TYPE
272			.*			11/04=01, 11/05=02, 11/20=03, 11/40=04, 11/45=05
273			.*			11/70=06, PDQ=07, Q=10
274			.*			BIT 10=REAL TIME CLOCK
275			.*			BIT 9=FLOATING POINT PROCESSOR
276			.*			BIT 8=MEMORY MANAGEMENT
277	001224	000	\$MAMS1:	.BYTE	AMAMS1	:: HIGH ADDRESS, M.S. BYTE
278	001225	000	\$MTYP1:	.BYTE	AMTYP1	:: MEM. TYPE, BLK#1
279			.*			MEM. TYPE BYTE -- (HIGH BYTE)
280			.*			900 NSEC CORE=001
281			.*			300 NSEC BIPOLAR=002
282			.*			500 NSEC MOS=003
283	001226	000000	\$MADR1:	.WORD	AMADR1	:: HIGH ADDRESS, BLK#1
284			.*			MEM. LAST ADDR.=3 BYTES, THIS WORD AND LOW OF "TYPE" ABOVE
285	001230	000	\$MAMS2:	.BYTE	AMAMS2	:: HIGH ADDRESS, M.S. BYTE
286	001231	000	\$MTYP2:	.BYTE	AMTYP2	:: MEM. TYPE, BLK#2
287	001232	000000	\$MADR2:	.WORD	AMADR2	:: MEM. LAST ADDRESS, BLK#2
288	001234	000	\$MAMS3:	.BYTE	AMAMS3	:: HIGH ADDRESS, M.S. BYTE
289	001235	000	\$MTYP3:	.BYTE	AMTYP3	:: MEM. TYPE, BLK#3
290	001236	000000	\$MADR3:	.WORD	AMADR3	:: MEM. LAST ADDRESS, BLK#3
291	001240	000	\$MAMS4:	.BYTE	AMAMS4	:: HIGH ADDRESS, M.S. BYTE
292	001241	000	\$MTYP4:	.BYTE	AMTYP4	:: MEM. TYPE, BLK#4
293	001242	000000	\$MADR4:	.WORD	AMADR4	:: MEM. LAST ADDRESS, BLK#4
294	001244	000640	\$VECT1:	.WORD	AVECT1	:: INTERRUPT VECTOR#1, BUS PRIORITY#1
295	001246	000000	\$VECT2:	.WORD	AVECT2	:: INTERRUPT VECTOR#2, BUS PRIORITY#2
296	001250	160150	\$BASE:	.WORD	ABASE	:: BASE ADDRESS OF EQUIPMENT UNDER TEST
297	001252	000000	\$DEVN:	.WORD	ADEVN	:: DEVICE MAP
298	001254	000000	\$CDW1:	.WORD	ACDW1	:: CONTROLLER DESCRIPTION WORD#1
299	001256		SETEND:			
300			.MEXIT			

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.SBTTL ERROR POINTER TABLE

;*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
;*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
;*LOCATION SITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
;*NOTE1: IF SITEMB IS 0 THE ONLY PERTINENT DATA IS (SERRPC).
;*NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

::* EM ::POINTS TO THE ERROR MESSAGE
::* DH ::POINTS TO THE DATA HEADER
::* DT ::POINTS TO THE DATA
::* DF ::POINTS TO THE DATA FORMAT

SERRTB:

;ITEM 1

EM1 :IBS FUNCTION ERROR
DH1 :TEST ERRPC IB ADDR
DT1 :STESTN,SERRPC,IBS
DF0 :ALL NUMBERS ARE IN OCTAL FORM.

;ITEM 2

EM2 :IBD FUNCTION ERROR
DH1 :TEST ERRPC IB ADDR
DT1 :STESTN,SERRPC,IBS
DF0 :ALL NUMBERS ARE IN OCTAL FORM.

;ITEM 3

EM3 :IBS DATA ERROR
DH3 :TEST ERRPC GOOD BAD
DT3 :STESTN,SERRPC,\$GDDAT,\$BDDAT
DF0 :ALL NUMBERS ARE IN OCTAL FORM.

;ITEM 4

EM4 :IBD DATA ERROR
DH3 :TEST ERRPC GOOD BAD
DT3 :STESTN,SERRPC,\$GDDAT,\$BDDAT
DF0 :ALL NUMBERS ARE IN OCTAL FORM.

;ITEM 5

EM5 :IBS/IBD ADDRESS ERROR
DH5 :TEST ERROR PC ADDRESS
DT5 :STSTNM,SERRPC,IBS
DF0 :ALL NUMBERS ARE IN OCTAL FORM.

;ITEM 6

001256

001256 012216
001260 012437
001262 012640
001264 012710

001266 012244
001270 012437
001272 012640
001274 012710

001276 012272
001300 012505
001302 012654
001304 012710

001306 012314
001310 012505
001312 012654
001314 012710

001316 012336
001320 012542
001322 012666
001324 012710

355 001326 012366
356 001330 012505
357 001332 012654
358 001334 012710
359
360

EM6
DH3
DT3
DFO

:IBMC/IBCA DATA ERROR
:TEST ERRPC GOOD BAD
:STSTN,SEERRPC,\$GDDAT,\$BDDAT
:ALL NUMBERS ARE IN OCTAL FORM.

361 001336 012415
362 001340 012573
363 001342 012676
364 001344 012710
365
366
367
368
369
370
371
372

:ITEM 7
EM7
DH7
DT7
DFO

:INTERRUPT ERROR
:TEST ERRPC TO FROM ADDR.
:TSTNM,#ERRPC,TRTO,TRFRO
:ALL NUMBERS ARE IN OCTAL FORM.

373
374
375

.SBTTL REG ADDRESS AND COMMON TAGS
:WARNING IF DEVICE # IS AT DIFFERENT ADDRESS OR VECTOR
:DO NOT PATCH THESE LOCATIONS - SEE PROGRAM DOCUMENTATION.

376 001346 160150
377 001350 160152
378 001352 160154
379 001354 160156
380 001356 000640
381 001360 000644
382 001362 000650
383 001364 000654
384 001366 160160
385 001370 160162
386 001372 000660
387 001374 000664
388 001376 000670
389 001400 000674
390
391
392

IBS: .WORD ABASE ;>NO ;CONTROL AND STATUS REGISTER.
IBD: .WORD ABASE+2 ;>PATCHES ;DATA REGISTER.
IBMC: .WORD ABASE+4 ;ADDRESS RESERVED FOR
IBCA: .WORD ABASE+6 ;FUTURE USE
VECTA: .WORD AVECT1 ;>ALLOWED ;VECTOR ADDRESS.
VECTB: .WORD AVECT1+4 ;>HERE! ;VECTOR ADDR. +4.
VECTC: .WORD AVECT1+10
VECTD: .WORD AVECT1+14
IBS2: .WORD ABASE+10
IBD2: .WORD ABASE+12
VECTA2: .WORD AVECT1+20
VECTB2: .WORD AVECT1+24
VECTC2: .WORD AVECT1+30
VECTD2: .WORD AVECT1+34

;VECTOR ADDRESSES +2 LOCATIONS.

393 001402 000642
394 001404 000646
395 001406 000652
396 001410 000656
397

PRA: .WORD AVECT1+2 ;NOTE: DO NOT ATTEMPT TO PATCH
PRB: .WORD AVECT1+6 ;THESE LOCATIONS IF A VECTOR
PRC: .WORD AVECT1+12 ;VARYIES - ALTER LOCATION
PRD: .WORD AVECT1+16 ;"SVECT1:".

398 001412 000662
399 001414 000666
400 001416 000672
401 001420 000676
402
403
404

PRA2: .WORD AVECT1+22 ;IF TEST MODULE VECTOR IS
PRB2: .WORD AVECT1+26 ;DIFFERENT, YOU MUST CHANGE
PRC2: .WORD AVECT1+32 ;LOCATION "VECTA2:".
PRD2: .WORD AVECT1+36

.SBTTL PROGRAM START

405
406
407 001422
408

START:
.SBTTL INITIALIZE THE COMMON TAGS


```

409          ;;CLEAR THE COMMON TAGS (SCHTAG) AREA
410 001422 012706 001100      MOV    #SCHTAG,R6      ;;FIRST LOCATION TO BE CLEARED
411 001426 005026             CLR    (R6)+           ;;CLEAR MEMORY LOCATION
412 001430 022706 001140      CMP    #SWR,R6      ;;DONE?
413 001434 001374             BNE    #-6            ;;LOOP BACK IF NO
414 001436 012706 001100      MOV    #STACK,SP    ;;SETUP THE STACK POINTER
415          ;;INITIALIZE A FEW VECTORS
416 001442 012737 007450 000020  MOV    #SCOPE,#IOTVEC ;;IOT VECTOR FOR SCOPE ROUTINE
417 001450 012737 000340 000022  MOV    #340,#IOTVEC+2 ;;LEVEL 7
418 001456 012737 007126 000030  MOV    #ERROR,#EMTVEC ;;EMT VECTOR FOR ERROR ROUTINE
419 001464 012737 000340 000032  MOV    #340,#EMTVEC+2 ;;LEVEL 7
420 001472 012737 012136 000034  MOV    #TRAP,#TRAPVEC ;;TRAP VECTOR FOR TRAP CALLS
421 001500 012737 000340 000036  MOV    #340,#TRAPVEC+2;LEVEL 7
422 001506 012737 011710 000024  MOV    #SPWRON,#PWVEC  ;;POWER FAILURE VECTOR
423 001514 012737 000340 000026  MOV    #340,#PWVEC+2  ;;LEVEL 7
424 001522 005037 001160             CLR    STIMES        ;;INITIALIZE NUMBER OF ITERATIONS
425 001526 005037 001162             CLR    #ESCAPE      ;;CLEAR THE ESCAPE ON ERROR ADDRESS
426 001532 112737 000001 001115  MOV    #1,#SERMAX    ;;ALLOW ONE ERROR PER TEST
427 001540 012737 001540 001106  MOV    #0,#SLPADR    ;;INITIALIZE THE LOOP ADDRESS FOR SCOPE
428 001546 012737 001546 001110  MOV    #0,#SLPERR    ;;SETUP THE ERROR LOOP ADDRESS
429          ;;SIZE FOR A HARDWARE SWITCH REGISTER. IF NOT FOUND OR IT IS
430          ;;EQUAL TO A "-1", SETUP FOR A SOFTWARE SWITCH REGISTER.
431 001554 013746 000004             MOV    #ERRVEC,-(SP) ;;SAVE ERROR VECTOR
432 001560 012737 001614 000004  MOV    #64#,#ERRVEC  ;;SET UP ERROR VECTOR
433 001566 012737 177570 001140  MOV    #DSWR,SWR     ;;SETUP FOR A HARDWARE SWICH REGISTER
434 001574 012737 177570 001142  MOV    #DISP,DISPLAY ;;AND A HARDWARE DISPLAY REGISTER
435 001602 022777 177777 177330  CMP    #-1,#SWR     ;;TRY TO REFERENCE HARDWARE SWR
436 001610 001012             BNE    #66#         ;;BRANCH IF NO TIMEOUT TRAP OCCURRED
437          ;;AND THE HARDWARE SWR IS NOT = -1
438 001612 000403             BR     #65#         ;;BRANCH IF NO TIMEOUT
439 001614 012716 001622 64#:     MOV    #65#,(SP)    ;;SET UP FOR TRAP RETURN
440 001620 000002             RTI
441 001622 012737 000176 001140 65#:     MOV    #SWREG,SWR   ;;POINT TO SOFTWARE SWR
442 001630 012737 000174 001142  MOV    #DISPREG,DISPLAY
443 001636 012637 000004 66#:     MOV    (SP)+,#ERRVEC ;;RESTORE ERROR VECTOR
444
445 001642 005037 001202             CLR    #PASS        ;;CLEAR PASS COUNT
446 001646 132737 000200 001215  BITB  #APTSIZE,SENVN ;;TEST USER SIZE UNDER APT
447 001654 001403             BEQ   #67#         ;;YES,USE NON-APT SWITCH
448 001656 012737 001216 001140 67#:     MOV    #SSWREG,SWR  ;;NO,USE APT SWITCH REGISTER
449 001664
450 001664 012737 012066 000004  MOV    #IOTRD,ERRVEC ;;SET TO HANDLE BUS ERRORS.
451 001672 012737 000200 000006  MOV    #200,ERRVEC+2
452
453 001700 013737 001250 001346  MOV    #BASE,IBS    ;;GET BASE ADDR.
454 001706 013737 001346 001350  MOV    #IBS,IBD     ;;FIX DATA BUFFER=
455 001714 062737 000002 001350  ADD    #2,IBD       ;;CSR+2
456 001722 013737 001350 001352  MOV    #IBD,IBWC
457 001730 062737 000002 001352  ADD    #2,IBWC
458 001736 013737 001352 001354  MOV    #IBWC,IBCA
459 001744 062737 000002 001354  ADD    #2,IBCA
460 001752 013737 001244 001356  MOV    #VECT1,VECTA ;;GET VECTOR ADDR.
461 001760 042737 170000 001356  BIC   #170000,VECTA ;;STRIP JUNK
462 001766 013737 001346 012646  MOV    #IBS,IBSA

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463 001774 013737 001350 012650      MOV      IBD,IBDA
464 002002 013737 001366 001370      MOV      IBS2,IBD2
465 002010 062737 000002 001370      ADD      #2,IBD2
466 002016 013737 001356 001360      MOV      VECTA,VECTB
467 002024 062737 000004 001360      ADD      #4,VECTB
468 002032 013737 001360 001362      MOV      VECTB,VECTC
469 002040 062737 000004 001362      ADD      #4,VECTC
470 002046 013737 001362 001364      MOV      VECTC,VECTD
471 002054 062737 000004 001364      ADD      #4,VECTD
472 002062 013737 001372 001374      MOV      VECTA2,VECTB2
473 002070 062737 000004 001374      ADD      #4,VECTB2
474 002076 013737 001374 001376      MOV      VECTB2,VECTC2
475 002104 062737 000004 001376      ADD      #4,VECTC2
476 002112 013737 001376 001400      MOV      VECTC2,VECTD2
477 002120 062737 000004 001400      ADD      #4,VECTD2
478
479 002126 013737 001356 001402      MOV      VECTA,PRA          ;SET UP VECTOR+2 ADDRESSES.
480 002134 062737 000002 001402      ADD      #2,PRA
481 002142 013737 001402 001404      MOV      PRA,PRB
482 002150 062737 000004 001404      ADD      #4,PRB
483 002156 013737 001404 001406      MOV      PRB,PRC
484 002164 062737 000004 001406      ADD      #4,PRC
485 002172 013737 001406 001410      MOV      PRC,PRD
486 002200 062737 000004 001410      ADD      #4,PRD
487 002206 013737 001372 001412      MOV      VECTA2,PRA2
488 002214 062737 000002 001412      ADD      #2,PRA2
489 002222 013737 001412 001414      MOV      PRA2,PRB2
490 002230 062737 000004 001414      ADD      #4,PRB2
491 002236 013737 001414 001416      MOV      PRB2,PRC2
492 002244 062737 000004 001416      ADD      #4,PRC2
493 002252 013737 001416 001420      MOV      PRC2,PRD2
494 002260 062737 000004 001420      ADD      #4,PRD2
495 002266
496
497 002266 013777 001402 177062      MOV      PRA,AVECTA ;/RESTORE VECTOR FOR
498 002274 012777 004700 177100      MOV      #4700,APRA ;/ILLEGAL INTRO.
499
500 002302 013777 001404 177050      MOV      PRB,AVECTB ;/RESTORE VECTOR FOR
501 002310 012777 004700 177066      MOV      #4700,APRB ;/ILLEGAL INTRO.
502
503 002316 013777 001406 177036      MOV      PRC,AVECTC ;/RESTORE VECTOR FOR
504 002324 012777 004700 177054      MOV      #4700,APRC ;/ILLEGAL INTRO.
505
506 002332 013777 001410 177024      MOV      PRD,AVECTD ;/RESTORE VECTOR FOR
507 002340 012777 004700 177042      MOV      #4700,APRD ;/ILLEGAL INTRO.
508
509
510 002346 005227 177777      .SBTTL TYPE PROGRAM NAME
511 002352 001033      ;;TYPE THE NAME OF THE PROGRAM IF FIRST PASS
512 002354 104401 002422      INC      #-1 ;/FIRST TIME?
513
514 002360 005737 000042      BNE      64$ ;/BRANCH IF NO
515 002364 001012      TYPE    ,65$ ;/TYPE ASCIZ STRING
516 002366 123727 001214 000001      .SBTTL GET VALUE FOR SOFTWARE SWITCH REGISTER
TST      #42 ;/ARE WE RUNNING UNDER XXDP/ACT?
BNE      66$ ;/BRANCH IF YES
CMPB    SENV,#1 ;/ARE WE RUNNING UNDER APT?

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517 002374 001406          BEQ      66$          ;; BRANCH IF YES
518 002376 023727 001140 000176  CMP      SWR,#SWREG  ;; SOFTWARE SWITCH REG SELECTED?
519 002404 001005          BNE      67$          ;; BRANCH IF NO
520 002406 104406          GTSWR                    ;; GET SOFT-SWR SETTINGS
521 002410 000403          BR       67$
522 002412 112737 000001 001134 66$:  MOVB    #1,SAUTOB    ;; SET AUTO-MODE INDICATOR
523 002420 67$:
524 002420 000410          BR       64$          ;; GET OVER THE ASCIZ
525 65$: .ASCIZ  <CRLF>#MD11-DVIBA-A<CRLF>
526 002442 64$:
527 002442 000005          RESET
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*****
;#TEST 1 #TEST THE ADDRESSABILITY OF THE IBS, IBD REGISTERS
*****
TST1:  NOP
      MOV    #50,STIMES    ;; DO 50 ITERATIONS
      MOV    #1$,SLPADR   ;; SET SCOPE LOOP ADDRESS
      MOV    #1,STSTNM    ;; SET TEST #1.
      MOV    #1,STSTN     ;; DON'T FORGET APT!
      MOV    #1$,SLPERR
1$:   MOV    ERRVEC,-(SP)  ;; SAVE CONTENTS OF ADDR. 4
      MOV    #2$,ERRVEC   ;; SET TIME-OUT TRAP VECTOR TO HANDLE
                          ;; IN CASE WE TIME OUT WHEN
                          ;; WE ADDR. THE IBV-11
      TST    @IBS         ;; ADDR THE IBS, IF NO RESPONSE,
                          ;; WILL TRAP TO 2$ FROM HERE
      MOV    #3$,ERRVEC   ;; CHANGE FOR ADDRESSING THE IBD REG.
      TST    @IBD         ;; ADDR THE IBD REG.
                          ;; WE'LL TRAP TO 3$ FROM HERE IF BAD.
2$:   BR     4$
      ADD    #4,SP        ;/ADD #4 TO STACK POINTER.

;;SSSSSSSSS>>> ERROR <<<SSSSSSSSSS

ERROR 5 ;/MODULE FAULT DETECTED:
        ;IBS REGISTER COULD NOT BE
        ;ADDRESSED

;;SSSSSSSSSS+++ ERROR +++SSSSSSSSSS
3$:   ADD    #4,SP        ;/ADD #4 TO STACK POINTER.

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

```

571 002550 104005
572

ERROR 5 ;/MODULE FAULT DETECTED:
;ADDRESSED

575 002552 012637 000004
576
577 002556 012746 000000
578 002562 012746 002570
579 002566 000002
580 002570
581
582
583
584
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586
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588
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590

4S: ;:SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS
MOV (SP)+,ERRVEC ;RESTORE CONTENTS OF LOC 4.
;PR
MOV #0,-(SP) ;SET CPU PRIORITY ON RETURN
MOV #64S,-(SP) ;SHOW RETURN ADDRESS
RTI ;CAUSE A RETURN (PUTS NEW STATUS
;IN STATUS REG.)
64S:

;#TEST 2 *TEST THAT BASE ADDRESSES +4,+6 RESPOND WHEN ADDRESSED

;*EVEN THOUGH THE BASE ADDRESS +4 AND +6 ARE NOT USED,
;*THE IBV11A SHOULD RESPOND TO THEM
;*

591 002570 000004
592 002572 012737 000010 001160
593
594 002600 013746 000004
595 002604 012737 002632 000004
596
597
598 002612 005777 176534
599
600
601 002616 012737 002642 000004
602
603 002624 005777 176524
604 002630 000407
605
606 002632
607 002632 062706 000004
608

↑ST2: SCOPE
MOV #10,STIMES ;;DO 10 ITERATIONS
MOV ERRVEC,-(SP) ;SAVE CONTENTS OF ADDR 4.
MOV #1S,ERRVEC ;SET TIME OUT TRAP VECTOR TO HANDLE
;IN CASE WE TIME OUT WHEN WE
;ADDRESS THE IBV-11 ADDRESSES +4,+6.
TST @IBWC ;TEST BASE ADDRESS +4, IF NO RESPONSE
;WILL TRAP TO 1S FROM HERE
MOV #2S,ERRVEC ;CHANGE FOR ADDRESSING +6 ADDR.
TST @IBCA ;ADDR THE +6 ADDR. - TRAP IF BAD.
BR 3S ;CONTINUE IF GOOD.
1S: ADD #4,SP ;/ADD #4 TO STACK POINTER.

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

612
613 002636 104005
614
615

ERROR 5 ;/MODULE FAULT DETECTED:
;BASE ADDR+4 COULD NOT
;BE ADDRESSED.

618 002640 000403
619
620 002642
621 002642 062706 000004
622

BR 3S
2S: ADD #4,SP ;/ADD #4 TO STACK POINTER.

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

```

626
627 002646 104005          ERROR 5          ;/MODULE FAULT DETECTED:
628                                     ;BASE ADDR+6 COULD NOT
629                                     ;BE ADDRESSED.

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

632
633 002650 012637 000004    3S:  MOV    (SP)+,ERRVEC  ;RESTORE CONTENTS OF LOC 4.
634
635                                     ;*****
636                                     ;*TEST 3          *TEST THAT IBS IS CLEAR AT INIT OF TESTING
637                                     ;*****
638 002654 000004          TST3:  SCOPE
639 002656 012737 000001 001160  MOV    #1,STIMES      ;;DO 1 ITERATION
640
641 002664 000005          RESET          ;ISSUE SYSTEM INIT.
642
643 002666 005037 001124    CLR    $GDDAT         ;EXPECT ZERO CSR.
644 002672 017737 176450 001126  MOV    @IBS,$BDDAT    ;READ CSR.
645 002700 001401          BEQ    TST4           ;;
646

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

650
651 002702 104003          ERROR 3          ;/MODULE FAULT DETECTED:
652                                     ;IBS NOT CLEAR ON INT.

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

655
656                                     ;*****
657                                     ;*TEST 4          *TEST THAT IBD IS CLEAR AT INIT OF TESTING
658                                     ;*****
659 002704 000004          TST4:  SCOPE
660 002706 012737 000001 001160  MOV    #1,STIMES      ;;DO 1 ITERATION
661
662 002714 000005          RESET          ;ISSUE SYSTEM INITIALIZE.
663
664 002716 005037 001124    CLR    $GDDAT         ;EXPECT ZERO CSR.
665 002722 117737 176422 001126  MOV    @IBD,$BDDAT    ;READ DBR
666 002730 001401          BEQ    TST5           ;;
667

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

671
672 002732 104004          ERROR 4          ;/MODULE FAULT DETECTED:
673                                     ;IBD NOT CLEAR ON INIT.

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

676
677
678                                     ;*****

```

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002734 000004
002736 012737 000010 001160
002744 005037 001124
002750 017737 176376 001126
002756 001402

```
; *TEST 5 *TEST THAT BASE ADDRESSES +4,+6 RETURN ZERO WHEN READ
; *
; *BASE ADDRESS +4 AND +6 SHOULD RETURN A ZERO WHEN
; *READ, IN THIS TEST WE WILL TRY THAT.
; *
; *****
†ST5: SCOPE
MOV #10,STIMES ;;DO 10 ITERATIONS
CLR $GDDAT ;EXPECT ZERO RETURN
MOV @IBWC,$BDDAT ;READ BASE ADDRESS+4
BEQ IS ;IF ZERO - GOOD.
```

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

695
696
697
698

002760 104006

```
ERROR 6 ;/MODULE FAULT DETECTED:
;SHOULD HAVE READ BACK ZERO FROM
;THIS ADDR.
```

;;SSSSSSSSSS††† ERROR †††SSSSSSSSSS

701
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705

002762 000405
002764 017737 176364 001126 IS:
002772 001401

```
BR TST6 ;;
MOV @IBCA,$BDDAT ;READ BASE ADDR+6, SHOULD BE ZERO
BEQ TST6 ;;
```

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

709
710
711
712

002774 104006

```
ERROR 6 ;/MODULE FAULT DETECTED:
;SHOULD HAVE READ BACK ZERO FROM
;BASE ADDR+6.
```

;;SSSSSSSSSS††† ERROR †††SSSSSSSSSS

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002776 000004
003000 005077 176342
003004 052777 000001 176334
003012 052737 002001 001124
003020 017737 176322 001126
003026 023737 001124 001126
003034 000402

```
; *****
; *TEST 6 *TEST THAT WE CAN SET TCS, TCS SETS CMD
; *****
†ST6: SCOPE
CLR @IBS ;CLEAR CLR
BIS @BIT0,@IBS ;SET TCS.
BIS @BIT0!BIT10,$GDDAT ;EXPECT ONLY TCS AND CMD TO SET
MOV @IBS,$BDDAT ;READ THE IBS.
CMP $GDDAT,$BDDAT ;DID TCS AND CMD SET?
BR IS ;YES - CONTINUE
```

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

731
732

003036 104003

```
ERROR 3 ;/MODULE FAULT DETECTED:
```

733 ;TCS AND/OR CMD FAILED TO SET

```

736 003040 000412          ;:SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS
737          BR          TST7          ;;
738 003042 042777 000001 176276 1S: BIC      @BIT0,@IBS      ;CLEAR TCS.
739 003050 005037 001124          CLR      $GDDAT      ;EXPECT TCS AND CMD TO CLEAR
740 003054 017737 176266 001126 MOV      @IBS,$BDDAT ;READ IBS, DID THEY CLEAR?
741 003062 001401          BEQ      TST7          ;;
742

```

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

```

746          ERROR 3          ;/MODULE FAULT DETECTED:
747 003064 104003          ;TCS AND/OR CMD FAILED TO CLEAR.
748

```

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

```

751          ;:*****
752          ;*TEST 7          *TEST THAT EOP WILL SET
753          ;:*****
754          ;*TEST 7          *TEST THAT EOP WILL SET
755 003066 000004          TST7:  SCOPE
756
757 003070 005077 176252          CLR      @IBS          ;CLEAR CSR.
758 003074 052777 000002 176244 BIS      @BIT1,@IBS   ;SET EOP
759 003102 012737 000002 001124 MOV      @BIT1,$GDDAT ;EXPECT ONLY EOP TO SET.
760 003110 017737 176232 001126 MOV      @IBS,$BDDAT ;READ IBS
761 003116 023737 001124 001126 CMP      $GDDAT,$BDDAT ;DID EOP SET?
762 003124 001402          BEQ      1S          ;YES - CONTINUE
763

```

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

```

767          ERROR 3          ;/MODULE FAULT DETECTED:
768 003126 104003          ;EOP BIT SETTING ERROR.
769

```

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

```

772 003130 000412          BR      TST10        ;;
773
774 003132 042777 000002 176206 1S: BIC      @BIT1,@IBS   ;CLEAR EOP
775 003140 005037 001124          CLR      $GDDAT      ;EXPECT A ZERO CSR.
776 003144 017737 176176 001126 MOV      @IBS,$BDDAT ;READ IBS, IS IT CLEAR?
777 003152 001401          BEQ      TST10        ;;
778

```

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

```

782          ERROR 3          ;/MODULE FAULT DETECTED:
783 003154 104003          ;IBS FAILED TO CLEAR
784

```

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

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003156 000004
003160 005077 176162
003164 052777 000004 176154
003172 012737 000004 001124
003200 017737 176142 001126
003206 023737 001126 001124
003214 001402

```

*****
;TEST 10      *TEST THAT RE WILL SET + CLEAR
*****
TST10: SCOPE
CLR      @IBS      ;CLEAR CSR.
BIS      @BIT02,@IBS ;SET REM
MOV      @BIT02,$GDDAT ;EXPECT ONLY REM TO SET.
MOV      @IBS,$BDDAT ;READ IBS.
CMP      $BDDAT,$GDDAT ;DID REM AND ONLY REM SET?
BEQ      IS        ;YES - CONTINUE

```

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

803
804
805

003216 104003

```

ERROR 3 ;/MODULE FAULT DETECTED:
;REM BIT SETTING ERROR.

```

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

808
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003220 000412
003222 042777 000004 176116 IS:
003230 005037 001124
003234 017737 176106 001126
003242 001401

```

BR      TST11      ;;
BIC      @BIT02,@IBS ;CLEAR REM BIT.
CLR      $GDDAT ;EXPECT ZERO CSR.
MOV      @IBS,$BDDAT ;READ IBS - IS IT CLEAR?
BEQ      TST11      ;;

```

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

818
819
820

003244 104003

```

ERROR 3 ;/MODULE FAULT DETECTED:
;IBS FAILED TO CLEAR.

```

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

823
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003246 000004
003250 005077 176072
003254 052777 000010 176064
003262 012737 000010 001124
003270 017737 176052 001126
003276 023737 001124 001126
003304 001414
003306 012777 000010 176032
003314 017737 176026 001126
003322 023737 001124 001126
003330 001402

```

*****
;TEST 11      *TEST THAT IBC WILL SET AND CLEAR
*****
TST11: SCOPE
CLR      @IBS      ;CLEAR CSR.
BIS      @BIT03,@IBS ;SET IBC
MOV      @BIT03,$GDDAT ;EXPECT ONLY IBC TO BE SET
MOV      @IBS,$BDDAT ;READ IBS.
CMP      $GDDAT,$BDDAT ;DID IBS SET?
BEQ      IS        ;YES CONTINUE
MOV      @BIT03,@IBS ;TRY SETTING IBC AGAIN.
MOV      @IBS,$BDDAT ;MEMORY REFRESH MIGHT HAVE
CMP      $GDDAT,$BDDAT ;GOT IN THE WAY.
BEQ      IS

```



```

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS
843
844 003332 104003 ERROR 3 ;/MODULE FAULT DETECTED:
845 ;IBS BIT SETTING ERROR.

```

```

..SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS
848 003334 000416 BR TST12 ;;
849
850 003336 004537 007106 15: JSR R5,DEL50 ;DELAY 150 US.
851 003342 000006 .WORD 6
852
853 003344 012737 002001 001124 MOV #BIT10:BIT0,$GDDAT ;EXP CMD AND TCS.
854 003352 017737 175770 001126 MOV @IBS,$BDDAT ;READ IBS - IS IT CLEAR?
855 003360 023737 001124 001126 CMP $GDDAT,$BDDAT
856 003366 001401 BEQ TST12 ;;
857

```

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

```

861
862 003370 104003 ERROR 3 ;/MODULE FAULT DETECTED:
863 ;IBS NOT CLEAR AFTER IBC

```

```

..SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS
:*****
: *TEST 12 *TEST THAT TON (BIT05) AND TKR SET AND CLEAR
:*****
TST12: SCOPE

```

```

866
867
868
869 003372 000004
870
871 003374 005077 175746 CLR @IBS ;CLEAR THE CSR.
872 003400 052777 000040 175740 BIS #BIT5,@IBS ;SET TON.
873 003406 012737 001040 001124 MOV #BIT5:BIT9,$GDDAT ;EXPECT ONLY TON AND TKR TO SET.
874 003414 017737 175726 001126 MOV @IBS,$BDDAT ;READ CSR.
875 003422 023737 001124 001126 CMP $GDDAT,$BDDAT ;DID THEY BOTH SET?
876 003430 001402 BEQ 15 ;YES - CONTINUE.
877
878

```

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

```

882
883 003432 104003 ERROR 3 ;/MODULE FAULT DETECTED:
884 ;ERROR IN SETTING TON BIT.

```

```

..SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS
887 003434 000412 BR TST13 ;;
888
889 003436 042777 000040 175702 15: BIC #BIT5,@IBS ;WHEN TON CLEARED, TKR SHOULD CLEAR.
890 003444 005037 001124 CLR $GDDAT ;EXPECT ZERO CSR.
891 003450 017737 175672 001126 MOV @IBS,$BDDAT ;DID IT CLEAR?
892 003456 001401 BEQ TST13 ;;
893

```

```

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS
897
898 003460 104003 ERROR 3 ;/MODULE FAULT DETECTED:
899 ;CSR FAILED TO CLEAR.

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS
902
903 ::*****
904 ;*TEST 13 *MAKE SURE WE CAN SET AND CLEAR BIT06 (IE)
905 ::*****
906 003462 000004 †TST13: SCOPE
907
908
909 003464 012746 000340 MOV #340,-(SP) ;/PR
910 003470 012746 003476 MOV #64$,-(SP) ;/SET CPU PRIORITY ON RETURN
911 003474 000002 RTI ;/SHOW RETURN ADDRESS
912 003476 64$: ;/CAUSE A RETURN (PUTS NEW STATUS
913 ;/IN STATUS REG.)
914 003476 005077 175644 CLR @IBS ;CLEAR CSR.
915
916 003502 052777 000100 175636 BIS #BIT6,@IBS ;SET IE.
917 003510 012737 000100 001124 MOV #BIT6,$GDDAT ;EXPECT ONLY BIT 6 TO SET.
918 003516 017737 175624 001126 MOV @IBS,$BDDAT ;READ IBS.
919 003524 023737 001124 001126 CMP $GDDAT,$BDDAT ;DID IE SET?
920 003532 001402 BEQ IS ;YES - CONTINUE.
921

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS
925
926 003534 104003 ERROR 3 ;/MODULE FAULT DETECTED:
927 ;ERROR IN SETTING IE BIT.

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS
930 003536 000412 BR TST14 ;;
931
932 003540 005037 001124 15: CLR $GDDAT ;EXPECT ZERO CSR AFTER.
933 003544 042777 000100 175574 BIC #BIT6,@IBS ;IE IS CLEARED.
934 003552 017737 175570 001126 MOV @IBS,$BDDAT ;READ CSR - IS IT CLEAR?
935 003560 001401 BEQ TST14 ;;
936

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS
940
941 003562 104003 ERROR 3 ;/MODULE FAULT DETECTED:
942 ;FAILED TO CLEAR CSR.

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS
945
946 ::*****
947 ;*TEST 14 *TEST THAT BIT 7 (ACC) CAN BE SET AND CLEARED
948 ::*****

```

MAINDEC-11-DVIBA-A
DVIBA.P11 T14

MACY11 27(663) 29-MAR-77 12:57 PAGE 19
*TEST THAT BIT 7 (ACC) CAN BE SET AND CLEARED

SEQ 0034

```

949 003564 000004          TST14: SCOPE
950
951 003566 005077 175554          CLR  @IBS          ;CLEAR CSR.
952 003572 052777 000200 175546          BIS  @BIT7,@IBS   ;SET ACC.
953 003600 012737 000200 001124          MOV  @BIT7,$GDDAT ;EXPECT ONLY ACC TO SET.
954 003606 017737 175534 001126          MOV  @IBS,$BDDAT  ;READ IBS.
955 003614 023737 001124 001126          CMP  $GDDAT,$BDDAT ;DID ACC SET?
956 003622 001402                      BEQ  IS            ;YES - CONTINUE.
957

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

961
962 003624 104003          ERROR 3           ;/MODULE FAULT DETECTED:
963                                     ;FAILURE IN SETTING BIT 7 (ACC).

..SSSSSSSSSS+++ ERROR +++SSSSSSSSSS
966 003626 000412          BR   TST15        ;;
967
968 003630 042777 000200 175510 1S:          BIC  @BIT7,@IBS   ;TRY CLEARING ACC.
969 003636 005037 001124                      CLR  $GDDAT       ;EXPECT ZERO CSR.
970 003642 017737 175500 001126          MOV  @IBS,$BDDAT ;READ IBS, IS IT CLEAR?
971 003650 001401          BEQ  TST15        ;;
972

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

976
977 003652 104003          ERROR 3           ;/MODULE FAULT DETECTED:
978                                     ;IBS FAILED TO CLEAR.

;;SSSSSSSSSS+++ ERROR +++SSSSSSSSSS

981
982
983
984
985
986
987 003654 000004          ;*****
;TEST 15          *TEST THAT IBD BIT 0 CAN BET SET + CLEARED
;*****
TST15: SCOPE
988
989
990 003656 012777 000060 175462          MOV  @BIT4!BITS,@IBS ;/MACRO BDT
991 003664 012737 000001 001124          MOV  @BIT0,$GDDAT  ;/SET TON AND LON.
992 003672 013777 001124 175450          MOV  $GDDAT,@IBD  ;/WE'RE GONNA TEST BIT 0.
993                                     ;/SET THE BIT.
994 003700 117737 175444 001126          MOVB @IBD,$BDDAT  ;/READ THE IBD.
995 003706 123737 001124 001126          CMPB $GDDAT,$BDDAT ;/DID IT GET THRU OK?
996 003714 001402          BEQ  IS            ;/YES - CONTINUE.
997
998

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

```

1003 003716 104004
1004

ERROR 4 ;/MODULE FAULT DETECTED:
;/ERROR IN SETTING IBD BIT 0.

1007 003720 000412
1008 003722 005037 001124 1S:
1009 003726 042777 000001 175414
1010 003734 117737 175410 001126
1011 003742 001401
1012

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS
BR TST16 ;/
CLR SGDDAT ;/EXPECT ZERO IBD WHEN
BIC #BIT0, IBD ;/BIT 0 IS CLEARED.
MOVB IBD, SBDDAT ;/READ IBD, IS IT CLEAR?
BEQ TST16 ;/

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

1016
1017 003744 104004
1018

ERROR 4 ;/MODULE FAULT DETECTED:
;/FAILED TO CLEAR IBD.

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

1021
1022
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1024
1025
1026 003746 000004
1027

;: *TEST 16 *TEST THAT IBD BIT 1 CAN BET SET + CLEARED
;: *****
TST16: SCOPE

1028
1029 003750 012777 000060 175370
1030 003756 012737 000002 001124
1031 003764 013777 001124 175356
1032
1033 003772 117737 175352 001126
1034 004000 123737 001124 001126
1035 004006 001402
1036
1037

MOV #BIT4:BITS, IBS ;/MACRO BDT
MOV #BIT1, SGDDAT ;/SET TON AND LON.
MOV SGDDAT, IBD ;/WE'RE GONNA TEST BIT 1.
;/SET THE BIT.
MOVB IBD, SBDDAT ;/READ THE IBD.
CMPB SGDDAT, SBDDAT ;/DID IT GET THRU OK?
BEQ IS ;/YES - CONTINUE.

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

1041
1042 004010 104004
1043

ERROR 4 ;/MODULE FAULT DETECTED:
;/ERROR IN SETTING IBD BIT 1.

1046 004012 000412
1047 004014 005037 001124 1S:
1048 004020 042777 000002 175322
1049 004026 117737 175316 001126
1050 004034 001401
1051

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS
BR TST17 ;/
CLR SGDDAT ;/EXPECT ZERO IBD WHEN
BIC #BIT1, IBD ;/BIT 1 IS CLEARED.
MOVB IBD, SBDDAT ;/READ IBD, IS IT CLEAR?
BEQ TST17 ;/

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

1055
1056 004036 104004

ERROR 4 ;/MODULE FAULT DETECTED:

1057

;FAILED TO CLEAR IBD.

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

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004040 000004

;TEST 17 *TEST THAT IBD BIT 2 CAN BET SET + CLEARED

TST17: SCOPE

004042 012777 000060 175276
004050 012737 000004 001124
004056 013777 001124 175264
004064 117737 175260 001126
004072 123737 001124 001126
004100 001402

MOV #BIT4!BITS, @IBS ;/MACRO BDT
MOV #BIT2, \$GDDAT ;/SET TON AND LON.
MOV \$GDDAT, @IBD ;/WE'RE GONNA TEST BIT 2.
; /SET THE BIT.
MOVB @IBD, \$BDDAT ;/READ THE IBD.
CMPB \$GDDAT, \$BDDAT ;/DID IT GET THRU OK?
BEQ IS ;/YES - CONTINUE.

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

1080
1081
1082

004102 104004

ERROR 4 ;/MODULE FAULT DETECTED:
;/ERROR IN SETTING IBD BIT 2.

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

1085
1086
1087
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1089
1090

004104 000412
004106 005037 001124
004112 042777 000004 175230
004120 117737 175224 001126
004126 001401

IS:

BR TST20 ;;
CLR \$GDDAT ;/EXPECT ZERO IBD WHEN
BIC #BIT2, @IBD ;/BIT 2 IS CLEARED.
MOVB @IBD, \$BDDAT ;/READ IBD, IS IT CLEAR?
BEQ TST20 ;;

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

1094
1095
1096

004130 104004

ERROR 4 ;/MODULE FAULT DETECTED:
;/FAILED TO CLEAR IBD.

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

1099
1100
1101
1102
1103
1104
1105
1106
1107
1108
1109
1110

004132 000004

;TEST 20 *TEST THAT IBD BIT 3 CAN BET SET + CLEARED

TST20: SCOPE

004134 012777 000060 175204
004142 012737 000010 001124
004150 013777 001124 175172

MOV #BIT4!BITS, @IBS ;/MACRO BDT
MOV #BIT3, \$GDDAT ;/SET TON AND LON.
MOV \$GDDAT, @IBD ;/WE'RE GONNA TEST BIT 3.
; /SET THE BIT.

MAINDEC-11-DVIBA-A
DVIBA.P11 T20

MACY11 27(663) 29-MAR-77 12:57 PAGE 22
*TEST THAT IBD BIT 3 CAN BET SET + CLEARED

SEQ 0037

```

1111 004156 117737 175166 001126      MOVB  JIBD,SBDDAT  ;/READ THE IBD.
1112 004164 123737 001124 001126      CMPB  SGDDAT,SBDDAT ;/DID IT GET THRU OK?
1113 004172 001402                      BEQ   IS           ;/YES - CONTINUE.
1114
1115

```

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

```

1119
1120 004174 104004      ERROR 4           ;/MODULE FAULT DETECTED:
1121                                     ;/ERROR IN SETTING IBD BIT 3.

```

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

```

1124 004176 000412      BR      TST21      ;/
1125 004200 005037 001124      CLR      SGDDAT    ;/EXPECT ZERO IBD WHEN
1126 004204 042777 000010 175136  IS:    BIC      #BIT3,JIBD ;/BIT 3 IS CLEARED.
1127 004212 117737 175132 001126      MOVB   JIBD,SBDDAT ;/READ IBD, IS IT CLEAR?
1128 004220 001401      BEQ     TST21      ;/
1129

```

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

```

1133
1134 004222 104004      ERROR 4           ;/MODULE FAULT DETECTED:
1135                                     ;/FAILED TO CLEAR IBD.

```

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

```

1138
1139
1140
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1142
1143 004224 000004      ;/*****
1144                                     ;/*TEST 21      *TEST THAT IBD BIT 4 CAN BET SET + CLEARED
1145                                     ;/*****
1146
1147
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1152
1153
1154

```

```

TST21: SCOPE

```

```

1146 004226 012777 000060 175112      MOV     #BIT4!BITS,JIBS ;/MACRO BIT
1147 004234 012737 000020 001124      MOV     #BIT4,SGDDAT    ;/SET TON AND LON.
1148 004242 013777 001124 175100      MOV     SGDDAT,JIBD    ;/WE'RE GONNA TEST BIT 4.
1149                                     ;/SET THE BIT.
1150 004250 117737 175074 001126      MOVB   JIBD,SBDDAT    ;/READ THE IBD.
1151 004256 123737 001124 001126      CMPB   SGDDAT,SBDDAT  ;/DID IT GET THRU OK?
1152 004264 001402                      BEQ     IS           ;/YES - CONTINUE.
1153
1154

```

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

```

1158
1159 004266 104004      ERROR 4           ;/MODULE FAULT DETECTED:
1160                                     ;/ERROR IN SETTING IBD BIT 4.

```

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

```

1163 004270 000412      BR      TST22      ;/
1164 004272 005037 001124      CLR      SGDDAT    ;/EXPECT ZERO IBD WHEN

```

MAINDEC-11-DVIBA-A
DVIBA.P11 T21

MACY11 27(663) 29-MAR-77 12:57 PAGE 23
*TEST THAT IBD BIT 4 CAN BET SET + CLEARED

SEQ 0038

1165	004276	042777	000020	175044	BIC	#BIT4, IBD	;/BIT 4 IS CLEARED.
1166	004304	117737	175040	001126	MOVB	IIBD, \$BDDAT	;/READ IBD, IS IT CLEAR?
1167	004312	001401			BEQ	TST22	;;
1168							

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

1172							
1173	004314	104004			ERROR	4	;/MODULE FAULT DETECTED:
1174							;/FAILED TO CLEAR IBD.

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

1177							
1178							
1179							
1180							*****
1181							;*TEST 22 *TEST THAT IBD BIT 5 CAN BET SET + CLEARED
1182	004316	000004					*****
1183					TST22:	SCOPE	

1184							;/MACRO BOT
1185	004320	012777	000060	175020	MOV	#BIT4!BITS, IIBS	;/SET ION AND LON.
1186	004326	012737	000040	001124	MOV	#BITS, \$GDDAT	;/WE'RE GONNA TEST BIT 5.
1187	004334	013777	001124	175006	MOV	\$GDDAT, IIBD	;/SET THE BIT.
1188							
1189	004342	117737	175002	001126	MOVB	IIBD, \$BDDAT	;/READ THE IBD.
1190	004350	123737	001124	001126	CMPEB	\$GDDAT, \$BDDAT	;/DID IT GET THRU OK?
1191	004356	001402			BEQ	IS	;/YES - CONTINUE.
1192							
1193							

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

1197							
1198	004360	104004			ERROR	4	;/MODULE FAULT DETECTED:
1199							;/ERROR IN SETTING IBD BIT 5.

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

1202	004362	000412			BR	TST23	;/
1203	004364	005037	001124		CLR	\$GDDAT	;/EXPECT ZERO IBD WHEN
1204	004370	042777	000040	174752	BIC	#BITS, IIBD	;/BIT 5 IS CLEARED.
1205	004376	117737	174746	001126	MOVB	IIBD, \$BDDAT	;/READ IBD, IS IT CLEAR?
1206	004404	001401			BEQ	TST23	;;
1207							

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

1211							
1212	004406	104004			ERROR	4	;/MODULE FAULT DETECTED:
1213							;/FAILED TO CLEAR IBD.

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

1216							
1217							
1218							*****

MAINDEC-11-DVIBA-A
DVIBA.P11 T23

MACY11 27(663) 29-MAR-77 12:57 PAGE 24
*TEST THAT IBD BIT 6 CAN BET SET + CLEARED

SEQ 0039

```

1219 ;*TEST 23 *TEST THAT IBD BIT 6 CAN BET SET + CLEARED
1220 ;*****
1221 004410 000004 TST23: SCOPE
1222
1223 ;/MACRO BOT
1224 004412 012777 000060 174726 MOV #BIT4!BITS, @IBS ;/SET TON AND LON.
1225 004420 012737 000100 001124 MOV #BIT6, $GDDAT ;/WE'RE GONNA TEST BIT 6.
1226 004426 013777 001124 174714 MOV $GDDAT, @IBD ;/SET THE BIT.
1227
1228 004434 117737 174710 001126 MOVB @IBD, $BDDAT ;/READ THE IBD.
1229 004442 123737 001124 001126 CMPB $GDDAT, $BDDAT ;/DID IT GET THRU OK?
1230 004450 001402 BEQ IS ;/YES - CONTINUE.
1231
1232

```

::SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

```

1236 ERROR 4 ;/MODULE FAULT DETECTED:
1237 004452 104004 ;/ERROR IN SETTING IBD BIT 6.
1238

```

::SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

```

1241 004454 000412 BR TST24 ;/
1242 004456 005037 001124 CLR $GDDAT ;/EXPECT ZERO IBD WHEN
1243 004462 042777 000100 174660 BIC #BIT6, @IBD ;/BIT 6 IS CLEARED.
1244 004470 117737 174654 001126 MOVB @IBD, $BDDAT ;/READ IBD, IS IT CLEAR?
1245 004476 001401 BEQ TST24 ;/
1246

```

::SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

```

1250 ERROR 4 ;/MODULE FAULT DETECTED:
1251 004500 104004 ;/FAILED TO CLEAR IBD.
1252

```

::SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

```

1255 ;*****
1256 ;*TEST 24 *TEST THAT IBD BIT 7 CAN BET SET + CLEARED
1257 ;*****
1258 TST24: SCOPE
1259
1260 004502 000004
1261
1262 ;/MACRO BOT
1263 004504 012777 000060 174634 MOV #BIT4!BITS, @IBS ;/SET TON AND LON.
1264 004512 012737 000200 001124 MOV #BIT7, $GDDAT ;/WE'RE GONNA TEST BIT 7.
1265 004520 013777 001124 174622 MOV $GDDAT, @IBD ;/SET THE BIT.
1266
1267 004526 117737 174616 001126 MOVB @IBD, $BDDAT ;/READ THE IBD.
1268 004534 123737 001124 001126 CMPB $GDDAT, $BDDAT ;/DID IT GET THRU OK?
1269 004542 001402 BEQ IS ;/YES - CONTINUE.
1270
1271

```



```

1275
1276 004544 104004
1277
;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS
ERROR 4 ;/MODULE FAULT DETECTED:
;/ERROR IN SETTING IBD BIT 7.

```

```

1280 004546 000412
1281 004550 005037 001124 1S:
1282 004554 042777 000200 174566
1283 004562 117737 174562 001126
1284 004570 001401
1285
;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS
BR TST25 ;/
CLR SGDDAT ;/EXPECT ZERO IBD WHEN
BIC #BIT7,IBD ;/BIT 7 IS CLEARED.
MOVB IBD,$BDDAT ;/READ IBD, IS IT CLEAR?
BEQ TST25 ;/

```

```

1289
1290 004572 104004
1291
;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS
ERROR 4 ;/MODULE FAULT DETECTED:
;/FAILED TO CLEAR IBD.

```

```

1294
1295
1296
1297
1298
1299 004574 000004
1300
1301 004576 005077 174544
1302 004602 112777 000252 174540
1303 004610 005037 001124
1304 004614 117737 174530 001126
1305 004622 001401
1306
;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS
*****
*TEST 25 *TEST THAT NO DATA GETS XFERRED, IF NOT ENABLED
*****
TST25: SCOPE
CLR IBS ;CLEAR CSR
MOVB #252,IBD ;TRY XFERRING DATA
CLR SGDDAT ;NO DATA SHOULD XFERR
MOVB IBD,$BDDAT ;READ BUFFER REG.
BEQ TST26 ;/

```

```

1310
1311 004624 104002
1312
1313
1314
1315
;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS
ERROR 2 ;/MODULE FAULT DETECTED:
;/DATA WAS XFERRED THROUGH IBD
;/EVEN THOUGH TON AND LON CLEARED.
;/SIGNAL "ENB XFER L" PROBABLY
;/STUCK LOW.

```

```

1318
1319
1320
1321
1322 004626 000004
1323
1324 004630 005077 174512
1325 004634 005077 174510
1326 004640 032777 000400 174502
;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS
*****
*TEST 25 *TEST IBD BITS DAC, AND DAV
*****
TST26: SCOPE
CLR IBS ;CLEAR CSR.
CLR IBD ;CLEAR DATA REG.
BIT #BIT8,IBD ;IS DAC SET?

```

1327 004646 001002
1328

BNE 45 ;YES (GOOD)

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

1332 004650 104002
1333
1334

ERROR 2 ;/MODULE FAULT DETECTED:
;DAC NOT SET.

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

1337 004652 000437
1338 004654
1339 004654 052777 000260 174464 45:
1340 004662 012777 000252 174460
1341 004670 017737 174454 001126
1342 004676 032737 001000 001126
1343 004704 001002
1344

BR TST27 ;;
BIS #BIT5!BIT4!BIT7, @IBS ;SET TON AND LON
MOV #252, @IBD ;PUT DATA IN IBD.
MOV @IBD, @BDDAT ;READ IBD.
BIT #BIT9, @BDDAT ;DID DAV SET?
BNE 15 ;YES - CONTINUE.

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

1348 004706 104002
1349
1350

ERROR 2 ;/MODULE FAULT DETECTED:
;DAV FAILED TO SET.

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

1353 004710 000420
1354
1355 004712 012777 000060 174426 15:
1356 004720 105777 174424 25:
1357 004724 032777 000400 174416
1358 004732 001402
1359

BR TST27 ;;
MOV #BIT4!BIT5, @IBS ;CLEAR ACC.
TSTB @IBD ;READ LOW BYTE OF IBD.
BIT #BIT8, @IBD ;DID DAC CLEAR?
BEQ 35 ;YES - CONTINUE.

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

1363 004734 104002
1364
1365

ERROR 2 ;/MODULE FAULT DETECTED:
;DAC FAILED TO CLEAR.

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

1368 004736 000405
1369 004740 032777 001000 174402 35:
1370 004746 001401
1371

BR TST27 ;;
BIT #BIT9, @IBD ;DID DAV CLEAR?
BEQ TST27 ;;

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

1375 004750 104002
1376
1377

ERROR 2 ;/MODULE FAULT DETECTED:
;DAV FAILED TO CLEAR.

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

1380

;/MACRO -SIGC-

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1397

004752 000004
004754 005077 174366
004760 052777 000004 174360
004766 032777 010000 174354
004774 001011
004776 052777 000004 174342
005004 032777 010000 174336
005012 001002

```
*****  
*TEST 27 *TEST THAT REN SETS WHEN REN SETS, ALSO TEST CLEAR  
*****  
TST27: SCOPE  
CLR @IBS ;/CLEAR CSR.  
BIS #BIT2,@IBS ;/SET REN, SHOULD SET REN.  
BIT #BIT12,@IBD ;/DID REN SET?  
BNE IS ;/YES - LETS TRY CLEARING IT.  
BIS #BIT2,@IBS ;/SET REN, MEMORY  
;/REFRESH COULD HAVE  
;/INTERRUPTED US.  
BIT #BIT12,@IBD ;/DID REN SET THIS TIME?  
BNE IS
```

;;SSSSSSSSS>>> ERROR <<<SSSSSSSSSS

1401
1402
1403

005014 104002

```
ERROR 2 ;/MODULE FAULT DETECTED:  
;/REN FILED TO SET WHEN REN SET.
```

;;SSSSSSSSS+++ ERROR +++SSSSSSSSSS

1406
1407
1408
1409
1410

005016 000410
005020 042777 000004 174320 IS:
005026 032777 010000 174314
005034 001401

```
BR TST30 ;/  
BIC #BIT2,@IBS ;/CLEAR REN, SHOULD CLEAR REN.  
BIT #BIT12,@IBD ;/DID REN CLEAR?  
BEQ TST30 ;/
```

;;SSSSSSSSS>>> ERROR <<<SSSSSSSSSS

1414
1415
1416

005036 104002

```
ERROR 2 ;/MODULE FAULT DETECTED:  
;/REN FAILED TO CLEAR.
```

;;SSSSSSSSS+++ ERROR +++SSSSSSSSSS

1419
1420
1421
1422
1423
1424
1425
1426
1427
1428
1429
1430
1431
1432
1433
1434

005040 000004
005042 005077 174300
005046 052777 000010 174272
005054 032777 020000 174266
005062 001011
005064 052777 000010 174254

```
*****  
*TEST 30 *TEST THAT IFC SETS WHEN IBS SETS, ALSO TEST CLEAR  
*****  
TST30: SCOPE  
CLR @IBS ;/CLEAR CSR.  
BIS #BIT3,@IBS ;/SET IBS, SHOULD SET IFC.  
BIT #BIT13,@IBD ;/DID IFC SET?  
BNE IS ;/YES - LETS TRY CLEARING IT.  
BIS #BIT3,@IBS ;/SET IBS, MEMORY  
;/REFRESH COULD HAVE  
;/INTERRUPTED US.
```

;/MACRO -SIGC-

E04

MAINDEC-11-DVIBA-A
DVIBA.P11 T30

MACY11 27(663) 29-MAR-77 12:57 PAGE 28
*TEST THAT IFC SETS WHEN IBS SETS, ALSO TEST CLEAR

SEQ 0043

1435 005072 032777 020000 174250
1436 005100 001002
1437

BIT #BIT13, @IBD ;/DID IFC SET THIS TIME?
BNE IS

::SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

1441
1442 005102 104002
1443

ERROR 2 ;/MODULE FAULT DETECTED:
;/IFC FILED TO SET WHEN IBS SET.

::SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

1446 005104 000411
1447
1448 005106 032777 000010 174232 1S:
1449 005114 001374
1450
1451 005116 032777 020000 174224
1452 005124 001401
1453

BR TST31 ;;
BIT #BIT3, @IBS ;/WAIT FOR IBS TO CLEAR.
BNE IS
BIT #BIT13, @IBD ;/IBS CLEAR, DID IFC CLEAR?
BEQ TST31 ;;

::SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

1457
1458 005126 104002
1459

ERROR 2 ;/MODULE FAULT DETECTED:
;/IFC FAILED TO CLEAR.

::SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

1462
1463
1464
1465
1466
1467
1468
1469 005130 000004
1470
1471 005132 005077 174210
1472 005136 052777 000001 174202
1473 005144 032777 040000 174176
1474 005152 001011
1475 005154 052777 000001 174164
1476
1477
1478 005162 032777 040000 174160
1479 005170 001002
1480

;/MACRO -SIGC-

::*****
; *TEST 31 *TEST THAT ATN SETS WHEN TCS SETS, ALSO TEST CLEAR
;*****
TST31: SCOPE

CLR @IBS ;/CLEAR CSR.
BIS #BIT0, @IBS ;/SET TCS, SHOULD SET ATN.
BIT #BIT14, @IBD ;/DID ATN SET?
BNE IS ;/YES - LETS TRY CLEARING IT.
BIS #BIT0, @IBS ;/SET TCS, MEMORY
;/REFRESH COULD HAVE
;/INTERRUPTED US.
BIT #BIT14, @IBD ;/DID ATN SET THIS TIME?
BNE IS

::SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

1484
1485 005172 104002
1486

ERROR 2 ;/MODULE FAULT DETECTED:
;/ATN FILED TO SET WHEN TCS SET.

::SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

MAINDEC-11-DVIBA-A
DVIBA.P11 T31

MACY11 27(663) 29-MAR-77 12:57 PAGE 29
*TEST THAT ATN SETS WHEN TCS SETS, ALSO TEST CLEAR

SEQ 0044

1489	005174	000410			BR	TST32	;;
1490	005176	042777	000001	174142	BIC	#BIT0, @IBS	;/CLEAR TCS, SHOULD CLEAR ATN.
1491	005204	032777	040000	174136	BIT	#BIT14, @IBD	;/DID ATN CLEAR?
1492	005212	001401			BEQ	TST32	;;
1493							

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

1497							
1498	005214	104002			ERROR	2	;/MODULE FAULT DETECTED:
1499							;/ATN FAILED TO CLEAR.

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

1502							
1503							
1504							;/MACRO -SIGC-
1505							

```

;*****
;TEST 32 *TEST THAT EOI SETS WHEN EOP SETS, ALSO TEST CLEAR
;*****
↑ST32: SCOPE

```

1509	005216	000004					
1510					CLR	@IBS	;/CLEAR CSR.
1511	005220	005077	174122		BIS	#BIT1, @IBS	;/SET EOP, SHOULD SET EOI.
1512	005224	052777	000002	174114	BIT	#BIT15, @IBD	;/DID EOI SET?
1513	005232	032777	100000	174110	BNE	IS	;/YES - LETS TRY CLEARING IT.
1514	005240	001011			BIS	#BIT1, @IBS	;/SET EOP, MEMORY
1515	005242	052777	000002	174076			;/REFRESH COULD HAVE
1516							;/INTERRUPTED US.
1517							;/DID EOI SET THIS TIME?
1518	005250	032777	100000	174072	BIT	#BIT15, @IBD	
1519	005256	001002			BNE	IS	
1520							

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

1524							
1525	005260	104002			ERROR	2	;/MODULE FAULT DETECTED:
1526							;/EOI FILED TO SET WHEN EOP SET.

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

1529	005262	000410			BR	TST33	;;
1530	005264	042777	000002	174054	BIC	#BIT1, @IBS	;/CLEAR EOP, SHOULD CLEAR EOI.
1531	005272	032777	100000	174050	BIT	#BIT15, @IBD	;/DID EOI CLEAR?
1532	005300	001401			BEQ	TST33	;;
1533							

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

1537							
1538	005302	104002			ERROR	2	;/MODULE FAULT DETECTED:
1539							;/EOI FAILED TO CLEAR.

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

1542

```

1543
1544
1545
1546
1547 005304 000004
1548
1549 005306 005077 174034 CLR @IBS ;CLEAR CSR.
1550 005312 032777 002000 174030 BIT #BIT10,@IBD ;DID RFD SET?
1551 005320 001002 BNE IS ;YES CONTINUE.
1552

```

:::SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

```

1556
1557 005322 104002 ERROR 2 ;/MODULE FAULT DETECTED:
1558 ;RFD FAILED TO SET.

```

:::SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

```

1561 005324 000410 BR TST34 ;
1562
1563 005326 052777 000200 174012 IS: BIS #BIT7,@IBS ;NOW SET ACC,RFD SHOULD CLEAR.
1564 005334 032777 002000 174006 BIT #BIT10,@IBD ;DID IT CLEAR?
1565 005342 001401 BEQ TST34 ;
1566

```

:::SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

```

1570
1571 005344 104002 ERROR 2 ;/MODULE FAULT DETECTED:
1572 ;RFD FAILED TO CLEAR.

```

:::SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

```

1575
1576
1577
1578
1579
1580 005346 000004
1581
1582 005350 005077 173772 CLR @IBS ;CLEAR THE STATUS REG.
1583 005354 052777 000041 173764 BIS #BITS!BIT0,@IBS ;SET TON; THIS SHOULD CAUSE AN
1584 ;ERROR SENCE NO LISTENERS ARE ON
1585 005362 105077 173762 CLRB @IBD ;AND WE SENT DATA TO THE BUS.
1586 ;BUS.
1587 005366 032777 040000 173752 BIT #BIT14,@IBS ;DID ER2 SET?
1588 005374 001001 BNE TST35 ;
1589

```

:::SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

```

1593
1594 005376 104001 ERROR 1 ;/MODULE FAULT DETECTED:
1595 ;ER2 FAILED TO SET.

```

1598
1599
1600
1601
1602
1603
1604
1605
1606
1607
1608
1609

005400 000004
005402 012737 000005 001160
005410 012777 000367 173730
005416 000005
005420 105777 173722
005424 001401

```

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS
:*****
:TEST 35 *TEST THAT BUS INIT CLEARS ACC,TON,LON,REM,EIP,TCS
:*****
TST35: SCOPE
MOV #5,STIMES ;;DO 5 ITERATIONS
MOV #367,QIBS ;SET ACC,TON,LON,REM,EOP, AND TCS.
RESET ;ISSUE SYS INIT.
TSTB QIBS ;DID THEY ALL CLEAR?
BEQ TST36 ;;

```

1613
1614
1615
1616

005426 104001

```

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS
ERROR 1 ;/MODULE FAULT DETECTED:
;BUS INIT FAILED TO CLEAR CSR.

```

1619
1620
1621
1622
1623
1624
1625
1626
1627
1628
1629
1630
1631
1632

005430 000004
005432 012737 000005 001160
005440 012777 000266 173700
005446 052777 000010 173672
005454 032777 000010 173664
005462 001374
005464 032777 000266 173654
005472 001401

```

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS
:*****
:TEST 36 *TEST IBC CLEARS ACC,TON,LON,REM AND EOP
:*****
TST36: SCOPE
MOV #5,STIMES ;;DO 5 ITERATIONS
MOV #266,QIBS ;SET ACC,TON,LON,REM, AND EOP.
BIS #BIT3,QIBS ;SET IBC, THIS SHOULD CLEAR ABOVE BITS.
IS: BIT #BIT3,QIBS ;WAIT TILL IBC CLEARS
BNE IS ;
BIT #266,QIBS ;DID THEY CLEAR?
BEQ TST37 ;;

```

1636
1637
1638
1639

005474 104001

```

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS
ERROR 1 ;/MODULE FAULT DETECTED:
;ACC,TON,LON,REM, AND/OR EOP
;FAILED TO CLEAR ON IBC

```

1642
1643
1644
1645
1646
1647
1648
1649
1650

005476 000004
005500 012737 000005 001160
005506 012777 000260 173632
005514 012777 000377 173626

```

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS
:*****
:TEST 37 *TEST THAT BUS INIT INDIRECTLY CLEARS IBD
:*****
TST37: SCOPE
MOV #5,STIMES ;;DO 5 ITERATIONS
MOV #BIT7!BIT5!BIT4,QIBS ;SET ACC,TON, AND LON.
MOV #377,QIBD ;LOAD IBD

```

MAINDEC-11-DVIBA-A
DVIBA.P11 T37

MACY11 27(663) 29-MAR-77 12:57 PAGE 32
*TEST THAT BUS INIT INDIRECTLY CLEARS IBD

SEQ 0047

1651 005522 000005
1652 005524 105777 173620
1653 005530 001401
1654

RESET ;ISSUE SYS INIT.
TSTB @IBD ;DID IT CLEAR?
BEQ TST40 ;;

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

1658
1659 005532 104002
1660
1661

ERROR 2 ;/MODULE FAULT DETECTED:
;FAILED TO CLEAR LOW BYTE OF IBD ON
;SYSTEM INIT.

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

1664
1665
1666
1667
1668
1669
1670
1671
1672 005534 000004
1673
1674 005536 005077 173604
1675 005542 012777 000200 173636
1676 005550 012777 005606 173604
1677 005556 052777 000101 173562
1678
1679 005564 012746 000000
1680 005570 012746 005576
1681 005574 000002
1682 005576
1683 005576 000240
1684 005600 000240
1685

.SBTTL
.SBTTL INTERRUPT TESTS
.SBTTL

; *TEST 40 *TEST THAT CMD CAN GENERATE AN INTERRUPT B

TST40: SCOPE

CLR @IBS ;CLEAR THE CSR.
MOV #200,@PRC
MOV #15,@VECTC ;SET UP INTERRUPT VECTOR
BIS #BIT0!BIT6,@IBS ;SET TCS, SHOULD CAUSE
;PR
MOV #0,-(SP) ;SET CPU PRIORITY ON RETURN
MOV #64\$,-(SP) ;SHOW RETURN ADDRESS
RTI ;CAUSE A RETURN (PUTS NEW STATUS
;IN STATUS REG.)
64\$: NOP ;CMD TO SET AND GIVE US AN
NOP ;INTERRUPT.

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

1686
1687 005602 104001
1688
1689
1690
1691

ERROR 1 ;/MODULE FAULT DETECTED:
;CMD FAILED TO GENERATE AN INTERRUPT.

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

1694 005604 000402
1695 005606
1696 005606 062706 000004
1697 005612 005077 173530
1698
1699 005616 013777 001406 173536
1700 005624 012777 004700 173554
1701
1702
1703
1704

BR 2\$
1\$: ADD #4,SP ;/ADD #4 TO STACK POINTER.
2\$: CLR @IBS ;CLEAR INTERRUPT
;/-RESV-
MOV PRC,@VECTC ;/RESTORE VECTOR FOR
MOV #4700,@PRC ;/ILLEGAL INTRO.

; *TEST 41 *TEST THAT TCR AND LNR CAN GENERATE INTERRUPTS

```

1705 005632 000004          TST41: SCOPE
1706 005634 012777 000200 173544      MOV      #200, @PRC
1707 005642 012777 005726 173512      MOV      #1$, @VECTC ;SET UP INTERRUPT VECTOR FOR TKR INTERRUPT
1708 005650 012777 000060 173470      MOV      #BIT4!BITS, @IBS ;SET TON AND LON
1709 005656 052777 000100 173462      BIS      #BIT6, @IBS ;ALLOW INTERRUPT
1710                                ;/PR
1711 005664 012746 000000          MOV      #0, -(SP) ;/SET CPU PRIORITY ON RETURN
1712 005670 012746 005676          MOV      #64$, -(SP) ;/SHOW RETURN ADDRESS
1713 005674 000002          RTI      ;/CAUSE A RETURN (PUTS NEW STATUS
1714 005676                                ;/IN STATUS REG.)
1715 005676 000240          64$: NOP
1716 005700 000240          NOP
1717

```

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

```

1721
1722 005702 104001          ERROR 1 ;/MODULE FAULT DETECTED:
1723                                ;/FAILED TO GENERATE A TKR INTERRUPT.

```

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

```

1726 005704 005077 173436      CLR      @IBS ;CLR CSR
1727                                ;/-RESV-
1728 005710 013777 001406 173444      MOV      PRC, @VECTC ;/RESTORE VECTOR FOR
1729 005716 012777 004700 173462      MOV      #4700, @PRC ;/ILLEGAL INTRO.
1730 005724 000443          BR      TST42 ;
1731
1732 005726          1$: ADD      #4, SP ;/ADD #4 TO STACK POINTER.
1733 005726 062706 000004          ;/-RESV-
1734
1735 005732 013777 001406 173422      MOV      PRC, @VECTC ;/RESTORE VECTOR FOR
1736 005740 012777 004700 173440      MOV      #4700, @PRC ;/ILLEGAL INTRO.
1737 005746 012777 000200 173434      MOV      #200, @PRD
1738 005754 012777 006010 173402      MOV      #2$, @VECTD ;SET UP FOR LNR INTERRUPT.
1739                                ;/PR
1740 005762 012746 000000          MOV      #0, -(SP) ;/SET CPU PRIORITY ON RETURN
1741 005766 012746 005774          MOV      #65$, -(SP) ;/SHOW RETURN ADDRESS
1742 005772 000002          RTI      ;/CAUSE A RETURN (PUTS NEW STATUS
1743 005774                                ;/IN STATUS REG.)
1744 005774 105277 173350          65$: INCB   @IBD ;SEND DATA - CLRS TKR SETS LNR
1745                                ;/FOR INTERRUPT .
1746 006000 000240          NOP
1747 006002 000240          NOP
1748

```

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

```

1752
1753 006004 104001          ERROR 1 ;/MODULE FAULT DETECTED:
1754                                ;/FAILED TO GENERATE LNR INTERRUPT

```

;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

```

1757 006006 000402          BR      3$
1758

```

```

1759 006010          25:
1760 006010 062706 000004          ADD    #4,SP          ;/ADD #4 TO STACK POINTER.
1761 006014 005077 173326          CLR    #IBS          ;/CLEAR THE STATUS REG.
1762                                     ;/RESV-
1763 006020 013777 001410 173336          MOV    PRA,#VECTD ;/RESTORE VECTOR FOR
1764 006026 012777 004700 173354          MOV    #4700,#PRA ;/ILLEGAL INTRO.
1765
1766                                     ;*****
1767                                     ;*TEST 42 *TEST THAT ER2 CAN GENERATE AN INTERRUPT
1768                                     ;*****
1769 006034 000004          TST42: SCOPE
1770
1771 006036 005077 173304          CLR    #IBS          ;START WITH CSR CLEAR
1772 006042 012777 000200 173332          MOV    #200,#PRA
1773 006050 012777 006126 173300          MOV    #15,#VECTA ;SET UP INTERRUPT VECTOR
1774                                     ;/PR
1775 006056 012746 000200          MOV    #200,-(SP) ;/SET CPU PRIORITY ON RETURN
1776 006062 012746 006070          MOV    #645,-(SP) ;/SHOW RETURN ADDRESS
1777 006066 000002          RTI ;/CAUSE A RETURN (PUTS NEW STATUS
1778 006070          645: ;/IN STATUS REG.)
1779 006070 052777 000140 173250          BIS    #BITS!BIT6,#IBS ;SET TON - NO LISTNERS ON
1780 006076 105077 173246          CLR    #IBD ;BUS BUT DATA PUT ON
1781 006102 000240          NOP ;BUS - THEREFORE AN INTERRUPT
1782 006104 000240          NOP ;SHOULD BE POSTED.
1783                                     ;/PR
1784 006106 012746 000000          MOV    #0,-(SP) ;/SET CPU PRIORITY ON RETURN
1785 006112 012746 006120          MOV    #655,-(SP) ;/SHOW RETURN ADDRESS
1786 006116 000002          RTI ;/CAUSE A RETURN (PUTS NEW STATUS
1787 006120          655: ;/IN STATUS REG.)
1788 006120 000240          NOP
1789
1790

```

::SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

```

1794
1795 006122 104001          ERROR 1          ;/MODULE FAULT DETECTED:
1796                                     ;/FAILED TO INTERRUPT ON ERROR2

```

::SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS

```

1799 006124 000402          BR     25
1800 006126
1801 006126 062706 000004          15:  ADD    #4,SP          ;/ADD #4 TO STACK POINTER.
1802 006132 005077 173210          25:  CLR    #IBS          ;/CLEAR CSR
1803                                     ;/RESV-
1804 006136 013777 001402 173212          MOV    PRA,#VECTA ;/RESTORE VECTOR FOR
1805 006144 012777 004700 173230          MOV    #4700,#PRA ;/ILLEGAL INTRO.
1806
1807                                     .SBTTL
1808                                     .SBTTL SECOND MODULE TESTS
1809                                     .SBTTL
1810
1811
1812

```

::*****
;*TEST 43 *TEST THAT MODULE PASSES "BIAKI"

1813
1814 006152 000004
1815
1816
1817
1818
1819
1820
1821
1822
1823
1824

::*****
TST43: SCOPE

::#WARNING! THIS TEST IS DESIGNED TO BE EXERCISED WITH A
::#SECOND MODULE (IBV-11) WITH SWITCH "ERI INH" SET.
::#ADDRESS OF THE SECOND MODULE IS IN LOCATION "IBS2" VECTOR
::#ADDRESS IS IN LOCATION "VECTA2". THE SECOND IBV-11 SHOULD BE ELECTRICALLY SEC
::#TO INHIBIT THE USE OF TESTING WITH A SECOND MODULE, MAKE
::#LOCATION "SCDW1" ZERO.
::*

1825 006154 005737 001254
1826 006160 001002
1827 006162 000137 006764
1828 006166

3S: TST SCDW1 ;TESTING WITH
BNE 3S ;SECOND IBV11?
JMP EOP ;NO-END PASS.

1830 006166 005077 173154
1831 006172 005077 173170
1832 006176 012777 000200 173212
1833 006204 012777 006242 173164
1834

CLR @IBS ;CLEAR CSR.
CLR @IBS2 ;CLEAR SECOND MODULE.
MOV #200,@PRC2
MOV #15,@VECTC2 ;SET UP VECTOR ADDR.

1835 006212 012746 000000
1836 006216 012746 006224
1837 006222 000002

MOV #0,-(SP) ;/SET CPU PRIORITY ON RETURN
MOV #64S,-(SP) ;/SHOW RETURN ADDRESS
RTI ;/CAUSE A RETURN (PUTS NEW STATUS
;/IN STATUS REG.)

1838 006224
1839 006224 012777 000140 173134
1840 006232 000240
1841 006234 000240
1842

64S: MOV #BIT6!BIT5,@IBS2 ;SET INTR ENABLE AND TON ON SECOND
NOP ;IBV - SHOULD CAUSE A TKR INTERRUPT.
NOP

::SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

1846
1847 006236 104001
1848
1849
1850

ERROR 1 ;/MODULE FAULT DETECTED:
;ASSUMING SECOND MODULE IS GOOD,
;MODULE (IBV-11) UNDER TEST FAILED
;TO PASS @ BUSS SIGNAL "BTAKI"

1853 006240 000402
1854 006242
1855 006242 062706 000004
1856 006246 005077 173114
1857
1858 006252 013777 001416 173116
1859 006260 012777 004700 173130
1860

::SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS
BR 2S
1S: ADD #4,SP ;/ADD #4 TO STACK POINTER.
2S: CLR @IBS2 ;CLEAR SECOND MODULE
;/-RESV-
MOV PRC2,@VECTC2 ;/RESTORE VECTOR FOR
MOV #4700,@PRC2 ;/ILLEGAL INTRO.

1861
1862
1863
1864 006266 000004
1865
1866

::*****
;#TEST 44 #TEST THAT SRQ CAN GENERATE AN INTERRUPT
;*****
TST44: SCOPE
;#WARNING! THIS TEST IS DESIGNED TO BE EXERCISED WITH A
;#SECOND MODULE (IBV-11) WITH SWITCH "ERI INH" SET.

M04

MAINDEC-11-DVIBA-A
DVIBA.P11 T44

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*TEST THAT SRQ CAN GENERATE AN INTERRUPT

SEQ 0051

```

1867                                     ;#ADDRESS OF THE SECONUD MODULE IS IN LOCATION "IBS2" VECTOR
1868                                     ;#ADDRESS IS IN LOCATION "VECTA2". THE SECONUD IBV-11 SHOULD BE ELECTRICALLY SEC
1869                                     ;#TO INHIBIT THE USE OF TESTING WITH A SECONUD MODULE, MAKE
1870                                     ;#LOCATION "SCDW1" ZERO.
1871                                     ;#
1872
1873 006270 005077 173052                 CLR    @IBS          ;CLEAR CSRS.
1874 006274 005077 173066                 CLR    @IBS2
1875
1876 006300 012777 000200 173076         MOV    #200,@PRB    ;SET UP INTERRUPT VECTOR.
1877 006306 012777 006352 173044         MOV    #15,@VECTB
1878
1879 006314 012746 000000                 MOV    #0,-(SP)     ;/PR
1880 006320 012746 006326                 MOV    #64$,-(SP)  ;/SET CPU PRIORITY ON RETURN
1881 006324 000002                         RTI                ;/SHOW RETURN ADDRESS
1882 006326                                ;/CAUSE A RETURN (PUTS NEW STATUS
1883 006326 012777 000100 173012         MOV    #100,@IBS   ;/IN STATUS REC.)
1884 006334 052777 100000 173024         BIS    #BIT15,@IBS2 ;ENABLE INTERRUPTS
1885                                     ;SETTING SRQ IN THE "CDW" MODULE
1886                                     ;WILL PUT SRQ ON THE IB BUS
1887 006342 000240                         NOP                ;IS ERI INH SW IS SET.
1888 006344 000240                         NOP
1889
                                     ;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS
1893
1894 006346 104001                         ERROR 1            ;/MODULE FAULT DETECTED:
1895                                     ;SRQ FAILED TO GENERATE
1896                                     ;AN INTERRUPT
                                     ;;SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS
1899 006350 000402                         BR      25
1900
1901 006352                                15:
1902 006352 062706 000004                 ADD    #4,SP        ;/ADD #4 TO STACK POINTER.
1903
1904 006356                                25:
1905
1906 006356 013777 001404 172774         MOV    PRB,@VECTB  ;/-RESV-
1907 006364 012777 004700 173012         MOV    #4700,@PRB ;/RESTORE VECTOR FOR
1908 006372 005077 172750                 CLR    @IBS        ;/ILLEGAL INTRO.
1909 006376 005077 172764                 CLR    @IBS2       ;CLEAR CSRS
1910
1911                                     ;*****
1912                                     ;#TEST 45 #TEST THAT ERROR1 IS GENERATED IF ATN IS ON THE IB BUS
1913                                     ;*****
1914 006402 000004                         ↑ST45: SCOPE
1915
1916                                     ;#WARNING! THIS TEST IS DESIGNED TO BE EXERCISED WITH A
1917                                     ;#SECONUD MODULE (IBV-11) WITH SWITCH "ERI INH" SET.
1918                                     ;#ADDRESS OF THE SECONUD MODULE IS IN LOCATION "IBS2" VECTOR
1919                                     ;#ADDRESS IS IN LOCATION "VECTA2". THE SECONUD IBV-11 SHOULD BE ELECTRICALLY SEC
1920                                     ;#TO INHIBIT THE USE OF TESTING WITH A SECONUD MODULE, MAKE

```

```

1921
1922
1923
1924 006404 005077 172756
1925 006410 005277 172752
1926
1927
1928
1929 006414 032777 020000 172724
1930 006422 001001
1931

```

```

;#LOCATION "SCDW1" ZERO.
;#
CLR  @IBS2      ;CLR CSR OF 2ND MODULE.
INC  @IBS2      ;ASSERT ATN ON IB BUS
;ASSERTED ATN ON IBV UNDER TEST-
;THIS SHOULD CAUSE AN ERROR 1
;SENCE THE 2ND IBV HAS ATN SET.
BIT  @BIT13,@IBS ;DID ERROR 1 SET?
BNE  TST46      ;

```

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

```

1935
1936 006424 104001
1937

```

```

ERROR 1 ;/MODULE FAULT DETECTED:
;FAILED TO GENERATE ERROR 1

```

;;SSSSSSSSSS+++ ERROR +++SSSSSSSSSS

```

1940
1941
1942
1943
1944 006426 000004
1945 006430 012737 000005 001160
1946
1947
1948
1949
1950
1951
1952
1953 006436 005077 172704
1954 006442 012777 000010 172716
1955 006450 032777 020000 172670
1956
1957 006456 001010
1958 006460 012777 000010 172700
1959 006466 032777 020000 172652
1960 006474 001001
1961

```

```

;*****
;#TEST 46 *TEST THAT ERROR 1 IS GENERATED IF IFC IS PUT ON IB BUS BY SECONUD MODUL
;*****
TST46: SCOPE

```

```

MOV  #5,@STIMES ;DO 5 ITERATIONS
;#WARNING! THIS TEST IS DESIGNED TO BE EXERCISED WITH A
;#SECONUD MODULE (IBV-11) WITH SWITCH "ERI INH" SET.
;#ADDRESS OF THE SECONUD MODULE IS IN LOCATION "IBS2" VECTOR
;#ADDRESS IS IN LOCATION "VECTA2". THE SECONUD IBV-11 SHOULD BE ELECTRICALLY SEC
;#TO INHIBIT THE USE OF TESTING WITH A SECONUD MODULE, MAKE
;#LOCATION "SCDW1" ZERO.
;#
CLR  @IBS      ;CLEAR CSR
MOV  @BIT3,@IBS2 ;ASSERT IFC FROM TESTOR
BIT  @BIT13,@IBS ;DID ERROR 1 GET SET?
BNE  TST47      ;IF SO - NEXT TEST
MOV  @BIT3,@IBS2 ;IF NOT WE'LL TRY AGAIN SENCE MEMORY
BIT  @BIT13,@IBS ;REFRESH COULD HAVE GO IN THE WAY.
BNE  TST47      ;

```

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

```

1965
1966 006476 104001
1967
1968
1969

```

```

ERROR 1 ;/MODULE FAULT DETECTED:
;ERROR 1 FAILED TO SET WHEN
;IFC WAS ON IB-BUS AND MODULE
;UNDER TEST DIDN'T PUT IT THERE.

```

;;SSSSSSSSSS+++ ERROR +++SSSSSSSSSS

```

1972
1973
1974

```

```

;*****
;#TEST 47 *TEST THAT ERROR 1 IS GENERATED IF REN IS ON IB BUS

```

```

1975
1976 006500 000004
1977
1978
1979
1980
1981
1982
1983
1984
1985
1986 006502 005077 172640
1987 006506 005077 172654
1988 006512 052777 000004 172646
1989
1990 006520 032777 020000 172620
1991 006526 001001
1992

```

```

*****
†TST47: SCOPE

```

```

;#WARNING! THIS TEST IS DESIGNED TO BE EXERCISED WITH A
;#SECOND MODULE (IBV-11) WITH SWITCH "ERI INH" SET.
;#ADDRESS OF THE SECOND MODULE IS IN LOCATION "IBS2" VECTOR
;#ADDRESS IS IN LOCATION "VECTA2". THE SECOND IBV-11 SHOULD BE ELECTRICALLY SEC
;#TO INHIBIT THE USE OF TESTING WITH A SECOND MODULE, MAKE
;#LOCATION "SCDW1" ZERO.
;#

```

```

CLR    @IBS           ;CLEAR CSRS.
CLR    @IBS2
BIS    @BIT2,@IBS2   ;ASSERT REN ON IB BUS FROM 2ND
;MODULE. 1ST IBV-11 SHOULD
BIT    @BIT13,@IBS   ;GENERATE AN ERROR 1;DID IT??
BNE    TST50         ;;

```

```

;;SSSSSSSSS>>> ERROR <<<SSSSSSSSSS

```

```

1996
1997 006530 104001
1998

```

```

ERROR 1           ;/MODULE FAULT DETECTED:
;FAILED TO GENERATE AN ERROR 1.

```

```

;;SSSSSSSSSS††† ERROR †††SSSSSSSSSS

```

```

2001
2002
2003
2004
2005 006532 000004
2006
2007
2008
2009
2010
2011
2012
2013
2014
2015 006534 005077 172626
2016 006540 005077 172602
2017
2018 006544 012777 000200 172630
2019 006552 012777 006616 172576
2020
2021 006560 052777 000100 172560
2022
2023 006566 012746 000000
2024 006572 012746 006600
2025 006576 000002
2026 006600
2027 006600 052777 000004 172560
2028 006606 000240

```

```

*****
;#TEST 50 *TEST THAT AN ERROR 1 CAN GENERATE AN INTERRUPT
*****
†TST50: SCOPE

```

```

;#WARNING! THIS TEST IS DESIGNED TO BE EXERCISED WITH A
;#SECOND MODULE (IBV-11) WITH SWITCH "ERI INH" SET.
;#ADDRESS OF THE SECOND MODULE IS IN LOCATION "IBS2" VECTOR
;#ADDRESS IS IN LOCATION "VECTA2". THE SECOND IBV-11 SHOULD BE ELECTRICALLY SEC
;#TO INHIBIT THE USE OF TESTING WITH A SECOND MODULE, MAKE
;#LOCATION "SCDW1" ZERO.
;#

```

```

CLR    @IBS2         ;CLEAR CSRS.
CLR    @IBS
MOV    @200,@PRA
MOV    @18,@VECTA   ;SET UP VECTOR ADDR.
BIS    @BIT06,@IBS  ;SET INTERRUPT ENABLE
;#PR
MOV    #0,-(SP)     ;/SET CPU PRIORITY ON RETURN
MOV    #64$,-(SP)  ;/SHOW RETURN ADDRESS
RTI                               ;/CAUSE A RETURN (PUTS NEW STATUS
;#IN STATUS REG.)
BIS    @BIT2,@IBS2  ;GENERATE AN ERROR 1 AS PER LAST TEST.
NOP

```

```

64$:

```

2029 006610 000240
2030

NOP

:::SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

2034 006612 104001
2035
2036

ERROR 1 ;/MODULE FAULT DETECTED:
;ERROR 1 FAILED TO GENERATE AN INTR.

:::SSSSSSSSSS+++ ERROR +++SSSSSSSSSS

2039 006614 000402
2040

BR 2S

2041 006616
2042 006616 062706 000004
2043 006622
2044

1S: ADD #4,SP ;/ADD #4 TO STACK POINTER.
2S:

2045 006622 013777 001402 172526
2046 006630 012777 004700 172544
2047 006636 005077 172524
2048 006642 005077 172500
2049
2050

MOV PRA, VECTA ;/RESTORE VECTOR FOR
MOV #4700, PRA ;/ILLEGAL INTRO.
CLR JIBS2 ;CLEAR CSRS.
CLR JIBS

2051
2052
2053
2054
2055
2056
2057
2058
2059
2060
2061

:::*****
;TEST 51 *TEST THAT DATA CAN BE XFERRED BETWEEN THE MODULE UNDER TEST AND THE KGM
; * NOTE: KGM = KNOWN GOOD MODULE
; * IN THIS TEST WE'LL MAKE THE KGM A LISTENER
; * AND THE MODULE UNDER TEST A TALKER.
; * WE'VE ALREADY XFERRED DATA TO AND FROM THE IB-BUS
; * VIA THE MODULE UNDER TEST. THE ONLY UNKNOWN
; * IS THE CABLE CONNECTING THE KGM TO THE MODULE UNDER TEST,
; * AS WELL AS THE KGM.
; *
:::*****

2062 006646 000004
2063
2064 006650 012737 006670 001110
2065 006656 012737 000000 001124
2066 006664 005037 001126
2067

TEST1: SCOPE
MOV #15, SLPERR ;SET ERROR LOOP.
MOV #0, SGDDAT ;START PATTERN.
CLR SBDDAT

2068 006670 005077 172452
2069 006674 005077 172466
2070 006700 052777 000041 172440
2071 006706 052777 000020 172452
2072 006714 013777 001124 172426
2073 006722 117737 172442 001126
2074 006730 123737 001124 001126
2075 006736 001402
2076

1S: CLR JIBS ;CLEAR CSRS.
CLR JIBS2
BIS #BITS!BIT0, JIBS ;SET TON AND TCS.
BIS #BIT4, JIBS2 ;SET LON ON KGM.
MOV SGDDAT, JIBD ;SEND PATTERN.
MOVB JIBD2, SBDDAT ;READ ATA FROM KGM.
CMPB SGDDAT, SBDDAT ;DATA SENT = DATA RECEIVED?
BEQ 2S ;YES, CONTINUE

:::SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

2080 006740 104004
2081
2082

ERROR 4 ;/MODULE FAULT DETECTED:
;ERROR - BAD DATA PASSED BETWEEN

2083

;MODULE UNDER TEST AND KGM.

```

2086 006742 000407          BR      TST52          ;;
2087                                ;;
2088 006744 105237 001124   25:    INCB   SCDDAT      ;CHANGE PATTERN.
2089 006750 001347          BNE     IS           ;IF NOT DONE, CONTINUE.
2090
2091 006752 005077 172370   CLR     JIBS        ;CLEAR CSR'S
2092 006756 005077 172404   CLR     JIBS2
2093
2094                                ;:*****
2095                                ;:TEST 52      *TEMP END OF TESTS
2096                                ;:*****
2097 006762 000004          TST52: SCOPE
2098
2099 006764          EOP:
2100
2101                                .SBTTL  SYSMAC ROUTINES:
2102
2103                                .SBTTL  END OF PASS ROUTINE
2104
2105                                ;:*****
2106                                ;:INCREMENT THE PASS NUMBER (SPASS)
2107                                ;:TYPE "END PASS #XXXXX" (WHERE XXXXX IS A DECIMAL NUMBER)
2108                                ;:IF THERES A MONITOR GO TO IT
2109                                ;:IF THERE ISN'T JUMP TO RSTART
2110
2111 006764          SEOP:
2112 006764 000004          CLR     SCOPE
2113 006766 005037 001102   CLR     STSTNM      ;:ZERO THE TEST NUMBER
2114 006772 005037 001160   CLR     STIMES      ;:ZERO THE NUMBER OF ITERATIONS
2115 006776 005237 001202   INC     SPASS       ;:INCREMENT THE PASS NUMBER
2116 007002 042737 100000 001202 BIC     #100000,SPASS ;:DON'T ALLOW A NEG. NUMBER
2117 007010 005327          DEC     (PC)+       ;:LOOP?
2118 007012 000001          SEOPCT: .WORD 1
2119 007014 003022          BGT     SDOAGN      ;:YES
2120 007016 012737          MOV     (PC)+,2(PC)+ ;:RESTORE COUNTER
2121 007020 000001          SENDCT: .WORD 1
2122 007022 007012          SEOPCT
2123 007024 104401 007071   TYPE   SENDMG      ;:TYPE "END PASS #"
2124 007030 013746 001202   MOV     $PASS,-(SP) ;:SAVE SPASS FOR TYPEOUT
2125 007034 104405          TYPDS   ;:GO TYPE--DECIMAL ASCII WITH SIGN
2126 007036 104401 007066   TYPE   $NULL       ;:TYPE A NULL CHARACTER
2127 007042 013700 000042   MOV     2#42,RO    ;:GET MONITOR ADDRESS
2128 007046 001405          BEQ     SDOAGN     ;:BRANCH IF NO MONITOR
2129 007050 000005          RESET   ;:CLEAR THE WORLD
2130 007052 004710          SENDAD: JSR     PC,(RO) ;:GO TO MONITOR
2131 007054 000240          NOP     ;:SAVE ROOM
2132 007056 000240          NOP     ;:FOR
2133 007060 000240          NOP     ;:ACT11
2134 007062
2135 007062 000137          SDOAGN: JMP     2(PC)+     ;:RETURN
2136 007064 002266          SRTNAD: .WORD  RSTART

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2137 007066 377 377 000 $ENULL: .BYTE -1,-1,0 ;;NULL CHARACTER STRING
2138 007071 015 042412 042116 $ENDMG: .ASCIZ <15><12>/END PASS #/
2139 007076 050040 051501 020123
2140 007104 000043
2141 ;/DELMA
2142
2143 ;/ROUTINE TO PROVIDE DELAYS IN INCREMENTS OF 25 US
2144 ;/
2145 ;/ CALL= JSR RS,DEL50
2146 ;/ .WORD X (# OF 25 US TO DELAY)
2147 ;/ ;RETURNS HERE
2148 ;/
2149
2150 007106 012500 DEL50: MOV (5)+,R0 ;/GET # OF 25 US DELAYS
2151 007110 012701 000002 15: MOV #2.,R1 ;/# FOR LOOP TO DO 50 US.
2152 007114 005301 25: DEC R1 ;/DEC IT
2153 007116 001376 BNE 25 ;/WAITED 25. TIMES?
2154 007120 005300 DEC R0 ;/DONE # OF 50 US DELAY DESIRED?
2155 007122 001372 BNE 15 ;/NO - NEXT ONE.
2156 007124 000205 RTS R5 ;/YES - EXIT.
2157
2158 .SBTTL ERROR HANDLER ROUTINE
2159
2160 ;:*****
2161 ;:THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT,
2162 ;:SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
2163 ;:AND GO TO SERRTYP ON ERROR
2164 ;:THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
2165 ;:*SW15=1 HALT ON ERROR
2166 ;:*SW13=1 INHIBIT ERROR TYPEOUTS
2167 ;:*SW10=1 BELL ON ERROR
2168 ;:*SW09=1 LOOP ON ERROR
2169 ;:*CALL
2170 ;:* ERROR N ;;ERROR=EMT AND N=ERROR ITEM NUMBER
2171
2172 007126 SERROR:
2173 007126 104407 CKSWR ;; TEST FOR CHANGE IN SOFT-SWR
2174 007130 105237 001103 75: INCB $ERFLG ;; SET THE ERROR FLAG
2175 007134 001775 BEQ 75 ;; DON'T LET THE FLAG GO TO ZERO
2176 007136 013777 001102 171776 MOV $TSTNM,$DISPLAY ;; DISPLAY TEST NUMBER AND ERROR FLAG
2177 007144 032777 002000 171766 BIT #BIT10,$SWR ;; BELL ON ERROR?
2178 007152 001402 BEQ 15 ;; NO - SKIP
2179 007154 104401 001164 TYPE $BELL ;; RING BELL
2180 007160 005237 001112 15: INC $ERTTL ;; COUNT THE NUMBER OF ERRORS
2181 007164 011637 001116 MOV (SP),$ERRPC ;; GET ADDRESS OF ERROR INSTRUCTION
2182 007170 162737 000002 001116 SUB #2,$ERRPC
2183 007176 117737 171714 001114 MOVB $ERRPC,$ITEMB ;; STRIP AND SAVE THE ERROR ITEM CODE
2184 007204 032777 020000 171726 BIT #BIT13,$SWR ;; SKIP TYPEOUT IF SET
2185 007212 001004 BNE 205 ;; SKIP TYPEOUTS
2186 007214 004737 007314 JSR PC,$ERRTYP ;; GO TO USER ERROR ROUTINE
2187 007220 104401 001171 TYPE $SCRLF
2188 007224
2189 007224 122737 000001 001214 205: CMPB #APTENV,$ENV ;; RUNNING IN APT MODE
2190 007232 001007 BNE 25 ;; NO, SKIP APT ERROR REPORT

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2191 007234 113737 001114 007246      MOVB    $ITEMB,21$      ;; SET ITEM NUMBER AS ERROR NUMBER
2192 007242 004737 011460              JSR     PC,$ATY4        ;; REPORT FATAL ERROR TO APT
2193 007246     000                      21$:    .BYTE    0
2194 007247     000                             .BYTE    0
2195 007250 000777                      22$:    BR        22$            ;; APT ERROR LOOP
2196 007252 005777 171662              25$:    TST     2$SWR            ;; HALT ON ERROR
2197 007256 100002                             BPL     3$            ;; SKIP IF CONTINUE
2198 007260 000000                             HALT                  ;; HALT ON ERROR!
2199 007262 104407                             CKSWR                ;; TEST FOR CHANGE IN SOFT-SWR
2200 007264 032777 001000 171646        3$:    BIT     #BIT09,2$SWR    ;; LOOP ON ERROR SWITCH SET?
2201 007272 001402                             BEQ     4$            ;; BR IF NO
2202 007274 013716 001110                     MOV     $LPERR,(SP)    ;; FUDGE RETURN FOR LOOPING
2203 007300 005737 001162              4$:    TST     $ESCAPE        ;; CHECK FOR AN ESCAPE ADDRESS
2204 007304 001402                             BEQ     5$            ;; BR IF NONE
2205 007306 013716 001162                     MOV     $ESCAPE,(SP)  ;; FUDGE RETURN ADDRESS FOR ESCAPE
2206 007312                      5$:                     
2207 007312 000002                             RTI                  ;; RETURN
2208                                           .SBTTL    ERROR MESSAGE TYPEOUT ROUTINE
2209
2210                                           ;:*****
2211                                           ;:THIS ROUTINE USES THE "ITEM CONTROL BYTE" ($ITEMB) TO DETERMINE WHICH
2212                                           ;:ERROR IS TO BE REPORTED. IT THEN OBTAINS, FROM THE "ERROR TABLE" ($ERRTB),
2213                                           ;:AND REPORTS THE APPROPRIATE INFORMATION CONCERNING THE ERROR.
2214
2215 007314                      $ERRTYP:
2216 007314 104401 001171                      TYPE     $SCRLF        ;; "CARRIAGE RETURN" & "LINE FEED"
2217 007320 010046                      MOV     RO,-(SP)        ;; SAVE RO
2218 007322 005000                      CLR     RO             ;; PICKUP THE ITEM INDEX
2219 007324 153700 001114                      BISB    2*$ITEMB,RO
2220 007330 001004                      BNE     1$             ;; IF ITEM NUMBER IS ZERO, JUST
2221                                                                                     TYPE THE PC OF THE ERROR
2222 007332 013746 001116                      MOV     $ERRPC,-(SP)    ;; SAVE $ERRPC FOR TYPEOUT
2223                                                                                     ERROR ADDRESS
2224 007336 104402                      TYPOC                 ;; GO TYPE--OCTAL ASCII(ALL DIGITS)
2225 007340 000426                      BR       6$            ;; GET OUT
2226 007342 005300                      1$:    DEC     RO            ;; ADJUST THE INDEX SO THAT IT WILL
2227 007344 006300                      ASL     RO            ;; WORK FOR THE ERROR TABLE
2228 007346 006300                      ASL     RO
2229 007350 006300                      ASL     RO
2230 007352 062700 001256                      ADD     #ERRTB,RO      ;; FORM TABLE POINTER
2231 007356 012037 007366                      MOV     (RO)+,2$        ;; PICKUP "ERROR MESSAGE" POINTER
2232 007362 001404                      BEQ     3$            ;; SKIP TYPEOUT IF NO POINTER
2233 007364 104401                      TYPE                  ;; TYPE THE "ERROR MESSAGE"
2234 007366 000000                      2$:    .WORD    0            ;; "ERROR MESSAGE" POINTER GOES HERE
2235 007370 104401 001171                      TYPE     $SCRLF        ;; "CARRIAGE RETURN" & "LINE FEED"
2236 007374 012037 007404                      3$:    MOV     (RO)+,4$        ;; PICKUP "DATA HEADER" POINTER
2237 007400 001404                      BEQ     5$            ;; SKIP TYPEOUT IF 0
2238 007402 104401                      TYPE                  ;; TYPE THE "DATA HEADER"
2239 007404 000000                      4$:    .WORD    0            ;; "DATA HEADER" POINTER GOES HERE
2240 007406 104401 001171                      TYPE     $SCRLF        ;; "CARRIAGE RETURN" & "LINE FEED"
2241 007412 011000                      5$:    MOV     (RO),RO        ;; PICKUP "DATA TABLE" POINTER
2242 007414 001004                      BNE     7$            ;; GO TYPE THE DATA
2243 007416 012600                      6$:    MOV     (SP)+,RO        ;; RESTORE RO
2244 007420 104401 001171                      TYPE     ,SCRLF        ;; "CARRIAGE RETURN" & "LINE FEED"
    
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2245 007424 000207          RTS      PC          ;;RETURN
2246 007426                7$:      MOV      @ (RO)+, -(SP)    ;;SAVE @ (RO)+ FOR TYPEOUT
2247 007426 013046          TYPOC          ;;GO TYPE--OCTAL ASCII(ALL DIGITS)
2248 007430 104402          TST      (RO)          ;;IS THERE ANOTHER NUMBER?
2249 007432 005710          BEQ      6$           ;;BR IF NO
2250 007434 001770          TYPE     8$           ;;TYPE TWO(2) SPACES
2251 007436 104401 007444          BR      7$           ;;LOOP
2252 007442 000771          .ASCIZ  / /          ;;TWO(2) SPACES
2253 007444 020040 000          .EVEN
2254 007450          .SBTTL SCOPE HANDLER ROUTINE
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007450          $SCOPE:
007450 104407          CKSWR          ;;TEST FOR CHANGE IN SOFT-SWR
007452 104407          CKSWR
007454 032777 040000 171456 1$:      BIT      #BIT14, @SWR    ;;LOOP ON PRESENT TEST?
007462 001114          BNE      $OVER      ;;YES IF SW14=1
007464 000416          #####START OF CODE FOR THE XOR TESTER#####
007466 013746 000004          $XTSTR: BR      6$      ;;IF RUNNING ON THE "XOR" TESTER CHANGE
007472 012737 007512 000004          MOV      @ERRVEC, -(SP)  ;;THIS INSTRUCTION TO A "NOP" (NOP=240)
007500 005737 177060          MOV      #55, @ERRVEC    ;;SAVE THE CONTENTS OF THE ERROR VECTOR
007504 012637 000004          TST      @177060        ;;SET FOR TIMEOUT
007510 000463          MOV      (SP)+, @ERRVEC  ;;TIME OUT ON XOR?
007512 022626          BR      $SVLAD        ;;RESTORE THE ERROR VECTOR
007514 012637 000004          5$:      CMP      (SP)+, (SP)+    ;;GO TO THE NEXT TEST
007520 000423          MOV      (SP)+, @ERRVEC  ;;CLEAR THE STACK AFTER A TIME OUT
007522          BR      7$           ;;RESTORE THE ERROR VECTOR
007522 032777 000400 171410 6$:      #####END OF CODE FOR THE XOR TESTER#####
007530 001404          BIT      #BIT08, @SWR    ;;LOOP ON SPEC. TEST?
007532 127737 171402 001102          BEQ      2$           ;;BR IF NO
007540 001465          CMPB    @SWR, $STNM     ;;ON THE RIGHT TEST? SWR<7:0>
007542 105737 001103          BEQ      $OVER        ;;BR IF YES
007546 001421          TSTB   $ERFLG         ;;HAS AN ERROR OCCURRED?
007550 123737 001115 001103          BEQ      3$           ;;BR IF NO
007556 101015          CMPB    $ERMAX, $ERFLG  ;;MAX. ERRORS FOR THIS TEST OCCURRED?
007560 032777 001000 171352          BHI      3$           ;;BR IF NO
007566 001404          BIT      #BIT09, @SWR    ;;LOOP ON ERROR?
007570 013737 001110 001106 7$:      BEQ      4$           ;;BR IF NO
007576 000446          MOV     $LPERR, $LPADR  ;;SET LOOP ADDRESS TO LAST SCOPE
007600 105037 001103          BR      $OVER
007600          4$:      CLRB   $ERFLG      ;;ZERO THE ERROR FLAG

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2299 007604 005037 001160 CLR STIMES ;; CLEAR THE NUMBER OF ITERATIONS TO MAKE
2300 007610 000415 BR IS ;; ESCAPE TO THE NEXT TEST
2301 007612 032777 004000 171320 3$: BIT #BIT11,2SWR ;; INHIBIT ITERATIONS?
2302 007620 001011 BNE IS ;; BR IF YES
2303 007622 005737 001202 TST $PASS ;; IF FIRST PASS OF PROGRAM
2304 007626 001406 BEQ IS ;; INHIBIT ITERATIONS
2305 007630 005237 001104 INC $ICNT ;; INCREMENT ITERATION COUNT
2306 007634 023737 001160 001104 CMP $TIMES,$ICNT ;; CHECK THE NUMBER OF ITERATIONS MADE
2307 007642 002024 BGE $OVER ;; BR IF MORE ITERATION REQUIRED
2308 007644 012737 000001 001104 1$: MOV #1,$ICNT ;; REINITIALIZE THE ITERATION COUNTER
2309 007652 013737 007730 001160 MOV $SMXCNT,$TIMES ;; SET NUMBER OF ITERATIONS TO DO
2310 007660 105237 001102 $SVLAD: INCB $STSTN ;; COUNT TEST NUMBERS
2311 007664 113737 001102 001200 MOVB $STSTN,$STSTN ;; SET TEST NUMBER IN APT MAILBOX
2312 007672 011637 001106 MOV (SP),$LPADR ;; SAVE SCOPE LOOP ADDRESS
2313 007676 011637 001110 MOV (SP),$LPERR ;; SAVE ERROR LOOP ADDRESS
2314 007702 005037 001162 CLR $ESCAPE ;; CLEAR THE ESCAPE FROM ERROR ADDRESS
2315 007706 112737 000001 001115 MOVB #1,$ERMAX ;; ONLY ALLOW ONE(1) ERROR ON NEXT TEST
2316 007714 013777 001102 171220 $OVER: MOV $STSTN,$DISPLAY ;; DISPLAY TEST NUMBER
2317 007722 013716 001106 MOV $LPADR,(SP) ;; FUDGE RETURN ADDRESS
2318 007726 000002 RTI ;; FIXES PS
2319 007730 003720 $SMXCNT: 2000. ;; MAX. NUMBER OF ITERATIONS
2320 .SBTTL TTY INPUT ROUTINE
2321
2322 ;;*****
2323 .ENABL LSB
2324
2325 ;;*****
2326 *SOFTWARE SWITCH REGISTER CHANGE ROUTINE.
2327 *ROUTINE IS ENTERED FROM THE TRAP HANDLER, AND WILL
2328 *SERVICE THE TEST FOR CHANGE IN SOFTWARE SWITCH REGISTER TRAP CALL
2329 *WHEN OPERATING IN TTY FLAG MODE.
2330 007732 022737 000176 001140 $CKSWR: CMP #SWREG,SWR ;; IS THE SOFT-SWR SELECTED?
2331 007740 001074 BNE IS ;; BRANCH IF NO
2332 007742 105777 171176 TSTB $STKS ;; CHAR THERE?
2333 007746 100071 BPL IS ;; IF NO, DON'T WAIT AROUND
2334 007750 117746 171172 MOVB $STKB,-(SP) ;; SAVE THE CHAR
2335 007754 042716 177600 BIC #177,(SP) ;; STRIP-OFF THE ASCII
2336 007760 022726 000007 CMP #7,(SP)+ ;; IS IT A CONTROL G?
2337 007764 001062 BNE IS ;; NO, RETURN TO USER
2338 007766 123727 001134 000001 CMPB $AUTOB,#1 ;; ARE WE RUNNING IN AUTO-MODE?
2339 007774 001456 BEQ IS ;; BRANCH IF YES
2340
2341 007776 104401 010457 $GTSWR: TYPE ,SCNTLG ;; ECHO THE CONTROL-G (↑G)
2342 010002 104401 010464 TYPE $MSWR ;; TYPE CURRENT CONTENTS
2343 010006 013746 000176 MOV $SWREG,-(SP) ;; SAVE SWREG FOR TYPEOUT
2344 010012 104402 TYPOC ;; GO TYPE--OCTAL ASCII(ALL DIGITS)
2345 010014 104401 010475 TYPE ,SMNEW ;; PROMPT FOR NEW SWR
2346 010020 005046 19$: CLR -(SP) ;; CLEAR COUNTER
2347 010022 005046 CLR -(SP) ;; THE NEW SWR
2348 010024 105777 171114 7$: TSTB $STKS ;; CHAR THERE?
2349 010030 100375 BPL 7$ ;; IF NOT TRY AGAIN
2350
2351 010032 117746 171110 MOVB $STKB,-(SP) ;; PICK UP CHAR
2352 010036 042716 177600 BIC #177,(SP) ;; MAKE IT 7-BIT ASCII

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2353
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2355
2356 010042 021627 000025      9$:  CMP      (SP),#25      ;; IS IT A CONTROL-U?
2357 010046 001005              BNE      10$           ;; BRANCH IF NOT
2358 010050 104401 010452      TYPE     $CNTLU       ;; YES, ECHO CONTROL-U (↑U)
2359 010054 062706 000006      20$:  ADD      #6,SP      ;; IGNORE PREVIOUS INPUT
2360 010060 000757              BR       19$           ;; LET'S TRY IT AGAIN
2361
2362
2363 010062 021627 000015      10$:  CMP      (SP),#15     ;; IS IT A <CR>?
2364 010066 001022              BNE      16$           ;; BRANCH IF NO
2365 010070 005766 000004      TST     4(SP)         ;; YES, IS IT THE FIRST CHAR?
2366 010074 001403              BEQ     11$           ;; BRANCH IF YES
2367 010076 016677 000002 171034  MOV     2(SP),@SWR    ;; SAVE NEW SWR
2368 010104 062706 000006      11$:  ADD      #6,SP      ;; CLEAR UP STACK
2369 010110 104401 001171      14$:  TYPE     $CRLF       ;; ECHO <CR> AND <LF>
2370 010114 123727 001135 000001  CMPB   $INTAG,#1     ;; RE-ENABLE TTY KBD INTERRUPTS?
2371 010122 001003              BNE     15$           ;; BRANCH IF NOT
2372 010124 012777 000100 171012  MOV     #100,@STKS   ;; RE-ENABLE TTY KBD INTERRUPTS
2373 010132 000002              RTI                    ;; RETURN
2374 010134 004737 011372      15$:  JSR     PC,$TYPEC    ;; ECHO CHAR
2375 010140 021627 000060      16$:  CMP     (SP),#60     ;; CHAR < 0?
2376 010144 002420              BLT     18$           ;; BRANCH IF YES
2377 010146 021627 000067      CMP     (SP),#67     ;; CHAR > 7?
2378 010152 003015              BGT     18$           ;; BRANCH IF YES
2379 010154 042726 000060      BIC     #60,(SP)+    ;; STRIP-OFF ASCII
2380 010160 005766 000002      TST     2(SP)        ;; IS THIS THE FIRST CHAR
2381 010164 001403              BEQ     17$           ;; BRANCH IF YES
2382 010166 006316              ASL     (SP)         ;; NO, SHIFT PRESENT
2383 010170 006316              ASL     (SP)         ;; CHAR OVER TO MAKE
2384 010172 006316              ASL     (SP)         ;; ROOM FOR NEW ONE.
2385 010174 005266 000002      17$:  INC     2(SP)        ;; KEEP COUNT OF CHAR
2386 010200 056616 177776      BIS     -2(SP),(SP)  ;; SET IN NEW CHAR
2387 010204 000707              BR      7$           ;; GET THE NEXT ONE
2388 010206 104401 001170      18$:  TYPE     $QUES       ;; TYPE ?<CR><LF>
2389 010212 000720              BR      20$         ;; SIMULATE CONTROL-U
2390
2391 .DSABL  LSB
2392
2393 *****
2394 *THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY
2395 *CALL:
2396 *      RDCHR          ;; INPUT A SINGLE CHARACTER FROM THE TTY
2397 *      RETURN HERE    ;; CHARACTER IS ON THE STACK
2398 *                    ;; WITH PARITY BIT STRIPPED OFF
2399 *
2400
2401 010214 011646 000004 000002  SRDCHR: MOV     (SP),-(SP)  ;; PUSH DOWN THE PC
2402 010216 016666 170714 000002  MOV     4(SP),2(SP)   ;; SAVE THE PS
2403 010224 105777 170714 000002  1$:  TSTB   @STKS         ;; WAIT FOR
2404 010230 100375 170710 000004  BPL     1$           ;; A CHARACTER
2405 010232 117766 170710 000004  MOVB   @STKB,4(SP)   ;; READ THE TTY
2406 010240 042766 177600 000004  BIC     #↑C(↑7?),4(SP) ;; GET RID OF JUNK IF ANY
    
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2407 010246 026627 000004 000023      CMP      4(SP),#23      ;; IS IT A CONTROL-S?
2408 010254 001013                    BNE      3$           ;; BRANCH IF NO
2409 010256 105777 170662                2$: TSTB      #STKS      ;; WAIT FOR A CHARACTER
2410 010262 100375                    BPL      2$           ;; LOOP UNTIL ITS THERE
2411 010264 117746 170656                MOV      #STKB,-(SP)   ;; GET CHARACTER
2412 010270 042716 177600                BIC      #C177,(SP)   ;; MAKE IT 7-BIT ASCII
2413 010274 022627 000021                CMP      (SP)+,#21    ;; IS IT A CONTROL-Q?
2414 010300 001366                    BNE      2$           ;; IF NOT DISCARD IT
2415 010302 000750                    BR       1$           ;; YES, RESUME
2416 010304 026627 000004 000140        3$: CMP      4(SP),#140  ;; IS IT UPPER CASE?
2417 010312 002407                    BLT      4$           ;; BRANCH IF YES
2418 010314 026627 000004 000175        CMP      4(SP),#175   ;; IS IT A SPECIAL CHAR?
2419 010322 003003                    BGT      4$           ;; BRANCH IF YES
2420 010324 042766 000040 000004        BIC      #40,4(SP)    ;; MAKE IT UPPER CASE
2421 010332 000002                    4$: RTI              ;; GO BACK TO USER
2422                                     ;; *****
2423                                     ;; *THIS ROUTINE WILL INPUT A STRING FROM THE TTY
2424                                     ;; *CALL:
2425                                     ;; *      RDLIN
2426                                     ;; *      RETURN HERE
2427                                     ;; *      INPUT A STRING FROM THE TTY
2428                                     ;; *      ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK
2429                                     ;; *      TERMINATOR WILL BE A BYTE OF ALL 0'S
2430 010334 010346                    SRDLIN: MOV      R3,-(SP) ;; SAVE R3
2431 010336 012703 010442                1$: MOV      #STYIN,R3  ;; GET ADDRESS
2432 010342 022703 010452                2$: CMP      #STYIN+8.,R3 ;; BUFFER FULL?
2433 010346 101405                    BLOS     4$           ;; BR IF YES
2434 010350 104410                    RDCHR    ;; GO READ ONE CHARACTER FROM THE TTY
2435 010352 112613                    MOV      (SP)+,(R3)   ;; GET CHARACTER
2436 010354 122713 000177                10$: CMP      #177,(R3) ;; IS IT A RUBOUT
2437 010360 001003                    BNE      3$           ;; SKIP IF NOT
2438 010362 104401 001170                4$: TYPE     $QUES    ;; TYPE A '?'
2439 010366 000763                    BR       1$           ;; CLEAR THE BUFFER AND LOOP
2440 010370 111337 010440                3$: MOV      (R3),9$   ;; ECHO THE CHARACTER
2441 010374 104401 010440                TYPE     9$
2442 010400 122723 000015                CMP      #15,(R3)+   ;; CHECK FOR RETURN
2443 010404 001356                    BNE      2$           ;; LOOP IF NOT RETURN
2444 010406 105063 177777                CLRB    -1(R3)       ;; CLEAR RETURN (THE 15)
2445 010412 104401 001172                TYPE     $LF         ;; TYPE A LINE FEED
2446 010416 012603                    MOV      (SP)+,R3    ;; RESTORE R3
2447 010420 011646                    MOV      (SP),-(SP)  ;; ADJUST THE STACK AND PUT ADDRESS OF THE
2448 010422 016666 000004 000002        MOV      4(SP),2(SP) ;; FIRST ASCII CHARACTER ON IT
2449 010430 012766 010442 000004        MOV      #STYIN,4(SP)
2450 010436 000002                    RTI
2451 010440 000                    9$: .BYTE    0        ;; RETURN
2452 010442 000                    .BYTE    0        ;; STORAGE FOR ASCII CHAR. TO TYPE
2453 010444 000                    .BLKB    8        ;; TERMINATOR
2454 010446 000010                    $TTYIN: .ASCIZ  /U<15><12> ;; RESERVE 8 BYTES FOR TTY INPUT
2455 010448 052536 005015 000          $CNTLU: .ASCIZ  /G<15><12> ;; CONTROL "U"
2456 010450 136 006507 000012          $CNTLG: .ASCIZ  /G<15><12> ;; CONTROL "G"
2457 010452 005015 053523 020122          $MSWR:  .ASCIZ  <15><12>/SWR = /
2458 010454 020075 000                    $MNEW:  .ASCIZ  / NEW = /
2459 010456 040 047040 053505          .SBTTL  BINARY TO OCTAL (ASCII) AND TYPE
2460 010502 036440 000040

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2484 010506 017646 000000
2485 010512 116637 000001 010731
2486 010520 112637 010733
2487 010524 062716 000002
2488 010530 000406
2489 010532 112737 000001 010731
2490 010540 112737 000006 010733
2491 010546 112737 000005 010730
2492 010554 010346
2493 010556 010446
2494 010560 010546
2495 010562 113704 010733
2496 010566 005404
2497 010570 062704 000006
2498 010574 110437 010732
2499 010600 113704 010731
2500 010604 016605 000012
2501 010610 005003
2502 010612 006105 1S:
2503 010614 000404
2504 010616 006105 2S:
2505 010620 006105
2506 010622 006105
2507 010624 010503
2508 010626 006103 3S:
2509 010630 105337 010732
2510 010634 100016
2511 010636 042703 177770
2512 010642 001002
2513 010644 005704
2514 010646 001403

```

```

*****
*THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT
*OCTAL (ASCII) NUMBER AND TYPE IT.
*STYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE
*CALL:
*   MOV     NUM,-(SP)      ;;NUMBER TO BE TYPED
*   TYPOS   ;;CALL FOR TYPEOUT
*   .BYTE  N              ;;N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE
*   .BYTE  M              ;;M=1 OR 0
*                               ;;1=TYPE LEADING ZEROS
*                               ;;0=SUPPRESS LEADING ZEROS
*
*STYPON---ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST
*STYPOS OR STYPOC
*CALL:
*   MOV     NUM,-(SP)      ;;NUMBER TO BE TYPED
*   TYPON   ;;CALL FOR TYPEOUT
*
*STYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER
*CALL:
*   MOV     NUM,-(SP)      ;;NUMBER TO BE TYPED
*   TYPOC   ;;CALL FOR TYPEOUT
*
*STYPOS: MOV     @ (SP),-(SP)  ;;PICKUP THE MODE
*        MOV     1(SP),SOFILL ;;LOAD ZERO FILL SWITCH
*        MOV     (SP)+,SOMODE+1 ;;NUMBER OF DIGITS TO TYPE
*        ADD     @2,(SP)      ;;ADJUST RETURN ADDRESS
*        BR     STYPON
*STYPOC: MOV     @1,SOFILL    ;;SET THE ZERO FILL SWITCH
*        MOV     @6,SOMODE+1 ;;SET FOR SIX(6) DIGITS
*STYPON: MOV     @5,SOCNT     ;;SET THE ITERATION COUNT
*        MOV     R3,-(SP)    ;;SAVE R3
*        MOV     R4,-(SP)    ;;SAVE R4
*        MOV     R5,-(SP)    ;;SAVE R5
*        MOV     SOMODE+1,R4 ;;GET THE NUMBER OF DIGITS TO TYPE
*        NEG     R4
*        ADD     @6,R4       ;;SUBTRACT IT FOR MAX. ALLOWED
*        MOV     R4,SOMODE   ;;SAVE IT FOR USE
*        MOV     SOFILL,R4   ;;GET THE ZERO FILL SWITCH
*        MOV     12(SP),R5   ;;PICKUP THE INPUT NUMBER
*        CLR     R3         ;;CLEAR THE OUTPUT WORD
*        ROL     R5         ;;ROTATE MSB INTO "C"
*        BR     3$         ;;GO DO MSB
*        ROL     R5         ;;FORM THIS DIGIT
*        ROL     R5
*        ROL     R5
*        MOV     R5,R3
*        ROL     R3         ;;GET LSB OF THIS DIGIT
*        DECB   SOMODE     ;;TYPE THIS DIGIT?
*        BPL   7$         ;;BR IF NO
*        BIC   @177770,R3 ;;GET RID OF JUNK
*        BNE   4$         ;;TEST FOR 0
*        TST   R4         ;;SUPPRESS THIS 0?
*        BEQ   5$         ;;BR IF YES

```

2515	010650	005204		4\$:	INC	R4	:: DON'T SUPPRESS ANYMORE 0'S
2516	010652	052703	000060		BIS	0'0,R3	:: MAKE THIS DIGIT ASCII
2517	010656	052703	000040	5\$:	BIS	0' R3	:: MAKE ASCII IF NOT ALREADY
2518	010662	110337	010726		MOVB	R3,0\$:: SAVE FOR TYPING
2519	010666	104401	010726		TYPE	0\$:: GO TYPE THIS DIGIT
2520	010672	105337	010730	7\$:	DECB	\$OCNT	:: COUNT BY 1
2521	010676	003347			BGT	2\$:: BR IF MORE TO DO
2522	010700	002402			BLT	6\$:: BR IF DONE
2523	010702	005204			INC	R4	:: INSURE LAST DIGIT ISN'T A BLANK
2524	010704	000744			BR	2\$:: GO DO THE LAST DIGIT
2525	010706	012605		6\$:	MOV	(SP)+,R5	:: RESTORE R5
2526	010710	012604			MOV	(SP)+,R4	:: RESTORE R4
2527	010712	012603			MOV	(SP)+,R3	:: RESTORE R3
2528	010714	016666	000002 000004		MOV	2(SP),4(SP)	:: SET THE STACK FOR RETURNING
2529	010722	012616			MOV	(SP)+,(SP)	
2530	010724	000002			RTI		:: RETURN
2531	010726	000		8\$:	.BYTE	0	:: STORAGE FOR ASCII DIGIT
2532	010727	000			.BYTE	0	:: TERMINATOR FOR TYPE ROUTINE
2533	010730	000		\$OCNT:	.BYTE	0	:: OCTAL DIGIT COUNTER
2534	010731	000		\$OFILL:	.BYTE	0	:: ZERO FILL SWITCH
2535	010732	000000		\$OMODE:	.WORD	0	:: NUMBER OF DIGITS TO TYPE

.SBTTL CONVERT BINARY TO DECIMAL AND TYPE ROUTINE

*THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 5-DIGIT
*SIGNED DECIMAL (ASCII) NUMBER AND TYPE IT. DEPENDING ON WHETHER THE
*NUMBER IS POSITIVE OR NEGATIVE A SPACE OR A MINUS SIGN WILL BE TYPED
*BEFORE THE FIRST DIGIT OF THE NUMBER. LEADING ZEROS WILL ALWAYS BE
*REPLACED WITH SPACES.
*CALL:

* MOV NUM,-(SP) ;:PUT THE BINARY NUMBER ON THE STACK
* TYPDS ;:GO TO THE ROUTINE

STYPDS:

MOV R0,-(SP) ;:PUSH R0 ON STACK
MOV R1,-(SP) ;:PUSH R1 ON STACK
MOV R2,-(SP) ;:PUSH R2 ON STACK
MOV R3,-(SP) ;:PUSH R3 ON STACK
MOV R5,-(SP) ;:PUSH R5 ON STACK
MOV #20200,-(SP) ;:SET BLANK SWITCH AND SIGN
MOV 20(SP),R5 ;:GET THE INPUT NUMBER
BPL 1\$;:BR IF INPUT IS POS.
NEG R5 ;:MAKE THE BINARY NUMBER POS.
MOVB #'-,1(SP) ;:MAKE THE ASCII NUMBER NEG.
CLR R0 ;:ZERO THE CONSTANTS INDEX
MOV #SDBLK,R3 ;:SETUP THE OUTPUT POINTER
MOVB #' ,(R3)+ ;:SET THE FIRST CHARACTER TO A BLANK
CLR R2 ;:CLEAR THE BCD NUMBER
MOV SDBLK(R0),R1 ;:GET THE CONSTANT
SUB R1,R5 ;:FORM THIS BCD DIGIT
BLT 4\$;:BR IF DONE
INC R2 ;:INCREASE THE BCD DIGIT BY 1
BR 3\$
ADD R1,R5 ;:ADD BACK THE CONSTANT
TST R2 ;:CHECK IF BCD DIGIT=0
BNE 5\$;:FALL THROUGH IF 0
TSTB (SP) ;:STILL DOING LEADING 0'S?
BMI 7\$;:BR IF YES
ASLB (SP) ;:MSD?
BCC 6\$;:BR IF NO
MOVB 1(SP),-1(R3) ;:YES--SET THE SIGN
BIS #'0,R2 ;:MAKE THE BCD DIGIT ASCII
BIS #' ,R2 ;:MAKE IT A SPACE IF NOT ALREADY A DIGIT
MOVB R2,(R3)+ ;:PUT THIS CHARACTER IN THE OUTPUT BUFFER
TST (R0)+ ;:JUST INCREMENTING
CMP R0,#10 ;:CHECK THE TABLE INDEX
BLT 2\$;:GO DO THE NEXT DIGIT
BGT 8\$;:GO TO EXIT
MOV R5,R2 ;:GET THE LSD
BR 6\$;:GO CHANGE TO ASCII
TSTB (SP)+ ;:WAS THE LSD THE FIRST NON-ZERO?
BPL 9\$;:BR IF NO
MOVB -1(SP),-2(R3) ;:YES--SET THE SIGN FOR TYPING
CLRB (R3) ;:SET THE TERMINATOR
MOV (SP)+,R5 ;:POP STACK INTO R5

2548 010734
2549 010734 010046
2550 010736 010146
2551 010740 010246
2552 010742 010346
2553 010744 010546
2554 010746 012746 020200
2555 010752 016605 000020
2556 010756 100004
2557 010760 005405
2558 010762 112766 000055 000001
2559 010770 005000
2560 010772 012703 011150
2561 010776 112723 000040
2562 011002 005002
2563 011004 016001 011140
2564 011010 160105
2565 011012 002402
2566 011014 005202
2567 011016 000774
2568 011020 060105
2569 011022 005702
2570 011024 001002
2571 011026 105716
2572 011030 100407
2573 011032 106316
2574 011034 103003
2575 011036 116663 000001 177777
2576 011044 052702 000060
2577 011050 052702 000040
2578 011054 110223
2579 011056 005720
2580 011060 020027 000010
2581 011064 002746
2582 011066 003002
2583 011070 010502
2584 011072 000764
2585 011074 105726
2586 011076 100003
2587 011100 116663 177777 177776
2588 011106 105013
2589 011110 012605

2600 011112 012603
 2601 011114 012602
 2602 011116 012601
 2603 011120 012600
 2604 011122 104401
 2605 011126 016666
 2606 011134 012616
 2607 011136 000002
 2608 011140 023420
 2609 011142 001750
 2610 011144 000144
 2611 011146 000012
 2612 011150 000004
 2613
 2614
 2615
 2616
 2617
 2618
 2619
 2620 011160 105737 001157
 2621 011164 100002
 2622 011166 000000
 2623 011170 000430
 2624 011172 010046
 2625 011174 017600 000002
 2626 011200 122737 000001 001214
 2627 011206 001011
 2628 011210 132737 000100 001215
 2629 011216 001405
 2630 011220 010037 011230
 2631 011224 004737 011450
 2632 011230 000000
 2633 011232 132737 000040 001215
 2634 011240 001003
 2635 011242 112046
 2636 011244 001005
 2637 011246 005726
 2638 011250 012600
 2639 011252 062716 000002
 2640 011256 000002
 2641 011260 122716 000011
 2642 011264 001430
 2643 011266 122716 000200

011150 000002 000004

```

MOV (SP)+,R3      ;; POP STACK INTO R3
MOV (SP)+,R2      ;; POP STACK INTO R2
MOV (SP)+,R1      ;; POP STACK INTO R1
MOV (SP)+,R0      ;; POP STACK INTO R0
TYPE $DBLK        ;; NOW TYPE THE NUMBER
MOV 2(SP),4(SP)  ;; ADJUST THE STACK
MOV (SP)+,(SP)
RTI               ;; RETURN TO USER

SDTBL: 10000.
       1000.
       100.
       10.

$DBLK: .BLKW 4
.SBTTL TYPE ROUTINE

```

```

*****
#ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
#THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
#NOTE1: $NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
#NOTE2: $FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
#NOTE3: $FILLC CONTAINS THE CHARACTER TO FILL AFTER.
*
#CALL:
#1) USING A TRAP INSTRUCTION
* TYPE ,MESADR ;; MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
*OR
* TYPE
* MESADR
*

```

```

$TYPE: TSTB $TPFLG      ;; IS THERE A TERMINAL?
        BPL 1$         ;; BR IF YES
        HALT          ;; HALT HERE IF NO TERMINAL
        BR 3$         ;; LEAVE
1$:     MOV RO,-(SP)    ;; SAVE RO
        MOV 22(SP),RO  ;; GET ADDRESS OF ASCIZ STRING
        CMPB #APTENV,SENV ;; RUNNING IN APT MODE
        BNE 62$       ;; NO GO CHECK FOR APT CONSOLE
        BITB #APTPOOL,SENVM ;; SPOOL MESSAGE TO APT
        BEQ 62$       ;; NO GO CHECK FOR CONSOLE
        MOV RO,61$    ;; SETUP MESSAGE ADDRESS FOR APT
        JSR PC,$ATY3  ;; SPOOL MESSAGE TO APT
        .WORD 0       ;; MESSAGE ADDRESS
        BITB #APTCSUP,SENVM ;; APT CONSOLE SUPPRESSED
        BNE 60$       ;; YES, SKIP TYPE OUT
        MOV 4$        ;; PUSH CHARACTER TO BE TYPED ONTO STACK
        BNE 4$        ;; BR IF IT ISN'T THE TERMINATOR
        TST (SP)+     ;; IF TERMINATOR POP IT OFF THE STACK
        MOV (SP)+,RO  ;; RESTORE RO
        ADD #2,(SP)   ;; ADJUST RETURN PC
        RTI          ;; RETURN
4$:     CMPB #HT,(SP)  ;; BRANCH IF <HT>
        BEQ 8$
        CMPB #CRLF,(SP) ;; BRANCH IF NOT <CRLF>
61$:   .WORD 0
62$:   BITB #APTCSUP,SENVM
60$:   BNE 60$

```

```

2644 011272 001006      BNE      55
2645 011274 005726      TST      (SP)+      ;; POP <CR><LF> EQUIV
2646 011276 104401      TYPE
2647 011300 001171      SCRLF      ;; TYPE A CR AND LF
2648 011302 105037 011436      CLRB      $CHARCNT      ;; CLEAR CHARACTER COUNT
2649 011306 000755      BR      25      ;; GET NEXT CHARACTER
2650 011310 004737 011372      55: JSR      PC,$TYPEC      ;; GO TYPE THIS CHARACTER
2651 011314 123726 001156      65: CMPB     $FILLC,(SP)+      ;; IS IT TIME FOR FILLER CHARS.?
2652 011320 001350      BNE      25      ;; IF NO GO GET NEXT CHAR.
2653 011322 013746 001154      MOV      $NULL,-(SP)      ;; GET # OF FILLER CHARS. NEEDED
2654                                ;; AND THE NULL CHAR.
2655 011326 105366 000001      75: DECB     1(SP)      ;; DOES A NULL NEED TO BE TYPED?
2656 011332 002770      BLT      65      ;; BR IF NO--GO POP THE NULL OFF OF STACK
2657 011334 004737 011372      JSR      PC,$TYPEC      ;; GO TYPE A NULL
2658 011340 105337 011436      DECB     $CHARCNT      ;; DO NOT COUNT AS A COUNT
2659 011344 000770      BR      75      ;; LOOP
2660
2661                                ;HORIZONTAL TAB PROCESSOR
2662
2663 011346 112716 000040      85: MOVB     #' (SP)      ;; REPLACE TAB WITH SPACE
2664 011352 004737 011372      95: JSR      PC,$TYPEC      ;; TYPE A SPACE
2665 011356 132737 000007 011436      BITB     #',$CHARCNT      ;; BRANCH IF NOT AT
2666 011364 001372      BNE      95      ;; TAB STOP
2667 011366 005726      TST      (SP)+      ;; POP SPACE OFF STACK
2668 011370 000724      BR      25      ;; GET NEXT CHARACTER
2669 011372 105777 167552      $TYPEC: TSTB     2$TPS      ;; WAIT UNTIL PRINTER IS READY
2670 011376 100375      BPL      $TYPEC
2671 011400 116677 000002 167544      MOVB     2(SP),2$TPB      ;; LOAD CHAR TO BE TYPED INTO DATA REG.
2672 011406 122766 000015 000002      CMPB     $CR,2(SP)      ;; IS CHARACTER A CARRIAGE RETURN?
2673 011414 001003      BNE      15      ;; BRANCH IF NO
2674 011416 105037 011436      CLRB     $CHARCNT      ;; YES--CLEAR CHARACTER COUNT
2675 011422 000406      BR      $TYPEC      ;; EXIT
2676 011424 122766 000012 000002      15: CMPB     $LF,2(SP)      ;; IS CHARACTER A LINE FEED?
2677 011432 001402      BEQ      $TYPEC      ;; BRANCH IF YES
2678 011434 105227      INCB     (PC)+      ;; COUNT THE CHARACTER
2679 011436 000000      $CHARCNT: WORD 0      ;; CHARACTER COUNT STORAGE
2680 011440 000207      $TYPEX: RTS      PC
2681
2682                                .SBTTL APT COMMUNICATIONS ROUTINE
2683
2684                                ;; *****
2685 011442 112737 000001 011706      $ATY1: MOVB     #1,$FFLG      ;; TO REPORT FATAL ERROR
2686 011450 112737 000001 011704      $ATY3: MOVB     #1,$MFLG      ;; TO TYPE A MESSAGE
2687 011456 000403      BR      $ATYC
2688 011460 112737 000001 011706      $ATY4: MOVB     #1,$FFLG      ;; TO ONLY REPORT FATAL ERROR
2689 011466      $ATYC:
2690 011466 010046      MOV      R0,-(SP)      ;; PUSH R0 ON STACK
2691 011470 010146      MOV      R1,-(SP)      ;; PUSH R1 ON STACK
2692 011472 105737 011704      TSTB     $MFLG      ;; SHOULD TYPE A MESSAGE?
2693 011476 001450      BEQ      55      ;; IF NOT: BR
2694 011500 122737 000001 001214      CMPB     $APTENV,$ENV      ;; OPERATING UNDER APT?
2695 011506 001031      BNE      35      ;; IF NOT: BR
2696 011510 132737 000100 001215      BITB     $APTPOOL,$ENVM      ;; SHOULD SPOOL MESSAGES?
2697 011516 001425      BEQ      35      ;; IF NOT: BR

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```

2698 011520 017600 000004      MOV      24(SP),R0      ;;GET MESSAGE ADDR.
2699 011524 062766 000002 000004      ADD      82,4(SP)      ;;BUMP RETURN ADDR.
2700 011532 005737 001174      1S:     TST      SMSGTYPE  ;;SEE IF DONE W/ LAST XMISSION?
2701 011536 001375      BNE      1S           ;;IF NOT: WAIT
2702 011540 010037 001210      MOV      R0,SMSGAD    ;;PUT ADDR IN MAILBOX
2703 011544 105720      2S:     TSTB     (R0)+    ;;FIND END OF MESSAGE
2704 011546 001376      BNE      2S
2705 011550 163700 001210      SUB      SMSGAD,R0    ;;SUB START OF MESSAGE
2706 011554 006200      ASR      R0           ;;GET MESSAGE LNTH IN WORDS
2707 011556 010037 001212      MOV      R0,SMSGLGT   ;;PUT LENGTH IN MAILBOX
2708 011562 012737 000004 001174      MOV      84,SMSGTYPE  ;;TELL APT TO TAKE MSG.
2709 011570 000413      BR       5S
2710 011572 017637 000004 011616 3S:     MOV      24(SP),4S    ;;PUT MSG ADDR IN JSR LINKAGE
2711 011600 062766 000002 000004      ADD      82,4(SP)      ;;BUMP RETURN ADDRESS
2712 011606 013746 177776      MOV      177776,-(SP) ;;PUSH 177776 ON STACK
2713 011612 004737 011160      JSR     PC,STYPE     ;;CALL TYPE MACRO
2714 011616 000000      4S:     .WORD    0
2715 011620      5S:
2716 011620 105737 011706      10S:    TSTB     SFFLG      ;;SHOULD REPORT FATAL ERROR?
2717 011624 001416      BEQ     12S          ;;IF NOT: BR
2718 011626 005737 001214      TST     SENV        ;;RUNNING UNDER APT?
2719 011632 001413      BEQ     12S          ;;IF NOT: BR
2720 011634 005737 001174      11S:    TST     SMSGTYPE    ;;FINISHED LAST MESSAGE?
2721 011640 001375      BNE     11S          ;;IF NOT: WAIT
2722 011642 017637 000004 001176      MOV      24(SP),SFATAL ;;GET ERROR #
2723 011650 062766 000002 000004      ADD      82,4(SP)      ;;BUMP RETURN ADDR.
2724 011656 005237 001174      INC     SMSGTYPE     ;;TELL APT TO TAKE ERROR
2725 011662 105037 011706      12S:    CLRB     SFFLG      ;;CLEAR FATAL FLAG
2726 011666 105037 011705      CLRB     SLFLG       ;;CLEAR LOG FLAG
2727 011672 105037 011704      CLRB     SMFLG       ;;CLEAR MESSAGE FLAG
2728 011676 012601      MOV     (SP)+,R1     ;;POP STACK INTO R1
2729 011700 012600      MOV     (SP)+,R0     ;;POP STACK INTO R0
2730 011702 000207      RTS     PC           ;;RETURN
2731 011704      000      SMFLG: .BYTE    0      ;;MESSG. FLAG
2732 011705      000      SLFLG: .BYTE    0      ;;LOG FLAG
2733 011706      000      SFFLG: .BYTE    0      ;;FATAL FLAG
2734      01171C      .EVEN
2735      000200      APTSIZE=200
2736      000001      APTENV=001
2737      000100      APTSPool=100
2738      000040      APTCSUP=040
2739      .SBTTL POWER DOWN AND UP ROUTINES
2740
2741      ;:*****
2742      ;:POWER DOWN ROUTINE
2743 011710 012737 012050 000024 SPWRDN: MOV      8$ILLUP,2#PWRVEC ;;SET FOR FAST UP
2744 011716 012737 000340 000026      MOV      8340,2#PWRVEC+2 ;;PRIO:7
2745 011724 010046      MOV      R0,-(SP)     ;;PUSH R0 ON STACK
2746 011726 010146      MOV      R1,-(SP)     ;;PUSH R1 ON STACK
2747 011730 010246      MOV      R2,-(SP)     ;;PUSH R2 ON STACK
2748 011732 010346      MOV      R3,-(SP)     ;;PUSH R3 ON STACK
2749 011734 010446      MOV      R4,-(SP)     ;;PUSH R4 ON STACK
2750 011736 010546      MOV      R5,-(SP)     ;;PUSH R5 ON STACK
2751 011740 017746 167174      MOV      2$WR,-(SP)   ;;PUSH 2$WR ON STACK

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2752 011744 010637 012054      MOV      SP,SSAVR6      ;;SAVE SP
2753 011750 012737 011762 000024  MOV      $SPWRUP,$PWRVEC ;;SET UP VECTOR
2754 011756 000000      HALT
2755 011760 000776      BR      .-2            ;;HANG UP
2756
2757                                     ;:*****
2758                                     ;:POWER UP ROUTINE
2759 011762 012737 012050 000024 $PWRUP: MOV      $SILLUP,$PWRVEC ;;SET FOR FAST DOWN
2760 011770 013706 012054      MOV      $SAVR6,SP      ;;GET SP
2761 011774 005037 012054      CLR      $SAVR6         ;;WAIT LOOP FOR THE TTY
2762 012000 005237 012054      IS:     INC      $SAVR6  ;;WAIT FOR THE INC
2763 012004 001375      BNE     IS              ;;OF WORD
2764 012006 012677 167126      MOV      (SP)+,$SWR     ;;POP STACK INTO $SWR
2765 012012 012605      MOV      (SP)+,R5      ;;POP STACK INTO R5
2766 012014 012604      MOV      (SP)+,R4      ;;POP STACK INTO R4
2767 012016 012603      MOV      (SP)+,R3      ;;POP STACK INTO R3
2768 012020 012602      MOV      (SP)+,R2      ;;POP STACK INTO R2
2769 012022 012601      MOV      (SP)+,R1      ;;POP STACK INTO R1
2770 012024 012600      MOV      (SP)+,R0      ;;POP STACK INTO R0
2771 012026 012737 011710 000024  MOV      $SPWRDN,$PWRVEC ;;SET UP THE POWER DOWN VECTOR
2772 012034 012737 000340 000026  MOV      $340,$PWRVEC+2 ;;PRIO:7
2773 012042 104401      TYPE
2774 012044 012056      SPWRMG: .WORD  $POWER   ;;REPORT THE POWER FAILURE
2775 012046 000002      RTI
2776 012050 000000      $SILLUP: HALT          ;;POWER FAIL MESSAGE POINTER
2777 012052 000776      BR      .-2            ;;THE POWER UP SEQUENCE WAS STARTED
2778 012054 000000      $SAVR6: 0              ;;BEFORE THE POWER DOWN WAS COMPLETE
2779 012056 005015 047520 042527 $POWER: .ASCIZ  <15><12>"POWER" ;;PUT THE SP HERE
2780 012064 000122
2781                                     .EVEN
2782
2783                                     ;:
2784                                     ;:*THIS ROUTINE WILL PROTECT THE PROGRAM
2785                                     ;:*FROM INTERRUPTS.
2786                                     ;:
2787                                     ;:*THE TRAP CATCHER IS SET UP FOR
2788                                     ;:*
2789                                     ;*.WORD  +2
2790                                     ;*.WORD  JSR    PC,R0
2791                                     ;:
2792                                     ;:*ILLEGAL INTERRUPTS OR INTERRUPTS TO THE WRONG VECTOR
2793                                     ;:*GOTO THE VECTOR AND PICK UP THE ".+2" AS AN ADDRESS
2794                                     ;:*AND "4700" AS NEW STATUS.
2795                                     ;:*THE .+2 AS A PC WILL CAUSE EXECUTION OF THE "JSR PC,R0" (AN ILLEGAL INSTR).
2796                                     ;:*AND TRAP TO LOCATION "4". IN LOCATION 10 WE HAVE A
2797                                     ;:*POINTER HERE. IF THIS CONDITION CAUSES A TRAP TO LOC. 4
2798                                     ;:*WE WILL REPORT IT IN THE SAME MANNER THAT WE WOULD
2799                                     ;:*REPORT ANY OTHER ERROR.
2800                                     ;:
2801                                     ;:*IF A BUSS ERROR TRAP DID OCCUR AND CAUSE A TRAP TO 4,
2802                                     ;:*WE WILL HALT.
2803 012066 011637 012132      IOTRD: MOV      (6),TRTO    ;GET WHERE WE CAME TO.
2804 012072 162737 000004 012132  SUB      #4,TRTO       ;FORM REAL ADDR.
2805
    
```

```

2806 012100 023727 012132 001000      CMP      TRTO,#1000      ;DID TRAP COME FROM LESS THAN ADDR. 1000?
2807 012106 003402                      BLE      2$
2808
2809 012110 000000                      1$:     HALT            ;NO! MUST BE A BUSS ILLEGAL ADDR. TIME OUT.
2810
2811
2812 012112 000776                      2$:     BR      1$      ;DON'T ALLOW A CONTINUE.
2813 012114
2814
2815 012114 016637 000004 012134      MOV      4(6),TRFRO     ;GET TRAPPED FROM ADDR.
2816
2817 012122 062706 000004      ADD      #4,SP          ;/ADD #4 TO STACK POINTER.
2818
2819

```

;;SSSSSSSSSS>>> ERROR <<<SSSSSSSSSS

```

2823
2824 012126 104007      ERROR 7      ;/MODULE FAULT DETECTED:
2825
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```

;ERROR! ILLEGAL INTERRUPT
;OR INTERRUPT TO WRONG
;VECTOR - IF TEST NUMBER
;IS LESS THAN 10, ITS LIKELY
;(BUT NOT EXCLUSIVELY) TO BE A
;DEVICE OTHER THAN THE IBV-11
;TO BLAME.
;IF THE INTERRUPT OCCURRED
;DURING AN INTERRUPT TEST, I'D
;SUSPECT A PROBLEM WITH THE
;IBV-11.
;IF THE ADDRESS THE INTERRUPT
;VECTOR TO IS WITHIN THE RANGE
;OF VECTORS ASSIGNED TO THE IBV-11,
;THEN I'D SUSPECT THE IBV-11
;INTERRUPTED ILLEGALLY.
;IF THE ADDRESS THE INTERRUPT
;VECTORED TO IS OUTSIDE OF THE
;RANGE ASSIGNED TO THE IBV-11,
;I'D SUSPECT THAT THE
;IBV-11 PUT THE WRONG VECTOR ON
;THE BUSS DURING THE INTERRUPT
;PROCESS.
;FOR THIS ERROR - DON'T
;USE "LOOP ON ERROR" OPTION.
;ALSO EXPECT THE INTERRUPT TEST TO
;REPORT THAT THE IBV-11 DIDN'T
;INTERRUPT.
;FOLLOW RECOMMENDED PROCEDURE
;IN THE DOCUMENT (ON THIS DIAGNOSTIC)
;FOR LOOPING ON ERROR

..SSSSSSSSSS↑↑↑ ERROR ↑↑↑SSSSSSSSSS
RTI

2858 012130 000002
2859

2860 012132 000000
2861 012134 000000
2862
2863
2864
2865
2866
2867
2868
2869
2870
2871 012136 010046
2872 012140 016600 000002
2873 012144 005740
2874 012146 111000
2875 012150 006300
2876 012152 016000 012172
2877 012156 000200
2878
2879
2880
2881
2882 012160 011646
2883 012162 016666 000004 000002
2884 012170 000002
2885
2886
2887
2888
2889
2890
2891
2892
2893 012172 012160
2894 012174 011160
2895 012176 010532
2896 012200 010506
2897 012202 010546
2898 012204 010734
2899
2900 012206 010002
2901
2902 012210 007732
2903 012212 010214
2904 012214 010334
2905
2906
2907
2908 012216 005007 044415 051502 EM1:
2909 012224 043040 047125 052103
2910 012232 047511 020116 051105
2911 012240 047522 000122
2912
2913 012244 005007 044415 042102 EM2:

TRTO: .WORD 0 ; ADDR THAT WE INTERRUPTED TO
TRFR0: .WORD 0 ; ADDR THAT WE INTERRUPTED FROM.

.SBTTL TRAP DECODER

; THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
; AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
; OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL
; GO TO THAT ROUTINE.

STRAP: MOV RO, -(SP) ; SAVE RO
MOV 2(SP), RO ; GET TRAP ADDRESS
TST -(RO) ; BACKUP BY 2
MOVB (RO), RO ; GET RIGHT BYTE OF TRAP
ASL RO ; POSITION FOR INDEXING
MOV STRPAD(RO), RO ; INDEX TO TABLE
RTS RO ; GO TO ROUTINE

;; THIS IS USE TO HANDLE THE "GETPRI" MACRO

STRAP2: MOV (SP), -(SP) ; MOVE THE PC DOWN
MOV 4(SP), 2(SP) ; MOVE THE PSW DOWN
RTI ; RESTORE THE PSW

.SBTTL TRAP TABLE

; THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
; BY THE "TRAP" INSTRUCTION.

ROUTINE

\$TRPAD: .WORD \$STRAP2
\$TYPE ; CALL=TYPE TRAP+1(104401) TTY TYPEOUT ROUTINE
\$TYPOC ; CALL=TYPOC TRAP+2(104402) TYPE OCTAL NUMBER (WITH LEADING ZEROS)
\$TYPOS ; CALL=TYPOS TRAP+3(104403) TYPE OCTAL NUMBER (NO LEADING ZEROS)
\$TYPON ; CALL=TYPON TRAP+4(104404) TYPE OCTAL NUMBER (AS PER LAST CALL)
\$TYPDS ; CALL=TYPDS TRAP+5(104405) TYPE DECIMAL NUMBER (WITH SIGN)
\$GTSWR ; ; CALL=GTSWR TRAP+6(104406) GET SOFT-SWR SETTING
\$CKSWR ; ; CALL=CKSWR TRAP+7(104407) TEST FOR CHANGE IN SOFT-SWR
\$RDCHR ; ; CALL=RDCHR TRAP+10(104410) TTY TYPEIN CHARACTER ROUTINE
\$RDLIN ; ; CALL=RDLIN TRAP+11(104411) TTY TYPEIN STRING ROUTINE

.SBTTL MESSAGES AND TABLES

EM1: .ASCIZ<7><12><15>#IBS FUNCTION ERROR#
EM2: .ASCIZ<7><12><15>#IBD FUNCTION ERROR#

2968		012640				.EVEN	
2969							
2970	012640	001200	001116	001346	DT1:	.WORD	\$TESTN,\$ERRPC,IBS
2971							
2972	012646	000000			IBSA:	.WORD	0
2973							
2974	012650	000000	000000		IBDA:	.WORD	0,0
2975							
2976	012654	001200	001116	001124	DT3:	.WORD	\$TESTN,\$ERRPC,\$GDDAT,\$BDDAT,0
2977	012662	001126	000000				
2978							
2979	012666	001200	001116	001346	DT5:	.WORD	\$TESTN,\$ERRPC,IBS,0
2980	012674	000000					
2981							
2982	012676	001200	001116	012132	DT7:	.WORD	\$TESTN,\$ERRPC,TRTO,TRFRO,0
2983	012704	012134	000000				
2984							
2985	012710	000000	000000		DF0:	.WORD	0,0
2986							
2987		000001				.END	

ABASE = 160150	167#	255	296	376	377	378	379	384	385											
ACDM1 = 000000	255	298																		
ACDM2 = 000000	255																			
ACPUOP = 000000	255	270																		
ADD0 = 000000	255																			
ADD1 = 000000	255																			
ADD10 = 000000	255																			
ADD11 = 000000	255																			
ADD12 = 000000	255																			
ADD13 = 000000	255																			
ADD14 = 000000	255																			
ADD15 = 000000	255																			
ADD2 = 000000	255																			
ADD3 = 000000	255																			
ADD4 = 000000	255																			
ADD5 = 000000	255																			
ADD6 = 000000	255																			
ADD7 = 000000	255																			
ADD8 = 000000	255																			
ADD9 = 000000	255																			
ADEVCT = 000000	255	261																		
ADEVN = 000000	255	297																		
AENV = 000000	255	266																		
AENVN = 000000	255	267																		
AFATAL = 000000	255	258																		
AMADR1 = 000000	255	283																		
AMADR2 = 000000	255	287																		
AMADR3 = 000000	255	290																		
AMADR4 = 000000	255	293																		
AMAMS1 = 000000	255	277																		
AMAMS2 = 000000	255	285																		
AMAMS3 = 000000	255	288																		
AMAMS4 = 000000	255	291																		
AMSGAD = 000000	255	263																		
AMSGLC = 000000	255	264																		
AMSGTY = 000000	255	257																		
AMTYP1 = 000000	255	278																		
AMTYP2 = 000000	255	286																		
AMTYP3 = 000000	255	289																		
AMTYP4 = 000000	255	292																		
APASS = 000000	255	260																		
APRIOR = 000200	169#	255																		
APTCSU = 000040	2633	2738#																		
APTEW = 000001	2189	2626	2694	2736#																
APTSIZ = 000200	446	2735#																		
APTSP0 = 000100	2628	2696	2737#																	
ASWREG = 000000	255	268																		
ATESTN = 000000	255	259																		
ALUNIT = 000000	255	262																		
AUSWR = 000000	255	269																		
AVECT1 = 000640	168#	255	294	380	381	382	383	386	387	388	389	393	394							
	395	396	398	399	400	401														
AVECT2 = 000000	255	295																		
BIT0 = 000001	151#	722	723	738	853	991	1009	1472	1475	1490	1583	1677	2070							

ERRVEC= 000004	154#	431	432*	443*	450*	451*	540	541*	547*	575*	594	595*	601*
	633#	2277	2278*	2280*	2283*								
GNS = ##### U	525	2894	2895	2896	2897	2898	2900	2902	2903	2904			
GTSWR = 104406	520	2900#											
HT = 000011	64#	2641	2682										
IBCA = 001354	379#	458#	459*	603	703								
IBD = 001350	377#	454#	455*	456	463	549	665	992*	994	1009*	1010	1031*	1033
	1048#	1049	1070*	1072	1087*	1088	1109*	1111	1126*	1127	1148*	1150	1165*
	1166	1187*	1189	1204*	1205	1226*	1228	1243*	1244	1265*	1267	1282*	1283
	1302*	1304	1325*	1326	1340*	1341	1356	1357	1369	1390	1395	1408	1430
	1435	1451	1473	1478	1491	1513	1518	1531	1550	1564	1585*	1650*	1652
	1744*	1780*	2072*										
IBDA = 012650	463#	2974#											
IBD2 = 001370	385#	464#	465*	2073									
IBS = 001346	376#	453#	454	462	545	644	721*	722*	724	738*	740	757*	758*
	760	774#	776	793*	794#	796	810*	812	829*	830*	832	835*	836
	854	871#	872*	874	889#	891	914*	916*	918	933*	934	951*	952*
	954	968#	970	990*	1029*	1068*	1107*	1146*	1185*	1224*	1263*	1301*	1324*
	1339*	1355*	1388*	1389*	1392*	1407*	1428*	1429*	1432*	1448	1471*	1472*	1475*
	1490*	1511*	1512*	1515*	1530*	1549*	1563*	1582*	1583*	1587	1605*	1607	1626*
	1627*	1628	1630	1649*	1674*	1677*	1697*	1708*	1709*	1726*	1761*	1771*	1779*
	1802*	1830*	1873*	1883*	1908*	1929	1953*	1955	1959	1986*	1990	2016*	2021*
	2048*	2068*	2070*	2091*	2970	2979							
IBSA = 012646	462#	2972#											
IBS2 = 001366	384#	464	1831*	1839*	1856*	1874*	1884*	1909*	1924*	1925*	1954*	1958*	1987*
	1988*	2015*	2027*	2047*	2069*	2071*	2092*						
IBWC = 001352	378#	456*	457*	458	598	689							
IOTRD = 012066	46	450	2803#										
IOTVEC= 000020	159#	416*	417*										
LF = 000012	65#	2676	2682										
PC = %000007	85#	2117*	2120*	2130*	2135	2186*	2192*	2245*	2374*	2631*	2650*	2657*	2664*
	2678*	2680*	2713*	2730*									
PIRQ = 177772	71#												
PIRQVE= 000240	165#												
PRA = 001402	393#	479*	480*	481	497	498*	1772*	1804	1805*	2018*	2045	2046*	
PRA2 = 001412	398#	487*	488*	489									
PRB = 001404	394#	481*	482*	483	500	501*	1876*	1906	1907*				
PRB2 = 001414	399#	489*	490*	491									
PRC = 001406	395#	483*	484*	485	503	504*	1675*	1699	1700*	1706*	1728	1729*	1735
	1736*												
PRC2 = 001416	400#	491*	492*	493	1832*	1858	1859*						
PRD = 001410	396#	485*	486*	506	507*	1737*	1763	1764*					
PRD2 = 001420	401#	493*	494*										
PRD = 000000	88#												
PR1 = 000040	89#												
PR2 = 000100	90#												
PR3 = 000140	91#												
PR4 = 000200	92#												
PR5 = 000240	93#												
PR6 = 000300	94#												
PR7 = 000340	95#												
PS = 177776	68#	69											
PSH = 177776	69#												
PWRVEC= 000024	160#	422*	423*	2743*	2744*	2753*	2759*	2771*	2772*				

ROCHR = 104410
ROLIN = 104411
RESVEC = 000010
RSTART 002266
RO = %000000

2433 2903#
2904#
155#
495# 2136
76# 2127# 2130 2150# 2154# 2217 2218# 2219# 2226# 2227# 2228# 2229# 2230#
2231 2236 2241# 2243# 2247 2249 2549 2559# 2563 2579 2580 2593# 2624
2625# 2630 2635 2638# 2690 2698# 2702 2703 2705# 2706# 2707 2729# 2745
2770# 2871 2872# 2873 2874# 2875# 2876# 2877# 2691 2728# 2746 2769#

R1 = %000001
R2 = %000002
R3 = %000003

77# 2151# 2152# 2550 2563# 2564 2568 2592# 2691 2728# 2746 2769#
78# 2551 2562# 2566# 2569 2576# 2577# 2578 2583# 2591# 2747 2768#
79# 2429 2430# 2431 2434# 2435 2439 2441 2443# 2445# 2492 2501# 2507#
2508# 2511# 2516# 2517# 2518 2527# 2552 2560# 2561# 2575# 2578# 2587# 2588#
2590# 2748 2767#

R4 = %000004
R5 = %000005

80# 2493 2495# 2496# 2497# 2498 2499# 2513 2515# 2523# 2526# 2749 2766#
81# 850# 2156# 2494 2500# 2502# 2504# 2505# 2506# 2507 2525# 2553 2555#
2557# 2564# 2568# 2583 2589# 2750 2765#

R6 = %000006
R7 = %000007
SP = %000006

82# 410# 411# 412
83#
84# 414# 431# 439# 443 540# 553# 565# 575 577# 578# 594# 607#
621# 633 909# 910# 1679# 1680# 1696# 1711# 1712# 1733# 1740# 1741# 1760#
1775# 1776# 1784# 1785# 1801# 1835# 1836# 1855# 1879# 1880# 1902# 2023# 2024#
2042# 2124# 2181 2202# 2205# 2217# 2222# 2243 2247# 2277# 2280 2282 2283 2283#
2312 2313 2317# 2334# 2335# 2336 2343# 2346# 2347# 2351# 2352# 2356 2359#
2363 2365 2367 2368# 2375 2377 2379# 2380 2382# 2383# 2384# 2385# 2386#
2401# 2402# 2405# 2406# 2407 2411# 2412# 2413 2416 2418 2420# 2429# 2434
2445 2446# 2447# 2448# 2484# 2485 2486 2487# 2492# 2493# 2494# 2500 2525
2526 2527 2528# 2529# 2549# 2550# 2551# 2552# 2553# 2554# 2555 2558# 2571
2573# 2575 2585 2587 2589 2590 2591 2592 2593 2595# 2596# 2624# 2625
2635# 2637 2638 2639# 2641 2643 2645 2651 2653# 2655# 2663# 2667 2671 2671#
2672 2676 2690# 2691# 2698 2699# 2710 2711# 2712# 2722 2723# 2728 2729
2745# 2746# 2747# 2748# 2749# 2750# 2751# 2752 2760# 2764 2765 2766 2767 2767#
2768 2769 2770 2817# 2871# 2872 2882# 2883#

STACK = 001100
START 001422
STKLMT = 177774
SMR 001140

59# 414
54 407#
70#
235# 412 433# 435 441# 448# 518 2177 2184 2196 2200 2272 2286
2288 2294 2301 2330 2367# 2751 2764#

SMREG 000176
SM0 = 000001
SM00 = 000001
SM01 = 000002
SM02 = 000004
SM03 = 000010
SM04 = 000020
SM05 = 000040
SM06 = 000100
SM07 = 000200
SM08 = 000400
SM09 = 001000
SM1 = 000002
SM10 = 002000
SM11 = 004000
SM12 = 010000
SM13 = 020000

49# 441 518
123# 123
113# 122
112# 121
111# 120
110# 119
109# 118
108# 117
107# 116
106# 115
105# 114
104#
122#
103#
102#
101#
100#

SM14	=	040000	99#				
SM15	=	100000	98#				
SL2	=	000004	121#				
SL3	=	000010	120#				
SL4	=	000020	119#				
SL5	=	000040	118#				
SL6	=	000100	117#				
SL7	=	000200	116#				
SL8	=	000400	115#				
SL9	=	001000	114#				
TBITVE	=	000014	156#				
TKVEC	=	000060	163#				
TPVEC	=	000064	164#				
TRAPVE	=	000034	162#	420#	421#		
TRFR0		012134	2815#	2861#	2982		
TRT0		012132	2803#	2804#	2806	2860#	2982
TRTVEC	=	000014	157#				
TST1		002444	532#				
TST10		003156	772	777	791#		
TST11		003246	808	813	827#		
TST12		003372	848	856	869#		
TST13		003462	887	892	906#		
TST14		003564	930	935	949#		
TST15		003654	966	971	987#		
TST16		003746	1007	1011	1026#		
TST17		004040	1046	1050	1065#		
TST2		002570	591#				
TST20		004132	1085	1089	1104#		
TST21		004224	1124	1128	1143#		
TST22		004316	1163	1167	1182#		
TST23		004410	1202	1206	1221#		
TST24		004502	1241	1245	1260#		
TST25		004574	1280	1284	1299#		
TST26		004626	1305	1322#			
TST27		004752	1337	1353	1368	1370	1386#
TST3		002654	638#				
TST30		005040	1406	1409	1426#		
TST31		005130	1446	1452	1469#		
TST32		005216	1489	1492	1509#		
TST33		005304	1529	1532	1547#		
TST34		005346	1561	1565	1580#		
TST35		005400	1588	1602#			
TST36		005437	1608	1623#			
TST37		005476	1631	1646#			
TST4		002704	645	659#			
TST40		005534	1653	1672#			
TST41		005632	1705#				
TST42		006034	1730	1769#			
TST43		006152	1814#				
TST44		006266	1864#				
TST45		006402	1914#				
TST46		006426	1930	1944#			
TST47		006500	1957	1960	1976#		
TST5		002734	666	685#			

SMXCNT 007730	2309	2319#														
SNULL 001154	241#	2653	2682													
SNWTST= 000001	529#	583#	585	635#	656#	678#	680	716#	752#	788#	824#	866#	903#			
	946#	984#	1023#	1062#	1101#	1140#	1179#	1218#	1257#	1296#	1319#	1383#	1423#			
	1466#	1506#	1544#	1577#	1599#	1620#	1643#	1669#	1702#	1766#	1811#	1861#	1911#			
	1941#	1973#	2002#	2051#	2053	2094#										
SOCNT 010730	2491#	2520#	2533#													
SOMODE 010732	2486#	2490#	2495	2498#	2509#	2535#										
SOVER 007714	2273	2289	2297	2307	2316#											
SPASS 001202	260#	445#	2115#	2116#	2124	2137	2303	2320								
SPASTH 001006	204#															
SPOMER 012056	2774	2779#														
SPWRON 011710	422	2743#	2771													
SPWRNG 012044	2774#															
SPWRUP 011762	2753	2759#														
SQUES 001170	248#	2208	2388	2437	2453	2682										
SROCHR 010214	2401#	2903														
SRODEC= ***** U	2905															
SROLIN 010334	2429#	2904														
SROCT= ***** U	2905															
SROSZ = 000010	2422#															
SRTNAD 007064	2136#															
SR2A = ***** U	2905															
SSAVRE= ***** U	2905															
SSAVR6 012054	2752#	2760	2761#	2762#	2778#											
SSCOPE 007450	416	2269#														
SSETUP= 000117	406#	415	416	418	420	422	424	425	427	512	513	2113	2173			
	2199	2207	2270	2325	2459											
SSTUP = 177777	406#															
SSVLAD 007660	2281	2310#														
SSVPC = 000204	176#	181														
SSWR = 167400	10#	22	29	30	31	32	33	34	35	245	246	247	424			
	425	427	428	533	592	639	660	686	720	756	792	828	870			
	907	950	988	1027	1066	1105	1144	1183	1222	1261	1300	1323	1387			
	1427	1470	1510	1548	1581	1603	1624	1647	1673	1706	1770	1815	1865			
	1915	1945	1977	2006	2063	2098	2108	2114	2129	2135	2137	2164	2165			
	2166	2167	2168	2177	2184	2196	2200	2208	2261	2262	2263	2264	2265			
	2272	2284	2286	2287	2290	2291	2292	2299	2300	2301	2313	2316	2319			
	2775															
SSWREG 001216	268#	448														
SSWRMK= 000000	35	36	2265	2266	2288											
STESTN 001200	259#	537#	2311#	2970	2976	2979	2982	686#								
STIMES 001160	245#	424#	533#	592#	639#	660#	686#	1603#	1624#	1647#	1945#	2114#	2299#			
	2306	2309#	2319													
STKB 001146	238#	2323	2334	2351	2405	2411										
STKS 001144	237#	2323	2332	2348	2372#	2403	2409									
STN = 000053	22#	170#	529	533#	583	592#	635	639#	645	656	660#	666	678			
	686#	701	704	716	720#	736	741	752	756#	772	777	788	792#			
	808	813	824	828#	848	856	866	870#	887	892	903	907#	930			
	935	946	950#	966	971	984	988#	1007	1011	1023	1027#	1046	1050			
	1062	1066#	1085	1089	1101	1105#	1124	1128	1140	1144#	1163	1167	1179			
	1183#	1202	1206	1218	1222#	1241	1245	1257	1261#	1280	1284	1296	1300#			
	1305	1319	1323#	1337	1353	1368	1370	1383	1387#	1406	1409	1423	1427#			
	1446	1452	1466	1470#	1489	1492	1506	1510#	1529	1532	1544	1548#	1561			

ADD	455	457	459	465	467	469	471	473	475	477	480	482	484	486	488
	490	492	494	553	565	607	621	1696	1733	1760	1801	1855	1902	2042	2230
ASL	2359	2368	2487	2497	2568	2639	2699	2711	2723	2817					
ASLB	2227	2228	2229	2382	2383	2384	2875								
ASR	2573														
BCC	2706														
BEQ	2574														
	447	517	645	666	690	704	741	762	777	798	813	834	838	856	876
	892	920	935	956	971	996	1011	1035	1050	1074	1089	1113	1128	1152	1167
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	2304	2339	2366	2381	2514	2629	2642	2677	2693	2697	2717	2719			
BGE	2307														
BGT	2119	2378	2419	2521	2582										
BHI	2293														
BIC	461	738	774	810	889	933	968	1009	1048	1087	1126	1165	1204	1243	1282
	1407	1490	1530	2116	2335	2352	2379	2406	2412	2420	2511				
BIS	722	723	758	794	830	872	916	952	1339	1389	1392	1429	1432	1472	1475
	1512	1515	1563	1583	1627	1677	1709	1779	1884	1988	2021	2027	2070	2071	2386
	2516	2517	2576	2577											
BISB	2219														
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	1518	1531	1550	1564	1587	1628	1630	1929	1955	1959	1990	2177	2184	2200	2272
	2286	2294	2301												
BITB	446	2628	2633	2665	2696										
BLE	2807														
BLOS	2432														
BLT	2376	2417	2522	2565	2581	2656									
BMI	2572														
BNE	413	436	511	515	519	1327	1343	1391	1396	1431	1436	1449	1474	1479	1514
	1519	1551	1588	1629	1826	1930	1957	1960	1991	2089	2153	2155	2185	2190	2220
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	2634	2636	2644	2652	2666	2673	2695	2701	2704	2721	2763				
BPL	2197	2333	2349	2404	2410	2510	2556	2586	2621	2670					
BR	438	521	524	551	604	618	701	726	736	772	808	848	887	930	966
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	2297	2300	2360	2387	2389	2415	2438	2488	2503	2524	2567	2584	2623	2649	2659
	2668	2675	2687	2709	2755	2777	2812								
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	890	914	932	951	969	1008	1047	1086	1125	1164	1203	1242	1281	1301	1303
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	1831	1856	1873	1874	1908	1909	1924	1953	1986	1987	2015	2016	2047	2048	2066
	2068	2069	2091	2092	2113	2114	2218	2299	2314	2346	2347	2501	2559	2562	2761
CLRB	1585	1780	2298	2443	2588	2648	2674	2725	2726	2727					
CMP	412	435	518	725	761	797	833	837	855	875	919	955	2282	2306	2330
	2336	2356	2363	2375	2377	2407	2413	2416	2418	2431	2580	2806	2292	2338	2370
CMPB	516	995	1034	1073	1112	1151	1190	1229	1268	2074	2189	2288			
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DEC	2117	2152	2154	2226											
DECB	2509	2520	2655	2658											
EMT	60														
HALT	2198	2622	2754	2776	2809										
INC	510	1925	2115	2180	2305	2385	2515	2523	2566	2724	2762				

INCB	1744	2088	2174	2310	2678													
IOT	61																	
JMP	54	1827	2135															
JSR	850	2130	2186	2192	2374	2631	2650	2657	2664	2713								
MOV	410	414	416	417	418	419	420	421	422	423	427	428	431	432	433			
	434	439	441	442	443	448	450	451	453	454	456	458	460	462	463			
	464	466	468	470	472	474	476	479	481	483	485	487	489	491	493			
	497	498	500	501	503	504	506	507	533	534	536	537	538	540	541			
	547	575	577	578	592	594	595	601	633	639	644	660	686	689	703			
	724	740	759	760	776	795	796	812	831	832	835	836	853	854	873			
	874	891	909	910	917	918	934	953	954	970	990	991	992	1029	1030			
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	2018	2019	2023	2024	2045	2046	2064	2065	2072	2120	2124	2127	2150	2151	2176			
	2181	2202	2205	2217	2222	2231	2236	2241	2243	2247	2277	2278	2280	2283	2296			
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MOVB	426	522	665	994	1010	1033	1049	1072	1088	1111	1127	1150	1166	1189	1205			
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NEG	2496	2557																
NOP	532	1683	1684	1715	1716	1746	1747	1781	1782	1788	1840	1841	1887	1888	2028			
	2029	2131	2132	2133														
RESET	527	641	662	1606	1651	2129												
ROL	2502	2504	2505	2506	2508													
RTI	440	579	911	1681	1713	1742	1777	1786	1837	1881	2025	2207	2318	2373	2421			
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RTS	2156	2245	2680	2730	2877													
SUB	2182	2564	2705	2804														
TRAP	2886	2895	2896	2897	2898	2900	2902	2903	2904									
TST	514	545	549	598	603	1825	2196	2203	2249	2279	2303	2365	2380	2513	2569			
	2579	2637	2645	2667	2700	2718	2720	2873										
TSTB	1356	1607	1652	2290	2332	2348	2403	2409	2571	2585	2620	2669	2692	2703	2716			
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.ASCIZ	247	250	526	2138	2253	2453	2454	2455	2457	2779	2908	2913	2918	2922	2926			
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.BLKW	2602																	
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.ENABL	3	4	2323															
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	297	298	301	406	414	415	418	420	422	424	425	427	429	450	512
	518	524	526	530	531	532	533	534	535	584	585	590	591	592	593
	636	637	638	639	640	646	657	658	659	660	661	667	679	680	684
	685	686	687	702	705	717	718	719	720	737	742	753	754	755	756
	773	778	789	790	791	792	809	814	825	826	827	828	849	857	867
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	967	972	985	986	987	988	1008	1012	1024	1025	1026	1027	1047	1051	1063
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.EVEN	144	145	146	147	148	149	150	151							
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	210	214	216	245	246	247	251	252	254	277	285	288	291	294	295
	296	297	298	299	301	406	409	414	416	418	420	422	424	425	427
	445	511	512	513	516	525	529	531	533	534	535	583	585	590	592
	593	635	637	639	640	645	656	658	660	661	666	678	680	684	686
	687	701	704	716	718	720	736	741	752	754	756	772	777	788	790
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	907	930	935	946	948	950	966	971	984	986	988	1007	1011	1023	1025
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	252	255	414	511	512	529	530	531	532	533	584	585	591	592	636
	637	638	639	640	646	657	658	659	660	661	667	679	680	685	686

	702	705	717	718	719	720	737	742	753	754	755	756	773	778	789
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	888	893	904	905	906	907	931	936	947	948	949	950	967	972	985
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	630	632	635	639	647	650	653	655	656	660	668	671	674	676	678
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	743	746	749	751	752	756	764	767	770	772	779	782	785	787	788
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	858	861	864	866	870	879	882	885	887	894	897	900	902	903	907
	922	925	928	930	937	940	943	945	946	950	958	961	964	966	973
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L07