

DMR-11

DMR-11 FCTNL DIAG
CZDMIDO

AH-F832D-MC
FICHE 1 OF 2

OCT 1981
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The main body of the document is a large, dense grid of data. Each cell in the grid contains a small, complex diagram or table, likely representing a functional diagram (FCTNL DIAG) for a specific component or system. The diagrams are arranged in a regular grid pattern across the page.

DMR-11

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CZDMIDO

AH-F832D-MC
FICHE 2 OF 2

OCT 1981
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IDENTIFICATION

PRODUCT CODE: AC-F830D-MC
PRODUCT NAME: CZDMIDO DMR-11 FCTNL DIAG
PRODUCT DATE: OCTOBER 1981
MAINTAINER: DIAGNOSTIC ENGINEERING
AUTHOR: MIKE O'CONNOR

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REVISION HISTORY

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REVISION	REASON	DATE
A	INITIAL RELEASE	5 FEB 80
B	1. SUPPORT REMOTE LOOPBACK IN TESTS 17-19 2. BUG FIX - CONTROL C EXIT OF PROGRAM WOULD CAUSE UNEXPECTED INTERRUPT ON RESTART - PUT MASTER CLEAR IN CLEAN UP CODE 3. BUG FIX - DTR WAS DROPPED WHEN WRITING MODEM MAINTENANCE BIT (AFFECTED MANUF. TURNAROUND.)	17 APR 80
C	ADDED CHECK OF M8207 PROGRAM TIMER IN TEST 10	20 AUG 80
D	1. AT ENGINEERING'S REQUEST, CHANGED TEST 15 SO THAT IT NO LONGER TESTS DMC MODE BASE IN RESUME. ALSO CHANGED TEST 16 SO THAT A MASTER CLEAR IS DONE BEFORE A BASE IN RESUME AND TEST 16 NOW ONLY RUNS IN INTERNAL LOOPBACK MAKING THESE CHANGES GETS AROUND A PROBLEM IN MANUFACTURING. THE DMR DOCUMENTATION WAS CHANGED TO SPECIFY THAT A MASTER CLEAR MUST BE DONE BEFORE A BASE IN RESUME. 2. BUG FIX - ALLOWED 1 REP TO BE SENT IN TEST 14. THIS IS NECESSARY TO MASK THE SOFT ERROR MESSAGE THAT CAN OCCUR WHEN LARGE MESSAGES ARE SENT AT LOW BAUD RATES.	26 OCT 81

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1.0 INTRODUCTION

THIS PROGRAM WILL BE IMPLEMENTED USING THE DIAGNOSTIC SUPERVISOR AND A STRUCTURED PROGRAMMING APPROACH. BECAUSE THE DESIGN WILL CONFORM TO THE SUPERVISOR (STANDALONE VERSION) THE PROGRAM WILL BE COMPATIBLE WITH ACT, APT, XXDP+, AND SLIDE.

THROUGH DIALOGUE WITH THE OPERATOR, THE PROGRAM WILL ALLOW MODIFICATION OF DEVICE PARAMETERS, SUCH AS UNIBUS ADDRESS, VECTOR ADDRESSES AND TEST CONFIGURATION. IN ADDITION, THE OPERATOR CAN SPECIFY PARTICULAR TESTS TO BE RUN AND A VARIETY OF LOOPING, RUNNING, AND REPORTING MODES.

DEVICE ERRORS WILL BE REPORTED AS THEY OCCUR. THE REPORT WILL INCLUDE A TEST NUMBER AND DESCRIPTION OF THE ERROR, GOOD AND BAD TEST DATA, AND APPLICABLE DEVICE REGISTER CONTENTS.

2.0 HARDWARE REQUIREMENTS

THE FOLLOWING HARDWARE IS REQUIRED TO RUN THE DMP-11 FUNCTIONAL DIAGNOSTIC TESTS:

PDP-11/04,05,10,20,30,34,35,40,45,50,60, OR 70
16K MEMORY
CONSOLE TERMINAL
DMR-11

3.0 PRELIMINARY PROGRAM REQUIREMENTS

IT IS ADVISED THAT THE STATIC DIAGNOSTICS BE RUN BEFORE THESE FUNCTIONAL DIAGNOSTICS. IT IS ASSUMED THAT THE PROCESSOR IS IN PROPER WORKING CONDITION.

ENSURE THAT THE SWITCH 1 AT LOCATION E-85 ON THE M8207 IS ON. IF THIS SWITCH IS OFF, THE MAINTENANCE BITS IN BSEL1 CAN'T BE USED AND CERTAIN TESTS WILL BE NOT BE CORRECTLY RUN.

WHEN CHOSING A CABLE TEST CONNECTION, ENSURE THAT THE SWITCH PACK E-39 ON THE M8203 IS PROPERLY SET UP FOR THE DESIRED INTERFACE. IF CHOSING TEST CONFIGURATION OPTIONS 1-4, IT IS NOT NECESSARY TO SELECT THE INTERFA F; HOWEVER THE BAUD RATE MUST BE CORRECT. FOR EXAMPLE IF IT IS DESIRED TO RUN CONFIGURATION 3 (H3255-EIA), IT IS NOT NECESSARY TO HAVE SWITCH 7 OF THE SWITCH PACK IN THE OFF POSITION.

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IT IS, HOWEVER, NECESSARY TO HAVE THE BAUD RATE SELETCTED TO BE WITHIN THE EIA RANGE.

NOTE THAT A MANUFACTURING-ONLY PATCH IS REQUIRED TO RUN WHEN USING THE SPECIAL MANUFACTURING TEST CONNECTORS. THIS PATCH WILL CHANGE THE FLAG WORD 'MANUF' TO A NON-ZERO VALUE. WHEN THE FLAG IS NON-ZERO, THE MAINTENANCE BIT IS SET BY A MODEM WRITE COMMAND IF THE V.35 OR EIA ONBOARD CONNECTORS ARE USED.

4.0 GENERAL PROGRAM CONSIDERATIONS

4.1 DIAGNOSTIC SUPERVISOR

THIS PROGRAM IS COMPATIBLE WITH THE STANDALONE DIAGNOSTIC SUPERVISOR, AND MUST BE LOADED TO BE CO-RESIDENT WITH THE SUPERVISOR, OR BE PREVIOUSLY COMBINED WITH THE SUPERVISOR AND LOADED AS A SINGLE FILE. IN EITHER CASE, THE COMBINED PROGRAM WILL NOT EXCEED 16K OF MEMORY.

4.2 EXECUTION TIME

EXECUTION TIME IS DEPENDENT ON THE PROCESSOR SPEED AND THE DMR BAUD RATE. EXAMPLES OF EXECUTION TIME

11/70 WITH CACHE AND DMR AT 2.4K	4 AND 1/2 MINUTES
11/70 WITHOUT CACHE AND DMR AT 2.4K	5 AND 1/2 MINUTES
11/34 AND DMR AT 2.4K	10 MINUTES

4.3 XXDP+

THIS PROGRAM MAY BE LOADED UNDER XXDP+, AND MAY BE RUN IN DUMP MODE OR CHAIN MODE.

4.4 ACT/SLIDE

THIS PROGRAM MAY BE LOADED UNDER ACT OR SLIDE AND MAY BE RUN IN DUMP MODE OR CHAIN MODE.

4.5 APT

THIS PROGRAM MAY BE LOADED BY THE APT SYSTEM (INCLUDING APT-RD) AND RUN IN PROGRAM MODE OR SCRIPT MODE.

4.6 MEMORY MANAGEMENT

IF MEMORY MANAGEMENT IS AVAILABLE, IT IS USED BY CERTAIN TESTS IN THIS FUNCTIONAL DIAGNOSTIC.

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4.7 MEMORY PARITY OPTION

IF PARITY MEMORY IS INSTALLED, MEMORY PARITY TRAPS ARE DISABLED BY THE PROGRAM.

4.8 ERROR LOGGING

AT THE END OF EACH PASS ON ALL UNITS, THE PROGRAM PRINTS OUT THE CUMULATIVE TOTAL NUMBER OF ERRORS SINCE THE LAST START OR RESTART COMMAND.

5.0 PROGRAM LOAD MEDIA

THIS PROGRAM CAN BE LOADED FROM PAPER TAPE USING THE ABSOLUTE LOADER OR FROM ACT, SLIDE, OR APT SYSTEMS, OR FROM ANY MEDIA SUPPORTED BY XXDP+. WHEN USING THE PAPER TAPE ABSOLUTE LOADER, THE PROGRAM SHOULD BE LOADED FIRST, FOLLOWED BY THE DIAGNOSTIC SUPERVISOR. WHEN USING XXDP+, THE DIAGNOSTIC SUPERVISOR SHOULD BE LOADED FIRST, FOLLOWED BY THE DIAGNOSTIC PROGRAM.

6.0 OPERATING INSTRUCTIONS

6.1 LOADING AND STARTING PROCEDURES

6.1.1 LOADING PROCEDURES

THIS PROGRAM MAY BE LOADED FROM PAPER TAPE USING THE ABSOLUTE LOADER. IT MAY ALSO BE LOADED FROM ANY XXDP+ LOAD MEDIA. WHEN LOADED UNDER XXDP+, THE DIAGNOSTIC SUPERVISOR WILL BE LOADED AUTOMATICALLY.

6.1.2 STARTING PROCEDURES

THE PROGRAM STARTS AT LOCATION 200. USE STANDARD DEC PROCEDURES TO START THE PROGRAM.

6.1.3 STEPS FOR QUICK AND SIMPLE EXECUTION

THE DIAGNOSTIC CAN BE EXECUTED STANDALONE UNDER XXDP+, WITHOUT READING THE REMAINDER OF THIS DOCUMENT, AS FOLLOWS:

- A) LOAD AND START DIAGNOSTIC USING RUN COMMAND
- B) RECEIVE DIAGNOSTIC SUPERVISOR IDENTIFICATION AND PROMPT (DRS-(C>))
- C) ENTER STA<CR>
- D) ANSWER HARDWARE AND SOFTWARE QUESTIONS
- E) GET END OF PASS MESSAGES OR ERROR MESSAGES
- F) TO END EXECUTION, ENTER CONTROL/C

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6.2 INITIAL DIALOGUE

AFTER THE PROGRAM AND THE SUPERVISOR ARE LOADED AND THE PROGRAM IS STARTED, THE FOLLOWING IDENTIFICATION IS TYPED :

DRS LOADED
DIAG. RUN-TIME SERVICES

DR>

THE OPERATOR THEN PROCEEDS BY TYPING ONE OR MORE OF THE COMMANDS DESCRIBED IN THE FOLLOWING SECTION 6.3. (FOR MORE DETAILED INFORMATION, REFER TO THE DIAGNOSTIC SUPERVISOR FUNCTIONAL SPECIFICATION).

6.3 PROGRAM OPTIONS

6.3.1 START COMMAND

```
*****  
STA(RT)/TESTS:<TEST-LIST>/PASS:<PASS-CNT>/FLAGS:  
<FLAG-LIST>/EOP:<INCR>  
*****
```

6.3.1.1 TESTS SWITCH (/TESTS:<TEST-LIST>)

<TEST-LIST> IS A SEQUENCE OF DECIMAL NUMBERS (1:2 ETC.) OR RANGES OF DECIMAL NUMBERS (1-5:8-10 ETC.) THAT SPECIFY THE TESTS TO BE EXECUTED. THE NUMBERS ARE SEPARATED BY COLONS. THE NUMBERS RANGE FROM 1 TO THE LARGEST TEST NUMBER IN THE DIAGNOSTIC. THEY MAY BE SPECIFIED IN ANY ORDER. TESTS WILL BE EXECUTED IN NUMERICAL ORDER REGARDLESS OF THE ORDER OF SPECIFICATION. THE DEFAULT IS TO EXECUTE ALL TESTS. ON THIS AND ALL SWITCHES, THE ANGLE BRACKETS <> ARE PUNCTUATION USED IN THE DEFINITION ONLY, AND ARE NOT TO BE TYPED BY THE OPERATOR. SEE EXAMPLE AT END OF 6.3.1.5.

6.3.1.2 PASS SWITCH (/PASS:<PASS-CNT>)

<PASS-CNT> IS A DECIMAL NUMBER INDICATING THE DESIRED NUMBER OF PASSES. A PASS IS DEFINED AS THE EXECUTION OF THE FULL DIAGNOSTIC (ALL SELECTED TESTS) AGAINST ALL UNITS SUBMITTED. THE DEFAULT IS NON-ENDING EXECUTION. IN THIS CASE EXIT FROM THE PROGRAM IS ACCOMPLISHED EITHER BY TYPING A CONTROL/C OR BY OCCURANCE OF AN ERROR WITH THE HALT ON ERROR FLAG BEING SET. THE EXIT IS A RETURN TO COMMAND MODE. SEE EXAMPLE AT END OF 6.3.1.5.

6.3.1.3 FLAGS SWITCH (/FLAGS:<FLAG-LIST>)

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<FLAG-LIST> IS A SEQUENCE OF ELEMENTS OF THE FORM <FLAG>, <FLAG=1>, OR <FLAG=0>, SEPARATED BY COLONS, WHERE <FLAG> HAS ONE OF THE FOLLOWING VALUES:

HOE HALT ON ERROR, CAUSING COMMAND MODE TO BE ENTERED WHEN AN ERROR IS ENCOUNTERED
LOE LOOP ON ERROR, CAUSING THE DIAGNOSTIC TO LOOP CONTINUOUSLY WITHIN THE SMALLEST DEFINED BLOCK OF CODING (SEGMENT, SUBTEST, OR TEST) CONTAINING THE ERROR
IER INHIBIT ERROR REPORTING
IBE INHIBIT BASIC ERROR REPORTS
IXE INHIBIT EXTENDED ERROR REPORTS
PRI DIRECT ALL MESSAGES TO A LINE PRINTER
PNT PRINT NUMBER OF TEST BEING EXECUTED
BOF BELL ON ERROR
UAM RUN IN UNATTENDED MODE, BYPASSING MANUAL INTERVENTION TESTS
ISR INHIBIT STATISTICAL REPORTS
IDU INHIBIT DROPPING OF UNITS BY DIAGNOSTIC
LOT LOOP ON TEST

THE FLAGS NAMED OR EQUATED TO 1 ARE SET, THOSE EQUATED TO 0 ARE CLEARED. A FLAG NOT SPECIFIED IS CLEARED. IF THE FLAGS SWITCH IS NOT GIVEN ALL FLAGS ARE CLEARED. SEE EXAMPLE AT END OF 6.3.1.5.

6.3.1.4 END OF PASS SWITCH (/EOP:<INCR>)

<INCR> IS A DECIMAL NUMBER INDICATING HOW OFTEN (IN TERMS OF PASSES) IT IS DESIRED THAT THE END OF PASS MESSAGE BE PRINTED. THE DEFAULT IS AT THE END OF EVERY PASS. SEE EXAMPLE AT END OF 6.3.1.5.

6.3.1.5 EFFECT OF START COMMAND

THE EFFECT OF THE START COMMAND IS TO INITIATE THE HARDWARE PARAMETER DIALOGUE, THE SOFTWARE PARAMETER DIALOGUE, AND THEN THE DIAGNOSTIC TESTS THEMSELVES.

THE HARDWARE PARAMETER DIALOGUE COMMENCES WITH THE QUESTION '# UNITS?' TO WHICH THE OPERATOR REPLIES WITH A DECIMAL NUMBER N FROM 1 TO 16. THE TERM 'UNIT' REFERS TO THE DEVICE TO WHICH THIS SERIES OF DIAGNOSTICS IS DEDICATED. FOLLOWING THIS ARE THE QUESTIONS WHEREBY THE P-TABLES THEMSELVES WILL BE BUILT. EACH P-TABLE IS A CORE-RESIDENT TABLE CONTAINING ALL THE HARDWARE INFORMATION FOR ONE UNIT. THE OPERATOR MUST SUPPLY N (NUMBER OF UNITS) VALUES FOR EACH QUESTION. HE MAY DO THIS BY GIVING ONE ANSWER TO EACH QUESTION (IN WHICH CASE THE SERIES OF QUESTIONS WILL BE POSED N TIMES) OR BY GIVING N VALUES, SEPARATED BY COMMAS, TO EACH QUESTION (SERIES WILL BE POSED ONCE). EACH QUESTION IS FOLLOWED BY THE RESPONSE RADIX (D FOR DECIMAL, B FOR BINARY, O FOR

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OCTAL, L FOR YES/NO) IN PARENTHESES AND THE DEFAULT VALUE AFTER THE PARENTHESES.

FOLLOWING THE HARDWARE QUESTIONS ARE THE SOFTWARE QUESTIONS TO BUILD THE SOFTWARE TABLES, WHICH DEFINE THE MODE (QUICK VERIFY ETC.) THAT THE DIAGNOSTIC WILL EXECUTE IN.

WHEN THE QUESTION '* UNITS?' IS ANSWERED, MEMORY STORAGE IS ALLOCATED FOR THE -TABLES, AND IF THERE IS NOT ENOUGH TO ACCOMMODATE THEM THE MESSAGE 'TOO MANY UNITS' IS ISSUED. IN THIS CASE THE DIAGNOSTIC MUST BE EXECUTED MORE THAN ONCE TO TEST ALL UNITS.

EXAMPLE:

STA/TESTS:1:2-4:6:8-10/PASS:3/FLAGS.IER:HOE=1:UAM:LOE

THIS COMMAND WILL CAUSE THREE PASSES TO BE MADE, EACH PASS CONSISTING OF TESTS 1,2,3,4,6,8,9, AND 10 EXECUTED AGAINST ALL UNITS. THERE IS NO DIFFERENCE BETWEEN SAYING <FLAG> AND SAYING <FLAG=1>. THE NOTATION <FLAG=0> IS MEANINGFUL ONLY ON A COMMAND OTHER THAN START TO CLEAR A FLAG THAT WAS PREVIOUSLY SET. NOTE THAT ON ALL COMMANDS ONLY THE FIRST THREE LETTERS ARE SCANNED.

6.3.2 RESTART COMMAND

```
*****  
RES(TART)/TESTS:<TEST-LIST>/PASS:<PASS-CNT>/FLAGS:  
  <FLAG-LIST>/UNITS:<UNIT-LIST>  
*****
```

6.3.2.1 TESTS, PASS, AND FLAGS SWITCHES

<TEST-LIST>, <PASS-CNT>, AND <FLAG-LIST> ARE AS IN THE START COMMAND.

6.3.2.2 UNITS SWITCH (/UNITS:<UNIT-LIST>)

<UNIT-LIST> IS A SEQUENCE OF DECIMAL NUMBERS (0,1 ETC.) OR RANGES OF DECIMAL NUMBERS (0-5, 8-10 ETC.) THAT SPECIFY THE UNITS TO BE TESTED. THE NUMBERS ARE SEPARATED BY COLONS. THE NUMBERS MAY RANGE FROM 0 THRU N-1 (N IS THE NUMBER OF UNITS SPECIFIED IN THE PREVIOUS START COMMAND). THE NUMBER INDICATES THE POSITION OF THE P-TABLE AS THE DATA WAS ENTERED DURING THE HARDWARE DIAGLOGUE. THE UNITS WHICH ARE SELECTED MUST NOT HAVE BEEN DROPPED BY THE DROP COMMAND. SEE THE DISCUSSION OF ADD AND DROP COMMANDS BELOW. DEFAULT IS TO TEST ALL UNITS WHICH HAVE NOT BEEN DROPPED BY A DROP COMMAND.

6.3.2.3 EFFECT OF RESTART COMMAND

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THE RESTART COMMAND DIFFERS FROM THE START COMMAND IN THAT THE P-TABLES FROM THE PREVIOUS START COMMAND (THERE MUST HAVE BEEN ONE) ARE USED, INSTEAD OF NEW ONES BEING BUILT. THE UNITS SWITCH GIVES THE ABILITY TO SELECT A SUBSET OF THESE. THE SOFTWARE DIALOGUE MAY OPTIONALLY BE REEXECUTED (OPERATOR WILL BE ASKED). THE COMMAND CAN BE USED AFTER COMMAND MODE HAS BEEN REENTERED IN ANY OF THE THREE NORMAL WAYS: A) THE REQUESTED NUMBER OF PASSES HAVE BEEN MADE B) AN ERROR WAS ENCOUNTERED WITH THE HALT ON ERROR FLAG SET C) A CONTROL/C WAS ENTERED BY THE OPERATOR.

6.3.3 CONTINUE COMMAND

```
*****  
CON(TINUE)/PASS:<PASS-CNT/FLAGS:<FLAG-LIST>  
*****
```

6.3.3.1 PASS SWITCH (/PASS:<PASS-CNT>)

<PASS-CNT> IS SAME AS IN START COMMAND, BUT THE DEFAULT IS THE UNSATISFIED PASS-CNT FROM THE PREVIOUS START OR RESTART. IF NONE REMAINS, THE DEFAULT IS NON-ENDING EXECUTION.

6.3.3.2 FLAG SWITCH (/FLAGS:<FLAG-LIST>)

<FLAG-LIST> IS SAME AS IN START COMMAND, BUT UNSPECIFIED FLAGS RETAIN THEIR CURRENT VALUE.

6.3.3.3 EFFECT OF CONTINUE COMMAND

CONTINUE MUST FOLLOW A START OR RESTART, AND COMMAND MODE MUST HAVE BEEN ENTERED DUE TO A HALT ON ERROR OR A CONTROL/C. THE EFFECT OF THE COMMAND IS TO GO TO THE BEGINNING OF THE TEST THAT WAS BEING EXECUTED WHEN THE HALT OR CONTROL/C TOOK PLACE. SOFTWARE DIALOGUE MAY OPTIONALLY BE REEXECUTED. HARDWARE PARAMETERS MAY NOT BE CHANGED.

6.3.4 PROCEED COMMAND

```
*****  
PRO(CEED)/FLAGS:<FLAG-LIST>  
*****
```

6.3.4.1 FLAGS SWITCH (/FLAGS:<FLAG-LIST>)

<FLAG-LIST> IS AS IN THE START COMMAND, BUT UNSPECIFIED FLAGS RETAIN THEIR CURRENT VALUE.

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6.3.4.2 EFFECT OF PROCEED COMMAND

PROCEED MUST FOLLOW A START, RESTART, OR CONTINUE. COMMAND MODE MUST HAVE BEEN ENTERED VIA A HALT ON ERROR. THE EFFECT OF THE COMMAND IS TO BEGIN EXECUTION AT THE LOCATION FOLLOWING THE ERROR CALL. NEITHER HARDWARE NOR SOFTWARE PARAMETERS MAY BE ALTERED.

6.3.5 ADD COMMAND

ADD/UNITS:<UNIT-LIST>

6.3.5.1 UNITS SWITCH (/UNITS:<UNIT-LIST>)

<UNIT-LIST> IS AS IN THE RESTART COMMAND.

6.3.5.2 EFFECT OF ADD COMMAND

THE UNITS SPECIFIED ARE ADDED TO THE TEST SEQUENCE. EACH UNIT MUST HAVE A P-TABLE IN MEMORY DUE TO AN EARLIER HARDWARE DIALOGUE. THIS COMMAND MUST BE FOLLOWED BY A RESTART OR CONTINUE. THE UNITS SWITCH MUST BE SPECIFIED. THE ADD COMMAND IS MEANINGFUL ONLY FOR UNITS THAT WERE PREVIOUSLY DROPPED.

6.3.6 DROP COMMAND

DRO(P)/UNITS:<UNIT-LIST>

6.3.6.1 UNITS SWITCH (/UNITS:<UNIT-LIST>)

<UNIT-LIST> IS AS IN THE RESTART COMMAND.

6.3.6.2 EFFECT OF DROP COMMAND

THE UNITS SPECIFIED WILL BE DROPPED FROM TESTING. THE UNITS WILL BE RESELECTED ONLY BY THE EXECUTION OF AN ADD OR START COMMAND. THE UNITS SWITCH MUST BE ENTERED. THIS COMMAND MUST BE FOLLOWED BY A RESTART OR A CONTINUE COMMAND.

6.3.7 PRINT COMMAND

PRI(NT)

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6.3.7.1 EFFECT OF PRINT COMMAND

THE TOTAL NUMBER OF ERRORS FOR EACH UNIT SINCE THE LAST START OR RESTART COMMAND ARE PRINTED. THE ISR (INHIBIT STATISTICAL REPORTING) FLAG IS CLEARED.

6.3.8 DISPLAY COMMAND

DIS(PLAY)/UNITS:<UNIT-LIST>

6.3.8.1 UNITS SWITCH (/UNITS:<UNIT-LIST>)

<UNIT-LIST> IS AS IN THE RESTART COMMAND.

6.3.8.2 EFFECT OF DISPLAY COMMAND

THE HARDWARE P-TABLES FOR ALL UNITS UNDER TEST ARE PRINTED OUT IN THE FORMAT IN WHICH THEY WERE ENTERED. ANY UNITS THAT WERE DROPPED BY THE OPERATOR 'DROP' COMMAND ARE SO DESIGNATED.

6.3.9 FLAGS COMMAND

FIA(GS)

6.3.9.1 EFFECT OF FLAGS COMMAND

THE CURRENT SETTINGS OF ALL FLAGS ARE PRINTED.

6.3.10 ZFLAGS COMMAND

ZFL(AGS)

6.3.10.1 EFFECT OF ZFLAGS COMMAND

ALL FLAGS ARE CLEARED.

6.3.11 CONTROL CHARACTERS

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A CONTROL C (C) ENTERED DURING THE EXECUTION OF A DIAGNOSTIC CAUSES A RETURN TO COMMAND MODE.

A CONTROL Z (Z) ENTERED DURING ONE OF THE THREE OPERATOR DIALOGUES- HARD CORE QUESTIONS (SEE 6.2), HARDWARE DIALOGUE (SEE 6.3.1.5), OR SOFTWARE DIALOGUE (SEE 6.3.1.5) CAUSES THE DEFAULTS TO BE TAKEN FOR THE REMAINDER OF THAT DIALOGUE.

A CONTROL O (O) ENTERED DURING THE EXECUTION OF A DIAGNOSTIC CAUSES ALL TELETYPE OUTPUT TO BE SURPRESSED FOR THE REMAINDER OF THE DIAGNOSTIC OR UNTIL ANOTHER O IS TYPED, WHICH RESTORES NORMAL TELETYPE OUTPUT.

6.3.12 HARDWARE PARAMETERS

THE FOLLOWING 3 QUESTIONS WILL BE ASKED ON A START COMMAND. THE VALUE LOCATED TO THE LEFT OF THE QUESTION MARK IS THE DEFAULT VALUE THAT WILL BE TAKEN ON A CARRIAGE RETURN RESPONSE.

1. CSR ADDRESS: (O) 160070?

THIS IS THE ADDRESS AT WHICH THE CSR REGISTERS (SELO) RESIDE ON THE UNIBUS. THE ALLOWABLE RANGE IS 160000-177776 (OCTAL), AND THE DEFAULT VALUE IS 160070.

2. VECTOR ADDRESS: (O) 300 ?

THIS IS THE ADDRESS OF THE INPUT INTERRUPT VECTOR FOR THIS DEVICE. THE ALLOWABLE RANGE IS 000-776 (OCTAL), AND THE DEFAULT VALUE IS 300.

3. TEST CONFIGURATION -

0 = INTERNAL (NO CONNECTOR)

1 = H3254 - V.35 (NOTE: MODE 1-4 ALLOWS

2 = H3254 - INTEGRAL PROGRAM INTERFACE SELECTION)

3 = H3255 - RS232C/423

4 = H3255 - RS422

5 = CABLE AND SW PACK INTERFACE SELECTED

(V.35-H3250, INTEGRAL-BC55A-10, RS232C---H325, RS423/422-H3251)

* SELECT THE FOLLOWING ONLY IF THE MODEM SUPPORTS LOOPBACK *

6 - LOCAL LOOP

7 = REMOTE LOOP

(O) 5 ?

THIS QUESTION WILL COVER ALL THE POSSIBLE TEST CONFIGURATIONS. THE DEFAULT IS FOR ACTUAL CABLE LOOPBACK (5). CONFIGURATION 0 WILL ENABLE LINE UNIT (TTL) LOOPBACK. IF THIS IS SELECTED NO CABLES OR CONNECTORS SHOULD BE CONNECTED. CONFIGURATIONS 1-4 WILL SELECT THE INTERFACE REGARDLESS OF THE SWITCH SETTING AS LONG AS THE PROPER BAUD RATE IS SELECTED (I.E. EIA - 2.4K-19.2K).

6.3.13 SOFTWARE PARAMETERS

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THE ONLY SOFTWARE PARAMETER QUESTION ASKED BY THE DIAGNOSTIC CONCERNS A SOFTWARE TIMEOUT VARIABLE THAT IS USED TO PREVENT SOFTWARE 'HUNG' CONDITIONS. THIS VARIABLE IS A VALUE FROM 1-5.

SELECTABLE PROGRAM LOOP TIME-OUT VARIABLE
[REFER TO LISTING 6.3.13] (MAX=5; MIN=1) (0) 5 ?

THERE ARE TWO FACTORS THAT SHOULD BE CONSIDERED WHEN ANSWERING THIS QUESTION. THE FIRST IS PROCESSOR SPEED; THE FASTER THE PROCESSOR THE HIGHER THE VARIABLE SHOULD BE. THE SECOND IS BAUD RATE; THE SLOWER THE DMR BAUD RATE THE HIGHER THE VARIABLE SHOULD BE. FOR EXAMPLE:

11/70 WITH CACHE AND DMR AT 1 MEG.: 4
11/34 AND DMR AT 56K: 2
11/40 AND DMR AT 2.4K: 3

THE DEFAULT IS 5. THIS WILL COVER THE WORST CASE (I.E. 11/70 WITH CACHE AND THE DMR AT 2.4K).

6.3.14 EXTENDED DISCUSSION OF P-TABLE DIALOGUE

THE FULL CAPABILITY OF THE HARDWARE DIALOGUE IS REVEALED BY THE FOLLOWING DISCUSSION OF WHAT HAPPENS INTERNALLY.

AS SOON AS THE QUESTION '# UNITS?' IS ANSWERED (WITH THE NUMBER N, SAY) SPACE IN CORE IS ALLOCATED FOR N P-TABLES. ALL OF THE P-TABLES ARE OF THE SAME FORMAT, AND THERE IS A ONE-TO ONE CORRESPONDENCE BETWEEN THE HARDWARE PARAMETER QUESTIONS AND THE SLOTS IN THE P-TABLE FORMAT.

ON THE FIRST TRIP THRU THE QUESTIONS, ALL OF THE SLOTS IN ALL OF THE P-TABLES ARE FILLED. IF THE OPERATOR TYPES IN LESS THAN N EXPLICIT VALUES IN RESPONSE TO A PARTICULAR QUESTION, THESE VALUES ARE PLACED IN THE P-TABLES (ONE VALUE GOING INTO THE PROPER SLOT OF EACH P-TABLE BEGINNING WITH THE FIRST P-TABLE) UNTIL THE STRING OF VALUES IS EXHAUSTED. THE LAST VALUE IN THE STRING BECOMES THE NEW DEFAULT AND IS USED TO FILL THAT SLOT IN THE REMAINING P-TABLES.

ON SUBSEQUENT TRIPS THRU THE QUESTIONS, THE SAME PROCESS IS CARRIED OUT, EXCEPT THAT THE EARLIEST P-TABLE NOT TO HAVE RECEIVED AN EXPLICIT VALUE IN ANY OF ITS SLOTS NOW ASSUMES THE ROLE THAT TABLE NUMBER ONE PLAYED IN THE FIRST TRIP.

THE SERIES OF QUESTIONS IS REISSUED UNTIL AT LEAST ONE QUESTION HAS RECEIVED N EXPLICIT VALUES FROM THE OPERATOR.

IN GIVING A STRING OF VALUES, COMMAS WITHOUT INTERVENING VALUES MAY BE USED TO INDICATE A REPETITION OF THE LAST NAMED VALUE.

A STRING OF VALUES MAY BE GIVEN AS A RANGE (6-10 FOR EXAMPLE). IF THE VALUES REPRESENT PURE NUMERICAL DATA, TH'S

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SAMPLE RANGE TRANSLATES TO THE STRING 6,7,8,9,10 (AN INCREMENT OF 1). IF THE VALUES ARE ADDRESSES, THE SAMPLE RANGE TRANSLATES TO THE STRING 6,8,10 (AN INCREMENT OF 2).

NOW LET US SEE HOW WE COULD USE THESE CAPABILITIES TO CONSTRUCT A SET OF P-TABLES. ASSUME THAT WE HAVE 16 UNITS, AND THAT THERE ARE THREE HARDWARE PARAMETERS FOR EACH (THREE SLOTS IN THE P-TABLE, THREE HARDWARE QUESTIONS IN THE DIALOGUE). LET THE DESIRED VALUE FOR THE FIRST PARAMETER BE THE NUMBER 75 FOR ALL 16 TABLES. LET THE DESIRED VALUE FOR THE SECOND PARAMETER BE EQUAL TO THE UNIT NUMBER (0,1,2,....,15) EXCEPT FOR UNIT 12, WHICH SHOULD RECEIVE THE VALUE 11. LET THE DESIRED VALUE FOR THE THIRD PARAMETER BE THE NUMBER 76 FOR THE FIRST 7 UNITS AND THE NUMBER 77 FOR THE LAST 9 UNITS.

THE FOLLOWING DIALOGUE WOULD ACCOMPLISH THIS GOAL:

UNITS (D) ? 16
UNIT 0
<QUESTION 1> ? 75
<QUESTION 2> ? 0-6
<QUESTION 3> ? 76

UNIT 7
<QUESTION 1> ?
<QUESTION 2> ? 7-11,,13-15
<QUESTION 3> ? 77

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THE FIRST TIME THE SERIES IS ASKED, SLOT ONE RECEIVES A 75 IN ALL 16 TABLES. SLOT TWO RECEIVES THE VALUES 0,1,2,...,6 IN TABLES 0 THRU 6 AND A CONSTANT 6 IN TABLES 7 THRU 15. SLOT THREE RECEIVES A CONSTANT 76 IN ALL 16 TABLES.

THE SECOND TIME THRU THE SERIES, TABLES 7 THRU THE END ARE GOING TO BE AFFECTED (NOTE THAT THIS PIECE OF INFORMATION IS PRINTED OUT FOR THE THE OPERATOR IN THE FORM 'UNIT XX' AT THE BEGINNING OF EACH SERIES). QUESTION 1 IS RESPONDED TO BY A <CR>, SO SLOT ONE STAYS AT CONSTANT 75 IN TABLES 7 THRU 15, SINCE NO NEW EXPLICIT VALUES ARE TYPED IN. SLOT TWO GETS THE VALUES 7,8,9,10,11 IN TABLES 7 THRU 11, AND GETS AN 11 IN SLOT 12, AND GETS THE VALUES 13,14,15 IN TABLES 13 THRU 15. SLOT THREE GETS THE VALUE 77 IN TABLES 7 THRU 15.

THE DIALOGUE IS TERMINATED WHEN THE SOFTWARE RECOGNIZES THAT 16 EXPLICIT VALUES HAVE BEEN GIVEN FOR AT LEAST ONE QUESTION (NAMELY QUESTION 2).

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7.0 DEVICE INFORMATION TABLES

SEE THE GLOBAL EQUATES SECTION FOR DEVICE CSR BIT DEFINITIONS

8.0 TEST DESCRIPTIONS

* TEST 1 - DMR-11
* VERIFY THAT ADDRESSING THE 4 UNIBUS CSRS DOES NOT CAUSE A NON-
* EXISTENT MEMORY TRAP.
*
* THE DMR IS AN NPR DEVICE RESIDING ON A UNIBUS. COMMUNICATION
* BETWEEN THE MAIN CPU AND THE DMR IS ACCOMPLISHED THROUGH A
* SET OF FOUR 16-BIT UNIBUS CONTROL AND STATUS REGISTERS (CSRS).
* THE FOUR REGISTERS ARE ASSIGNED ADDRESSES IN THE I/O PAGE
* FLOATING ADDRESS SPACE: 76XXX0 - 76XXX6
*
* NOTE: THIS TEST IS REDUNDANT IN THAT STATIC LOGIC TESTS SHOULD
* HAVE BEEN RUN BEFORE THESE FREE-RUNNING TESTS WERE STARTED, AND
* THEY SHOULD HAVE DETECTED ANY CSR ADDRESSING PROBLEMS.
* BUT JUST IN CASE THOSE STATIC TESTS AREN'T RUN, WE'LL BE SAFE.

* TEST 2 - DMR-11
* ROM CRC/CCITT - CHECK ROM POSITION AND CALCULATE CRC/CCITT. THE
* LAST 4 BYTES CONTAIN INFORMATION ABOUT THE ROM TO CHECK. THE 1ST
* OF THESE BYTES CONTAINS THE ASCII VERSION NUMBER. THE 2ND BYTE
* CONTAINS THE ROM NUMBER. THE 3RD AND 4TH BYTES CONTAIN A NEGATIVE
* CRC/CCITT WORD FOR THE ROM.
*
* CHIP ADDRESS RANGE
* LOCATION CHIP NO. BYTE ADDRESS RANGE
* E03 0 LOW 0000 - 1777
* E02 1 HIGH 0000 - 1777
* E04 2 LOW 2000 - 3777
* E01 3 HIGH 2000 - 3777
* E05 4 LOW 4000 - 5777
* E14 5 HIGH 4000 - 5777

***** IMPORTANT !!!!!!!!!!!!! *****
* FOR THIS TEST TO RUN CORRECTLY, ENSURE THAT SWITCH 1 AT LOCATION
* E85 ON THE M8207 IS ON. IF THIS SWITCH IS OFF, BSEL1 WILL BE
* LOCKED OUT AND THE MAINTENANCE FEATURES WILL NOT BE ENABLED.

* SUBTEST 1 - ON THE FIRST PASS PRINT THE VERSION # IN EACH ROM
* SUBTEST 2 - GENERATE THE CRC-CCITT IN EACH ROM AND COMPARE IT
* IT AGAINST THE CRC BLASTED IN THE ROM
* SUBTEST 3 - COMPARE THE ROM # BLASTED IN THE ROM AGAINST THE
* EXPECTED ROM #.

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*****  
* TEST 3 - DMR-11  
* MASTER CLEAR  
* THIS TEST WILL ISSUE 2 MASTER CLEARS. EACH CALL TO THE MASTER  
* CLEAR ROUTINE WILL ENSURE THAT THE RUN BIT WILL BE SET. ALSO  
* THE MASTER CLEAR WILL CAUSE THE DIAGNOSTIC MICROTESTS TO BE  
* RUN WHEN THE MICRODIAGNOSTIC BIT (BIT 13 IN SEL0) IS CORRECTLY  
* SET OR CLEARED. BECAUSE THE RUNNING OF MICROTESTS DEPENDS ON THE  
* EXCLUSIVE OR OF THE HARDWARE SWITCH 10 ON E134 OF THE M8203 AND  
* THE MICRODIAGNOSTIC BIT, WE CAN'T KNOW WHETHER THE SETTING OR  
* CLEARING OF BIT 13 WILL RESULT IN THE RUNNING OF MICROTESTS.  
* THEREFORE THE MASTER CLEAR SUBROUTINE WILL TOGGLE (I.E. SET  
* BIT 13 ONLY ON EVERY OTHER MASTER CLEAR) THE SOFTWARE BIT.  
* THIS WILL ENSURE THAT REGARDLESS OF THE POSITION OF THE  
* HARDWARE SWITCH, MICROTESTS WILL BE RUN EVERY OTHER MASTER CLEAR.  
* WHEN RUNNING THIS TEST, WE EXPECT TO ADD THE RESULTS OF BSEL3  
* AFTER EACH MASTER CLEAR.  
* BSEL3 = 100 - MICROTESTS DISABLED  
* BSEL3 = 200 - MICROTESTS RUN SUCCESSFULLY  
* IF THE RESULT OF THE 2 MASTER CLEARS IS NOT 300, AN ERROR IS  
* REPORTED.  
*  
* ADDITIONALLY THIS ROUTINE WILL REPORT WHENEVER THE RESULT OF  
* BSEL3 IS 0. THIS WILL MEAN THAT THE DEVICE IS NOT A DMR  
* (I.E. DMC)  
*****
```

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*****  
* TEST 4 - DMR-11  
* BASE IN COMMANDS  
*  
* SUBTEST 1 - ISSUE A BASE IN - DMR MODE.  
* ENSURE THAT THE DMR MODE BIT (BIT 4) IS SET IN  
* THE MICROCODE SCRATCH PAD 7 AND THAT THE DDCMP  
* MESSAGE VARIABLES ARE PROPERLY INITIALIZED.  
*  
* SUBTEST 2 - ISSUE A BASE IN - DMC MODE.  
* ENSURE THAT THE DMC MODE BIT (BIT 4) IS CLEAR IN  
* THE MICROCODE SCRATCH PAD 7 AND THAT THE DDCMP  
* MESSAGE VARIABLES ARE PROPERLY INITIALIZED.  
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*                               TEST 5 - DMR-11  
* DMR COMMANDS  
* SUBTEST 1 - ISSUE AN ENABLE EXTENDED ERROR COMMAND AND CHECK THAT  
*             THE EXT. ENABLE BIT IS SET IN SCRATCH PAD 13. THEN  
*             DISABLE EXTENDED ERROR AND CHECK THAT THE ENABLE BIT  
*             IS CLEAR.  
* SUBTEST 2 - SET REP/SEL TIMER VALUE AND SET THE DMR THRESHOLD  
*             VALUES. CHECK THAT THE VALUES ARE CORRECT IN  
*             THE BASE TABLE AFTER HALTING THE DMR.  
*****
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*****  
*                               TEST 6 - DMR-11  
* CONTROL IN COMMAND TEST -  
* SUBTEST 1 - CONTROL IN, FULL DUPLEX, DDCMP MODE. ENSURE THAT  
*             THE HALF-DUPLEX BIT IS CLEAR IN THE MODEM STATUS WORD,  
*             ALSO ENSURE THAT DDCMP MODE BIT IS SET IN SCRATCH PAD 7.  
* SUBTEST 2 - CONTROL IN, HALF DUPLEX. ENSURE THAT THE HALF DUPLEX  
*             BIT IS SET.  
* SUBTEST 3 - CONTROL IN, MAINTENANCE MODE. ENSURE THAT MAINT. MODE  
*             BIT IS SET IN SCRATCH PAD 7.  
* SUBTEST 4 - CONTROL IN USING SELECTED LOOPBACK. ISSUE A CONTROL IN  
*             USING THE USER SELECTED LOOPBACK. IF THE LOOPBACK IS  
*             NOT CORRECT, DMR RUN MODE ACKNOWLEDGE WILL NOT BE  
*             RECEIVED.  
*****
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*****  
*                               TEST 7 - DMR-11  
* MODEM WRITE COMMAND  
* SUBTEST 1 - WRITE DATA PATTERNS INTO THE MODEM WRITE REGISTER.  
*             ENSURE THAT ON THE NEXT MODEM READ THAT THE  
*             MICROCODE RETURNS THE PATTERN WRITTEN INTO BSEL6.  
* SUBTEST 2 - ATTEMPT TO WRITE BOTH THE HALF-DUPLEX BIT AND THE  
*             RTS HOLD BIT. THE MICROCODE SHOULD NOT ALLOW THIS  
*             TO HAPPEN. WHEN READING THE MODEM STATUS, ONLY  
*             THE HALF-DUPLEX SHOULD BE SET.  
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* TEST 8 - DMR-11  
* SUBTEST 1 - TRANSMIT A BUFFER THREE TIMES WITHOUT ASSIGNING A  
* RECEIVE BUFFER. BY ASSIGNING A NO BUFFER THRESHOLD  
* OF THREE, ENSURE THAT A NO BUFFER ERROR IS RECEIVED  
* AFTER THE THIRD TRANSMISSION.  
* SUBTEST 2 - TRANSMIT A BUFFER WITHOUT A RECEIVE BUFFER.  
* ASSIGN THE NAKS THRESHOLD OF 3 AND A NO BUFFER  
* THRESHOLD OF 7. CHECK THAT THE NAKS ERROR COUNT IS  
* THREE AFTER SHUTDOWN.  
*****
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*****  
* TEST 9 - DMR-11  
* NON-EXISTENT MEMORY (NXM) ERROR CHECK  
* PERFORM DMR COMMANDS USING NXM ADDRESSES; VERIFY THAT NXM ERROR IS  
* REPORTED IN EACH OF THE FOLLOWING SUBTESTS:  
* SUBTEST 1 - BASE IN RESUME COMMAND - BASE TABLE ADDRESS IS NXM  
* SUBTEST 2 - BA/CC IN RECEIVE COMMAND - BA/CC IN ADDRESS IS NXM  
* SUBTEST 3 - BA/CC IN TRANSMIT COMMAND - BA/CC IN ADDRESS IS NXM  
*****
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*****  
* TEST 10 - DMR-11  
* TIME OUT - FORCE A TIMEOUT AND VERIFY THAT THE ERROR IS REPORTED.  
* THIS TEST WILL ALSO USE AN APPROXIMATE TIMER TO DETERMINE IF THE  
* M8207 1 MSEC. PROGRAM TIMER IS OUT OF RANGE.  
*****
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*****  
* TEST 11 - DMR-11  
* MESSAGE TOO LONG - TRANSMIT A MESSAGE THAT IS TOO LONG FOR THE  
* RECEIVE BUFFER AND VERIFY THAT THE "TOO LONG" ERROR IS RECEIVED.  
*****
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* TEST 12 - DMR-11  
* PROCEDURE ERRORS -  
* THE FOLLOWING SHOULD CAUSE THE DMR-11 TO HALT AND RESPOND WITH  
* A PROCEDURE ERROR:  
* SUBTEST 1 - A SECOND BASE IN COMIAND  
* SUBTEST 2 - A CONTROL IN BEFORE A BASE IN  
* SUBTEST 3 - A BA/CC IN BEFORE A BASE IN  
* SUBTEST 4 - A BA/CC IN RCV WITH A BUFFER LENGTH OF 0  
* SUBTEST 5 - A BA/CC IN XMIT. WITH A BUFFER LENGTH OF 0  
*  
*****
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*****  
* TEST 13 - DMR-11  
* FREE RUNNING FLAG MODE DATA TEST  
* TRANSMIT A MESSAGE AND VERIFY THE RECEIVED DATA IS CORRECT.  
* IN THIS TEST NO INTERRUPTS ARE USED AND THE LINE UNIT IS IN  
* INTERNAL (TTL) LOOPBACK. THIS TEST IS THE FIRST TEST IN WHICH  
* THE DMR IS USED IN A DATA TRANSMISSION MODE.  
*****
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*****  
* TEST 14 - DMR-11  
* IN THIS TEST - SEE IF WE HAVE MEMORY MANAGEMENT, IF SO SEE IF WE  
* HAVE THE MEMORY TO CHECK BITS 16 & 17 IN SEL6. THIS WILL ALLOW  
* US TO TRANSFER DATA USING THOSE EXTENDED ADDRESSING BITS. AS IN  
* TEST 13 THE TEST IS NON-INTERRUPT AND INTERNAL (TTL) LOOPBACK IS  
* USED.  
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*****  
*                                     TEST 15 - DMR-11  
* DMC MODE  
* IN THIS TEST THE DMR WILL TRANSMIT AND RECEIVE 7 BUFFERS.  
*  
* ALL BA/CC OUTS RECEIVES AND TRANSMITS WILL BE ACCOUNTED FOR AND  
* THE CHARACTER COUNTS AND BUFFER ADDRESSES WILL BE CHECKED AGAINST  
* THE RECEIVE/TRANSMIT TABLE.  
*  
* THE BUFFERS ARE DETERMINED IN THE SUBROUTINE $BUFFS. THIS  
* SUBROUTINE WILL DETERMINE THE ADDRESS AND CHARACTER COUNT OF  
* SEVEN RECEIVE AND SEVEN TRANSMIT BUFFERS. THE ROUTINE WILL  
* ATTEMPT TO USE AS LARGE BUFFERS AS POSSIBLE IN THE FOLLOWING  
* HIERARCHY:  
*   A. IF THERE IS MEMORY MANAGEMENT, USE A PAGE ABOVE 32K.  
*   B. IF THERE IS FREE MEMORY ABOVE THE SUPERVISOR GREATER  
*     THAN 2K BYTES, USE THAT MEMORY  
*   C. IF NEITHER OF THE PRECEEDING TWO ARE POSSIBLE, USE  
*     THE 2K BYTE DEFAULT BUFFER WITHIN THIS DIAGNOSTIC.  
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* TEST 16 - DMR-11  
* RESUME BASE IN - DMR MODE  
* IN THIS TEST THE DMR WILL TRANSMIT AND RECEIVE 7 BUFFERS. DURING THE  
* TEST THE DMR WILL BE HALTED AND RESTARTED BY A BASE-IN RESUME IN THE  
* FOLLOWING MANNER:  
* BASE IN  
* CONTROL IN  
* HALT MASTER CLEAR BASE IN RESUME  
* 2 BA/CC IN RECEIVE  
* HALT MASTER CLEAR BASE IN RESUME  
* 2 BA/CC IN RECEIVE  
* HALT MASTER CLEAR BASE IN RESUME  
* 2 BA/CC IN RECEIVE  
* HALT MASTER CLEAR BASE IN RESUME  
* 1 BA/CC IN RECEIVE  
* HALT MASTER CLEAR BASE IN RESUME  
* 2 BA/CC IN TRANSMIT  
* HALT MASTER CLEAR BASE IN RESUME  
* 2 BA/CC IN TRANSMIT  
* HALT MASTER CLEAR BASE IN RESUME  
* 2 BA/CC IN TRANSMIT  
* HALT MASTER CLEAR BASE IN RESUME  
* 1 BA/CC IN TRANSMIT  
* HALT MASTER CLEAR BASE IN RESUME  
*  
* ALL BA/CC OUTS RECEIVES AND TRANSMITS WILL BE ACCOUNTED FOR AND  
* THE CHARACTER COUNTS AND BUFFER ADDRESSES WILL BE CHECKED AGAINST  
* THE RECEIVE/TRANSMIT TABLE.  
*  
* THE BUFFERS ARE DETERMINED IN THE SUBROUTINE $BUFFS. THIS  
* SUBROUTINE WILL DETERMINE THE ADDRESS AND CHARACTER COUNT OF  
* SEVEN RECEIVE AND SEVEN TRANSMIT BUFFERS. THE ROUTINE WILL  
* ATTEMPT TO USE AS LARGE BUFFERS AS POSSIBLE IN THE FOLLOWING  
* HIERARCHY:  
* A. IF THERE IS MEMORY MANAGEMENT, USE A PAGE ABOVE 32K.  
* B. IF THERE IS FREE MEMORY ABOVE THE SUPERVISOR GREATER  
* THAN 2K BYTES, USE THAT MEMORY  
* C. IF NEITHER OF THE PRECEEDING TWO ARE POSSIBLE, USE  
* THE 2K BYTE DEFAULT BUFFER WITHIN THIS DIAGNOSTIC.  
*  
* THIS TEST RUNS IN INTERNAL TTL ONLY  
*****
```

1122
1123
1124
1125
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```
*****  
* TEST 17 - DMR-11  
* INTERRUPT DRIVEN EXERCISE  
* IN THIS TEST 64 BUFFERS WILL BE TRANSMITTED AND RECEIVED  
*  
* ALL BA/CC OUTS RECEIVES AND TRANSMITS WILL BE ACCOUNTED FOR AND  
* THE CHARACTER COUNTS AND BUFFER ADDRESSES WILL BE CHECKED AGAINST  
* THE RECEIVE/TRANSMIT TABLE.  
*  
* THE BUFFERS ARE DETERMINED IN THE SUBROUTINE $BUFFS. THIS  
* SUBROUTINE WILL DETERMINE THE ADDRESS AND CHARACTER COUNT OF  
* 64 RECEIVE AND 64 TRANSMIT BUFFERS. THE ROUTINE WILL  
* ATTEMPT TO USE AS LARGE BUFFERS AS POSSIBLE IN THE FOLLOWING  
* HIERARCHY:  
* A. IF THERE IS MEMORY MANAGEMENT, USE A PAGE ABOVE 32K.  
* B. IF THERE IS FREE MEMORY ABOVE THE SUPERVISOR GREATER  
* THAN 2K BYTES, USE THAT MEMORY  
* C. IF NEITHER OF THE PRECEEDING TWO ARE POSSIBLE, USE  
* THE 2K BYTE DEFAULT BUFFER WITHIN THIS DIAGNOSTIC.  
*****
```

```
*****  
* TEST 18 - DMR-11  
* LARGE MESSAGE  
* IN THIS MODE TRANSMIT AND RECEIVE 1 LARGE BUFFER  
*  
* THE BA/CC OUT RECEIVE AND TRANSMIT WILL BE ACCOUNTED FOR AND  
* THE CHARACTER COUNTS AND BUFFER ADDRESSES WILL BE CHECKED AGAINST  
* THE RECEIVE/TRANSMIT TABLE.  
*  
* THE BUFFERS ARE DETERMINED IN THE SUBROUTINE $BUFFS. THIS  
* SUBROUTINE WILL DETERMINE THE ADDRESS AND CHARACTER COUNT OF  
* ONE RECEIVE AND ONE TRANSMIT BUFFER. THE ROUTINE WILL  
* ATTEMPT TO USE AS LARGE BUFFERS AS POSSIBLE IN THE FOLLOWING  
* HIERARCHY:  
* A. IF THERE IS MEMORY MANAGEMENT, USE A PAGE ABOVE 32K.  
* B. IF THERE IS FREE MEMORY ABOVE THE SUPERVISOR GREATER  
* THAN 2K BYTES, USE THAT MEMORY  
* C. IF NEITHER OF THE PRECEEDING TWO ARE POSSIBLE USE  
* THE 2K BYTE DEFAULT BUFFER WITHIN THIS DIAGNOSTIC.  
*****
```


1168
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1174
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1177
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1200
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1223

```
*****  
* TEST 19 - DMR-11  
* MAINTENANCE MODE OPERATION  
*  
* THE BA/CC OUT RECEIVE AND TRANSMIT WILL BE ACCOUNTED FOR AND  
* THE CHARACTER COUNTS AND BUFFER ADDRESSES WILL BE CHECKED AGAINST  
* THE RECEIVE/TRANSMIT TABLE.  
*  
* THE BUFFERS ARE DETERMINED IN THE SUBROUTINE $BUFFS. THIS  
* SUBROUTINE WILL DETERMINE THE ADDRESS AND CHARACTER COUNT OF  
* ONE RECEIVE AND ONE TRANSMIT BUFFER. THE ROUTINE WILL  
* ATTEMPT TO USE AS LARGE BUFFERS AS POSSIBLE IN THE FOLLOWING  
* HIERARCHY:  
* A. IF THERE IS MEMORY MANAGEMENT, USE A PAGE ABOVE 32K.  
* B. IF THERE IS FREE MEMORY ABOVE THE SUPERVISOR GREATER  
* THAN 2K BYTES, USE THAT MEMORY  
* C. IF NEITHER OF THE PRECEEDING TWO ARE POSSIBLE, USE  
* THE 2K BYTE DEFAULT BUFFER WITHIN THIS DIAGNOSTIC.  
*****
```

9.0 ERROR INFORMATION

9.1 ERROR REPORTING

ERRORS ARE REPORTED BY THE PROGRAM AS THEY OCCUR (IF NOT INHIBITED). THE REPORT CONFORMS TO THE DIAGNOSTIC SUPERVISOR ERROR REPORT FORMAT, AND CONSISTS OF A DESCRIPTION OF THE ERROR, THE TEST NUMBER, SUBTEST NUMBER, PC OF THE ERROR CALL, DEVICE ADDRESS, AND BASIC AND EXTENDED ERROR INFORMATION.

THE FOLLOWING EXAMPLE PROVIDES A TYPICAL ERROR REPORT, WHICH DESCRIBES AN "IRDY NOT SET" ERROR, AND PROVIDES THE PC OF THE ERROR CALL AND THE PC OF THE CALL TO THE SUBROUTINE REPORTING IT, THE FAILING REGISTER NAME, AND DEVICE REGISTER CONTENTS :

```
CZDMI DVC FTL ERR 00002 ON UNIT 00 TST 006 SUB 000 PC: 016210  
TIME OUT  
ERROR IN SUBROUTINE CALLED AT PC: 036174  
BUFFER STATUS  
# OF BUFFERS: 7  
BUFFER SIZE: 2048  
IN - RCV ASSIGNED: 7 XMIT ASSIGNED: 7  
OUT - RCV RETURNED: 0 XMIT RETURNED: 0  
DMR RUN ACKNOWLEDGE NOT RCVD  
(CHECK INTERFACE, BAUD AND TURNAROUND)
```

ALL THE MESSAGES IN THE DIAGNOSTIC USE BASIC MESSAGE CALLS. THEREFORE THE INHIBIT EXTENDED ERROR FLAG WILL HAVE NO EFFECT ON THE MESSAGE OUTPUT. THE INHIBIT BASIC MESSAGES WILL INHIBIT THE ERROR MESSAGES.

@

```
1224          002000          .=2000
1225
1226
1227
1228
1229          .MCALL SVC
1230 002000          SVC          ; INITIALIZE SUPERVISOR MACROS
1231
1232
1233 002000          BGNMOD
1234
1235
1236          000001          $LSTIN= 1          ; LIST INSTRUCTIONS
1237          000001          $LSTTAG= 1
1238          000001          SVCINS= 1          ; LIST INSTRUCTIONS, SHIFTED RIGHT
1239          000001          SVCTST= 1          ; LIST TEST TAGS, SHIFTED RIGHT
1240          000001          SVCSUB= 1          ; LIST SUBTEST TAGS, SHIFTED RIGHT
1241          000001          SVCGBL= 1          ; LIST GLOBAL TAGS, SHIFTED RIGHT
1242          000001          SVCTAG= 1          ; LIST OTHER TAGS, SHIFTED RIGHT
1243
1244          ; CHANGE THE VALUES OF THE SVC... SYMBOLS TO BE ZERO IF YOU WISH
1245          ; TO ALIGN THE MACRO CALLS AND THEIR EXPANSIONS. CHANGE THE
1246          ; SYMBOLS TO BE MINUS-ONE TO NOT LIST THE EXPANSIONS. YOU MAY
1247          ; CHANGE THE SYMBOLS AT ANY POINT IN YOUR PROGRAM.
1248
1249 002000          POINTER BGNSW,BGNDU,BGNSFT
1250
1251
1252
1253
```

1254
1255
1256
1257
1258
1259
1260
1261
1262
1263
1264 002000
1265 002000
1266 002000 103
1267 002001 132
1268 002002 104
1269 002003 115
1270 002004 111
1271 002005 000
1272 002006 000
1273 002007 000
1274 002010
1275 002010 104
1276 002011
1277 002011 060
1278 002012
1279 002012 000000
1280 002014
1281 002014 001130
1282 002016
1283 002016 036744
1284 002020
1285 002020 037700
1286 002022
1287 002022 002174
1288 002024
1289 002024 002224
1290 002026
1291 002026 040100
1292 002030
1293 002030 000000
1294 002032
1295 002032 000000
1296 002034
1297 002034 000000
1298 002036
1299 002036 000000
1300 002040
1301 002040 002124
1302 002042
1303 002042 000000
1304 002044
1305 002044 000000
1306 002046
1307 002046 000000
1308 002050
1309 002050 003

.SBTTL PROGRAM HEADER

:++
:
:
:
:
:
:
:--

THE PROGRAM HEADER MACRO CHARACTERIZES THIS DIAGNOSTIC. THE
HEADER MACRO'S ARGUMENTS ARE FILE NAME, RELEASE LEVEL, PATCH
DISPOSITION OF THE MOST RECENT PATCH, MAXIMUM TEST TIME IN SEC.,
AND THE TYPE OF DIAGNOSTIC (0-SEQUENTIAL, 1-EXERCISER). THESE
ARGUMENTS ARE IN RESPECTIVE ORDER.

HEADER CZDMI,D,0,600.,0

L\$NAME::
.ASCII /C/
.ASCII /Z/
.ASCII /D/
.ASCII /M/
.ASCII /I/
.BYTE 0
.BYTE 0
.BYTE 0
L\$REV::
.ASCII /D/
L\$DEPO::
.ASCII /O/
L\$UNIT::
.WORD 0
L\$TIML::
.WORD 600.
L\$HPCP::
.WORD L\$HARD
L\$SPCP::
.WORD L\$SOFT
L\$HPTP::
.WORD L\$HW
L\$SPTP::
.WORD L\$SW
L\$LADP::
.WORD L\$LAST
L\$STA::
.WORD 0
L\$CO::
.WORD 0
L\$DTYP::
.WORD 0
L\$APT::
.WORD 0
L\$DTP::
.WORD L\$DISPATCH
L\$PRIO::
.WORD 0
L\$ENVI::
.WORD 0
L\$EXP1::
.WORD 0
L\$MREV::
.BYTE C\$REVISION

1310 002051 003
 1311 002052
 1312 002052 000000
 1313 002054 000000
 1314 002056
 1315 002056 000000
 1316 002060
 1317 002060 010240
 1318 002062
 1319 002062 000000
 1320 002064
 1321 002064 000000
 1322 002066
 1323 002066 000000
 1324 002070
 1325 002070 000000
 1326 002072
 1327 002072 023640
 1328 002074
 1329 002074 000000
 1330 002076
 1331 002076 010246
 1332 002100
 1333 002100 104035
 1334 002102
 1335 002102 000000
 1336 002104
 1337 002104 020452
 1338 002106
 1339 002106 022046
 1340 002110
 1341 002110 021756
 1342 002112
 1343 002112 020444
 1344 002114
 1345 002114 000000
 1346 002116
 1347 002116 000000
 1348 002120
 1349 002120 000000
 1350
 1351
 1352
 1353
 1354
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 1356
 1357
 1358
 1359
 1360
 1361

L\$EF:: .BYTE C\$EDIT
 .WORD 0
 .WORD 0
 L\$SPC:: .WORD 0
 L\$DEVP:: .WORD L\$DVTYP
 L\$REPP:: .WORD 0
 L\$EXP4:: .WORD 0
 L\$EXP5:: .WORD 0
 L\$AUT:: .WORD 0
 L\$DUT:: .WORD L\$DU
 L\$LUN:: .WORD 0
 L\$DESP:: .WORD L\$DESC
 L\$LOAD:: EMT E\$LOAD
 L\$ETP:: .WORD 0
 L\$IICP:: .WORD L\$INIT
 L\$ICCP:: .WORD L\$CLEAN
 L\$ACP:: .WORD L\$AUTO
 L\$PRT:: .WORD L\$PROT
 L\$TEST:: .WORD 0
 L\$DLY:: .WORD 0
 L\$HIME:: .WORD 0

.EVEN

1362
1363
1364
1365
1366
1367
1368
1369 002122
1370 002122 000023
1371 002124
1372 002124 023720
1373 002126 024230
1374 002130 025544
1375 002132 025676
1376 002134 026724
1377 002136 030136
1378 002140 030716
1379 002142 031340
1380 002144 032042
1381 002146 032554
1382 002150 033000
1383 002152 033160
1384 002154 034064
1385 002156 034614
1386 002160 036360
1387 002162 036462
1388 002164 036550
1389 002166 036620
1390 002170 036670
1391
1392
1393
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.SBTTL DISPATCH TABLE

:/ THE DISPATCH TABLE CONTAINS THE STARTING ADDRESS OF EACH TEST.
:/ IT IS USED BY THE SUPERVISOR TO DISPATCH TO EACH TEST.

DISPATCH 19

.WORD 19
L\$DISPATCH::
.WORD T1
.WORD T2
.WORD T3
.WORD T4
.WORD T5
.WORD T6
.WORD T7
.WORD T8
.WORD T9
.WORD T10
.WORD T11
.WORD T12
.WORD T13
.WORD T14
.WORD T15
.WORD T16
.WORD T17
.WORD T18
.WORD T19

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.SBTTL DEFAULT HARDWARE P-TABLE

:/ THE DEFAULT HARDWARE P-TABLE CONTAINS DEFAULT VALUES OF
:/ THE TEST-DEVICE PARAMETERS. **NOTE - MANY OF THE P-TABLE VALUES LISTED
:/ BELOW ARE NOT USED IN THIS DIAGNOSTIC BUT ARE INCLUDED TO AGREE WITH
:/ M8207 & M8203 DIAGNOSTIC P-TABLES.

BGNHW DFPTBL

002172
002172 000013
002174
002174
002174 000000
002176 160170
002200 000300
002202 000000
002204 000000
002206 000000
002210 000000
002212 000000
002214 000005
002216 000000
002220 000000

.WORD 0
.WORD 160170
.WORD 300
.WORD 0
.WORD 0
.WORD 000
.WORD 000
.WORD 000
.WORD 5
.WORD 0
.WORD 0

**NOT USED - MICROPROCESSOR TYPE
DMR11 CSR UNIBUS ADDRESS DEFAULT
DMR11 INTERRUPT VECTOR DEFAULT
**NOT USED - PRIORITY LEVEL
**NOT USED - LINE UNIT
**NOT USED - SWITCH PACK #1 (REG 11)
**NOT USED - SWITCH PACK #2 (REG 15)
**NOT USED - SWITCH PACK #3 (REG 16)
CABLE TURNAROUND (DEFAULT = CABLE(5))
**NOT USED - BAUD RATE
**NOT USED - RUN SWITCH

.WORD L10000-L\$HW/2

L\$HW::
DFPTBL::

ENDHW

L10000:

1431
1432
1433
1434
1435
1436
1437
1438
1439
1440
1441
1442
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1450
1451

.SBTTL DEFAULT SOFTWARE P-TABLE

:/ THE SOFTWARE P-TABLE CONTAINS THE VALUE OF THE PROGRAM
:/ PARAMETERS THAT CAN BE CHANGED BY THE OPERATOR.

002222
002222 000001
002224
002224
002224 000005
002226
002226

BGNSW SFPTBL

SPEED: .WORD 5
ENDSW

.WORD L10001-LSSW/2
LSSW::
SFPTBL::
;PROCESSOR SPEED VARIABLE USED
;TO ALTER THE WAIT VARIABLES.
L10001:

```
1452 .SBTTL GLOBAL EQUATES SECTION
1453
1454 :////////////////////
1455 :// THE GLOBAL EQUATES SECTION CONTAINS PROGRAM EQUATES THAT
1456 :// ARE USED IN MORE THAN ONE TEST.
1457 :////////////////////
1458
1459 002226 EQUALS
1460
1461 : BIT DIFINITIONS
1462
1463 100000 BIT15== 100000
1464 040000 BIT14== 40000
1465 020000 BIT13== 20000
1466 010000 BIT12== 10000
1467 004000 BIT11== 4000
1468 002000 BIT10== 2000
1469 001000 BIT09== 1000
1470 000400 BIT08== 400
1471 000200 BIT07== 200
1472 000100 BIT06== 100
1473 000040 BIT05== 40
1474 000020 BIT04== 20
1475 000010 BIT03== 10
1476 000004 BIT02== 4
1477 000002 BIT01== 2
1478 000001 BIT00== 1
1479
1480 BIT9== BIT09
1481 BIT8== BIT08
1482 BIT7== BIT07
1483 BIT6== BIT06
1484 BIT5== BIT05
1485 BIT4== BIT04
1486 BIT3== BIT03
1487 BIT2== BIT02
1488 BIT1== BIT01
1489 BIT0== BIT00
1490
1491 : EVENT FLAG DEFINITIONS
1492 : EF32:EF17 RESERVED FOR SUPERVISOR TO PROGRAM COMMUNICATION
1493
1494 000040 EF.START== 32. ; START COMMAND WAS ISSUED
1495 000037 EF.RESTART== 31. ; RESTART COMMAND WAS ISSUED
1496 000036 EF.CONTINUE== 30. ; CONTINUE COMMAND WAS ISSUED
1497 000035 EF.NEW== 29. ; A NEW PASS HAS BEEN STARTED
1498 000034 EF.PWR== 28. ; A POWER-FAIL/POWER-UP OCCURRED
1499
1500
1501 : PRIORITY LEVEL DEFINITIONS
1502
1503 000340 PRI07== 340
1504 000300 PRI06== 300
1505 000240 PRI05== 240
1506 000200 PRI04== 200
1507 000140 PRI03-- 140
```



```

1508      00010C      PRI02== 100
1509      000040      PRI01== 40
1510      000000      PRI00== 0
1511      :
1512      :OPERATOR FLAG BITS
1513      :
1514      000004      EVL==      4
1515      000010      LOT==      10
1516      000020      ADR==      20
1517      000040      IDU==      40
1518      000100      ISR==     100
1519      000200      UAM==     200
1520      000400      BOE==     400
1521      001000      PNT==    1000
1522      002000      PRI==    2000
1523      004000      IXE==    4000
1524      010000      IBE==   10000
1525      020000      IER==   20000
1526      040000      LOE==   40000
1527      100000      HOE==  100000
1528      :*****
1529      :
1530      :*****
1531      :SWITCH REGISTER OPTIONS
1532      :
1533      100000      SW15=   100000
1534      040000      SW14=   40000
1535      020000      SW13=   20000
1536      010000      SW12=   10000
1537      004000      SW11=   4000
1538      002000      SW10=   2000
1539      001000      SW09=   1000
1540      000400      SW08=   400
1541      000200      SW07=   200
1542      000100      SW06=   100
1543      000040      SW05=   40
1544      000020      SW04=   20
1545      000010      SW03=   10
1546      000004      SW02=   4
1547      000002      SW01=   2
1548      000001      SW00=   1
1549      :
1550      :*****
1551      :CSR AND STAU WORD DEFINITIONS
1552      :SELO (CSR) - BSELO/BSEL1
1553      100000      RUN=    BIT15      ;SET IF RUNNING
1554      040000      MCLR=   BIT14      ;MASTER CLEAR OF PROCESSOR AND LINE UNIT
1555      020000      MDIAG=  BIT13      ;CSR MAINTENANCE - ENABLE MICRODIAGNOSTICS
1556      010000      STLU=   BIT12      ;CSR MAINTENANCE - STEP LINE UNIT
1557      004000      LPLU=   BIT11      ;CSR MAINTENANCE - LINE UNIT LOOP
1558      002000      ROMO=   BIT10      ;CSR MAINTENANCE
1559      001000      ROMI=   BIT9       ;CSR MAINTENANCE
1560      000400      STUP=   BIT8       ;CSR MAINTENANCE - USED WITH LOOP LU
1561      :WHEN ASSERTED, XMITTER SHIFTS; CLEAR, REC. SHIFTS
1562      000200      RDI=    BIT7       ;CSR - DMR11 READY RESPONSE
1563      000100      IESET=  BIT6       ;CSR - INTERRUPT ENABLE INPUT - DMR11 INTERRUPTS
    
```

```

1564                                     ;CPU WHEN RDI SET IN RESPONSE TO RQI BEING SET.
1565      000040      RQI=   BIT5      ;CSR - REQUEST IN
1566      000020      IECLR= BIT4      ;CSR - INTERRUPT ENABLE INPUT - DMR11 INTERRUPTS
1567                                     ;CPU WHEN RDI CLEARS IN RESPONSE TO RQI BEING CLEAR.
1568                                     ;(DMR RUN MODE ONLY)
1569      000004      RCV=   BIT2      ;CSR - IF 0, TRANSMIT & IF 1, RECEIVE
1570
1571                                     ;;SEL2 - BSEL2/BSEL3
1572      000200      RDO=   BIT7      ;SEL2 - DMR11 SETS TO INDICATE DATA READY FOR OUTPUT
1573      000100      IEO=   BIT6      ;SEL2 - SET TO ENABLE DMR11 TO INTERRUPT WHEN RDO
1574
1575                                     ;;SEL6 - BSEL6/BSEL7
1576      020000      BASEUP= BIT13     ;SEL6 - CONTROL OUT - RESPONSE TO DMR MODE BASE
1577                                     ;TABLE UPDATE COMMAND.
1578      010000      RES=   BIT12     ;SEL6 - BASE IN -- WHEN SET CAUSES
1579                                     ;RESUMPTION OF OPERATION
1580      010000      CTS=   BIT12     ;SEL6 - CONTROL OUT - CTS FAILED
1581      004000      SECN=  BIT11     ;SEL6 - CONTROL IN -- START TIME (3 SEC IF SET
1582                                     ;1 SEC IF CLEAR)
1583      002000      HDX=   BIT10     ;SEL6 - HALF-DUPLEX & CLEAR FOR FULL-DUPLEX
1584      002000      CD=    BIT10     ;SEL6 - CONTROL OUT - CD GLITCHED
1585      001000      HALTC= BIT9      ;SEL6 - EXTENDED CONTROL OUT - HALT COMPLETED
1586      000400      MAINT= BIT8      ;SEL6 - DDCMP MAINTENANCE DURING CONTROL IN
1587      000522      DMR=   BIT8.122 ;SEL6 - BASE IN -- SET FOR DMR11 MODE
1588                                     ;122 IS THE DMR PASSWORD FOR BSEL6 AND
1589                                     ;BIT8 SETS THE DMR MODE BIT IN BSEL7
1590      000400      NXM=   BIT8      ;SEL6 - CONTROL OUT - NON EXISTENT MEMORY
1591      000200      STREC= BIT7      ;SEL6 - CONTROL OUT - START RECEIVED
1592      000100      DISCON= BIT6     ;SEL6 - CONTROL OUT - DISCONNECT
1593      000100      DTR=   BIT6      ;SEL6 - MODEM WRITE - DATA TERMINAL READY
1594      000040      DMRRUN= BIT5     ;SEL6 - CONTROL OUT - DMR RUN MODE
1595      000020      TOLONG= BIT4     ;SEL6 - CONTROL OUT - MESSAGE TOO LONG
1596      000010      MAINT1= BIT3     ;SEL6 - MODEM WRITE - LOCAL MODEM LOOPBACK
1597      000010      MNTREC= BIT3     ;SEL6 - CONTROL OUT - MAINTENANCE MSG. RECEIVED
1598      000004      NOBFR= BIT2     ;SEL6 - CONTROL OUT - NO BUFFER
1599      000004      MAINT2= BIT2     ;SEL6 - MODEM WRITE - REMOTE MODEM LOOPBACK
1600      000002      TOUT=  BIT1     ;SEL6 - CONTROL OUT - TIME OUT
1601      000001      NAKS=  BIT0     ;SEL6 - CONTROL OUT - NAKS THRESHOLD EXCEEDED
1602
1603
1604                                     ;:*****
1605                                     ;:DDCMP COMMANDS - BITS 0 & 1 IN SEL0 AND SEL2
1606
1607                                     ;:INPUT (SEL0)
1608      000000      BACCT= 0          ;:BUF ADDRESS AND CHARACTER COUNT TRANSMIT
1609      000001      CNTRL= 1          ;:CONTROL COMMAND (IN OR OUT)
1610      000002      HLT=   2          ;:HALT COMMAND
1611      000003      BASEI= 3          ;:BASE IN COMMAND
1612      000004      BACCR= 4          ;:BUF ADDRESS AND CHARACTER COUNT RECEIVE
1613      000005      WMODEM= 5        ;:WRITE MODEM STATUS REGISTER
1614      000006      EXERR= 6          ;:ENABLE EXTENDED ERROR NOTIFICATION
1615      000007      DXERR= 7          ;:DISABLE EXTENDED ERROR NOTIFICATION
1616      000010      DDMC=  10         ;:DESELECT DMC LINE MODE
1617      000011      UPDATE= 11        ;:REQUEST BASE TABLE UPDATE
1618      000012      TIMER=  12        ;:SET REP/SELECT TIMER VALUE
1619      000013      THRESH= 13        ;:SET THE FOLLOWING THRESHOLDS:
    
```

```
1620 ;NAKS RECVD
1621 ;NAKS SENT
1622 ;REP/SEL
1623 ;NO BUFFER
1624 000014 RRAM= 14 ;READ M8207 RAM (0-377)
1625 000015 INTER= 15 ;WRITE INTERFACE IN AX3-15
1626 000017 RMODEM= 17 ;READ MODEM STATUS (=NOP)
1627
1628 ;OUTPUT (SEL2) NOTE: CNTRL IS USED FOR SEL2
1629 000007 CMD= 7 ; ** MASK USED TO CLEAR COMMAND BITS 0-2 **
1630
1631 ;:*****
1632 ;:BASE TABLE OFFSETS
1633 ;:NOTE: THE OFFSETS FOR BASE+3.-BASE+10 WERE
1634 ;:INTENTIONALLY NOT LABELLED, BECAUSE THOSE LOCATIONS
1635 ;:MUST NOT BE CHANGED IN ORDER TO BE DMC COMPATIBLE.
1636 ;:THE LABELS BELOW CORRESPOND WITH THOSE USED IN THE
1637 ;:DMR MICROCODE.
1638 000042 R- 42 ;#R - MESSAGE RECEIVED
1639 000043 N= 43 ;#N - MESSAGE TRANSMITTED
1640 000044 A= 44 ;#A - MESSAGE ACKNOWLEDGED
1641 000045 T= 45 ;#T - NEXT MESSAGE TO BE TRANSMITTED
1642 000046 X= 46 ;#X - LAST COMPLETED TRANSMISSION
1643 000055 PRETIM= 55 ;PROGRAMMABLE REP/SEL TIMER VALUE.
1644 000060 TH1L= 60 ;THRESHOLD LEVEL - NAKS RECEIVED .
1645 000062 TH2L= 62 ;THRESHOLD LEVEL - NAKS SENT.
1646 000064 TH3L= 64 ;THRESHOLD LEVEL - REP SENT.
1647 000066 TH4L= 66 ;THRESHOLD LEVEL - NO BUFFER AVAILABLE.
1648 000072 ISP7= 72 ;IMAGE OF SCRATCH PAD 7
1649 000076 ISP13= 76 ;IMAGE OF SCRATCH PAD 13
1650
1651 ;:*****
1652 ;:INSTRUCTION DEFINITIONS
1653
1654 000207 RETURN-207 ;RETURN FROM SUB. [= JSR PC]
1655
1656
1657 ;:*****
1658 ;:MISC. EQUATES
1659
1660 000006 LLOOP= 6 ;LOCAL MODEM LOOPBACK
1661 000007 RLOOP= 7 ;REMOTE MODEM LOOPBACK.
1662 000015 CR= 15 ;ASCII CARRIAGE RETURN
1663 000012 LF= 12 ;ASCII LINE FEED
1664
1665
```

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1666 .SBTTL GLOBAL DATA SECTION
1667
1668 :////////////////////
1669 :/ THE GLOBAL DATA SECTION CONTAINS DATA THAT ARE USED
1670 :/ IN MORE THAN ONE TEST.
1671 :////////////////////
1672
1673
1674
1675 :*****
1676 ;DMR11 VECTOR AND REGISTER INDIRECT POINTERS
1677
1678 002226 000000 DMRVEC: .WORD 0 ;DMR11 RECEIVER INTERRUPT VECTOR
1679 002230 000000 DMTVEC: .WORD 0 ;DMR11 TRANSMITTER INT. VECTOR
1680 002232 000000 CSR: .WORD 0 ;POINTER TO DMR11 CONTROL STATUS REGISTER
1681 002234 000000 SEL2: .WORD 0 ;POINTER TO DMR11 CONTROL OUT REGISTER (SEL 2)
1682 002236 000000 SEL4: .WORD 0 ;POINTER TO DMR11 PORT REGISTER (SEL 4)
1683 002240 000000 SEL6: .WORD 0 ;POINTER TO DMR11 PORT REGISTER (SEL 6)
1684 002232 SELO= CSR ;CSR IS SELO
1685 002232 BSELO= CSR ;LOW BYTE OF CSR
1686 002242 000000 BSEL1: .WORD 0 ;POINTER TO DMR11 CSR HIGH BYTE
1687 002234 BSEL2= SEL2 ;LOW BYTE OF SEL2
1688 002244 000000 BSEL3: .WORD 0 ;POINTER TO SEL2 HIGH BYTE
1689 002236 BSEL4= SEL4 ;LOW BYTE OF SEL4
1690 002246 000000 BSEL5: .WORD 0 ;POINTER TO SEL4 HIGH BYTE
1691 002240 BSEL6= SEL6 ;LOW BYTE OF SEL6
1692 002250 000000 BSEL7: .WORD 0 ;POINTER TO SEL6 HIGH BYTE
1693
1694 :*****
1695 ;OTHER HARDWARE PARAMETERS
1696
1697 002252 000000 WTYPE: .WORD 0 ;MICROPROCESSOR TYPE
1698 002254 000000 DMTURN: .WORD 0 ;TURN AROUND TYPE (0-7)
1699 002256 000000 MICRO: .WORD 0 ;MICRODIAGNOSTICS (IF 1(YES) - ENABLED)
1700
1701 :*****
1702 ;PROGRAM CONTROL PARAMETERS
1703
1704
1705
1706 002260 000000 DMRFLG: .WORD 0 ;FLAG SET WHEN DMR MODE IS REQUESTED IN
1707 ;THE BASE IN COMMAND. USED TO FLAG THAT
1708 ;A DMR MODE ACKNOWLEDGE IS EXPECTED.
1709 002262 000000 INFACE: .WORD 0 ;FLAG TO ALLOW CHANGE OF INTERFACE TYPE
1710 ;BY WRITING AX3-15. FLAG SET/CLEARED IN INIT.
1711 002264 000000 FRSTIM: .WORD 0 ;FLAG=0 IF PROGRAM JUST LOADED
1712 002266 000000 FRSPAS: .WORD 0 ;FLAG=0 IF FIRST PASS AFTER LOAD
1713 002270 000000 STARES: .WORD 0 ;FLAG=0 IF 1ST TIME THRU AFTER STA OR RES
1714
1715 ;FOLLOWING PARAMETERS ARE USED IN THE
1716 ;INTERRUPT TESTS (TESTS 15-19):
1717 002272 000000 START: .WORD 0 ;FLAG SET WHEN A CONTROL IN HAS BEEN ISSUED.
1718 002274 000000 RESUME: .WORD 0 ;FLAG SET WHEN A BASE IN WITH RESUME DESIRED.
1719 002276 000000 DMCMDE: .WORD 0 ;FLAG SET WHEN A BASE IN WITH DMC MODE DESIRED
1720 002300 000000 MNTMDE: .WORD 0 ;FLAG SET WHEN MAINTENANCE MODE IS DESIRED.
1721 002302 000000 MMANAG: .WORD 0 ;FLAG RETURNED IN THE SUBROUTINE $BUFFS
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1722                                     ;MMANAG=1, MEMORY MANAGED BUFFERS USED
1723
1724 002304 000000 AX3: .WORD 0          ;BIT PATTERN TO WRITE INTO AX3-15, WHEN
1725                                     ;IT IS REQUESTED TO ALLOW INTERFACE
1726                                     ;SELECTION. (TEST CONFIGURATION 1-4)
1727                                     ;BIT0 = TEST BIT (MUST BE SET TO ALLOW SELECT)
1728                                     ;BIT3 - INTEGRAL MODEM
1729                                     ;BIT4 = V.35
1730                                     ;BIT6 = EIA
1731                                     ;BIT7 = RS422
1732 002306 000000 WMAINT: .WORD 0       ;FLAG SET WHEN IT IS NECESSARY TO WRITE
1733                                     ;MODEM MAINTENANCE BITS (MAINTENANCE 1 & 2)
1734                                     ;THIS FLAG IS SET OR CLEARED IN THE INIT CODE.
1735 002310 000000 MANUF: .WORD 0       ;***** MANUFACTURING USE ONLY *****
1736                                     ;THIS WORD MAY BE PATCHED TO A NON ZERO WHEN
1737                                     ;MANUFACTURING SPECIAL TEST CONNECTORS ARE
1738                                     ;USED. THIS WILL ALLOW MAINTENANCE BITS
1739                                     ;TO BE SET.
1740
1741
1742                                     ;:*****
1743                                     ;PROGRAM VARIABLES
1744
1745                                     ;WORD1-WORD3 VALUES DETERMINED IN INIT
1746 002312 000000 WAIT1: .WORD 0        ;CODE DEPENDING ON THE BAUD RATE.
1747                                     ;VALUE FOR TIMEOUT COUNTER
1748 002314 000000 WAIT2: .WORD 0        ;USED IN $WAIT SUBROUTINE
1749                                     ;VALUE FOR TIMEOUT COUNTER USED IN $MSCLR
1750 002316 000000 WAIT3: .WORD 0        ;AND $CLRQI SUBROUTINES.
1751 002320 000000 WAIT4: .WORD 0        ;VALUE FOR TIMEOUT COUNTER USED IN $INOUT.
1752 002322 000000 BUFSIZ: .WORD 0       ;WORD USED AS OUTER LOOP COUNTER IN $INOUT.
1753 002324 000000 BUFNUM: .WORD 0      ;CALCULATED BUFFER SIZE IN BYTES.
1754                                     ;# OF RECEIVE & TRANSMIT BUFFERS. THIS
1755                                     ;VARIABLE IS USED IN THE SUBROUTINE $BUFFS
1756 002326 000000 INRCV: .WORD 0        ;COUNTER FOR # OF BA/CC IN RECEIVES.
1757 002330 000000 INXMIT: .WORD 0      ;COUNTER FOR # OF BA/CC IN TRANSMITS.
1758 002332 000000 OUTRCV: .WORD 0     ;COUNTER FOR # OF BA/CC OUT RECEIVES.
1759 002334 000000 OUTXMT: .WORD 0     ;COUNTER FOR # OF BA/CC OUT TRANSMITS.
1760
1761                                     ;:*****
1762                                     ;* MISCELLANEOUS STORAGE
1763 002336 000000 TEMP1: .WORD 0        ;STORAGE FOR TURNAROUND TYPE
1764 002340 000000 TEMP: .WORD 0        ;SCRATCH WORD USED FOR MISC. STORAGE IN SUB.
1765 002342 000000 SAVE: .WORD 0       ;SCRATCH WORD USED FOR MISC. STORAGE IN SUB.
1766 002344 000000 FLAG: .WORD 0       ;SCRATCH WORD USED FOR MISC. FLAG IN SUB.
1767 002346 000000 SFLAG: .WORD 0     ;FLAG USED IN TEST 15 FOR LOOP CONTROL.
1768 002350 000000 SKIP: .WORD 0     ;FLAG USED IN TEST 7 TO MARK WHETHER TO SKIP
1769                                     ;A PORTION OF THE TEST.
1770 002352 000000 NXMFLG: .WORD 0     ;FLAG USED TO MARK THAT THE DMR ADDRESS IS NYM
1771
1772 002354 000000 INFLAG: .WORD 0     ;FLAG USED IN INISR TO FLAG WHEN ALL THE
1773                                     ;BA/CC INS HAVE BEEN DONE.
1774
1775 002356 000000 OUTFLG: .WORD 0     ;FLAG USED IN OUTISR TO FLAG WHEN ALL THE
1776                                     ;BA/CC OUTS HAVE BEEN DONE.
1777 002360 000000 RESFLG: .WORD 0     ;FLAG USED IN IN ISR TO FLAG THAT THE RESUME

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1778                                     ;COMMAND HAS JUST BEEN ISSUED.
1779 002362 000000 ERRFLG: .WORD 0      ;FLAG USED IN THE WAIT SUBROUTINES ($WAIT
1780                                     ; & $CLRqi) TO RETURN ERROR CONDITON (SEC)
1781
1782
1783 002364 000000 LAST: .WORD 0        ;WORD USED TO STORE LAST COMMAND PROCESSED IN
1784                                     ;THE INPUT INTERRUPT ROUTINE.
1785 002366 000000 ERROR: .WORD 0        ;ERROR STORAGE
1786 002370 000000 LOGDEV: .WORD 0       ;LOGICAL DEVICE NUMBER
1787 002372 000000 PSTACK: .WORD 0      ;CONTAINS BASE LEVEL PROGRAM SP
1788 002374 000000 SUBRPC: .WORD 0      ;PC OF SUBR CALL FOR ERROR REPORTS
1789 002376 000000 NESTPC: .WORD 0     ;FLAG TO NOTIFY WHEN A SUBR IS NESTED
1790                                     ;IN ANOTHER SUBROUTINE (WHEN SET)
1791 002400 000000 CLRNO: .WORD 0        ;THIS WORD IS INCREMENTED DURING EACH MASTER
1792                                     ;CLEAR. THIS WILL ALLOW EVERY OTHER MASTER
1793                                     ;CLEAR TO RUN THE MICRO TESTS.
1794
1795                                     ;ROM CHECK VARAIBLES
1796 002402 000000 LOCRC: .WORD 0        ;CRC STORAGE FOR LOW BYTE CHIP
1797 002404 000000 HICRC: .WORD 0        ;CRC STORAGE FOR HIGH BYTE CHIP
1798 002406 000000 LOWORD: .WORD 0       ;TEMP. WORD CONTAINING 2 CONSECUTIVE LOW BYTES
1799 002410 000000 HIWORD: .WORD 0      ;TEMP. WORD CONTAINING 2 CONSECUTIVE HI BYTES
1800 002412 000000 ROMADR: .WORD 0     ;POINTER TO ROM ADDRESS.
1801 002414 000000 CHIPNO: .WORD 0     ;CHIP NUMBER BEING CHECKED.
1802 002416 000000 COUNT: .WORD 0     ;COUNTER USED IN THE $WAIT SUBROUTINE.
1803                                     .EVEN
  
```

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1804
1805 ;:*****
1806 ;:*****
1807 ;:BUFFER AREA
1808 ;:
1809 ;: ** CCITT PSUEDO-RANDOM TEST PATTERN **
1810 ;: THE FOLLOWING 32 WORDS TRANSLATE INTO A 512 BIT PATTERN
1811 ;: THAT WAS GENERATED ACCORDING TO CCITT RECOMMENDATION V.52. THIS
1812 ;: PATTERN WAS GENERATED BY A 9 BIT SHIFT REGISTER (INITIALIZED
1813 ;: AS 1S) WHOSE 5TH AND 9TH BITS ARE XORED. THIS XOR RESULT IS SHIFTED
1814 ;: INTO THE 1ST BIT OF THE REGISTER AS THE REGISTER IS SHIFTED RIGHT.
1815 ;: THE 9TH BIT (OR BIT SHIFTED OUT) IS SHIFTED INTO THE BIT PATTERN.
1816 ;: NOTE: CCITT RECOMMENDED 511 BITS, I'VE EXTENDED THIS BY 1 BIT TO END
1817 ;: ON A WORD BOUNDARY.
  
```

```

1817 002420 $CCITT:
1818 002420 177603 157427 031011 .WORD 177603,157427,031011
1819 002426 047321 163715 105221 .WORD 047321,163715,105221
1820 002434 143325 142304 040041 .WORD 143325,142304,040041
1821 002442 014116 052606 172334 .WORD 014116,052606,172334
1822 002450 105025 123754 111337 .WORD 105025,123754,111337
1823 002456 111523 030030 145064 .WORD 111523,030030,145064
1824 002464 137642 143531 063617 .WORD 137642,143531,063617
1825 002472 135015 066730 026575 .WORD 135015,066730,026575
1826 002500 052012 053627 070071 .WORD 052012,053627,070071
1827 002506 151172 165044 031605 .WORD 151172,165044,031605
1828 002514 166632 016741 .WORD 166632,016741
  
```

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1829
1830 ;:*****
1831 ;: TRANSMIT BUFFER (SMALL)
1832
1833 002520 000000 TFLAG: .WORD 0      ;FLAG FOR STATUS OF TRANSMIT BUFFER
  
```

```

1834          000044          TCOUNT= 36.          ;CHARACTER COUNT OF TBUF
1835 002522 041101 042103 043105 TBUF: .ASCIZ /ABCDEFGHIJKLMNOPQRSTUVWXYZ0123456789/
1836 002530 044107 045111 046113
1837 002536 047115 050117 051121
1838 002544 052123 053125 054127
1839 002552 055131 030460 031462
1840 002560 032464 033466 034470
1841 002566          000
1842          002570          .EVEN
1843
1844
1845          ;:*****
1846          ;: RECEIVE BUFFER (SMALL)
1847
1848 002570 000000          RFLAG: .WORD 0          ;FLAG FOR STATUS OF RECEIVE BUFFER
1849          000044          RCOUNT= 36.          ;CHARACTER COUNT OF RBUF
1850 002572 000046          RBUF: .BLKB 38.          ;36. BYTE BUFFER + 2 BYTES USED
1851          ;:TO MARK THE END OF THE RECEIVE BUFFER
1852          .EVEN
1853
1854          ;:*****
1855          ;: BASE TABLE
1856
1857 002640 000400          BASE: .BLKB 256.          ;MICROPROCESSOR MEMORY ALLOCATION
1858
1859          ;:*****
1860          ;: TRANSMIT AND RECEIVE BUFFER POINTERS
1861
1862 003240 000200          XMTBUF: .BLKW 128.          ;POINTERS TO TRANSMIT BUFFERS (UP TO 64)
1863          ;:1 WORD FOR ADDRESS AND 1 WORD FOR CHAR. COUNT
1864 003640 000200          RCVBUF: .BLKW 128.          ;POINTERS TO RECEIVE BUFFERS (UP TO 64).
1865
1866          ;:*****
1867          ;: BUFFER AREA (LARGE)
1868
1869 004240 004000          BIGBUF: .BLKB 4000          ;MAX BUFFER (2K BYTES)
    
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.SBTTL GLOBAL TEXT SECTION

:XXX
: THE GLOBAL TEXT SECTION CONTAINS FORMAT STATEMENTS,
: MESSAGES, AND ASCII INFORMATION THAT ARE USED IN
: MORE THAN ONE TEST.
:XXX

:*****
:* NAMES OF DEVICES SUPPORTED BY PROGRAM
:*****
: DEVTYP <DMR11>

010240
010240
010240 046504 030522 000061

L\$DV TYP::
.ASCIZ /DMR11/
.EVEN

:*****
:* TITLE OF PROGRAM
:*****
: DESCRIPT <DMR-11 FUNCTIONAL TESTS>

010246
010246
010246 046504 026522 030461
010254 043040 047125 052103
010262 047511 040516 020114
010270 042524 052123 000123

L\$DESC::
.ASCIZ /DMR-11 FUNCTION
.EVEN

:
: FORMAT STATEMENTS USED IN PRINT CALLS
:

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```
.SBTTL GLOBAL SUBROUTINES

:////////////////////
:/ THE GLOBAL SUBROUTINES ARE CALLED BY MORE THAN ONE TEST
:////////////////////

:*****
:
: MACROS - THERE ARE 2 BASIC TYPES OF MACROS USED
:           1. NORMAL MACROS -
:           2. DMR11 FUNCTIONAL MACROS - THESE MACROS MAY
:              BE NOTHING MORE THAN A CALL TO A SUBROUTINE,
:              BUT THEY ARE DISTINCT DMR FUNCTIONS WHICH CAN
:              DISTINGUISHED BY THE IN-LINE MACRO NAME.
:*****

:*****
: CALL MACRO - CALL ROUTINE = JSR PC, ROUTINE
:              (NOTE: RETURN IS EQUATED TO A RTS PC)
:*****

.MACRO CALL ROUTIN
  .IF B, ROUTIN
  .ERROR ROUTINE; ## MISSING ROUTINE-EXPANSION ABORT ##
  .MEXIT
  .ENDC
JSR PC,ROUTIN
.ENDM

:*****
: WAIT $FLAG MACRO - THIS MACRO INTERPUTS THE $FLAG AS RDI, RQI OR RDO.
: IF RDI OR RDO, THE SUBROUTINE CALLED WILL WAIT UNTIL
: THE RESPECTIVE BIT IS SET. IF RQI, THE SUBROUTINE
: CALIED WILL CLEAR RQI AND WAIT UNTIL RDI IS CLEARED.
:*****

.MACRO WAIT $FLAG
.NLIST
.LIST ME
.LIST
:***** MACRO EXPANSION *****
  .IF B, $FLAG
  .ERROR FLAG ;## MISSING FLAG FOR WAIT - EXPANSION ABORT ##
  .MEXIT
  .ENDC
  .IF IDN $FLAG,RQI
  JSR PC, $CLRQI ;CLEAR RQI AND WAIT FOR IT TO BE CLEARED.
  .ENDC
  .IF IDN $FLAG,RDI
  JSR PC, $WAIT ;CALL WAIT ROUTINE
  .WORD 0 ;FLAG THAT WE'RE WAITING FOR RDI
  .ENDC
  .IF IDN $FLAG,RDO
  JSR PC, $WAIT ;CALL WAIT ROUTINE
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```
.WORD 1 ;FLAG THAT WE'RE WAITING FOR RDO  
.ENDC ;****  
.NLIST ME  
.ENDM
```

```
*****  
: CLEAR MACRO - THIS IS A DMR FUNCTIONAL MACRO WHICH CALLS THE  
: SMSCLR SUBROUTINE  
*****
```

```
.MACRO CLEAR  
.NLIST  
.LIST ME  
.LIST  
JSR PC, SMSCLR ;**** MACRO EXPANSION ****  
;ISSUE A DMR MASTER CLEAR  
;****  
.NLIST ME  
.ENDM
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```
*****  
: BASEIN MACRO - THIS IS A DMR FUNCTIONAL MACRO WHICH CALLS THE  
: $BASEIN SUBROUTINE (WITH DEFAULT ARGUMENTS  
: IF ARGUMENTS NOT GIVEN)  
:*****
```

```
.MACRO BASEIN $A,$B,$C  
.NLIST  
.LIST ME  
.LIST
```

```
:***** MACRO EXPANSION *****
```

```
.IF B $A  
JSR PC, $BASEI ;CALL BASE IN ROUTINE WITH DEFAULTS  
.WORD LPLU ;SET LINE UNIT LOOP  
.WORD BASE ;BASE TABLE ADDRESS  
.WORD DMR ;DMR-11 MODE
```

```
.IFF  
JSR PC, $BASEI ;CALL BASE IN ROUTINE  
.WORD $A ;MAINTENANCE MODE BITS TO SET IN BSEL1  
.WORD $B ;BASE TABLE ADDRESS  
.WORD $C ;MODE
```

```
.ENDC
```

```
:***** *****
```

```
.NLIST ME  
.ENDM
```

```
*****  
: CNTRIN MACRO - THIS IS A DMR FUNCTIONAL MACRO WHICH CALLS THE  
: $CNTIN SUBROUTINE (WITH DEFAULT ARGUMENTS  
: IF ARGUMENTS NOT GIVEN)  
:*****
```

```
.MACRO CNTRIN $A  
.NLIST  
.LIST ME  
.LIST
```

```
:***** MACRO EXPANSION *****
```

```
.IF B $A  
JSR FC, $CNTIN ;CALL CONTROL IN ROUTINE WITH DEFAULT  
.WORD 0 ;SEL6 - FULL DUPLEX, RUN MODE, ^ SEC START.
```

```
.IFF  
JSR PC, $CNTIN ;CALL CONTROL IN ROUTINE  
.WORD $A ;SEL6 - (DUPLEX, MODE)
```

```
.ENDC
```

```
:***** *****
```

```
.NLIST ME  
.ENDM
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```
*****  
: DMRIN MACRO - THIS IS A DMR FUNCTIONAL MACRO WHICH CALLS THE  
: $DMRIN SUBROUTINE  
*****  
: .MACRO DMRIN $A,$B,$C  
: .NLIST  
: .LIST ME  
: .LIST  
: ***** MACRO EXPANSION *****  
: .IF B $A  
: .ERROR DMRIN; ## MISSING ARGUMENTS-EXPANSION ABORT ##  
: .MEXIT  
: .ENDC  
: JSR PC, $DMRIN :CALL DMR MODE INPUT ROUTINE  
: .WORD $A :INPUT COMMAND  
: .IF B $B  
: .WORD 0 :NO SEL4  
: .IFF  
: .WORD $B :SEL4 VALUE (OR BITS TO CLEAR IN BSEL6)  
: .ENDC  
: .IF B $C  
: .WORD 0 :NO SEL6  
: .IFF  
: .WORD $C :SEL6 VALUE (OR BITS TO SET IN BSEL6)  
: .ENDC  
: *****  
: *****  
: .NLIST ME  
: .ENDM  
  
*****  
: SHUTDN MACRO - THIS IS A DMR FUNCTIONAL MACRO WHICH CALLS THE  
: $HALT SUBROUTINE  
*****  
: .MACRO SHUTDN  
: .NLIST  
: .LIST ME  
: .LIST  
: ***** MACRO EXPANSION *****  
: JSR PC, $HALT :DMR HALT ROUTINE.  
: *****  
: .NLIST ME  
: .ENDM
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```
*****  
: BACCIR MACRO - THIS IS A DMR FUNCTIONAL MACRO WHICH CALLS THE  
: $BACC SUBROUTINE (WITH DEFAULT ARGUMENTS  
: IF ARGUMENTS NOT GIVEN)  
*****
```

```
.MACRO BACCIR $A,$B  
.NLIST  
.LIST ME  
.LIST  
;**** MACRO EXPANSION ****  
.IF B $A  
JSR PC, $BACC ;CALL BA/CC IN ROUTINE WITH DEFAULTS  
.WORD RQI.BACCR ;BA/CC IN RECEIVE COMMAND  
.WORD RBUF ;RECEIVE BUFFER  
.WORD RCOUNT ;RECEIVE CHARACTER COUNT  
.IFF  
JSR PC, $BACC ;CALL BA/CC IN ROUTINE  
.WORD RQI!BACCR ;BA/CC IN RECEIVE COMMAND  
.WORD $A ;BUFFER ADDRESS BITS 0-15  
.WORD $B ;BA BITS 16/17 AND CHAR. COUNT  
.ENDC  
;**** ****  
.NLIST ME  
.ENDM
```

```
*****  
: BACCIT MACRO - THIS IS A DMR FUNCTIONAL MACRO WHICH CALLS THE  
: $BACC SUBROUTINE (WITH DEFAULT ARGUMENTS  
: IF ARGUMENTS NOT GIVEN)  
*****
```

```
.MACRO BACCIT $A,$B  
.NLIST  
.LIST ME  
.LIST  
;**** MACRO EXPANSION ****  
.IF B $A  
JSR PC, $BACC ;CALL BA/CC IN ROUTINE WITH DEFAULTS  
.WORD RQI!BACCT ;BA/CC IN TRANSMIT COMMAND  
.WORD TBUF ;TRANSMIT BUFFER ADDRESS  
.WORD TCOUNT ;TRANSMIT CHARACTER COUNT  
.IFF  
JSR PC, $BACC ;CALL BA/CC IN ROUTINE  
.WORD RQI!BACCT ;BA/CC IN TRANSMIT COMMAND  
.WORD $A ;BUFFER ADDRESS BITS 0-15  
.WORD $B ;BA BITS 16 & 17 AND CHAR. COUNT  
.ENDC  
;**** ****  
.NLIST ME  
.ENDM
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010276 005037 002362
010302 005037 002416
010306 005737 002376
010312 001005
010314 011637 002374
010320 162737 000004 002374
010326
010326 017637 000000 002340
010334 062716 000002
010340 010046
010342 010146
010344 013701 002312
010350
010350 005000
010352
010352 032777 000200 171654
010360 001036

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*****
*****
SUBROUTINE $WAIT
FUNCTION - TO WAIT FOR RDI TO BE SET IN SEL0
OR RDO TO BE SET IN SEL2

CALLING FORMAT:      JSR    PC,    $WAIT
                    .WORD  FLAG
                    (MACRO CALL -- WAIT RDI)

NESTING LEVEL      - MAY BE CALLED FROM ANOTHER SUBROUTINE

ENTRY CONDITIONS - FLAG = 1 - WAIT FOR RDO
                    = 0 - WAIT FOR RDI
WAIT1 = DELAY COUNTER (DETERMINED IN INIT.)
NESTPC= 1 - ROUTINE NESTED WITHIN ANOTHER
                    SUBROUTINE.
                    = 0 - ROUTINE NOT NESTED.

EXIT CONDITIONS - EITHER RDI OR RDO BIT SET AS EXPECTED
OR (ERROR CONDITONS):
1. RDI OR RDO SET, BUT NOT THE EXPECTED ONE
THE USER WILL BE INFORMED. HOWEVER,
THIS WILL NOT NECESSARILY BE AN ERROR.
2. BIT NOT SET BEFORE DELAY EXPIRED.
THIS WILL RESULT IN A HARD ERROR MESSAGE
AND THE CARRY BIT WILL BE SET. THE CARRY
BIT SET FLAG THE ERROR CONDITION.

REGISTERS DESTROYED - RESTORED
*****
*****
$WAIT:
CLR    ERRFLG      ;CLEAR ERROR FLAG
CLR    COUNT       ;CLEAR DELAY COUNTER
TST    NESTPC      ;IS THIS NESTED IN ANOTHER SUBROUTINE?
BNE    10$         ;YES - USE THE SUBRPC ALREADY CALCULATED.
MOV    (SP),SUBRPC ;SAVE PC AFTER THE CALL TO $WAIT.
SUB    #4,SUBRPC   ;BACKUP TO THE PC OF THE ACTUAL CALL

10$:
MOV    @ (SP),TEMP ;GET THE FLAG FOR RDI OR RDO
ADD    #2,(SP)     ;INC THE PC LEFT ON THE STACK TO POINT
                    ;PAST THE FLAG ARGUMENT
MOV    R0,-(SP)   ;SAVE R0
MOV    R1,-(SP)   ;SAVE R1
MOV    WAIT1,R1   ;DELAY COUNTER DETERMINED BY BAUD RATE
                    ;(DETERMINED IN INIT ROUTINE).

30$:
CLR    R0         ;INNER LOOP COUNT OF DELAY COUNTER

40$:
BIT    #RDO,@SEL2 ;IS THE RDO BIT SET IN SEL2?
BNE    60$       ;YES - EXIT BIT CHECK LOOP.
60$

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2187 010362 032777 000200 171642      BIT      #RDI,@SELO      ;IS THE RDI BIT SET IN SELO?
2188 010370 001064                      BNE      70$           ;YES - EXIT
2189 010372                      BREAK                      ;CALL SUPERVISOR - ALLOW CONSOLE INTERRUPT.
2190 010372 104422                      TRAP     C$BRK
2191 010374 005237 002416      INC      COUNT          ;INCREMENT DELAY COUNTER.
2192 010400 005300                      DEC      R0             ;LOOP UNTIL R0 RETURNS TO 0
2193 010402 001363                      BNE      40$
2194 010404                      DELAY    1              ;DELAY 100 MICROSECONDS
2195 010404 012727 000001                      MOV      #1,(PC)+
2196 010410 000000                      .WORD   0
2197 010412 013727 002116                      MOV      L$DLY,(PC)+
2198 010416 000000                      .WORD   0
2199 010420 005367 177772                      DEC      -6(PC)
2200 010424 001375                      BNE     -4
2201 010426 005367 177756                      DEC     -22(PC)
2202 010432 001367                      BNE     -20
2203                      ;BETWEEN LOOPS.
2204 010434 005301                      DEC      R1
2205 010436 001344                      BNE     30$
2206 010440                      ERRDF   1,EMG1,ERRG2   ;TIME OUT ERROR
2207 010440 104455                      TRAP     C$ERDF
2208 010442 000001                      .WORD   1
2209 010444 017716                      .WORD   EMG1
2210 010446 015124                      .WORD   ERRG2
2211 010450 005237 002362      INC      ERRFLG         ;SET ERROR FLAG
2212 010454 000445                      BR       100$          ;BRANCH TO COMMON EXIT.
2213 010456                      60$:
2214 010456 005737 002340      TST      TEMP           ;WERE WE WAITING FOR THE RDO FLAG?
2215 010462 001042                      BNE     100$          ;YES - OK, EXIT.
2216 010464 022737 000001 002366      CMP      #CNTRL,ERROR  ;IS THIS CONTROL OUT ERROR EXPECTED?
2217 010472 001436                      BEQ     100$          ;IF YES, DON'T REPORT THE FOLLOWING ERRORS.
2218 010474                      PRINTB  #FMS1         ;RECEIVED AN RDO, WHEN WAITING FOR RDI
2219 010474 012746 010620                      MOV      #FMS1,-(SP)
2220 010500 012746 000001                      MOV      #1,-(SP)
2221 010504 010600                      MOV      SP,R0
2222 010506 104414                      TRAP     C$PNTB
2223 010510 062706 000004                      ADD      #4,SP
2224 010514 032777 000001 171512      BIT      #CNTRL,@SEL2  ;IS THIS A CONTROL OUT?
2225 010522 001422                      BEQ     100$          ;NO NEED TO CHECK ERROR CODES.
2226 010524                      ERRDF   9,EMG9,ERRG2  ;UNEXPECTED CONTROL OUT.
2227 010524 104455                      TRAP     C$ERDF
2228 010526 000011                      .WORD   9
2229 010530 020040                      .WORD   EMG9
2230 010532 015124                      .WORD   ERRG2
2231 010534 005237 002362      INC      ERRFLG         ;SET ERROR FLAG.
2232 010540 000413                      BR       100$
2233 010542                      70$:
2234 010542 005737 002340      TST      TEMP           ;WERE WE WAITING FOR THE RDI FLAG?
2235 010546 001410                      BEQ     100$          ;YES - OK, EXIT
2236 010550                      PRINTB  #FMS2         ;RECEIVED AN RDI, WHEN WAITING FOR RDO
2237 010550 012746 010653                      MOV      #FMS2,-(SP)
2238 010554 012746 000001                      MOV      #1,-(SP)
2239 010560 010600                      MOV      SP,R0
2240 010562 104414                      TRAP     C$PNTB
2241 010564 062706 000004                      ADD      #4,SP
2242 010570                      100$:
    
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2243 010570 005737 002376      TST      NESTPC      ;WAS THIS NESTED IN ANOTHER SUBROUTINE?
2244 010574 001002              BNE      105$        ;IF YES - LEAVE THE SUBROUTINE PC ALONE
2245 010576 005037 002374      CLR      SUBRPC      ;CLEAR THE PC
2246 010602              105$:
2247 010602 012601      MOV      (SP)+,R1    ;RESTORE R1
2248 010604 012600      MOV      (SP)+,R0    ;RESTORE R0
2249 010606 005737 002362      TST      ERRFLG      ;WAS THERE AN ERROR (CARRY CLEARED ON TST)
2250 010612 001401      BEQ      110$        ;IF NOT, RETURN WITH CARRY CLEAR
2251 010614 000261      SEC                      ;SET CARRY.
2252 010616              110$:
2253 010616 000207      RETURN
2254
2255 010620 047045 040445 042122  FMS1:  .ASCIZ  /%N%ARDO SET EXPECTED RDI%N/
2256 010626 020117 042523 020124
2257 010634 054105 042520 052103
2258 010642 042105 051040 044504
2259 010650 047045      000
2260 010653      045 022516 051101  FMS2:  .ASCIZ  /%N%ARDI SET EXPECTED RDO%N/
2261 010660 044504 051440 052105
2262 010666 042440 050130 041505
2263 010674 042524 020104 042122
2264 010702 022517 000116
2265              .EVEN
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010712 042777 000040 171312
010720 005737 002376
010724 001005
010726 011637 002374
010732 162737 000004 002374
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010740 010046
010742 010146
010744 013701 002314
010750
010750 005000
010752
010752 032777 000200 171252
010760 001427
010762
010762 104422
010764 005300
010766 001371
010770
010770 012727 000001
010774 000000
010776 013727 002116
011002 000000
011004 005367 177772
011010 001375

SUBROUTINE \$CLRQI

FUNCTION - TO CLEAR RQI AND WAIT FOR RDI TO BE CLEARED

CALLING FORMAT: JSR PC, \$CLRQI
(MACRO CALL -- WAIT RQI)

NESTING LEVEL - MAY BE NESTED WITHIN ANOTHER SUBROUTINE

ENTRY CONDITIONS - WAIT2 = DELAY COUNTER (DETERMINED IN INIT. ROUTINE)
NESTPC= 1 - ROUTINE NESTED WITHIN ANOTHER SUBROUTINE.
= 0 - ROUTINE NOT NESTED.

EXIT CONDITIONS - 1. NON ERROR, DMR READY TO RECEIVE THE NEXT COMMAND
2. ERROR IF RDI DOES NOT CLEAR BEFORE THE DELAY ROUTINE EXPIRES. AN ERROR MESSAGE WILL OCCUR. ALSO A CARRY BIT WILL BE SET TO FLAG THE ERROR FOR THE USER.

REGISTERS DESTROYED - RESTORED

\$CLRQI:

CLR ERRFLG ;CLEAR ERROR FLAG
BIC #RQI,@SELO ;REQUEST INPUT CLEAR
TST NESTPC ;IS THIS NESTED IN ANOTHER SUBROUTINE?
BNE 10\$;YES - USE SUBRPC CALCULATED
MOV (SP),SUBRPC ;SAVE THE PC AFTER THE CALL TO \$WAIT.
SUB #4,SUBRPC ;BACKUP TO THE PC OF THE ACTUAL CALL.
10\$:
MOV R0,-(SP) ;SAVE R0
MOV R1,-(SP) ;SAVE R1
MOV WAIT2,R1 ;GET THE DELAY COUNTER (DETERMINED BY
;BAUD RATE IN INIT ROUTINE)
12\$:
CLR R0 ;INNER LOOP COUNT
20\$:
BIT #RDI,@SELO ;IS THE RDI BIT CLEAR IN SELO?
BEQ 30\$;YES - EXIT
BREAK ;CALL SUPERVISOR - ALLOW CONSOLE INTERRUPT.
TRAP C\$BRK
DEC R0 ;LOOP UNTIL R0 RETURNS TO 0
BNE 20\$
DELAY 1 ;DELAY 100 MICROSECONDS
MOV #1,(PC)+
.WORD 0
MOV L\$DLY,(PC)+
.WORD 0
DEC -6(PC)
BNE .-4

```

2324 011012 005367 177756
2325 011016 001367
2326 011020 005301
2327 011022 001352
2328 011024
2329 011024 104455
2330 011026 000001
2331 011030 017716
2332 011032 015124
2333 011034 005237 002362
2334 011040
2335 011040 005737 002376
2336 011044 001002
2337 011046 005037 002374
2338 011052
2339 011052 012601
2340 011054 012600
2341 011056 005737 002362
2342 011062 001401
2343 011064 000261
2344 011066
2345 011066 000207
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DEC R1 ;REPEAT UNTIL MAXIMUM LOOP SATISFIED.
BNE 12$
ERRDF 1,EMG1,ERRG2 ;TIME OUT FRROR
TRAP C$ERDF
.WORD 1
.WORD EMG1
.WORD ERRG2
INC ERRFLG ;SET ERROR FLAG
30$: TST NESTPC ;WAS THIS A NESTED ROUTINE?
BNE 40$ ;IF YES - LEAVE THE SUBRPC ALONE
CLR SUBRPC ;CLEAR THE PC
40$: MOV (SP)+,R1 ;RESTORE R1
MOV (SP)+,R0 ;RESTORE R0
TST ERRFLG ;WAS THERE AN ERROR? (CARRY CLEARED ON TST)
BEQ 50$ ;IF NOT - RETURN WITH CARRY CLEAR
SEC ;SET CARRY.
50$: RETURN

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*****
*****
SUBROUTINE $MSCLR
FUNCTION - TO PERFORM A MASTER CLEAR FOR THE DMR11
CALLING FORMAT: JSR PC, $MSCLR
(MACRO CALL -- CLEAR)
NESTING LEVEL - MAY ONLY BE CALLED FROM IN-LINE CODE (TEST,
SUBTEST OR TEST SEGMENT)
ENTRY CONDITIONS - WAIT2 = DELAY COUNTER (DETERMINED BY INIT. ROUTINE)
CLRNO = EVEN OR ODD COUNT. THE ACTUAL # IS NOT
SIGNIFICANT, HOWEVER IF BIT 0 IS SET
THEN THE MICROTTEST IS SET ALONG WITH
THE MASTER CLEAR. THIS ROUTINE WILL INCR.
THE VALUE. THIS WILL RESULT IN THE MICRO
TESTS BEING RUN ON EVERY OTHER MASTER CLEAR
EXIT CONDITIONS - 1. NO ERROR - DMR11 MICROPROCESSOR INITIALIZED
2. IF RUN BIT NOT SET BEFORE DELAY TIMEOUT, ERROR
WILL RESULT. ADDITONALLY THE ERROR MESSAGE WILL
RELAY THE RESULTS OF THE MICROTTESTS IF THE RUN
BIT IS NOT SET.
NOTE: THERE IS A PATCH AREA TO ALLOW THESE DIAGNOSTICS
TO RUN ON A M8206 (INSTEAD OF M8207). THIS
SHOULD BE FOR DEVELOPMENT USE ONLY.
  
```

REGISTERS DESTROYED - RESTORED

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*****
*****
$MSCLR:
MOV (SP),SUBRPC ;SAVE PC AFTER THE CALL TO $WAIT.
SUB #4,SUBRPC ;BACKUP TO THE PC OF THE ACTUAL CALL
MOV R0,-(SP) ;SAVE R0
MOV R1,-(SP) ;SAVE R1
CLRB @BSEL3 ;CLEAR BSEL3
NOP ;*****
NOP ;** PATCH AREA FOR 8206 IF NEEDED **
NOP ;CLR @ASEL6 -
NOP ;*****
BIT #BIT0,CLRNO ;IS THIS AN ODD MASTER CLEAR.
BNE 7$ ;IF YES - BR
MOV #MCLR,@SELO ;ISSUE A MASTER CLEAR.
BR 8$
7$: MOV #MCLR!MDIAG,@SELO ;ISSUE THE MASTER CLEAR AND TOGGLE
;MICRO TEST SWITCH.
8$:
  
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011070
011070 011637 002374
011074 162737 000004 002374
011102 010046
011104 010146
011106 105077 171132
011112 000240
011114 000240
011116 000240
011120 000240
011122 032737 000001 002400
011130 001004
011132 012777 040000 171072
011140 000403
011142
011142 012777 060000 171062
011150
  
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2404 011150 000240      NOP      :*****
2405 011152 000240      NOP      :** PATCH AREA FOR 8206 IF NEEDED **
2406 011154 000240      NOP      :MOV #RUN,@#SELO -
2407 011156 000240      NOP      :*****
2408
2409 011160 005237 002400      INC      CLRNO      :INCR WORD (CHANGE ODD TO EVEN ETC.)
2410 011164 013701 002314      MOV      WAIT2,R1   :GET THE # OF 100 MICRO SECOND DELAYS
2411
2412 011170          10$:          :TO WAIT BEFORE EXITING THE ROUTINE.
2413 011170 005000          CLR      R0          :INNER LOOP COUNT
2414 011172          20$:
2415 011172 032777 100000 171032      BIT      #RUN,@SELO :IS THE RUN BIT SET IN SELO?
2416 011200 001025          BNE      40$        :YES - EXIT
2417 011202          BREAK      :CALL SUPERVISOR - ALLOW CONSOLE INTERRUPT.
2418 011202 104422          DEC      R0          :LOOP UNTIL R0 RETURNS TO 0
2419 011204 005300          BNE      20$        TRAP      C$BRK
2420 011206 001371          DELAY    1          :DELAY 100 MICROSECONDS
2421 011210
2422 011210 012727 000001          MOV      #1,(PC)+
2423 011214 000000          .WORD   0
2424 011216 013727 002116          MOV      L$DLY,(PC)+
2425 011222 000000          .WORD   0
2426 011224 005367 177772          DEC      -6(PC)
2427 011230 001375          BNE      -.4
2428 011232 005367 177756          DEC      -22(PC)
2429 011236 001367          BNE      .-20
2430 011240 005301          DEC      R1          :REPEAT UNTIL MAX LOOP SATISFIED.
2431 011242 001352          BNE      10$
2432 011244          ERRDF    1,EMG1,ERRG3 :REPORT RUN NOT SET
2433 011244 104455          TRAP    C$ERDF
2434 011246 000001          .WORD   1
2435 011250 017716          .WORD   EMG1
2436 011252 015240          .WORD   ERRG3
2437 011254          40$:
2438 011254 012601          MOV      (SP)+,R1   :RESTORE R1
2439 011256 012600          MOV      (SP)+,R0   :RESTORE R0
2440 011260 005037 002374          CLR      SUBRPC     :TIDY UP SUBRPC
2441 011264 000207          RETURN
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*****
*****
SUBROUTINE $BASEI
FUNCTION - TO PERFORM A BASE IN COMMAND
CALLING FORMAT:      JSR    PC,    $BASEI
                      .WORD A (SELO MAINTENANCE BITS)
                      .WORD B (SEL4 - ADDRESS)
                      .WORD C (SEL6 - MODE AND/OR RESUME)
                      (MACRO CALL -- BASEIN OR BASEIN A,B,C)
NESTING LEVEL - MAY ONLY BE CALLED FROM IN-LINE CODE (TEST,
SUBTEST OR TEST SEGMENT)
ENTRY CONDITIONS - A = MAINTENANCE BITS (I.E. LINE UNIT LOOP BACK)
                  B = BASE TABLE ADDRESS (SEL4)
                  C = MODE + RESUME (SEL6)
                  INFACE = 0 - NO INTERFACE WRITE REQUIRED
                          1 - WRITE INTERFACE (AX3-15)
EXIT CONDITIONS - 1. IF NO ERROR - DMR11 BASE TABLE ASSIGNED
                  2. IF IN DMR MODE, AND INTERFACE WRITE REQUESTED
                     WRITE REQUESTED AX3-15.
                  3. TIMEOUT ERRORS ARE DETECTED IN WAIT SUBROUTINES.
                     DMRFLG = -1 DMR MODE REQUESTED (USED IN CONTROL IN
                               ROUTINE)
                               0 DMC MODE OR RESUME REQUESTED.
REGISTERS DESTROYED - RESTORED
*****
*****

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011266
011266 011637 002374
011272 162737 000004 002374
011300 112777 000043 170724
011306 012737 000001 002376
011314
011314 004737 010276
01320 000000
011322
011322 103003
011324 062716 000006
011330 000467
011332
011332 057677 000000 170672
011340 062716 000002
011344 017677 000000 170664
011352 062716 000002
011356 017677 000000 170654

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$BASEI:
MOV    (SP),SUBRPC      ;SAVE PC AFTER THE CALL TO $WAIT.
SUB    #4,SUBRPC        ;BACKUP TO THE PC OF THE ACTUAL CALL
MOVVB  #RQI!BASEI,@BSELO ;ISSUE THE BASE IN COMMAND.
MOV    #1,NESTPC       ;FLAG THAT THE NEXT SUBROUTINE IS NESTED.
WAIT   RDI              ;WAIT FOR RDI
                      ;**** MACRO EXPANSION ****
JSR    PC, $WAIT        ;CALL WAIT ROUTINE
      .WORD 0           ;FLAG THAT WE'RE WAITING FOR RDI
                      ;****
BNERROR 10$             ;IF NO ERROR, RDI SET - PROCEED
                      BCC    10$
ADD    #6,(SP)          ;CORRECT STACK FOR ERROR EXIT
BR     30$              ;EXIT
10$:
BIS    @(SP),@SELO      ;SET ANY MAINTENANCE BITS
ADD    #2,(SP)          ;INC. POINTER.
MOV    @(SP),@SEL4      ;SET UP BASE ADDRESS
ADD    #2,(SP)          ;INC. POINTER AGAIN
MOV    @(SP),@SEL6      ;SET UP RESUME BIT AND THE HIGH 2 BITS

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2500                                     ;OF THE BASE TABLE ADDRESS
2501 011364 062716 000002             ADD    #2,(SP)             ;INC. POINTER AGAIN (SHOULD BE AT RETURN PC)
2502 011370                               WAIT   RQI                ;CLEAR RQI AND WAIT FOR RDI TO CLEAR
2503                                     ;**** MACRO EXPANSION ****
2504 011370 004737 010706             JSR    PC,$CLRQI         ;CLEAR RQI AND WAIT FOR IT TO BE CLEARED.
2505                                     ;****                               ****
2506 011374                               BERROR 30$              ;IF ERROR, EXIT
2507 011374 103445                               BCS    30$
2508 011376 122777 000122 170634      CMPB   #122,@BSEL6       ;WAS THIS A DMR BASE IN?
2509 011404 001004                               BNE    15$              ;IF NOT, CLEAR DMR FLAG (DMC MODE)
2510 011406 032777 010000 170624      BIT    #RES,@SEL6       ;IS THIS A RESUME?
2511 011414 001403                               BEQ    16$              ;IF NOT, PROCEED
2512 011416                               15$:
2513 011416 005037 002260             CLR    DMRFLG           ;CLEAR DMR FLAG (NO DMR RUN ACKNOWLEDGE).
2514 011422 000432                               BR     30$              ;SKIP - TO END
2515 011424                               16$:
2516 011424 012737 177777 002260      MOV    #-1,DMRFLG       ;FLAG THAT DMR MODE WAS REQUESTED.
2517 011432 005737 002262             TST   INFACE           ;IS AN INTERFACE WRITE REQUIRED?
2518 011436 001424                               BEQ    30$              ;IF NOT - SKIP TO END
2519 011440 022737 000001 002366      CMP    #CNTRL,ERROR     ;ARE WE EXPECTING AN ERROR (IN TEST THAT
2520                                     ;FORCES AN ERROR)
2521 011446 001004                               BNE    17$              ;IF NOT PROCEED
2522 011450 032777 000200 170554      BIT    #RDO,@SELO       ;IF EXPECTING AN ERROR - IS RDO SET
2523 011456 001014                               BNE    30$              ;IF YES - DON'T BOTHER CHANGING THE INTERFACE.
2524 011460                               17$:
2525 011460 112777 000055 170544      MOVB   #RQI!INTER,@BSELO ;ISSUE WRITE INTERFACE COMMAND.
2526 011466                               WAIT   RDI              ;WAIT FOR RDI
2527                                     ;**** MACRO EXPANSION ****
2528 011466 004737 010276             JSR    PC,$WAIT         ;CALL WAIT ROUTINE
2529 011472 000000                               .WORD  0                ;FLAG THAT WE'RE WAITING FOR RDI
2530                                     ;****                               ****
2531 011474                               BERROR 30$              ;IF ERROR, BR TO END.
2532 011474 103405                               BCS    30$
2533 011476 113777 002304 170544      MOVB   AX3,@BSEL7       ;WRITE AX3-15. INTERFACE SELECTED
2534                                     ;BY AX3 DETERMINED IN INIT. CODE.
2535 011504                               WAIT   RQI              ;CLEAR RQI AND WAIT FOR RDI TO CLEAR.
2536                                     ;**** MACRO EXPANSION ****
2537 011504 004737 010706             JSR    PC,$CLRQI         ;CLEAR RQI AND WAIT FOR IT TO BE CLEARED.
2538                                     ;****                               ****
2539 011510                               30$:
2540 011510 005037 002376             CLR    NESTPC           ;CLEAR THE NEST FLAG
2541 011514 005037 002374             CLR    SUBRPC           ;TIDY UP SUBRPC
2542 011520 000207             RETURN
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SUBROUTINE \$CNTIN

FUNCTION - TO PERFORM A CONTROL IN COMMAND

CALLING FORMAT: JSR PC, \$CNTIN
.WORD A (SEL6 - MAINTENANCE MODE & HDX)
(MACRO CALL -- CNTRIN OR CNTRIN A)

NESTING LEVEL - MAY ONLY BE CALLED FROM IN-LINE CODE (TEST,
SUBTEST OR TEST SEGMENT)

ENTRY CONDITIONS - DMRFLG = -1 EXPECT CONTROL OUT IF IN DMR MODE
= 0 NO CONTROL OUT, IN DMC MODE OR RESUME.

EXIT CONDITIONS - 1. IF NO ERROR - DMR11 CONTROL IN PERFORMED
2. TIMEOUTS REPORTED IN WAIT SUBROUTINES
3. IF THIS IS A DMR MODE START UP CONTROL IN,
THIS ROUTINE WILL WAIT FOR A CONTROL
OUT - DMR RUN. IF THIS CONTROL OUT IS
NOT RECEIVED, THIS WILL RESULT IN AN ERROR
MESSAGE AND A REMINDER TO CHECK THE BAUD RATE,
INTERFACE AND TURNAROUND (PROBABLE REASON).

REGISTERS DESTROYED


```
$CNTIN:
MOV (SP),SUBRPC ;SAVE PC FROM WHERE THIS SUBR. WAS CALLED.
SUB #4,SUBRPC ;BACKUP TO PC OF ACTUAL CALL
MOVB #RQI+CNTRL,@BSEL0 ;SET UP CONTROL IN COMMAND
MOV #1,NESTPC ;FLAG THAT THE NEXT SUBROUTINE IS NESTED.
WAIT RDI ;WAIT FOR SETTING OF RDI
;**** MACRO EXPANSION ****
JSR PC,$WAIT ;CALL WAIT ROUTINE
.WORD 0 ;FLAG THAT WE'RE WAITING FOR RDI
;****
BNERROR 1$ ;IF NO ERROR - PROCEED BCC 1$
ADD #2,(SP) ;CORRECT RETURN ADDRESS
BR 20$ ;ERROR - EXIT
1$:
MOV @(SP),@SEL6 ;SET MODE DESIRED
ADD #2,(SP) ;INC. RETURN PC LEFT ON STACK.
BIT #MAINT,@SEL6 ;WAS MAINTENANCE MODE REQUESTED?
BEQ 5$ ;IF NOT, LEAVE DMRFLG AS IS.
CLR DMRFLG ;CLEAR FLAG - NO RUN MODE CONTROL OUT.
5$:
WAIT RQI ;CLEAR RQI AND WAIT FOR RDI TO CLEAR
;**** MACRO EXPANSION ****
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011522 002374
011522 011637 002374 000004 002374
011534 112777 000041 170470
011542 012737 000001 002376
011550
011550 004737 010276
011554 000000
011556 103003
011560 062716 000002
011564 000463
011566
011566 017677 000000 170444
011574 062716 000002
011600 032777 000400 170432
011606 001402
011610 005037 002260
011614
011614

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2601 011614 004737 010706      JSR    PC, $CLRQI      ;CLEAR RQI AND WAIT FOR IT TO BE CLEARED.
2602                                ;*****
2603 011620 005737 002260      TST    DMRFLG          ;WAS DMR MODE REQUESTED ON BASE IN?
2604 011624 001443              BEQ    20$             ;BR IF NOT (DMC MODE)
2605 011626 005037 002260      CLR    DMRFLG          ;CLEAR DMR RUN MODE FLAG
2606 011632                    WAIT   RDO                ;EXPECT RDO TO BE SET
2607                                ;***** MACRO EXPANSION *****
2608 011632 004737 010276      JSR    PC, $WAIT      ;CALL WAIT ROUTINE
2609 011636 000001              .WORD  1              ;FLAG THAT WE'RE WAITING FOR RDO
2610                                ;*****
2611 011640                    BNERROR 7$           ;IF NO ERROR - PROCEED
2612 011640 103011                    BCC    7$
2613 011642                    PRINTB #FMS3          ;PRINT RUN ACKNOWLEDGE NOT RECEIVED.
2614 011642 012746 011746                    MOV    #FMS3, -(SP)
2615 011646 012746 000001                    MOV    #1, -(SP)
2616 011652 010600                    MOV    SP, R0
2617 011654 104414                    TRAP  C$PNTB
2618 011656 062706 000004                    ADD    #4, SP
2619 011662 000421                    BR     15$
2620 011664                    7$:
2621 011664 032777 000001 170342      BIT    #CNTRL, @SEL2   ;DID WE RECEIVE A CONTROL OUT?
2622 011672 001005              BNE    10$             ;IF YES - PROCEED.
2623 011674                    ERRDF  8, EMG8, ERRG2 ;EXPECTED CONTROL OUT NOT RECEIVED.
2624 011674 104455                    TRAP  C$ERDF
2625 011676 000010                    .WORD 8
2626 011700 017774                    .WORD EMG8
2627 011702 015124                    .WORD ERRG2
2628 011704 000410                    BR     15$
2629 011706                    10$:
2630 011706 032777 000040 170324      BIT    #DMRRUN, @SEL6  ;WAS THE DMR RUN MODE BIT SET?
2631 011714 001004              BNE    15$             ;BR IF OK.
2632 011716                    ERRDF  9, EMG9, ERRG2 ;WRONG CONTROL OUT RECEIVED.
2633 011716 104455                    TRAP  C$ERDF
2634 011720 000011                    .WORD 9
2635 011722 020040                    .WORD EMG9
2636 011724 015124                    .WORD ERRG2
2637
2638 011726                    15$:
2639 011726 042777 000207 170300      BIC    #RDO.CMD, @SEL2 ;CLEAR RDO AND THE COMMAND BITS
2640 011734                    20$:
2641 011734 005037 002376      CLR    NESTPC          ;CLEAR THE NEST FLAG
2642 011740 005037 002374      CLR    SUBRPC          ;CLEAR PC
2643 011744 000207      RETURN
2644
2645 011746 040445 046504 020122      FMS3: .ASCII /%ADMR RUN ACKNOWLEDGE NOT RCVD.%N/
2646 011754 052522 020116 041501
2647 011762 047113 053517 042514
2648 011770 043504 020105 047516
2649 011776 020124 041522 042126
2650 012004 022456 116
2651 012007 045 024101 044103      .ASCIZ /%A(CHECK INTERFACE, BAUD AND TURNAROUND)%N/
2652 012014 041505 020113 047111
2653 012022 042524 043122 041501
2654 012030 026105 041040 052501
2655 012036 020104 047101 020104
2656 012044 052524 047122 051101

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2657 012052 052517 042116 02245
2658 012060 000116
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.EVEN

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012062
012062 005737 002376
012066 001005
012070 011637 002374
012074 162737 000004 002374
012102
012102 117637 000000 002342
012110 117677 000000 170114
012116 062716 000002
012122 052777 000040 170102
012130 013746 002376
012134 012737 000001 002376
012142
012142 004737 010276
012146 000000
012150 012637 002376
012154
012154 103003
012156 062716 000004

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*****
*****
SUBROUTINE $DMRIN
FUNCTION - TO PERFORM A DMR MODE INPUT COMMAND

CALLING FORMAT:      JSR      PC,      $DMRIN
                    .WORD   COMMAND
                    .WORD   B
                    .WORD   C
                    (MACRO CALL -- DMRIN A,B,C)

NESTING LEVEL - MAY BE CALLED FROM IN-LINE CODE (TEST,
SUBTEST OR TEST SEGMENT) OR FROM THE $LOOP
SUBROUTINE

ENTRY CONDITIONS - MUST BE IN DMR MODE
FOR ALL COMMANDS EXCEPT WRITE MODEM
                    B = SEL4
                    C = SEL6
FOR MODEM WRITE
                    B = BITS TO CLEAR IN SEL6
                    C = BITS TO SET IN SEL6
NESTPC = 1 - SUBROUTINE NESTED WITHIN ANOTHER SUB.
          = 0 - SUBROUTINE NOT NESTED.

EXIT CONDITIONS - IF NO ERROR - DMR11 MODE INPUT COMMAND PERFORMED.

REGISTERS DESTROYED
*****
*****
$DMRIN:
TST      NESTPC      ;IS THIS SUBROUTINE NESTED?
BNE      1$          ;IF YES - DON'T CHANGE SUBRPC.
MOV      (SP),SUBRPC ;SAVE PC FROM WHERE THIS SUBR. WAS CALLED.
SUB      #4,SUBRPC   ;BACKUP TO PC OF ACTUAL CALL

1$:
MOVB    @ (SP),SAVE  ;SAVE DMR INPUT COMMAND
MOVB    @ (SP),@BSELO ;SET UP DMR INPUT COMMAND.
ADD     #2,(SP)      ;INC RETURN PC LEFT ON STACK.
BIS     #RQI,@SELO  ;REQUEST INPUT.
MOV     NESTPC,-(SP) ;SAVE THE CURRENT NEST FLAG.
MOV     #1,NESTPC   ;USE THE FLAG TO SHOW THE WAIT
                    ;ROUTINE IS NESTED.
WAIT    RDI         ;WAIT FOR SETTING OF RDI
                    ;**** MACRO EXPANSION ****
JSR     PC,$WAIT    ;CALL WAIT ROUTINE
        .WORD 0      ;FLAG THAT WE'RE WAITING FOR RDI
                    ;****
MOV     (SP)+,NESTPC ;RESTORE THE ORIGINAL NEST FLAG.
BNERROR 5$          ;IF NO ERROR, OK - PROCEED.
ADD     #4,(SP)     ;UPDATE RETURN ADDRESS.
                    BCC     5$

```

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2718 012162 000433          BR      10$          :ERROR EXIT.
2719 012164          5$:          CMPB     #WMODEM,SAVE  :IS THIS A MODEM WRITE?
2720 012164 122737 000005 002342 BEQ      6$          :IF YES - SET/CLEAR BITS.
2721 012172 001413          MOV      @($P),@SEL4  :PASS VALUE FOR SEL4 (VALUE, IF ANY,
2722 012174 017677 000000 170034          :DEPENDS ON THE DMR COMMAND)
2723          ADD      #2,($P)  :INC. RETURN PC LEFT ON STACK.
2724 012202 062716 000002          MOV      @($P),@SEL6  :PASS VALUE FOR SEL6 (VALUE, IF ANY,
2725 012206 017677 000000 170024          :DEPENDS ON THE DMR COMMAND)
2726          ADD      #2,($P)  :INC. RETURN PC LEFT ON STACK.
2727 012214 062716 000002          BR      7$
2728 012220 000412          6$:          BIC      @($P),@SEL6  :CLEAR MODEM BITS
2729 012222          ADD      #2,($P)  :INC. RETURN PC LEFT ON STACK
2730 012222 047677 000000 170010          BIS      @($P),@SEL6  :SET MODEM BITS
2731 012230 062716 000002          ADD      #2,($P)  :INC. RETURN PC LEFT ON STACK.
2732 012234 057677 000000 167776          7$:          WAIT     RQI          :CLEAR RQI AND WAIT FOR RDI TO CLEAR
2733 012242 062716 000002          JSR      PC, $CLRQI  :**** MACRO EXPANSION ****
2734 012246          :CLEAR RQI AND WAIT FOR IT TO BE CLEARED.
2735 012246          :****
2736          10$:          TST      NESTPC       :WAS THIS ROUTINE NESTED?
2737 012246 004737 010706          BNE      15$         :BR IF YES
2738          CLR      SUBRPC       :CLEAR PC
2739 012252          15$:          CLR      SAVE          :RESTORE TEMP VALUE
2740 012252 005737 002376          RETURN
2741 012256 001002
2742 012260 005037 002374
2743 012264
2744 012264 005037 002342
2745 012270 000207
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012272
 012272 011637 002374
 012276 162737 000004 002374
 012304 117677 000000 167720
 012312 062716 000002
 012316 012737 000001 002376
 012324
 012324 004737 010276
 012330 000000
 012332
 012332 103003
 012334 062716 000004
 012340 000414
 012342
 012342 017677 000000 167666
 012350 062716 000002
 012354 017677 000000 167656
 012362 062716 000002
 012366 010706
 012372
 012372 005037 002376
 012376 005037 002374

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*****
*****
SUBROUTINE $BACC
FUNCTION - TO PERFORM A BUFFER ADDRESS/CHARACTER
COUNT IN COMMAND

CALLING FORMAT:      JSR    PC,    $BACC
                     .WORD  SEL0   ;BA/CC IN COMMAND
                     .WORD  SEL4   ;BUFFER ADDRESS
                     .WORD  SEL6   ;BA BITS 16 & 17 AND
                               ;CHARACTER COUNT
                     (MACRO CALL -- BACCIT OR BACCIT A,B)
                     OR (MACRO CALL -- BACCIR OR BACCIR A,B)

NESTING LEVEL - MAY ONLY BE CALLED FROM IN-LINE CODE (TEST,
SUBTEST OR TEST SEGMENT)

ENTRY CONDITIONS -

EXIT CONDITIONS - IF NO ERROR - DMR11 BA/CC COMMAND IN PERFORMED

REGISTERS DESTROYED - NOT AFFECTED
*****
*****
$BACC:
MOV    (SP),SUBRPC    ;SAVE PC FROM WHERE THIS SUBR. WAS CALLED.
SUB    #4,SUBRPC      ;BACKUP TO PC OF ACTUAL CALL
MOVB  @ (SP),@BSEL0   ;SET UP BA/CC COMMAND IN (TRANSMIT OR RECEIVE)
ADD   #2,(SP)         ;INC POINTER ON STACK
MOV   #1,NESTPC      ;FLAG THAT THE NEXT SUBROUTINE IS NESTED.
WAIT  RDI             ;WAIT FOR SETTING OF RDI
                     ;**** MACRO EXPANSION ****
JSR   PC,$WAIT       ;CALL WAIT ROUTINE
      .WORD 0         ;FLAG THAT WE'RE WAITING FOR RDI
                     ;****
BNERROR 10$          ;IF NO ERROR - PROCEED
      BCC    10$
ADD   #4,(SP)        ;CORRECT STACK FOR ERROR EXIT.
BR    20$            ;EXIT
10$:
MOV   @ (SP),@SEL4   ;SET BUFFER ADDRESS
ADD   #2,(SP)        ;INC POINTER ON STACK
MOV   @ (SP),@SEL6   ;SET UP BUFFER COUNT AND BUFFER ADDRESS
      ;BITS 16 & 17
ADD   #2,(SP)        ;INC POINTER ON STACK
WAIT  RQI            ;CLEAR RQI AND WAIT FOR RDI TO CLEAR
                     ;**** MACRO EXPANSION ****
JSR   PC,$CLRQI     ;CLEAR RQI AND WAIT FOR IT TO BE CLEARED.
      ;****
20$:
CLR   NESTPC         ;CLEAR THE NEST FLAG
CLR   SUBRPC         ;CLEAR PC

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2806 012402 000207

RETURN

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012404
012404 005737 002376
012410 001005
012412 011637 002374
012416 162737 000004 002374

012424
012424 010046
012426 010146
012430 012700 002643
012434 012701 000006

012440
012440 105720
012442 001022
012444 005301
012446 001374
012450 122010
012452 001016
012454 022737 000022 002114
012462 001407
012464 022737 000016 002114
012472 001403
012474 105710
012476 001004
012500 000407
012502
012502 122710 000001
012506 002004

012510
012510

SUBROUTINE \$ERROR

FUNCTION - TO CHECK THE FIRST 8. BASE TABLE ERROR COUNTS
FOR NON-ZERO VALUES.

CALLING FORMAT: JSR PC, \$ERROR

NESTING LEVEL - CAN BE NESTED WITHIN ANOTHER ROUTINE

ENTRY CONDITIONS - SHOULD BE DONE AFTER PROPER SHUTDOWN
NESTPC = 1 - SUBROUTINE NESTED WITHIN ANOTHER SUB.
= 0 - SUBROUTINE NOT NESTED.

EXIT CONDITIONS - IF ANY NON-ZERO VALUE FOUND IN THE BASE TABLE A
SOFT ERROR IS DECLARED.

REGISTERS DESTROYED - RESTORED

\$ERROR:

TST NESTPC ;IS THIS ROUTINE NESTED?
BNE 10\$;BR IF YES (PC ALREADY SAVED)
MOV (SP),SUBRPC ;SAVE PC AFTER THE CALL TO \$WAIT.
SUB #4,SUBRPC ;BACKUP TO THE PC OF THE ACTUAL CALL
;THE INSTRUCTION AFTER THE CALL.

10\$:
MOV R0,-(SP) ;SAVE R0
MOV R1,-(SP) ;SAVE R1
MOV #BASE+3,R0 ;POINTER TO ACTUAL BASE TABLE COUNTS.
MOV #6.,R1 ;CHECK THE 6 NAK BYTES IN THE TABLE

20\$:
TSTB (R0)+ ;IS THE NAK COUNT NON-ZERO?
BNE 30\$;IF YES - REPORT SOFT ERROR
DEC R1 ;LOOP UNTIL DONE.
BNE 20\$
CMPB (R0)+,(R0) ;ARE THE REPS THE SAME?
BNE 30\$;IF NOT - REPORT ERROR.
CMP #18.,L\$TEST ;IS THIS TEST 18 (LARGE BUFFER TEST)
BEQ 25\$;IF YES - ALLOW 1 REP
CMP #14.,L\$TEST ;IS THIS TEST 14 ?
BEQ 25\$;IF YES - ALSO ALLOW 1 REP.
TSTB (R0) ;IF NOT TEST 18 - REPORT IF NON ZERO.
BNE 30\$
BR 40\$;IF ZERO - OK.

25\$:
CMPB #1,(R0) ;IS THE REP 0 OR 1?
BGE 40\$;IF YES - OK (WE ALLOW 1 REP BECAUSE
;IN TEST 18 AT LOW BAUD RATES 1 REP IS
;EXPECTED.)

30\$:
ERRSOFT 5,EMS3,ERRG4 ;REPORT SOFT ERROR

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2863 012510 104457
2864 012512 000005
2865 012514 012540
2866 012516 015470
2867 012520
2868 012520 005737 002376
2869 012524 001002
2870 012526 005037 002374
2871 012532
2872 012532 012601
2873 012534 012600
2874 012536 000207
2875
2876 012540 040502 042523 052040 EMS3: .ASCIZ /BASE TABLE ERRORS/
2877 012546 041101 042514 042440
2878 012554 051122 051117 000123
2879
2880

40$: TST NESTPC ;IS THE ROUTINE NESTED?
      BNE 45$ ;BR IF YES
      CLR SUBRPC ;CLEAR SAVED PC

45$: MOV (SP)+,R1 ;RESTORE R1
      MOV (SP)+,R0 ;RESTORE R0
      RETURN

      .EVEN
  
```

TRAP C\$ERSOFT
 .WORD 5
 .WORD EMS3
 .WORD ERRG4

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012562 011637 002374
012566 162737 000004 002374
012574 112777 000042 167430
012602 105077 167426
012606 012737 000001 002376
012614
012614 004737 010276
012620 000000
012622
012622 103430
012624
012624 004737 010706
012630
012630 103425
012632
012632 004737 010276
012636 000001
012640
012640 103421
012642 032777 000001 167364
012650 001005
012652 104455
012654 000004
012656 012724
012660 015124
012662 000410
012664
012664 032777 001000 167346
012672 001004
012674
012674 104455
012676 000004

```

*****
*****
SUBROUTINE $HALT
FUNCTION - TO SHUTDOWN THE DMR11
ENTRY CONDITIONS - NONE
EXIT CONDITIONS - DMR SHUTDOWN
REGISTERS - NO EFFECT
*****
*****

```

```

$HALT:
MOV (SP),SUBRPC ;SAVE THE PC WHEN THE SUBROUTINE WAS CALLED.
SUB #4,SUBRPC ;BACK UP TO THE ADDRESS OF THE ACTUAL CALL.
MOVB #RQI!HLT,@SEL0 ;ISSUE A HALT
CLRB @SEL2 ;CLEAR ANY OUTPUT PENDING
MOV #1,NESTPC ;FLAG THAT THE NEXT SUBROUTINE IS NESTED.
WAIT RDI ;WAIT FOR RDI
;**** MACRO EXPANSION ****
JSR PC,$WAIT ;CALL WAIT ROUTINE
.WORD 0 ;FLAG THAT WE'RE WAITING FOR RDI
;****
;IF ERROR, EXIT BCS 20$
WAIT RQI ;CLEAR RQI AND WAIT FOR RDI TO CLEAR
;**** MACRO EXPANSION ****
;CLEAR RQI AND WAIT FOR IT TO BE CLEARED.
;****
;IF ERROR, EXIT BCS 20$
WAIT RDO ;WAIT FOR RDO
;**** MACRO EXPANSION ****
JSR PC,$WAIT ;CALL WAIT ROUTINE
.WORD 1 ;FLAG THAT WE'RE WAITING FOR RDO
;****
;IF ERROR, EXIT BCS 20$
BIT #CNTRL,@SEL2 ;IS THIS A CONTROL OUT?
BNE 10$ ;IF YES - PROCEED
ERRDF 4,EMS4,ERRG2 ;ERROR
TRAP C$ERDF
.WORD 4
.WORD EMS4
.WORD ERRG2
BR 20$
10$:
BIT #HALTC,@SEL6 ;IS THE DMR HALTED?
BNE 20$ ;IF YES - EXIT
ERRDF 4,EMS4,ERRG2 ;ERROR - NOT EXPECTED CONTROL OUT.
TRAP C$FRDF
.WORD 4

```

2937	012700	012724									.WORD	EMS4
2938	012702	015124									.WORD	ERRG2
2939	012704				20\$:							
2940	012704	042777	000207	167322		BIC	#RDO!CMD,@SEL2	:	CLEAR RDO AND COMMAND BITS.			
2941	012712	005037	002376			CLR	NESTPC	:	CLEAR THE NEST FLAG			
2942	012716	005037	002374			CLR	SIBRPC	:	CLEAR THE PC.			
2943	012722	000207				RETURN						
2944												
2945	012724	044123	052125	047504	EMS4:	.ASCIZ	/SHUTDOWN ERROR/					
2946	012732	047127	042440	051122								
2947	012740	051117	000									
2948		012744				.EVEN						

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012744
012744 005077 167262
012750 113777 002413 67256
012756 052777 001000 167246
012764 012777 121053 167246

012772 052777 000400 167232
013000 042777 001400 167224
013006 042737 000377 013062
013014 153737 002412 013062
013022 052777 001000 167202
013030 013777 013062 167202

013036 052777 000400 167166

013044 042777 001400 167160
013052 052777 002000 167152

013060 000207

013062 100000

```
*****  
*****  
SUBROUTINE $ROMO  
FUNCTION - TO READ THE CONTENTS OF THE ROM  
ENTRY CONDITIONS - ROMADR = ROM ADDRESS  
EXIT CONDITIONS - BSEL6 = CONTENTS OF ROM ADDRESS  
REGISTERS - NO EFFECT  
*****  
*****  
$ROMO:  
CLR @SELO ;INIT  
MOVB ROMADR+1,@SEL2 ;SET HIGH BYTE OF ROM ADDRESS  
BIS #ROMI,@SELO ;ENABLE SEL6 TO BE USED AS MAINTENANCE REG.  
MOV #121053,@SEL6 ;SET UP MICROINSTRUCTION TO  
;MOVE IBUS* 2 TO OBUS* 13  
; (OBUS* 13 IS A SHADOW REGISTER FOR  
;BITS 8-11 OF THE PC)  
BIS #STUP,@SELO ;CLOCK THE INSTRUCTION  
BIC #ROMI!STUP,@SELO ;CLEAR  
BIC #377,1$ ;CLEAR ADDRESS FIELD OF BRANCH INST.  
BISB ROMADR,1$ ;ADD ADDRESS OF BRANCH.  
BIS #ROMI,@SELO ;ENABLE SEL6  
MOV 1$,@SEL6 ;SET UP MICROINSTRUCTION TO  
;BRANCH IMMEDIATELY TO PC. BRANCH IS  
;NECESSARY TO TRANSFER PC SHADOW REG TO PC  
BIS #STUP,@SELO ;CLOCK THE INSTRUCTION  
;ROM PC = ROM ADDRESS  
BIC #ROMI!STUP,@SELO ;CLEAR  
BIS #ROMO,@SELO ;CLOCK IN A MAINTENANCE ROM OUT  
;ROM CONTENTS ARE NOW IN SEL6.  
RETURN  
1$: .WORD 100000 ;MICRO INSTRUCTION OPCODE FOR IMMEDIATE  
;BRANCH (ROM ADDRESS IS ADDED INTO BITS 0-7)
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013072 005737 002306
013076 001436
013100 011637 002374
013104 162737 000004 002374
013112 022737 000006 002254
013120 001007
013122 012737 000004 013170
013130 012737 000010 013172
013136 000406
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013140 012737 000010 013170
013146 012737 000004 013172
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013154 012737 000001 002376
013162
013166 000005
013170 000000
013172 000000
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013174 005037 002376
013200 005037 002374
013204 000207

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*****
SUBROUTINE $LOOP
FUNCTION - TO ISSUE AN EXTENDED CONTROL IN TO SET
          UP THE MODEM LOOPBACK DESIRED BY THE USER.

ENTRY CONDITIONS - WMAINT = 0 - DON'T WRITE MAINT. BITS
                   WMAINT = 1 - SET BITS
                   (WMAINT SET IN INIT CODE)
                   DMCMD = 0 - DMR MODE
                   DMTURN = TURN AROUND CONNECTOR

EXIT CONDITIONS -

REGISTERS      - NOT DESTROYED

*****
*****
$LOOP:
TST    DMCMD     ;IS THE DMR IN DMC MODE?
BNE    30$      ;IF SO, EXIT (CAN'T DO DMR MODE INPUT)
TST    WMAINT   ;DO WE NEED TO WRITE THE MAINTENANCE BITS?
BEQ    30$      ;IF NOT - EXIT.
MOV    (SP),SUBRPC ;SAVE THE PC AFTER THE CALL TO $LOOP
SUB    #4,SUBRPC ;BACKUP TO THE PC OF THE ACTUAL CALL.
CMP    #LLOOP,DMTURN ;IS LOCAL MODEM LOOPBACK DESIRED?
BNE    10$      ;IF NOT - PROCEED.
MOV    #MAINT2,100$ ;ENSURE REMOTE LOOPBACK IS CLEAR.
MOV    #MAINT1,101$ ;SET MAINT BIT FOR LOCAL LOOPBACK
BR     20$

10$:
;IN ALL OTHER LOOPBACK CONFIGURATIONS
;SET MAINTENANCE 2 (CONFIG. TYPE 1,3,7)
MOV    #MAINT1,100$ ;ENSURE REMOTE LOOPBACK IS CLEAR.
MOV    #MAINT2,101$ ;SET MAINT BIT FOR REMOTE LOOPBACK

20$:
MOV    #1,NESTPC ;FLAG THAT THE NEXT SUBROUTINE IS NESTED.
CALL   $DMRIN   ;DMR MODE INPUT COMMAND
        .WORD   WMODEM ;WRITE MODEM COMMAND
100$:   .WORD   0      ;BITS TO CLEAR IN MODEM REGISTER
101$:   .WORD   0      ;BITS TO SET IN MODEM REGISTER

30$:
CLR    NESTPC   ;CLEAR THE NEST FLAG
CLR    SUBRPC   ;CLEAR PC.
RETURN

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013212 162737 000004 002374
013220 005037 002352
013224
013224 012746 000340
013230 012746 023630
013234 012746 000004
013240 012746 000003
013244 104437
013246 062706 000010
013252 005737 177572
013256 005737 002352

013262 001143
013264 023727 002120 003000

013272 002537
013274 012737 000001 002302
013302
013302 012700 000340
013306 104441

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*****
*****
SUBROUTINE $BUFFS
FUNCTION - TO DETERMINE BUFFERS FOR TEST 15 - 19. THIS
SUBROUTINE WILL USE ONE OF THE FOLLOWING
THREE BUFFER AREAS:
1. IF MEMORY MANAGED, 32K - 48K
2. FREE MEMORY, IF MORE THAN 4K BYTES.
3. IF 2 OR 3 NOT POSSIBLE, DEFAULT 4K
   DEFAULT BUFFER WITHIN THIS DIAGNOSTIC.

CALL - JSR PC,$BUFFS

NESTING LEVEL - CALLED ONLY BY TESTS 16-20

ENTRY CONDITIONS - BUFNUM = # OF RCV & XMIT BUFFERS

EXIT CONDITIONS - MMANAG = 1 MEMORY MANAGEMENT USED
                  MMANAG = 0 MEMORY MANAGEMENT NOT USED
                  RCVBUF = ADDRESS OF RECEIVE BUFFER (VIRTUAL)
                  RCVBUF+2 = CHARACTER COUNT
                  RCVBUF+4 = ADDRESS OF NEXT RECEIVE BUFFER
                           (UP TO 64 ADDRESSES AND COUNTS)
                  XMTBUF = ADDRESS OF TRANSMIT BUFFER (VIRTUAL)
                           (UP TO 64 ADDRESSES AND COUNTS)

REGISTERS - NOT DESTROYED

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*****
$BUFFS:
MOV (SP),SUBRPC ;SAVE PC AFTER THE CALL TO $BUFFS
SUB #4,SUBRPC ;BACKUP TO THE PC OF THE CALL.
CLR NXMFLG
SETVEC #4,#NOXMEM,#PRI07 ;SET UP TRAP 4 (WILL SET FLAG FOR NXM)
MOV #PRI07,-(SP)
MOV #NOXMEM,-(SP)
MOV #4,-(SP)
MOV #3,-(SP)
TRAP C$SVEC
ADD #10,SP

TST @#177572 ;ADDRESS MEMORY MANAGEMENT REG
TST NXMFLG ;IS THE FLAG STILL CLEARED?
;NOTE: THE FLAG WILL BE SET BY THE TRAP
;IF THERE IS NO MEMORY MANAGEMENT.
BNE 30$ ;BR TO USE NON-MEMORY MANAG. BUFFERS.
CMP L$HIMEM,#3000 ;IS THERE AT LEAST 48K WORDS? (16K WORDS
;FOR BUFFERS)
BLT 30$ ;IF NOT, USE NON-MEMORY MANAG. BUFFERS.
MOV #1,MMANAG ;FLAG THAT MEMORY MANAGEMENT IS USED
SETPRI #PRI07 ;MAKE SURE WE ARE IN KERNEL MODE.
MOV #PRI07,R0
TRAP C$SPRI

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3152 013556 21$:
3153 013556 012737 000376 002322 MOV #376,BUFSIZ ;EACH BUFFER IS 254. BYTES.
3154 013564 000511 BR 60$
3155
3156 013566 29$:
3157 013566 005037 177572 CLR @#177572 ;TURN OFF MEMORY MANAGEMENT
3158 013572 30$:
3159 013572 005037 002302 CLR MMANAG ;FLAG THAT MEMORY MANAGEMENT NOT USED.
3160 013576 CLRVEC #4 ;RESTORE TRAP 4.
3161 013576 012700 000004 MOV #4,R0
3162 013602 104436 TRAP C$CVEC
3163 013604 MEMORY R2 ;FIND THE FREE MEMORY AVA`LABLE BETWEEN
3164 013604 104431 TRAP C$MEM
3165 013606 010002 MOV R0,R2
3166 ;THE DIAGNOSTIC AND THE DRS (SUPERVISOR).
3167 013610 021227 002000 CMP @R2,#2000 ;IS THERE AT LEAST 1K WORDS? (NOTE: CONTENTS
3168 ;OF THE RETURNED ADDRESS OF THE START OF FREE
3169 ;MEMORY CONTAIN THE AMOUNT OF AVAILABLE MEM.)
3170 013614 003406 BLE 35$ ;IF NOT AT LEAST 1K, USE DEFAULT BUFFER.
3171 013616 010237 003240 MOV R2,XMTBUF ;USE THE FREE MEMORY BUFFER.
3172 013622 011200 MOV @R2,R0 ;SAVE THE WORD SIZE OF THE BUFFER.
3173 013624 042700 000001 BIC #BIT0,R0 ;START WITH AN EVEN # OF WORDS.
3174 013630 000405 BR 40$
3175 013632 35$:
3176 013632 012737 004240 003240 MOV #BIGBUF,XMTBUF ;USE THE DEFAULT BUFFER (1ST HALF FOR XMIT).
3177 013640 012700 002000 MOV #2000,R0 ;1K WORD SIZE.
3178 013644 40$:
3179 013644 013737 003240 003640 MOV XMTBUF,RCVBUF ;CALCULATE THE RECEIVE BUFFER ADDRESS
3180 013652 060037 003640 ADD R0,RCVBUF ;AS STARTING IN THE 2ND HALF OF THE BUFFER.
3181 013656 010001 MOV R0,R1 ;BUFFER SIZE IN WORDS.
3182 013660 022737 000001 002324 CMP #1,BUFNUM ;ARE WE SETTING UP 1 RECEIVE AND XMIT BUFFER?
3183 013666 001415 BEQ 47$ ;IF YES - R1 = BYTE SIZE FOR BOTH BUFFERS.
3184 013670 022737 000007 002324 CMP #7,BUFNUM ;ARE WE SETTING UP 7 RCV & 7 XMIT BUFFERS?
3185 013676 001004 BNE 45$ ;IF NOT WE MUST NEED 64 RCV & 64 XMIT BUFFERS.
3186 013700 006201 ASR R1 ;R1 = # BYTES IN THE BUFFERS/8
3187 013702 006201 ASR R1
3188 013704 006201 ASR R1
3189 013706 000405 BR 47$
3190 013710 45$:
3191 013710 012704 000007 MOV #7,R4 ;DIVIDE BYTES BY 128.
3192 013714 46$:
3193 013714 006201 ASR R1 ;SHIFT RIGHT 7 TIMES
3194 013716 005304 DEC R4
3195 013720 001375 BNE 46$
3196 013722 47$:
3197 013722 010137 002322 MOV R1,BUFSIZ ;SAVE THE BUFFER SIZE IN BYTES.
3198 013726 162737 000002 002322 SUB #2,BUFSIZ ;ADJUST BUFFER SIZE BECAUSE WE
3199 ;WILL ADJUST BUFFER STARTING ADDRESS.
3200 013734 042737 000001 002322 BIC #1,BUFSIZ ;ENSURE WE START WITH AN EVEN # OF BYTES.
3201 013742 006200 ASR R0 ;# OF WORDS IN ALL XMIT BUFFERS.
3202 013744 010001 MOV R0,R1 ;SAVE # OF WORDS IN ALL RCV BUFFERS.
3203 013746 013702 J03240 MOV XMTBUF,R2 ;ADDRESS OF START OF XMIT BUFFERS.
3204 013752 50$:
3205 013752 012703 002420 MOV #C$CITT,R3 ;ADDRESS OF TEST PATTERN
3206 013756 012704 000040 MOV #32.,R4 ;# OF WORDS IN THE TEST PATTERN.
3207 013762 51$:

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3208	013762	012312		MOV	(R3)+,(R2)	;WRITE TEST PATTERN INTO ALL XMIT BUFFERS.
3209	013764	005300		DEC	R0	;ARE ALL THE XMIT BUFFERS WRITTEN?
3210	013766	001403		BEQ	55\$;IF YES PROCEED.
3211	013770	005304		DEC	R4	;CONTINUE WITH TEST PATTERN TILL DONE.
3212	013772	001373		BNE	51\$	
3213	013774	000766		BR	50\$;START AT BEGINNING OF TEST PATTERN.
3214	013776		55\$:			
3215	013776	013702	003640	MOV	RCVBUF,R2	;ADDRESS OF RECEIVE BUFFERS
3216	014002		55\$:			
3217	014002	005022		CLR	(R2)+	;CLEAR ALL RECEIVE BUFFERS.
3218	014004	005301		DEC	R1	
3219	014006	001375		BNE	56\$	
3220						
3221						
3222	014010		60\$:			
3223	014010	013700	003640	MOV	RCVBUF,R0	;ADDRESS OF RECEIVE BUFFER
3224	014014	012701	003640	MOV	#RCVBUF,R1	;TABLE ADDRESS OF RCV BUFFER POINTERS.
3225	014020	013702	002324	MOV	BUFNUM,R2	;# OF RCV. BUFFERS.
3226	014024		65\$:			
3227	014024	010021		MOV	R0,(R1)+	;SAVE THE RECEIVE BUFFER ADDRESS
3228	014026	013721	002322	MOV	BUFSIZ,(R1)+	;SAVE THE BUFFER SIZE
3229	014032	063700	002322	ADD	BUFSIZ,R0	;CALCULATE THE NEXT BUFFER ADDRESS.
3230	014036	005200		INC	R0	;CHANGE EVEN ADDRESS TO ODD & ODD TO EVEN.
3231	014040	005302		DEC	R2	;CALCULATE ALL THE BUFFER ADDRESSES.
3232	014042	001370		BNE	65\$	
3233						
3234	014044	013700	003240	MOV	XMTBUF,R0	;ADDRESS OF TRANSMIT BUFFERS
3235	014050	012701	003240	MOV	#XMTBUF,R1	;TABLE OF XMIT BUFFER POINTERS.
3236	014054	013702	002324	MOV	BUFNUM,R2	;#OF XMIT BUFFERS.
3237	014060	012703	000004	MOV	#4,R3	;R3 IS USED TO VARY THE CHARACTER COUNT.
3238	014064		70\$:			
3239	014064	010021		MOV	R0,(R1)+	;SAVE THE XMIT BUFFER ADDRESS.
3240	014066	013711	002322	MOV	BUFSIZ,(R1)	;SAVE THE BUFFER SIZE.
3241	014072	160321		SUB	R3,(R1)+	;VARY THE BUFFER SIZE
3242	014074	063700	002322	ADD	BUFSIZ,R0	;CALCULATE THE NEXT BUFFER ADDRESS
3243	014100	005303		DEC	R3	;CHANGE THE CHARACTER COUNT VARIABLE.
3244	014102	032703	000001	BIT	#BIT0,R3	;IS THE CONTENTS OF R3 ODD
3245	014106	001001		BNE	72\$;IF YES, DON'T ADJUST BUFFER ADDRESS.
3246	014110	005200		INC	R0	;CHANGE EVEN TO ODD ETC.
3247	014112		72\$:			
3248	014112	005703		TST	R3	;WHAT IS R3.
3249	014114	002002		BGE	75\$;CONTINUE UNTIL R3 = -1
3250	014116	012703	000004	MOV	#4,R3	;RE-INIT. THE R3 VARIABLE AGAIN.
3251	014122		75\$:			
3252	014122	005302		DEC	R2	;CALCULATE ALL THE XMIT BUFFERS.
3253	014124	001357		BNE	70\$	
3254						
3255	014126	005037	002352	CLR	NXMFLG	;RESTORE FLAG USED IN TRAP VECTOR.
3256	014132	005037	002374	CLR	SUBRPC	;CLEAR PC.
3257	014136	000207		RETURN		

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014144 162737 000004 002374
014152 012737 000001 002376
014160 013737 002324 002326
014166 013737 002324 002330
014174 013737 002324 002332
014202 013737 002324 002334
014210 005037 002354
014214 005037 002356
014220 005037 002272
014224 012702 003640
014230 012703 003240
014234 012704 003640
014240 012705 003240
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014244 012700 000200
014250 104441
014252 013737 002316 002320
014260 112777 000143 165744
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014266 012701 001000
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014272 104422
014274 005737 002354
014300 001403
014302 005737 002356
014306 001026
014310

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*****  
*****  
SUBROUTINE $INOUT  
FUNCTION - TO MANAGE THE INTERRUPT FROM BASE IN  
TO BA/CC OUT IN THE INTERRUPT TESTS 15-19  
ENTRY CONDITIONS - BUFNUM = # OF RCV AND XMIT BUFFERS  
ALL BUFFERS SET UP IN THE $BUFFS SUBROUTINE.  
WAIT3 = # OF OUTER LOOP TIMEOUT COUNTERS.  
THIS VALUE IS DETERMINED BY THE BAUD  
RATE IN THE INIT. SECTION OF CODE.  
EXIT CONDITIONS -  
REGISTERS - R0 - R5 DESTROYED  
*****  
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```
$INOUT:  
MOV (SP),SUBRPC ;SAVE THE PC AFTER THE CALL TO $LOOP  
SUB #4,SUBRPC ;BACKUP TO THE PC OF THE ACTUAL CALL.  
MOV #1,NESTPC ;FLAG THAT ANY SUBROUTINE USED WILL BE NESTED.  
MOV BUFNUM,INRCV ;# OF BA/CC IN RECEIVES  
MOV BUFNUM,INXMIT ;# OF BA/CC IN TRANSMITS  
MOV BUFNUM,OUTRCV ;# OF BA/CC OUT RECEIVES  
MOV BUFNUM,OUTXMT ;# OF BA/CC OUT TRANSMITS  
CLR INFLAG ;CLEAR INPUT BA/CC FLAG  
CLR OUTFLG ;CLEAR OUTPUT BA/CC FLAG  
CLR START ;CLEAR FLAG TO SHOW START UP NOT DONE (SET  
;AFTER CONTROL IN)  
MOV #RCVBUF,R2 ;ADDR OF RCV. BUFFER TABLE (FOR INPUT)  
MOV #XMTBUF,R3 ;ADDR OF XMIT BUFFER TABLE (FOR INPUT)  
MOV #RCVBUF,R4 ;ADDR OF RCV. BUFFER TABLE (OUTPUT CHECKING)  
MOV #XMTBUF,R5 ;ADDR OF XMIT BUFFER TABLE (OUTPUT CHECKING)  
SETPRI #PRI04 ;SET THE PRIORITY TO LEVEL 4 TO ALLOW THE  
MOV #PRI04,R0  
TRAP C$SPRI  
;DMR TO INTERRUPT AT LEVEL 5  
MOV WAIT3,WAIT4 ;TIMEOUT COUNTER DETERMINED BY BAUD RATE.  
MOVB #IESET!RQI!BASEI, @BASELO ;FIRST COMMAND - BASE IN.  
8$:  
MOV #1000,R1 ;INNER LOOP COUNTER  
10$:  
BREAK ;OPERATOR INTERRUPT ENABLE. CALL TO  
TRAP C$BRK  
;THE SUPERVISOR TO ALLOW CONSOLE INTERRUPT  
;(NOTE: INFLAG AND OUTFLG SET IN THE INTERRUPT  
;SERVICE ROUTINES)  
TST INFLAG ;ARE THE INPUTS DONE? (INISR DONE?)  
BEQ 12$ ;IF NOT KEEP CHECKING.  
TST OUTFLG ;ARE THE OUTPUTS DONE? (OUTISR DONE?)  
BNE 20$ ;IF YES EXIT WAIT LOOP.  
12$:
```

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3314 014310          DELAY 1          ;WAIT 100 MICROSECONDS.
3315 014310 012727 000001          MOV #1,(PC)+
3316 014314 000000          .WORD 0
3317 014316 013727 002116          MOV L$DLY,(PC)+
3318 014322 000000          .WORD 0
3319 014324 005367 177772          DEC -6(PC)
3320 014330 001375          BNE -4
3321 014332 005367 177756          DEC -22(PC)
3322 014336 001367          BNE -20
3323 014340 005301          DEC R1          ;CONTINUE IN LOOP UNTIL R1 = 0.
3324 014342 001353          BNE 10$
3325 014344 005337 002320          DEC WAIT4      ;DECREMENT OUTER LOOP COUNTER
3326 014350 001346          BNE 8$          ;IF NOT DONE - GO THROUGH INNER LOOP AGAIN.
3327 014352          ERRDF 2,EMG2,ERRG1 ;TIMEOUT MESSAGE.
3328 014352 104455          TRAP C$ERDF
3329 014354 000002          .WORD 2
3330 014356 017727          .WORD EMG2
3331 014360 014616          .WORD ERRG1
3332          ;ALSO PRINT # OF E$JFFRS NOT COMPLETE.
3333
3334 014362 000453          BR 60$         ;EXIT
3335 014364          20$:
3336
3337 014364 012700 003640          MOV #RCVBUF,R0 ;RECEIVE BUFFER POINTER TABLE ADDRESS.
3338 014370 012701 003240          MOV #XMTBUF,R1 ;TRANSMIT BUFFERS
3339 014374 013702 002324          MOV BUFNUM,R2 ;# OF RCV. AND XMIT BUFFERS.
3340 014400 005737 002302          TST M$MANAG    ;ARE THE BUFFERS MEMORY MANAGED?
3341 014404 001403          BEQ 40$        ;IF YES - PROCEED.
3342 014406 012737 000001 177572    MOV #1,@#177572 ;TURN ON MEMORY MANAGEMENT
3343 014414          40$:
3344 014414 012003          MOV (R0)+,R3   ;ADDRESS OF A RECEIVE BUFFER.
3345 014416 012104          MOV (R1)+,R4   ;ADDRESS OF A TRANSMIT BUFFER.
3346 014420 011005          MOV @R0,R5     ;CHARACTER COUNT.
3347 014422 022021          CMP (R0)+,(R1)+ ;ARE THE CHARACTER COUNTS THE SAME?
3348 014424 001412          BEQ 45$        ;IF YES - PROCEED.
3349 014426 005737 002302          TST M$MANAG    ;IS MEMORY MANAGEMENT TURNED ON?
3350 014432 001402          BEQ 41$        ;IF NOT - SKIP TURN OFF.
3351 014434 005037 177572          CLR @#177572   ;TURN OFF MEMORY MANAGEMENT.
3352 014440          41$:
3353 014440          ERRDF 12,EMG12,ERRG10
3354 014440 104455          TRAP C$ERDF
3355 014442 000014          .WORD 12
3356 014444 020141          .WORD EMG12
3357 014446 016164          .WORD ERRG10
3358 014450 000420          BR 60$         ;EXIT
3359 014452          45$:
3360 014452 122324          CMPB (R3)+,(R4)+ ;ARE THE CHARACTERS THE SAME?
3361 014454 001005          BNE 50$        ;IF NOT - ERROR EXIT
3362 014456 005305          DEC R5         ;CHECK ALL THE CHARACTERS
3363 014460 001374          BNE 45$
3364 014462 005302          DEC R2         ;CHECK ALL THE BUFFERS.
3365 014464 001353          BNE 40$
3366 014466 000411          BR 60$
3367 014470          50$:
3368 014470 005737 002302          TST M$MANAG    ;IS MEMORY MANAGEMENT TURNED ON?
3369 014474 001402          BEQ 51$        ;IF NOT - SKIP TURN OFF.
  
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3370 014476 005037 177572          CLR      @#177572          ;TURN OFF MEMORY MANAGEMENT.
3371 014502          51$:
3372 014502          ERRDF    15,EMG15,ERRG12
3373 014502 104455          TRAP    C$ERDF
3374 014504 000017          .WORD   15
3375 014506 020235          .WORD   EMG15
3376 014510 016250          .WORD   ERRG12
3377 014512          60$:
3378 014512 005737 002302      TST     MMANAG          ;IS MEMORY MANAGEMENT TURNED ON?
3379 014516 001402          BEQ     61$            ;IF NOT - SKIP TURN OFF.
3380 014520 005037 177572      CLR     @#177572          ;TURN OFF MEMORY MANAGEMENT.
3381 014524          61$:
3382 014524 042777 000120 165500      BIC     #IESET!IECLR,@SELO ;DISABLE BOTH INPUT INTERRUPTS
3383 014532 042777 000100 165474      BIC     #IEO,@SEL2      ;DISABLE OUTPUT INTERRUPT
3384 014540 022737 000021 002114      CMP     #17.,L$TEST     ;IS THIS TEST 17, 18 OR 19 ?
3385 014546 003011          BGT     62$            ;IF NOT - SHUTDOWN.
3386          ;NOTE:
3387          ;DOING AN UPDATE IN TESTS 17 - 19, ALLOWS
3388          ;THE USER TO CHECK OUT REMOTE LOOPBACK BETER.
3389          ;A SHUTDOWN WHEN TESTING THE REMOTE LOOPBACK,
3390          ;WOULD CAUSE THE CONNECTION TO BE DROPPED.
3391 014550          DMRIN   UPDATE        ;DO A DMR UPDATE.
3392          ;**** MACRO EXPANSION ****
3393 014550 004737 012062      JSR     PC, $DMRIN      ;CALL DMR MODE INPUT ROUTINE
3394 014554 000011          .WORD   UPDATE        ;INPUT COMMAND
3395 014556 000000          .WORD   0              ;NO SEL4
3396 014560 000000          .WORD   0              ;NO SEL6
3397          ;****
3398 014562          WAIT   RDO           ;WAIT FOR RDO
3399          ;**** MACRO EXPANSION ****
3400 014562 004737 010276      JSR     PC, $WAIT      ;CALL WAIT ROUTINE
3401 014566 000001          .WORD   1              ;FLAG THAT WE'RE WAITING FOR RDO
3402          ;****
3403 014570 000402          BR      63$
3404 014572          62$:
3405 014572          SHUTDN          ;SHUT DOWN THE DMR
3406          ;**** MACRO EXPANSION ****
3407 014572 004737 012562      JSR     PC, $HALT      ;DMR HALT ROUTINE.
3408          ;****
3409          63$:
3410 014576          SETPRI #PRI07         ;RETURN PROCESSOR PRIORITY TO 7
3411 014576 012700 000340          MOV     #PRI07,R0      TRAP    C$SPRI
3412 014602 104441          TRAP    C$SPRI
3413 014604 005037 002376      CLR     NESTPC        ;CLEAR NESTED FLAG.
3414 014610 005037 002374      CLR     SUBRPC        ;CLEAR PC.
3415 014614 000207          RETURN
3416
3417
3418
3419
3420
3421
3422
3423
    
```



```

3424 .SBTTL GLOBAL ERROR REPORT REPORT SECTION
3425 :////////////////////////////////////////////////////
3426 :/ THE GLOBAL ERROR REPORT SECTION CONTAINS ERROR MESSAGES
3427 :/ THAT ARE USED IN MORE THAN ONE TEST.
3428 :////////////////////////////////////////////////////
3429 .EVEN
3430
3431 BGNMSG ERRG1
3432 014616 ERRG1::
3433 014616 PRINTB #FMG3,SUBRPC ;PC THAT SUBROUTINE WAS CALLED.
3434 014616 013746 002374 MOV SUBRPC,-(SP)
3435 014622 012746 016366 MOV #FMG3,-(SP)
3436 014626 012746 000002 MOV #2,-(SP)
3437 014632 010600 MOV SP,R0
3438 014634 104414 TRAP C$PNTB
3439 014636 062706 000006 ADD #6,SP
3440 014642 PRINTB #FMG1,@SEL0,@SEL2 ;PRINT SEL0 AND SEL2 CONTENTS.
3441 014642 017746 165366 MOV @SEL2,-(SP)
3442 014646 017746 165360 MOV @SEL0,-(SP)
3443 014652 012746 016302 MOV #FMG1,-(SP)
3444 014656 012746 000003 MOV #3,-(SP)
3445 014662 010600 MOV SP,R0
3446 014664 104414 TRAP C$PNTB
3447 014666 062706 000010 ADD #10,SP
3448 014672 PRINTB #FMG2,@SEL4,@SEL6 ;PRINT SEL4 AND SEL2 CONTENTS.
3449 014672 017746 165342 MOV @SEL6,-(SP)
3450 014676 017746 165334 MOV @SEL4,-(SP)
3451 014702 012746 016334 MOV #FMG2,-(SP)
3452 014706 012746 000003 MOV #3,-(SP)
3453 014712 010600 MOV SP,R0
3454 014714 104414 TRAP C$PNTB
3455 014716 062706 000010 ADD #10,SP
3456 014722 PRINTB #FMG21,BUFNUM ;# OF BUFFERS
3457 014722 013746 002324 MOV BUFNUM,-(SP)
3458 014726 012746 017461 MOV #FMG21,-(SP)
3459 014732 012746 000002 MOV #2,-(SP)
3460 014736 010600 MOV SP,R0
3461 014740 104414 TRAP C$PNTB
3462 014742 062706 000006 ADD #6,SP
3463 014746 PRINTB #FMG22,BUFSIZ ;BUFFER SIZE
3464 014746 013746 002322 MOV BUFSIZ,-(SP)
3465 014752 012746 017527 MOV #FMG22,-(SP)
3466 014756 012746 000002 MOV #2,-(SP)
3467 014762 010600 MOV SP,R0
3468 014764 104414 TRAP C$PNTB
3469 014766 062706 000006 ADD #6,SP
3470 014772 005437 002326 NEG INRCV ;NEGATE BUFFER VALUES
3471 014776 005437 002330 NEG INXMIT
3472 015002 005437 002332 NEG OUTRCV
3473 015006 005437 002334 NEG OUTXMT
3474 015012 063737 002324 002326 ADD BUFNUM,INRCV ;CALCULATE BUFFERS ASSIGNED.
3475 015020 063737 002324 002330 ADD BUFNUM,INXMIT
3476 015026 063737 002324 002332 ADD BUFNUM,OUTRCV ;CALCULATE BUFFERS RECEIVED.
3477 015034 063737 002324 002334 ADD BUFNUM,OUTXMT
3478 015042 PRINTB #FMG23,INRCV,INXMIT
3479 015042 013746 002330 MOV INXMIT,-(SP)

```

3480	015046	013746	002326			MOV	INRCV,-(SP)
3481	015052	012746	017554			MOV	#FMG23,-(SP)
3482	015056	012746	000003			MOV	#3,-(SP)
3483	015062	010600				MOV	SP,R0
3484	015064	104414				TRAP	C\$PNTB
3485	015066	062706	000010			ADD	#10,SP
3486	015072			PRINTB	#FMG24,OUTRCV,CUTXMT		
3487	015072	013746	002334			MOV	OUTXMT,-(SP)
3488	015076	013746	002332			MOV	OUTRCV,-(SP)
3489	015102	012746	017635			MOV	#FMG24,-(SP)
3490	015106	012746	000003			MOV	#3,-(SP)
3491	015112	010600				MOV	SP,R0
3492	015114	104414				TRAP	C\$PNTB
3493	015116	062706	000010			ADD	#10,SP
3494	015122			ENDMSG			
3495	015122						
3496	015122	104423				L10002:	TRAP
3497							C\$MSG
3498							
3499	015124			BGNMSG	ERRG2		
3500	015124						
3501	015124	005737	002374				
3502	015130	001412		TST	SUBRPC		ERRG2::
3503	015132			BEQ	10\$:IS THE ERROR IN A SUBROUTINE?
3504	015132	013746	002374	PRINTB	#FMG3,SUBRPC		:IF NOT, DON'T PRINT SUBR. PC
3505	015136	012746	016366				:PC THAT SUBROUTINE WAS CALLED.
3506	015142	012746	000002			MOV	SUBRPC,-(SP)
3507	015146	010600				MOV	#FMG3,-(SP)
3508	015150	104414				MOV	#2,-(SP)
3509	015152	062706	000006			MOV	SP,R0
3510	015156			10\$:		TRAP	C\$PNTB
3511	015156					ADD	#6,SP
3512	015156	017746	165052		PRINTB	#FMG1,@SEL0,@SEL2 ;PRINT SEL0 AND SEL2 CONTENTS.	
3513	015162	017746	165044			MOV	@SEL2,-(SP)
3514	015166	012746	016302			MOV	@SEL0,-(SP)
3515	015172	012746	000003			MOV	#FMG1,-(SP)
3516	015176	010600				MOV	#3,-(SP)
3517	015200	104414				MOV	SP,R0
3518	015202	062706	000010			TRAP	C\$PNTB
3519	015206					ADD	#10,SP
3520	015206	017746	165026		PRINTB	#FMG2,@SEL4,@SEL6 ;PRINT SEL4 AND SEL2 CONTENTS.	
3521	015212	017746	165020			MOV	@SEL6,-(SP)
3522	015216	012746	016334			MOV	@SEL4,-(SP)
3523	015222	012746	000003			MOV	#FMG2,-(SP)
3524	015226	010600				MOV	#3,-(SP)
3525	015230	104414				MOV	SP,R0
3526	015232	062706	000010			TRAP	C\$PNTB
3527	015236			ENDMSG		ADD	#10,SP
3528	015236						
3529	015236	104423				L10003:	TRAP
3530							C\$MSG
3531	015240			BGNMSG	ERRG3		
3532	015240						
3533	015240	005737	002374				
3534	015244	001412		TST	SUBRPC		ERRG3::
3535	015246			BEQ	10\$:IS THE ERROR IN A SUBROUTINE?
				PRINTB	#FMG3,SUBRPC		:IF NOT, DON'T PRINT SUBR. PC
							:PC THAT SUBROUTINE WAS CALLED.

3536	015246	013746	002374						MOV	SUBRPC,-(SP)
3537	015252	012746	016366						MOV	#FMG3,-(SP)
3538	015256	012746	000002						MOV	#2,-(SP)
3539	015262	010600							MOV	SP,R0
3540	015264	104414							TRAP	C\$PNTB
3541	015266	062706	000006						ADD	#6,SP
3542	015272			10\$:						
3543	015272					PRINTB	#FMG1,@SELO,@SEL2	;PRINT SELO AND SEL2 CONTENTS.		
3544	015272	017746	164736						MOV	@SEL2,-(SP)
3545	015276	017746	164730						MOV	@SELO,-(SP)
3546	015302	012746	016302						MOV	#FMG1,-(SP)
3547	015306	012746	000003						MOV	#3,-(SP)
3548	015312	010600							MOV	SP,R0
3549	015314	104414							TRAP	C\$PNTB
3550	015316	062706	000010						ADD	#10,SP
3551	015322	032777	100000	164702		BIT	#RUN,@SELO	;IS THE RUN BIT SET		
3552	015330	001043				BNE	20\$;IF RUN SET, CHECK.		
3553	015332	122777	000001	164704		CMPB	#1,@SEL3	;DID CPU MICRO. FAIL?		
3554	015340	001011				BNE	12\$;IF NOT SEE IF LU FAILED.		
3555	015342					PRINTB	#FMG4	;CPU MICRO. FAILED.		
3556	015342	012746	016440						MOV	#FMG4,-(SP)
3557	015346	012746	000001						MOV	#1,-(SP)
3558	015352	010600							MOV	SP,R0
3559	015354	104414							TRAP	C\$PNTB
3560	015356	062706	000004						ADD	#4,SP
3561	015362	000441				BR	25\$			
3562	015364			12\$:						
3563	015364	122777	000002	164652		CMPB	#2,@SEL3	;DID LINE UNIT MICRO. FAIL?		
3564	015372	001011				BNE	15\$			
3565	015374					PRINTB	#FMG5	;LINE UNIT FAILED.		
3566	015374	012746	016471						MOV	#FMG5,-(SP)
3567	015400	012746	000001						MOV	#1,-(SP)
3568	015404	010600							MOV	SP,R0
3569	015406	104414							TRAP	C\$PNTB
3570	015410	062706	000004						ADD	#4,SP
3571	015414	000424				BR	25\$			
3572	015416			15\$:						
3573	015416					PRINTB	#FMG5	;NO RUN - MASTER CLEAR FAILED.		
3574	015416	012746	016471						MOV	#FMG5,-(SP)
3575	015422	012746	000001						MOV	#1,-(SP)
3576	015426	010600							MOV	SP,R0
3577	015430	104414							TRAP	C\$PNTB
3578	015432	062706	000004						ADD	#4,SP
3579	015436	000413				BR	25\$			
3580	015440			20\$:						
3581	015440	105777	164600			TSTB	@SEL3	;IS BSEL3 STILL 0?		
3582	015444	001010				BNE	25\$;IF NOT - SEE IF MICRODIAG. RUN.		
3583	015446					PRINTB	#FMG19	;DEVICE IS NOT DMR (DMC?)		
3584	015446	012746	017436						MOV	#FMG19,-(SP)
3585	015452	012746	000001						MOV	#1,-(SP)
3586	015456	010600							MOV	SP,R0
3587	015460	104414							TRAP	C\$PNTB
3588	015462	062706	000004						ADD	#4,SP
3589	015466			25\$:						
3590	015466			ENDMSG						
3591	015466									

L10004:

Address	PC	Offset	Instruction	Comment	Register
3592	015466	104423			TRAP C\$MSG
3593					
3594					
3595	015470		BGNMSG	ERRG4	
3596	015470				
3597	015470	005737	002374		
3598	015474	001412			
3599	015476				
3600	015476	013746	002374		
3601	015502	012746	016366		
3602	015506	012746	000002		
3603	015512	010600			
3604	015514	104414			
3605	015516	062706	000006		
3606	015522				
3607	015522	105737	002643		
3608	015526	001003			
3609	015530	105737	002646		
3610	015534	001416			
3611	015536				
3612	015536				
3613	015536	005046			
3614	015540	153716	002646		
3615	015544	005046			
3616	015546	153716	002643		
3617	015552	012746	016522		
3618	015556	012746	000003		
3619	015562	010600			
3620	015564	104414			
3621	015566	062706	000010		
3622	015572				
3623	015572	105737	002645		
3624	015576	001003			
3625	015600	105737	002650		
3626	015604	001416			
3627	015606				
3628	015606				
3629	015606	005046			
3630	015610	153716	002650		
3631	015614	005046			
3632	015616	153716	002645		
3633	015622	012746	016573		
3634	015626	012746	000003		
3635	015632	010600			
3636	015634	104414			
3637	015636	062706	000010		
3638	015642				
3639	015642	105737	002644		
3640	015646	001003			
3641	015650	105737	002647		
3642	015654	001416			
3643	015656				
3644	015656				
3645	015656	005046			
3646	015660	153716	002647		
3647	015664	005046			

ERRG4::

TST SUBRPC ;IS THE ERROR IN A SUBROUTINE?
 BEQ 10\$;IF NO, DON'T PRINT SUBR. PC
 PRINTB #FMG3, SUBRPC ;PC THAT SUBROUTINE WAS CALLED.

MOV SUBRPC, -(SP)
 MOV #FMG3, -(SP)
 MOV #2, -(SP)
 MOV SP, R0
 TRAP C\$PNTB
 ADD #6, SP

10\$: TSTB BASE+3 ;ONLY PRINT NON-ZERO VALUES
 BNE 11\$
 TSTB BASE+6
 BEQ 12\$

11\$: PRINTB #FMG7, <B, BASE+3>, <B, BASE+6>

CLR -(SP)
 BISB BASE+6, (SP)
 CLR -(SP)
 BISB BASE+3, (SP)
 MOV #FMG7, -(SP)
 MOV #3, -(SP)
 MOV SP, R0
 TRAP C\$PNTB
 ADD #10, SP

12\$: TSTB BASE+5
 BNE 13\$
 TSTB BASE+8.
 BEQ 14\$

13\$: PRINTB #FMG8, <B, BASE+5>, <B, BASE+8.>

CLR -(SP)
 BISB BASE+8., (SP)
 CLR -(SP)
 BISB BASE+5, (SP)
 MOV #FMG8, -(SP)
 MOV #3, -(SP)
 MOV SP, R0
 TRAP C\$PNTB
 ADD #10, SP

14\$: TSTB BASE+4
 BNE 15\$
 TSTB BASE+7
 BEQ 16\$

15\$: PRINTB #FMG9, <B, BASE+4>, <B, BASE+7>

CLR -(SP)
 BISB BASE+7, (SP)
 CLR -(SP)

3704	016062			BGNMSG	ERRG8				
3705	016062							ERRG8::	
3706	016062				PRINTB	#FMG11	;BA/CC OUT XMIT		
3707	016062	012746	016753					MOV	#FMG11,-(SP)
3708	016066	012746	000001					MOV	#1,-(SP)
3709	016072	010600						MOV	SP,R0
3710	016074	104414						TRAP	C\$PNTB
3711	016076	062706	000004					ADD	#4,SP
3712	016102				PRINTB	#FMG13,@SEL4,@SEL6	;ACTUAL BA/CC		
3713	016102	017746	164132					MOV	@SEL6,-(SP)
3714	016106	017746	164124					MOV	@SEL4,-(SP)
3715	016112	012746	017036					MOV	#FMG13,-(SP)
3716	016116	012746	000003					MOV	#3,-(SP)
3717	016122	010600						MOV	SP,R0
3718	016124	104414						TRAP	C\$PNTB
3719	016126	062706	000010					ADD	#10,SP
3720	016132				PRINTB	#FMG14,-4(R5),-2(R5)	;EXPECTED BA/CC		
3721	016132	016546	177776					MOV	-2(R5),-(SP)
3722	016136	016546	177774					MOV	-4(R5),-(SP)
3723	016142	012746	017112					MOV	#FMG14,-(SP)
3724	016146	012746	000003					MOV	#3,-(SP)
3725	016152	010600						MOV	SP,R0
3726	016154	104414						TRAP	C\$PNTB
3727	016156	062706	000010					ADD	#10,SP
3728	016162			ENDMSG					
3729	016162							L10007:	
3730	016162	104423						TRAP	C\$MSG
3731									
3732									
3733	016164			BGNMSG	ERRG10				
3734	016164							ERRG10::	
3735	016164				PRINTB	#FMG16,-2(R0),-2(R1)	;RCV CC & XMIT CC		
3736	016164	016146	177776					MOV	-2(R1),-(SP)
3737	016170	015046	177776					MOV	-2(R0),-(SP)
3738	016174	012746	017215					MOV	#FMG16,-(SP)
3739	016200	012746	000003					MOV	#3,-(SP)
3740	016204	010600						MOV	SP,R0
3741	016206	104414						TRAP	C\$PNTB
3742	016210	062706	000010					ADD	#10,SP
3743	016214			ENDMSG					
3744	016214							L10010:	
3745	016214	104423						TRAP	C\$MSG
3746									
3747	016216			BGNMSG	ERRG11				
3748	016216							ERRG11::	
3749	016216				PRINTB	#FMG17,-4(R0),-4(R1)	;RCV BUFFER & XMIT BUFFER		
3750	016216	016146	177774					MOV	-4(R1),-(SP)
3751	016222	016046	177774					MOV	-4(R0),-(SP)
3752	016226	012746	017274					MOV	#FMG17,-(SP)
3753	016232	012746	000003					MOV	#3,-(SP)
3754	016236	010600						MOV	SP,R0
3755	016240	104414						TRAP	C\$PNTB
3756	016242	062706	000010					ADD	#10,SP
3757	016246			ENDMSG					
3758	016246							L10011:	
3759	016246	104423						TRAP	C\$MSG

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3760
3761 016250          BGNMSG  ERRG12
3762 016250
3763 016250 005303          DEC    R3          ;BACKUP TO RECEIVE ADDRESS
3764 016252 005304          DEC    R4          ;BACKUP TO TRANSMIT ADDRESS
3765 016254          PRINTB #FMG18,R3,R4 ;PRINT OUT ADDRESS
3766 016254 010446
3767 016256 010346
3768 016260 012746 017347
3769 016264 012746 000003
3770 016270 010600
3771 016272 104414
3772 016274 062706 000010
3773 016300          ENDMSG
3774 016300
3775 016300 104423
3776
3777
3778
3779
3780
3781
3782 016302 040445 042523 030114 FMG1: .ASCIZ  /%ASEL0: %06%A SEL2: %06%N/
3783 016310 020072 047445 022466
3784 016316 020101 042523 031114
3785 016324 020072 047445 022466
3786 016332 000116
3787 016334 040445 042523 032114 FMG2: .ASCIZ  /%ASEL4: %06%A SEL6: %06%N/
3788 016342 020072 047445 022466
3789 016350 020101 042523 033114
3790 016356 020072 047445 022466
3791 016364 000116
3792 016366 040445 051105 047522 FMG3: .ASCIZ  /%AERROR IN SUBROUTINE CALLED AT PC: %06%N/
3793 016374 020122 047111 051440
3794 016402 041125 047522 052125
3795 016410 047111 020105 040503
3796 016416 046114 042105 040440
3797 016424 020124 041520 020072
3798 016432 047445 022466 000116
3799 016440 040445 050103 020125 FMG4: .ASCIZ  /%ACPU MICROTTEST FAILED%N/
3800 016446 044515 051103 052117
3801 016454 051505 020124 040506
3802 016462 046111 042105 047045
3803 016470          000
3804 016471          045 046101 027125 FMG5: .ASCIZ  /%ALU. MICROTTEST FAILED%N/
3805 016476 046440 041511 047522
3806 016504 042524 052123 043040
3807 016512 044501 042514 022504
3808 016520 000116
3809 016522 040445 040516 051513 FMG7: .ASCIZ  /%ANAKS-NO BUFFER RCV: %D3%A SENT: %D3%N/
3810 016530 047055 020117 052502
3811 016536 043106 051105 020040
3812 016544 041522 035126 022440
3813 016552 031504 040445 051440
3814 016560 047105 035124 022440
3815 016566 031504 047045          000

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L10012:

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MOV    P4,-(SP)
MOV    R3,-(SP)
MOV    #FMG18,-(SP)
MOV    #3,-(SP)
MOV    SP,R0
TRAP   C$PNTB
ADD    #10,SP
TRAP   C$MSG

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3816	016573	045	047101	045501	FMG8:	.ASCIZ	/%ANAKS-BAD DATA	RCV: %D3%A	SENT: %D3%N/
3817	016600	026523	040502	020104					
3818	016606	040504	040524	020040					
3819	016614	051040	053103	020072					
3820	016622	042045	022463	020101					
3821	016630	042523	052116	020072					
3822	016636	042045	022463	000116					
3823	016644	040445	040516	051513	FMG9:	.ASCIZ	/%ANAKS-BAD HEADER	RCV: %D3%A	SENT: %D3%N/
3824	016652	041055	042101	044040					
3825	016660	040505	042504	020122					
3826	016666	041522	035126	022440					
3827	016674	031504	040445	051440					
3828	016702	047105	035124	022440					
3829	016710	031504	047045	000					
3830	016715	045	051101	050105	FMG10:	.ASCIZ	/%AREPS-RCV: %D3%A	SENT: %D3%N/	
3831	016722	026523	041522	035126					
3832	016730	022440	031504	040445					
3833	016736	051440	047105	035124					
3834	016744	022440	031504	047045					
3835	016752	000							
3836	016753	045	054101	044515	FMG11:	.ASCIZ	/%AXMIT BACC OUT COMMAND%N/		
3837	016760	020124	040502	041503					
3838	016766	047440	052125	041440					
3839	016774	046517	040515	042116					
3840	017002	047045	000						
3841	017005	045	051101	053103	FMG12:	.ASCIZ	/%ARCV BACC OUT COMMAND%N/		
3842	017012	041040	041501	020103					
3843	017020	052517	020124	047503					
3844	017026	046515	047101	022504					
3845	017034	000116							
3846	017036	040445	041501	052524	FMG13:	.ASCIZ	/%AACTUAL ADDR. %06%A	ACTUAL COUNT	%D5%N/
3847	017044	046101	020040	040440					
3848	017052	042104	027122	022440					
3849	017060	033117	040445	040440					
3850	017066	052103	040525	020114					
3851	017074	047503	047125	020124					
3852	017102	020040	042045	022465					
3853	017110	000116							
3854	017112	040445	054105	042520	FMG14:	.ASCIZ	/%AEXPECTED ADDR. %06%A	EXPECTED COUNT	%D5%N/
3855	017120	052103	042105	040440					
3856	017126	042104	027122	022440					
3857	017134	033117	040445	042440					
3858	017142	050130	041505	042524					
3859	017150	020104	047503	047125					
3860	017156	020124	042045	022465					
3861	017164	000116							
3862	017166	040445	054105	042520	FMG15:	.ASCIZ	/%AEXPECTED ADDR. %06%N/		
3863	017174	052103	042105	040440					
3864	017202	042104	027122	022440					
3865	017210	033117	047045	000					
3866	017215	045	051101	053103	FMG16:	.ASCIZ	/%ARCV CHAR. COUNT %D5%A	XMIT CHAR. COUNT	%D5%N/
3867	017222	041440	040510	027122					
3868	017230	041440	052517	052116					
3869	017236	022440	032504	040445					
3870	017244	054040	044515	020124					
3871	017252	044103	051101	020056					

3872	017260	047503	047125	020124	
3873	017266	042045	022465	000116	
3874	017274	040445	041522	020126	FMG17: .ASCIZ /%ARCV BUFFER AT %06%A XMIT BUFFER AT %06%N/
3875	017302	052502	043106	051105	
3876	017310	040440	020124	047445	
3877	017316	022466	020101	046530	
3878	017324	052111	041040	043125	
3879	017332	042506	020122	052101	
3880	017340	022440	033117	047045	
3881	017346	000			
3882	017347	045	042101	052101	FMG18: .ASCIZ /%ADATA DIFFERS AT RCV ADDR. %06%A AND XMIT ADDR. %06%N/
3883	017354	020101	044504	043106	
3884	017362	051105	020123	052101	
3885	017370	051040	053103	040440	
3886	017376	042104	027122	022440	
3887	017404	033117	040445	040440	
3888	017412	042116	054040	044515	
3889	017420	020124	042101	051104	
3890	017426	020056	047445	022466	
3891	017434	000116			
3892	017436	040445	042504	044526	FMG19: .ASCIZ /%ADEVICE NOT DMR%N/
3893	017444	042503	047040	052117	
3894	017452	042040	051115	047045	
3895	017460	000			
3896	017461	045	041101	043125	FMG21: .ASCIZ /%ABUFFER STATUS%N%A# OF BUFFERS:%D3%N/
3897	017466	042506	020122	052123	
3898	017474	052101	051525	047045	
3899	017502	040445	020043	043117	
3900	017510	041040	043125	042506	
3901	017516	051522	022472	031504	
3902	017524	047045	000		
3903	017527	045	041101	043125	FMG22: .ASCIZ /%ABUFFER SIZE: %D5%N/
3904	017534	042506	020122	044523	
3905	017542	042532	020072	042045	
3906	017550	022465	000116		
3907	017554	040445	047111	020040	FMG23: .ASCIZ /%AIN - RCV ASSIGNED:%D3%A XMIT ASSIGNED:%D3%N/
3908	017562	020055	041522	020126	
3909	017570	051501	044523	047107	
3910	017576	042105	022472	031504	
3911	017604	040445	020040	054040	
3912	017612	044515	020124	051501	
3913	017620	044523	047107	042105	
3914	017626	022472	031504	047045	
3915	017634	000			
3916	017635	045	047501	052125	FMG24: .ASCIZ /%AOUT - RCV RETURNED:%D3%A XMIT RETURNED:%D3%N/
3917	017642	026440	051040	053103	
3918	017650	051040	052105	051125	
3919	017656	042516	035104	042045	
3920	017664	022463	020101	020040	
3921	017672	046530	052111	051040	
3922	017700	052105	051125	042516	
3923	017706	035104	042045	022463	
3924	017714	000116			
3925					
3926	017716	044524	042515	047440	EMG1: .ASCIZ /TIME OUT/
3927	017724	052125	000		

3928	017727	124	046511	020105	EMG2:	.ASCIZ /TIME OUT - DURING INTERRUPT EXERCISE/
3929	017734	052517	020124	020055		
3930	017742	052504	044522	043516		
3931	017750	044440	052116	051105		
3932	017756	052522	052120	042440		
3933	017764	042530	041522	051511		
3934	017772	000105				
3935	017774	054105	042520	052103	EMG8:	.ASCIZ /EXPECTED CONTROL OUT - NOT RECEIVED/
3936	020002	042105	041440	047117		
3937	020010	051124	046117	047440		
3938	020016	052125	026440	047040		
3939	020024	052117	051040	041505		
3940	020032	044505	042526	000104		
3941	020040	047125	054105	042520	EMG9:	.ASCIZ /UNEXPECTED CONTROL OUT/
3942	020046	052103	042105	041440		
3943	020054	047117	051124	046117		
3944	020062	047440	052125	000		
3945	020067	105	051122	051117	EMG10:	.ASCIZ /ERROR - MULTIPLE XMITS/
3946	020074	026440	046440	046125		
3947	020102	044524	046120	020105		
3948	020110	046530	052111	000123		
3949	020116	052502	043106	051105	EMG11:	.ASCIZ /BUFFER ADDR. ERROR/
3950	020124	040440	042104	027122		
3951	020132	042440	051122	051117		
3952	020140	000				
3953	020141	103	040510	040522	EMG12:	.ASCIZ /CHARACTER COUNT ERROR/
3954	020146	052103	051105	041440		
3955	020154	052517	052116	042440		
3956	020162	051122	051117	000		
3957	020167	105	051122	051117	EMG13:	.ASCIZ /ERROR - MULTIPLE RCVS/
3958	020174	026440	046440	046125		
3959	020202	044524	046120	020105		
3960	020210	041522	051526	000		
3961	020215	122	053103	020104	EMG14:	.ASCIZ /RCVD EXTRA DATA/
3962	020222	054105	051124	020101		
3963	020230	040504	040524	000		
3964	020235	104	052101	020101	EMG15:	.ASCIZ /DATA ERROR/
3965	020242	051105	047522	000122		
3966	020250	047125	054105	042520	EMG16:	.ASCIZ /UNEXPECTED HALT RECEIVED/
3967	020256	052103	042105	044040		
3968	020264	046101	020124	042522		
3969	020272	042503	053111	042105		
3970	020300	000				
3971	020301	103	047117	051124	EMG17:	.ASCIZ /CONTROL IN PROBLEM - IN INTERRUPT ROUTINE/
3972	020306	046117	044440	020116		
3973	020314	051120	041117	042514		
3974	020322	020115	020055	047111		
3975	020330	044440	052116	051105		
3976	020336	052522	052120	051040		
3977	020344	052517	044524	042516		
3978	020352	000				
3979	020353	123	052520	044522	EMG18:	.ASCIZ /SPURIOUS RDO INTERRUPT/
3980	020360	052517	020123	042122		
3981	020366	020117	047111	042524		
3982	020374	051122	050125	000124		
3983	020402	034115	030062	020067	EMG19:	.ASCIZ /M8207 PROGRAM TIMER OUT OF RANGE/

3984	020410	051120	043517	040522
3985	020416	020115	044524	042515
3986	020424	020122	052517	020124
3987	020432	043117	051040	047101
3988	020440	042507	000	
3989		020444		

.EVEN


```
4010 .SBTTL INITIALIZE SECTION
4011
4012 :////////////////////////////////////////////////////////////////////
4013 :// THE INITIALIZE SECTION CONTAINS THE CODING THAT IS PERFORMED
4014 :// AT THE BEGINNING OF THE TEST SEQUENCE ON THE NEXT UNIT.
4015 :////////////////////////////////////////////////////////////////////
4016
4017 BGNINIT
4018
4019
4020 L$INIT::
4021 SETPRI #PRI07 ;SET DIAGNOSTIC PRIORITY = 7
4022
4023 MOV #PRI07,R0
4024 TRAP C$SPRI
4025
4026 MOV SP,PSTACK ;STORE BASE LEVEL PROGRAM STACK POINTER
4027 CLR SUBRPC ;CLEAR STORAGE WORD FOR SUBROUTINE PC CALL
4028 CLR ERROR ;CLEAR ERROR FLAGS
4029 CLR RESUME ;CLEAR FLAG USED TO ALLOW BASE IN - RESUME.
4030 CLR DMCMDL ;CLEAR FLAG USED TO INDICATE DMC MODE
4031 CLR CLRNO ;CLEAR WORD USED TO RUN MICRO TESTS ON
4032 ;EVERY OTHER MASTER CLEAR.
4033 CLR NXMFLG ;FLAG USED TO MARK A NXM DMR ADDRESS.
4034 TST FRSTIM ;IS THIS THE TIME THROUGH AFTER LOAD?
4035 BNE 1$ ;IF NOT - ERROR TRAP VECTOR ALREADY SAVED
4036 MOV #1,FRSTIM ;FLAG THAT WE'VE BEEN THRU THE 1ST TIME
4037 CLR FRSPAS ;CLEAR COUNTER FOR # OF PASSES AFTER LOAD
4038
4039 1$:
4040 CLRVEC #4 ;ENSURE VECTOR 4 IS IN NORMAL STATE.
4041
4042 MOV #4,R0
4043 TRAP C$CVEC
4044
4045 READEF #EF.START ;IS THIS JUST STARTED?
4046
4047 MOV #EF.START,R0
4048 TRAP C$REFG
4049
4050 BCOMPLETE STARST ;IF YES - BRANCH.
4051
4052 BCS STARST
4053
4054 READEF #EF.RESTART ;IS THIS A RESTART ?
4055
4056 MOV #EF.RESTART,R0
4057 TRAP C$REFG
4058
4059 BCOMPLETE STARST ;IF YES - BRANCH.
4060
4061 BCS STARST
4062
4063 READEF #EF.NEW ;IS THIS A NEW PASS?
4064
4065 MOV #EF.NEW,R0
4066 TRAP C$REFG
4067
4068 BCOMPLETE NEWST ;IF YES - BRANCH
4069
4070 BCS NEWST
4071
4072 READEF #EF.CONTINUE ;IS THIS A CONTINUATION?
4073
4074 MOV #EF.CONTINUE,R0
4075 TRAP C$REFG
4076
4077 BNCOMPLETE GETPRM ;IF NOT - GET PARAMETERS
4078
4079 BCC GETPRM
4080
4081 JMP END ;OTHERWISE - DON'T INITIALIZE.
4082
4083 STARST:
4084 CLR STARES ;CLEAR THE FLAG TO SHOW START/RESTART.
4085
```

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4066 020612          NEWST:
4067 020612 012737 177777 002370  MOV    #-1,LOGDEV    ;INITIALIZE LOGICAL UNIT NUMBER.
4068 020620 005237 002266          INC    FRSPAS        ;INCREMENT # OF PASSES AFTER LOAD.
4069 020624 005237 002270          INC    STARES        ;INCREMENT # OF PASSES SINCE START/RESTART.
4070 020630          GETPRM:
4071 020630 005237 002370          INC    LOGDEV        ;NEXT LOGICAL UNIT TO BE TESTED
4072 020634 023737 002370 002012  CMP    LOGDEV,L$UNIT ;IS THE MAXIMUM UNIT # EXCEEDED?
4073 020642 002363          BGE    NEWST         ;IF YES - DO A NEW START
4074 020644          GPWARD LOGDEV,R1    ;GET THE P-TABLE POINTER INTO R1
4075 020644 013700 002370          MOV    LOGDEV,R0
4076 020650 104442          TRAP  C$GPWRD
4077 020652 010001          MOV    R0,R1
4078 020654          BNCOMPLETE GETPRM    ;IF NOT AVAILABLE, GET THE NEXT ONE
4079 020654 103365          BCC   GETPRM
4080 020656 012137 002252          MOV    (R1)+,WTYPE   ;MICROPROCESSOR TYPE
4081 020662 011100          MOV    (R1),R0      ;SAVE THE ADDRESS
4082 020664 032700 000007          BIT    #7,R0        ;DOES THIS DEVICE ADDRESS END IN NON-ZERO?
4083 020670 001414          BEQ    10$         ;IF NOT - OK (76XXX0)
4084 020672 042711 000007          BIC    #7,(R1)      ;MAKE IT 76XXX0
4085 020676          PRINTB #FINIT1,(R1),R0 ;INFORM THE USER
4086 020676 010046          MOV    R0,-(SP)
4087 020700 011146          MOV    (R1),-(SP)
4088 020702 012746 021512          MOV    #FINIT1,-(SP)
4089 020706 012746 000003          MOV    #3,-(SP)
4090 020712 010600          MOV    SP,R0
4091 020714 104414          TRAP  C$PNTB
4092 020716 062706 000010          ADD    #10,SP
4093 020722          10$:
4094 020722 011137 002232          MOV    (R1),CSR     ;CSR ADDRESS
4095 020726 011137 002242          MOV    (R1),BSEL1
4096 020732 005237 002242          INC    BSEL1        ;HIGH BYTE ADDRESS OF CSR
4097 020736 011137 002234          MOV    (R1),SEL2
4098 020742 062737 000002 002234  ADD    #2,SEL2      ;CONTROL OUT REGISTER ADDRESS
4099 020750 011137 002244          MOV    (R1),BSEL3
4100 020754 062737 000003 002244  ADD    #3,BSEL3     ;HIGH BYTE OF SEL2
4101 020762 011137 002236          MOV    (R1),SEL4
4102 020766 062737 000004 002236  ADD    #4,SEL4     ;PORT REG (SEL 4) ADDRESS
4103 020774 011137 002246          MOV    (R1),BSEL5
4104 021000 062737 000005 002246  ADD    #5,BSEL5     ;HIGH BYTE OF SEL4
4105 021006 011137 002240          MOV    (R1),SEL6
4106 021012 062737 000006 002240  ADD    #6,SEL6     ;PORT REG (SEL 6) ADDRESS
4107 021020 012137 002250          MOV    (R1)+,BSEL7
4108 021024 062737 000007 002250  ADD    #7,BSEL7     ;HIGH BYTE OF SEL6
4109 021032 011100          MOV    (R1),R0      ;GET VECTOR
4110 021034 032700 000007          BIT    #7,R0        ;DOES THIS VECTOR END IN NON-ZERO?
4111 021040 001414          BEQ    11$         ;IF NOT - OK (XX0)
4112 021042 042711 000007          BIC    #7,(R1)      ;MAKE IT XX0
4113 021046          PRINTB #FINIT2,(R1),R0 ;INFORM THE USER
4114 021046 010046          MOV    R0,-(SP)
4115 021050 011146          MOV    (R1),-(SP)
4116 021052 012746 021601          MOV    #FINIT2,-(SP)
4117 021056 012746 000003          MOV    #3,-(SP)
4118 021062 010600          MOV    SP,R0
4119 021064 104414          TRAP  C$PNTB
4120 021066 062706 000010          ADD    #10,SP
4121 021072          11$:

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4178
4179 021260 022737 000001 002254      CMP      #1,DMTURN      ;BE THE INTERFACE SELECTED.
4180 021266 001004                    BNE      20$           ;IS V.35 REQUESTED?
4181 021270 042737 000020 002304      BIC      #BIT4,AX3     ;IF NOT - CONTINUE
4182 021276 000427                    BR       30$           ;SELECT V.35
4183 021300
4184 021300 022737 000002 002254      CMP      #2,DMTURN     ;IS INTEGRAL REQUESTED?
4185 021306 001004                    BNE      22$           ;IF NOT - CONTINUE.
4186 021310 042737 000010 002304      BIC      #BIT3,AX3     ;SELECT INTEGRAL MODEM.
4187 021316 000417                    BR       30$
4188 021320
4189 021320 022737 000003 002254      CMP      #3,DMTURN     ;IS EIA REQUESTED?
4190 021326 001004                    BNE      25$           ;IF NOT - CONTINUE.
4191 021330 042737 000100 002304      BIC      #BIT6,AX3     ;SELECT EIA(XYZ).
4192 021336 000407                    BR       30$
4193 021340
4194 021340 022737 000004 002254      CMP      #4,DMTURN     ;IS RS422 REQUESTED?
4195 021346 001007                    BNE      35$           ;IF NOT, DON'T ALLOW INTERFACE CHANGE.
4196 021350 042737 000200 002304      BIC      #BIT7,AX3     ;SELECT RS422.
4197 021356
4198 021356 012737 000001 002262      MOV      #1,INFACE     ;SET FLAG THAT ALLOWS INTERFACE CHANGE.
4199 021364 000404                    BR       40$
4200 021366
4201 021366 005037 002262                    CLR      INFACE        ;CLEAR FLAG - NO INTERFACE CHANGE.
4202 021372 005037 002304                    CLR      AX3           ;CLEAR AX3 BITS
4203 021376
4204
4205 021376 005737 002310                    TST      MANUF         ;*****
4206 021402 001410                    BEQ      42$           ;IS THIS A SPECIAL MANUFACTURING TEST CON.?
4207
4208 021404 022737 000001 002254      CMP      #1,DMTURN     ;*****
4209 021412 001430                    BEQ      45$           ;IS THIS V.35 WITH SPECIAL CONNECTOR?
4210 021414 022737 000003 002254      CMP      #3,DMTURN     ;IF YES - SET WRITE MAINT. BIT
4211 021422 001424                    BEQ      45$           ;IS THIS EIA WITH SPECIAL CONNECTOR?
4212 021424
4213 021424 022737 000006 002254      CMP      #6,DMTURN     ;IF YES - SET WRITE MAINT. BIT
4214 021432 001420                    BEQ      45$
4215 021434 022737 000007 002254      CMP      #7,DMTURN     ;IS THIS LOCAL LOOP?
4216 021442 001020                    BNE      50$           ;IF YES - SET WRITE MAINT. BIT.
4217 021444 022737 000001 002270      CMP      #1,STARES     ;IS THIS REMOTE LOOP?
4218 021452 001010                    BNE      45$           ;IF NOT - CLEAR MAINT. BIT FLAG
4219 021454                    PRINTB  #FINIT3        ;IS THIS THE FIRST PASS?
4220 021454 012746 021670                    ;IF NOT - SKIP MESSAGE
4221 021460 012746 000001                    ;WARN TO USE ONLY TESTS 17-19
4222 021464 010600                    MOV      #FINIT3,-(SP)
4223 021466 104414                    MOV      #1,-(SP)
4224 021470 062706 000004                    MOV      SP,RO
4225
4226 021474
4227 021474 012737 000001 002306      MOV      #1,WMAINT     ;SET FLAG TO WRITE MODEM MAINTENANCE BITS.
4228 021502 000402                    BR       END
4229 021504
4230 021504 005037 002306                    CLR      WMAINT        ;CLEAR FLAG - DON'T WRITE MAINT. 1 OR 2.
4231 021510
4232 021510
4233 021510
  
```

L10014:

4234	021510	104411									TRAP	C\$INIT
4235	021512	040445	025052	053440	FINIT1: .ASCIZ	/%A** WARNING - WILL ASSUME ADDRESS	%06%A (NOT	%06%A)%N/				
4236	021520	051101	044516	043516								
4237	021526	026440	053440	046111								
4238	021534	020114	051501	052523								
4239	021542	042515	040440	042104								
4240	021550	042522	051523	022440								
4241	021556	033117	040445	024040								
4242	021564	047516	020124	047445								
4243	021572	022466	024501	047045								
4244	021600	000										
4245	021601	045	025101	020052	FINIT2: .ASCIZ	/%A** WARNING - WILL ASSUME VECTOR	%03%A (NOT	%03%A)%N/				
4246	021606	040527	047122	047111								
4247	021614	020107	020055	044527								
4248	021622	046114	040440	051523								
4249	021630	046525	020105	042526								
4250	021636	052103	051117	020040								
4251	021644	047445	022463	020101								
4252	021652	047050	052117	022440								
4253	021660	031517	040445	022451								
4254	021666	000116										
4255	021670	040445	025052	053440	FINIT3: .ASCIZ	/%A** WITH REMOTE LOOPBACK USE TESTS	17 - 19 ONLY	**%N/				
4256	021676	052111	020110	042522								
4257	021704	047515	042524	046040								
4258	021712	047517	041120	041501								
4259	021720	020113	051525	020105								
4260	021726	042524	052123	020123								
4261	021734	033461	026440	030440								
4262	021742	020071	047117	054514								
4263	021750	025040	022452	000116								
4264												

.EVEN

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.SBTTL AUTO DROP UNIT SECTION

:/ THE AUTO DROP CODING DETERMINES WHETHER OR NOT THE DEVICE WHOSE P-TABLE
:/ WAS JUST OBTAINED IS READY FOR TESTING, AND IT IS DROPPED IF NOT READY.

BGNAUTO

L\$AUTO::

SETVEC #4,#NOXMEM,#PRI07 ;SET UP NON -EXISTENT MEMORY TRAP VECTOR.

MOV #PRI07,-(SP)
MOV #NOXMEM,-(SP)
MOV #4,-(SP)
MOV #3,-(SP)
TRAP C\$SVEC
ADD #10,SP

CLR NXMFLG ;CLEAR FLAG THAT WILL BE SET IF NXM OCCURS.
TST @CSR ;REFERENCE MEMORY ADDRESS FOR THE DEVICE
;TO SEE IF IT EXISTS.

:/ *****
:/ IF THE DEVICE DOESN'T EXIST THE RESULTANT TRAP TO VECTOR 04 WILL
:/ CAUSE THE DEVICE TO BE DROPPED (SEE INTERRUPT ROUTINE 'DROPO4').
:/ OTHERWISE THE MEMORY REFERENCE IS UNEVENTFUL AND THE DEVICE IS READY.
:/ *****

CLRVEC #4 ;RETURN VECTOR 04 TO NORMAL STATE

MOV #4,RO
TRAP C\$CVEC

TST NXMFLG ;DID NXM OCCUR?
BEQ 1\$;IF NOT EXIT
DODU LOGDEV ;DROP THE DEVICE

MOV LOGDEV,RO
TRAP C\$DODU

DOCLN ;DO CLEAN UP - FORCE BACK TO INIT CODE.

TRAP C\$DCLN

CLR NXMFLG ;RESTORE FLAG.

1\$:
ENDAUTO

L10015:

TRAP C\$AUTO

```
4314 .SBTTL CLEANUP CODING SECTION
4315
4316 :////////////////////////////////////////////////////////////////////
4317 :// THE CLEANUP CODING SECTION CONTAINS THE CODING THAT IS PERFORMED AT THE
4318 :// END OF THE TEST SEQUENCE ON A PARTICULAR UNIT. THIS SECTION IS REQUIRED
4319 :// EVEN IF IT IS A NULL CLEANUP
4320 :////////////////////////////////////////////////////////////////////
4321
4322 022046 BGNCLN
4323 022046 L$CLEAN::
4324
4325 022046 005737 002352 TST NXMFLG ;WAS THERE A NXM ERROR?
4326 022052 001003 BNE 10$ ;IF YES - SKIP MASTER CLEAR.
4327 022054 012777 040000 160150 MOV #MCLR,@SELO ;ISSUE A MASTER CLEAR.
4328 022062 10$:
4329
4330 022062 ENDCLN
4331 022062
4332 022062 104412 L10016: TRAP C$CLEAN
4333
4334
4335
```

```
4336 .SBTTL GLOBAL INTERRUPT HANDLING ROUTINES
4337
4338 ;////////////////////////////////////
4339 ;// THE INTERRUPT HANDLING SECTION CONTAINS CODING REQUIRED TO USE
4340 ;// THE 'SETVEC' MACRO. NOTE EVERY INTERRUPT ROUTINE SHOULD SAVE
4341 ;// AND RESTORE R0.
4342 ;////////////////////////////////////
4343
4344 022064 BGNSRV INISR ;INPUT INTERRUPT SERVICE ROUTINE
4345 022064 ; INISR::
4346 022064 010046 MOV R0,-(SP) ;SAVE R0
4347 022066 010146 MOV R1,-(SP) ;SAVE R1
4348 022070 017701 160136 MOV @SELO,R1 ;SAVE THE CONTROL IN COMMAND.
4349 022074 042701 177760 BIC #177760,R1 ;CLEAR ALL BUT THE COMMAND BITS (0-3)
4350 022100 032777 000200 160124 BIT #RDI,@SELO ;IS RDI SET
4351 022106 001002 BNE 1$ ;IF YES - PROCESS INPUT COMMAND.
4352 022110 000137 022620 JMP NEXT ;ISSUE NEXT INPUT COMMAND.
4353 ;*****
4354 ;
4355 ; PROCESS INPUT COMMAND
4356 ;
4357 ;*****
4358 022114 1$:
4359 022114 022701 000004 CMP #BACCR,R1 ;IS THIS A RCV. BA/CC?
4360 022120 001533 BEQ 29$ ;BR IF YES.
4361 022122 022701 000000 CMP #BACCT,R1 ;IS THIS A XMIT. BA/CC?
4362 022126 001537 BEQ 30$ ;BR IF YES.
4363 022130 022701 000003 CMP #BASEI,R1 ;IS THIS A BASE IN?
4364 022134 001461 BEQ 20$ ;BR IF YES.
4365 022136 022701 000001 CMP #CNTRL,R1 ;IS THIS A CONTROL IN?
4366 022142 001444 BEQ 15$ ;BR IF YES.
4367 022144 022701 000005 CMP #WMODEM,R1 ;IS THIS A WRITE MODEM?
4368 022150 001417 BEQ 10$ ;BR IF YES.
4369 022152 022701 000015 CMP #INTER,R1 ;IS THIS AN INTERFACE WRITE.
4370 022156 001410 BEQ 5$ ;BR IF YES.
4371 022160 022701 000002 CMP #HLT,R1 ;IS THIS A HALT?
4372 022164 001572 BEQ 70$ ;EXIT - IF YES (NOTHING TO SET UP)
4373 022166 ERRDF 17,EMG17,ERRG2 ;PROBLEM IF IT'S NOT ONE OF THE ABOVE.
4374 022166 104455 TRAP C$ERDF
4375 022170 000021 .WORD 17
4376 022172 020301 .WORD EMG17
4377 022174 015124 .WORD ERRG2
4378 022176 000565 BR 70$ ;EXIT
4379
4380 022200 5$:
4381 ;
4382 ; WRITE AX3-15
4383 ;
4384 022200 113777 002304 160042 MOVB AX3,@BSEL7 ;WRITE NECESSARY AX3-15 INTERFACE.
4385 ;AX3 HAS BEEN DETERMINED IN THE INIT
4386 ;CODE.
4387 022206 000561 BR 70$
4388
4389 022210 10$:
4390 ;
4391 ;
```

```

4392 ; MODEM WRITE
4393 ;
4394 022210 022737 000006 002254 CMP #LLOOP,DMTURN ;IS LOCAL MODIEM LOOPBACK DESIRED?
4395 022216 001007 BNE 11$ ;BR IF NOT
4396 022220 042777 000004 160012 BIC #MAINT2,@SEL6 ;ENSURE REMOTE LOOPBACK IS CLEAR.
4397 022226 052777 000110 160004 BIS #DTR.MAINT1,@SEL6 ;SET MAINTENANCE 1 BIT AND DTR.
4398 022234 000546 BR 70$
4399 022236 11$:
4400 022236 042777 000010 157774 BIC #MAINT1,@SEL6 ;ENSURE LOCAL LOOPBACK IS CLEAR.
4401 022244 052777 000104 157766 BIS #DTR!MAINT2,@SEL6 ;SET MAINTENANCE 2 BIT AND DTR.
4402 022252 000537 BR 70$ ;CLEAR RQI
4403 022254 15$:
4404 ;
4405 ;CONTROL IN
4406 ;
4407 022254 005737 002300 TST MNTMDE ;IS MAINTENANCE MODE REQUESTED
4408 022260 001404 BEQ 17$ ;BR IF NOT
4409 022262 012777 000400 157750 MOV #MAINT,@SEL6 ;REQUEST MAINT. MODE
4410 022270 000530 BR 70$
4411 022272 17$:
4412 022272 005077 157742 CLR @SEL6 ;FULL DUPLEX - NON-MAINT. MODE.
4413 022276 000525 BR 70$
4414 022300 20$:
4415 ;
4416 ;BASE IN
4417 ;
4418 022300 012777 002640 157730 MOV #BASE,@SEL4 ;BASE TABLE ADDRESS.
4419 ;
4420 022306 005737 002276 TST DMCMDMDE ;ARE WE IN DMC MODE?
4421 022312 001004 BNE 22$ ;BR IF YES
4422 022314 012777 000522 157716 MOV #DMR,@SEL6 ;DMR MODE.
4423 022322 000402 BR 23$ ;CHECK LOOPBACK.
4424 022324 22$:
4425 022324 005077 157710 CLR @SEL6 ;DMC MODE
4426 022330 23$:
4427 022330 005737 002272 TST START ;IS THIS THE FIRST BASE IN?
4428 022334 001004 BNE 24$ ;IF NOT - SET RESUME.
4429 022336 052777 000100 157670 BIS #IEO,@SEL2 ;ON FIRST BASE IN SET RDO INT.ENABLE.
4430 022344 000406 BR 24$
4431 022346 24$:
4432 022346 052777 010000 157664 BIS #RES,@SEL6 ;SET RESUME
4433 022354 012737 177777 002360 MOV #-1,RESFLG ;FLAG THAT THIS IS A BASE IN RESUME COMMAND
4434 ;(THIS WILL BE USED LATER IN THIS ISR TO
4435 ;DECIDE WHAT THE NEXT COMMAND WILL BE)
4436 022362 25$:
4437 022362 005737 002254 TST DMTURN ;IS INTERNAL LOOPACK REQUESTED?
4438 022366 001004 BNE 27$ ;BR IF NOT - CLEAR LU LOOP
4439 022370 052777 004000 157634 BIS #LPLU,@SEL0 ;SET THE LINE UNIT LOOPBACK BIT
4440 022376 000465 BR 70$ ;CLEAR RQI AND EXIT.
4441 022400 27$:
4442 022400 042777 004000 157624 BIC #LPLU,@SEL0 ;CLEAR LINE UNIT LOOPBACK (CONNECTOR OR
4443 ;CABLE)
4444 022406 000461 BR 70$ ;CLEAR RQI AND EXIT
4445 ;
4446 ;
4447 ;BA/CC IN RCV
    
```

```

4448
4449 022410
4450 022410 005337 002326
4451 022414 012277 157616
4452 022420 012277 157614
4453 022424 000406
4454
4455
4456
4457 022426
4458 022426 005337 002330
4459 022432 012377 157600
4460 022436 012377 157576
4461 022442
4462 022442 005737 002302
4463 022446 001441
4464
4465 022450 052777 040000 157562
4466
4467 022456 010246
4468 022460 017702 157552
4469 022464 042777 160000 157544
4470
4471 022472 042702 017777
4472 022476 022702 060000
4473 022502 001421
4474 022504 022702 100000
4475 022510 001004
4476 022512 052777 020000 157516
4477
4478 022520 000412
4479 022522
4480 022522 022702 120000
4481 022526 001004
4482 022530 052777 040000 157500
4483
4484 022536 000403
4485 022540
4486 022540 052777 060000 157470
4487
4488 022546
4489 022546 012602
4490 022550 000400
4491
4492
4493 022552
4494 022552 010137 002364
4495
4496 022556 005737 002276
4497 022562 001011
4498
4499
4500 022564 012601
4501 022566 012600
4502 022570 052777 000020 157434
4503 022576 042777 000040 157426

```

```

:
29$: DEC INRCV ;DECREMENT COUNTER
MOV (R2)+,@SEL4 ;RCV BUFFER ADDRESS
MOV (R2)+,@SEL6 ;RCV CHARACTER COUNT
BR 40$
:
:BA/CC IN XMIT
:
30$: DEC INXMIT ;DECREMENT COUNTER
MOV (R3)+,@SEL4 ;XMIT BUFFER ADDRESS.
MOV (R3)+,@SEL6 ;XMIT CHARACTER COUNT.
40$: TST MMANAG ;ARE THE BUFFERS MEMORY MANAGED?
BEQ 70$ ;IF NOT SKIP CONVERTING VIRTUAL ADDR
;TO PHYSICAL ADDR.
BIS #BIT14,@SFL6 ;SET BIT 16 OF PHYSICAL ADDRESS (I.E.
;VIRTUAL ADDR 60000 = PHYSICAL ADDR 200000
MOV R2,-(SP) ;SAVE R2 (NEXT RCV BUFFER ADDRESS)
MOV @SEL4,R2 ;SAVE THE VIRTUAL ADDRESS.
BIC #160000,@SEL4 ;CLEAR BITS CORRESPONDING TO THE PAGE #
;IN THE VIRTUAL ADDRESS.
BIC #17777,R2 ;SAVE ONLY THE PAGE # IN THE SAVED ADDR.
CMP #60000,R2 ;IS THIS PAGE 3?
BEQ 44$ ;IF YES, PHYSICAL ADDRESS CALCULATED
CMP #100000,R2 ;IS THIS PAGE 4?
BNE 41$ ;IF NOT SEE IF IT'S PAGE 4 OR 5
BIS #BIT13,@SEL4 ;SET BIT FOR PHYSICAL ADDR (I.E. VIRTUAL
;ADDR 100000 = PHYSICAL ADDR. 220000
BR 44$
41$: CMP #120000,R2 ;IS THIS PAGE 4?
BNE 42$ ;IF NOT, MUST BE PAGE 5.
BIS #BIT14,@SEL4 ;SET BIT FOR PHYSICAL ADDR (I.E. VIRTUAL
;ADDR 120000 = PHYSICAL ADDR. 240000
BR 44$
42$: BIS #BIT14!BIT13,@SEL4 ;SET BIT FOR PHYSICAL ADDR (I.E. VIRTUAL
;ADDR 140000 = PHYSICAL ADDR. 260000
44$: MOV (SP)+,R2 ;RESTORE R2 (NEXT RCV BUFFER ADDRESS)
BR 70$ ;CLEAR RQI AND EXIT
70$: MOV R1,LAST ;SAVE THE INPUT COMMAND (USED
;TO DETERMINE NEXT INPUT COMMAND)
TST DMCMDL ;ARE WE IN DMC MODE?
BNE 80$ ;IF YES - DON'T USE IECLR
;NOTE: INTERRUPT CAPABILITY FOR RQI
;CLEAR IS ONLY AVAILABLE IN DMR MODE.
MOV (SP)+,R1 ;RESTORE R1
MOV (SP)+,R0 ;RESTORE R0
BIS #IECLR,@SELO ;SET INTERRUPT ENABLE FOR RDI CLEAR.
BIC #RQI,@SELO ;CLEAR RQI - INT. GENERATED WHEN RDI

```

```

4504                                     ;CLEARS IN RESPONSE.
4505 022604 000002                       RTI                               ;RETURN AND WAIT FOR RQI CLEAR INTERRUPT.
4506
4507 022606                               80$:
4508 022606 042777 000020 157416       BIC    #IECLR,@SELO ;ENSURE INTERRUPT ENABLE FOR RDI CLEAR IS CLR.
4509 022614                               CALL   $CLRQI        ;CLEAR RQI AND WAIT FOR RDI TO CLEAR.
4510                                     ;*****
4511                                     ;
4512                                     ; RDI CLEAR - DETERMINE NEXT INPUT COMMAND.
4513                                     ;
4514                                     ;*****
4515 022620                               NEXT:
4516 022620 022737 000002 002364       CMP    #HLT, LAST   ;WAS THE LAST COMMAND A HALT?
4517 022626 001011                       BNE    110$         ;IF NOT - PROCEED.
4518 022630 005737 002274               TST    RESUME       ;ARE WE TESTING RESUME?
4519 022634 001537                       BEQ    170$         ;IF NOT, DON'T ISSUE ANOTHER COMMAND.
4520 022636 005737 002354               TST    INFLAG       ;INPUT BUFFER DONE?
4521 022642 001403                       BEQ    110$         ;IF NOT, PROCEED.
4522 022644 005737 002356               TST    OUTFLG       ;OUTPUT BUFFER DONE?
4523 022650 001131                       BNE    170$         ;IF YES, DON'T ISSUE ANOTHER COMMAND.
4524 022652                               110$:
4525 022652 005737 002276               TST    DMCMD E      ;ARE WE IN DMC MODE?
4526 022656 001404                       BEQ    111$         ;IF NOT (DMR MODE) - CHECK INTERFACE.
4527 022660 005737 002272               TST    START        ;HAVE WE DONE A CONTROL IN?
4528 022664 001434                       BEQ    130$         ;IF NOT, DO IT.
4529 022666 000441                       BR     150$         ;OTHERWISE - KEEP CHECKING ON NEXT COMMAND.
4530 022670                               111$:
4531 022670 022737 000003 002364       CMP    #BASEI, LAST ;WAS THE LAST COMMAND A BASE IN ?
4532 022676 001411                       BEQ    115$         ;IF YES - SEE IF INTER. OR M. WRITE IS NEEDED?
4533 022700 022737 000015 002364       CMP    #INTER, LAST ;WAS THE LAST COMMAND AN AX3-15 WRITE?
4534 022706 001414                       BEQ    117$         ;IF YES - CHECK FOR MODEM WRITE.
4535 022710 022737 000005 002364       CMP    #WMODEM, LAST ;WAS THE LAST COMMAND A WRITE MODEM.
4536 022716 001417                       BEQ    130$         ;IF YES - ISSUE A CONTROL IN.
4537 022720 000424                       BR     150$         ;KEEP CHECKING FOR NEXT COMMAND.
4538 022722                               115$:
4539 022722 005737 002262               TST    INFACE       ;IS AN AX3-15 WRITE NEEDED?
4540 022726 001404                       BEQ    117$         ;BR IF NOT
4541 022730 112777 000155 157274       MOV B #IESET!RQI!INTER,@BSELO ;ISSUE AN AX3-15 WRITE COMMAND.
4542 022736 000476                       BR     170$
4543 022740                               117$:
4544 022740 005737 002306               TST    WMAINT        ;WRITE MAINT 1 OR 2?
4545 022744 001404                       BEQ    130$         ;IF NOT - SKIP WRITE MODEM COMMAND.
4546 022746 112777 000145 157256       MOV B #IESET!RQI!WMODEM,@BSELO ;ISSUE A MODEM WRITE COMMAND
4547 022754 000467                       BR     170$
4548 022756                               130$:
4549 022756 005237 002272               INC    START        ;SET FLAG THAT CONTROL IN WAS ISSUED.
4550 022762 112777 000141 157242       MOV B #IESET!RQI!CNTRL,@BSELO ;ISSUE A CONTROL IN
4551 022770 000461                       BR     170$
4552 022772                               150$:
4553 022772 005737 002326               TST    INRCV        ;ARE ALL THE BA/CC IN RCVS DONE?
4554 022776 001424                       BEQ    160$         ;IF YES - BR TO SEE IF XMIT S DONE.
4555 023000 005737 002274               TST    RESUME       ;IS A TEST OF RESUME REQUESTED?
4556 023004 001415                       BEQ    153$         ;BR IF NOT.
4557 023006 032737 000001 002326       BIT    #BIT0, INRCV ;IS THIS AN ODD COUNT?
4558 023014 001411                       BEQ    153$         ;BR IF NOT.
4559 023016 005737 002360               TST    RESFLG       ;WAS THE LAST COMMAND A BASE IN RESUME?
    
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4560 023022 001004          BNE      152$          ;IF YES, ISSUE BA/CC
4561                          ;HALT - TO TEST RESUME. NOTE: THIS WILL
4562                          ;OCCUR ONLY WHEN RESUME IS REQUESTED,
4563                          ;FOLLOWING EVERY OTHER BA/CC
4564                          ;COMMAND (NEVER FOLLOWING A RESUME)
4565 023024 112777 000142 157200  MOVB   #IESET!RQI!HLT,@BSELO ;HALT IT
4566 023032 000440          BR      170$
4567 023034          152$:
4568 023034 005037 002360          CLR      RESFLG          ;CLEAR FLAG.
4569 023040          153$:
4570 023040 112777 000144 157164  MOVB   #IESET!RQI!BACCR,@BSELO ;ISSUE A BA/CC IN RCV. COMMAND.
4571 023046 000432          BR      170$
4572 023050          160$:
4573 023050 005737 002330          TST     INXMIT          ;ARE ALL THE BA/CC IN XIMITS DONE?
4574 023054 001424          BEQ     165$          ;IF YES, SET THE FLAG
4575 023056 005737 002274          TST     RESUME          ;IS A TEST OF RESUME REQUESTED?
4576 023062 001415          BEQ     163$          ;BR IF NOT.
4577 023064 032737 000001 002330  BIT     #BIT0,INXMIT    ;IS THIS AN ODD COUNT?
4578 023072 001411          BEQ     163$          ;BR IF NOT.
4579 023074 005737 002360          TST     RESFLG          ;WAS THE LAST COMMAND A BASE IN RESUME?
4580 023100 001004          BNE     162$          ;IF YES, ISSUE BA/CC
4581                          ;HALT - TO TEST RESUME. NOTE: THIS WILL
4582                          ;OCCUR ONLY WHEN RESUME IS REQUESTED,
4583                          ;FOLLOWING EVERY OTHER BA/CC
4584                          ;COMMAND (NEVER FOLLOWING A RESUME)
4585 023102 112777 000142 157122  MOVB   #IESET!RQI!HLT,@BSELO ;HALT IT
4586 023110 000411          BR      170$
4587 023112          162$:
4588 023112 005037 002360          CLR      RESFLG          ;CLEAR BASE IN RESUME FLAG.
4589 023116          163$:
4590 023116 112777 000140 157106  MOVB   #IESET!RQI!BACCT,@BSELO ;ISSUE A BA/CC IN XMIT COMMAND.
4591 023124 000403          BR      170$
4592 023126          165$:
4593 023126 012737 177777 002354  MOV     #-1,INFLAG      ;FLAG THAT ALL BA/CC INS DONE.
4594
4595          170$:
4596 023134          MOV     (SP)+,R1          ;RESTORE R1
4597 023136 012601          MOV     (SP)+,R0          ;RESTORE R0
4598
4599          ENDSRV
4600 023140
4601 023140 000002          L10017:
4602                          RTI
4603
4604          ;*****
4605          ;*****
4606          BGNSRV  OUTISR          ;OUTPUT INTERRUPT SERVICE ROUTINE
4607          OUTISR::
4608          MOV     R0,-(SP)          ;SAVE R0
4609 023142 010046          BIT     #RDO,@SEL2      ;IS THE RDO OUT BIT SET?
4610 023144 032777 000200 157062  BNE     5$              ;IF YES - OK TO PROCEED.
4611 023152 001006          ;OTHERWISE REPORT SPURIOUS INTERRUPT
4612 023154          ERRDF  18,EMG18,ERRG2
4613 023154 104455          TRAP   C$ERDF
4614 023156 000022          .WORD  18
4615 023160 020353          .WORD  EMG18
  
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4616 023162 015124                                     .WORD  ERRG2
4617 023164 000137 023566
4618 023170
4619 023170 032777 000001 157036 5$:  JMP 60$
4620 023176 001452
4621 023200 032777 001000 157032 BIT #CNTRL,@SEL2 ;IS THIS A CONTROL OUT
4622 023206 001013 BNE 10$ ;IF NOT - PROCESS BA/CC OUT
4623 023210 032777 000040 157022 BIT #HALTC,@SEL6 ;IS THIS CONTROL OUT A HALT?
4624 023216 001407 BEQ 10$ ;IF IT IS - SEE IF WE SHOULD RESUME.
4625 023220 000137 023616 JMP 65$ ;IS THIS DMR RUN MODE ACKNOWLEDGE?
4626 023224 7$: ERRDF 9,EMG9,ERRG2 ;IF NOT - REPORT ERROR
4627 023224 ;EXIT
4628 023224 104455 TRAP C$ERDF
4629 023226 000011 .WORD 9
4630 023230 020040 .WORD EMG9
4631 023232 015124 .WORD  ERRG2
4632 023234 000570
4633 023236 10$: BR 65$ ;EXIT ROUTINE
4634 023236 005737 002354 TST INFLAG ;ARE THE INPUTS DONE?
4635 023242 001403 BEQ 15$ ;BR IF NOT
4636 023244 005737 002356 TST OUTFLG ;ARE THE OUTPUTS DONE?
4637 023250 001146 BNE 60$ ;IF YES - ALL DONE, EXIT
4638 023252 15$:
4639 023252 005737 002274 TST RESUME ;IS A RESUME REQUESTED?
4640 023256 001414 BEQ 16$ ;IF NOT - BRANCH TO ERROR.
4641 023260 012777 040000 156744 MOV #MCLR,@SELO ;ISSUE A MASTER CLEAR.
4642 023266 000240 NOP
4643 023270 000240 NOP
4644 023272 052777 000100 156734 BIS #IEO,@SEL2 ;RESET INTERRUPT ENABLE (MASTER
4645 ;CLEAR - CLEARS ALL BITS)
4646 023300 112777 000143 156724 MOVB #IESET!RQI!BASEI,@SELO ;ISSUE A BASE IN
4647 023306 000543 BR 65$ ;BRANCH OUT.
4648
4649 023310 16$:
4650 023310 ERRDF 16,EMG16 ;ERROR - UNEXPECTED HALT.
4651 023310 104455 TRAP C$ERDF
4652 023312 000020 .WORD 16
4653 023314 020250 .WORD EMG16
4654 023316 000000 .WORD 0
4655 023320 000137 023566 JMP 60$
4656 023324 20$:
4657 023324 005737 002302 TST MMANAG ;ARE THE BUFFERS MEMORY MANAGED?
4658 023330 001452 BEQ 40$ ;IF NOT - NO NEED TO DETERMINE PHYS. ADDR.
4659 023332 032777 040000 156700 BIT #BIT14,@SEL6 ;IS BIT 16 OF THE PHYSICAL ADDR SET?
4660 ;(I.E. BUFFER SHOULD BE IN PHYSICAL
4661 ;ADDRESS RANGE: 200000 - 277776)
4662 023340 001005 BNE 21$ ;PROCEED - IF BIT SET.
4663 023342 ERRDF 11,EMG11,ERRG2
4664 023342 104455 TRAP C$ERDF
4665 023344 000013 .WORD 11
4666 023346 020116 .WORD EMG11
4667 023350 015124 .WORD  ERRG2
4668 023352 000505
4669 023354 21$: BR 60$
4670 023354 042777 140000 156656 BIC #BIT15!BIT14,@SEL6 ;CLEAR THE EXTENDED ADDRESS BITS.
4671 023362 017702 156650 MOV @SEL4,R2 ;SAVE BITS 0-15 OF THE PHYSICAL ADDRESS.

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4728 023566 005737 002334          TST      OUTXMT          ;HAVE ALL THE XMITTS BEEN DONE?
4729 023572 001011                   BNE      65$            ;IF NOT, CONTINUE
4730 023574 005737 002332          TST      OUTRCV         ;HAVE ALL THE RECEIVES BEEN DONE?
4731 023600 001006                   BNE      65$            ;IF NOT, CONTINUE
4732 023602                   61$:
4733 023602 042777 000100 156424       BIC      #IEO,@SEL2     ;CLEAR THE OUTPUT INTERRUPT
4734 023610 012737 177777 002356       MOV      #-1,OUTFLG     ;FLAG AS DONE.
4735 023616                   65$:
4736 023616 042777 000207 156410       BIC      #RDO!CMD,@SEL2 ;CLEAR THE RDO BIT.
4737 023624 012600                   MOV      (SP)+,R0       ;RESTORE R0
4738 023626                   ENDSRV
4739 023626                                     L10020:
4740 023626 000002                                     RTI
4741
4742                                     ;*****
4743                                     ;*****
4744
4745 023630                   BGNSRV  NOXMEM
4746 023630                                     NOXMEM::
4747
4748 023630 012737 000001 002352       MOV      #1,NXMFLG     ;SET FLAG IF MEMORY ADDRESSED IS NON-EXISTENT.
4749
4750 023636                   ENDSRV
4751 023636                                     L10021:
4752 023636 000002                                     RTI
4753
4754

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```
4755 .SBTTL DROP UNIT SECTION
4756
4757 :////////////////////////////////////////////////////////////////////
4758 :// THE DROP-UNIT SECTION CONTAINS THE CODING THAT CAUSES A DEVICE
4759 :// TO NO LONGER BE TESTED.
4760 :////////////////////////////////////////////////////////////////////
4761
4762 023640 BGNDU
4763 023640 L$DU::
4764
4765 023640 BRESET ;ISSUE UNIBUS RESET TO CLEAN UP
4766 023640 104433 TRAP C$RESET
4767 023642 PRINTF #FMDROP,LOGDEV
4768 023642 013746 002370 MOV LOGDEV,-(SP)
4769 023646 012746 023670 MOV #FMDROP,-(SP)
4770 023652 012746 000002 MOV #2,-(SP)
4771 023656 010600 MOV SP,R0
4772 023660 104417 TRAP C$PNTF
4773 023662 062706 000006 ADD #6,SP
4774
4775 023666 ENDDU
4776 023666 L10022:
4777 023666 104453 TRAP C$DU
4778
4779
4780 023670 047045 040445 047125 FMDROP: .ASCIZ /%N%AUNIT %D2%A DROPPED/
4781 023676 052111 022440 031104
4782 023704 040445 042040 047522
4783 023712 050120 042105 000
4784 023720 .EVEN
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4804 023720
4805
4806 023720
4807 023720 012746 000340
4808 023724 012746 024042
4809 023730 012746 000004
4810 023734 012746 000003
4811 023740 104437
4812 023742 062706 000010
4813 023746 005037 002352
4814 023752 005001
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4823 023754 005777 156252
4824 023760 012701 000002
4825 023764 005777 156244
4826 023770 012701 000004
4827 023774 005777 156236
4828 024000 012701 000006
4829 024004 005777 156230
4830 024010 005737 002352
4831 024014 001406
4832 024016
4833 024016 013700 002370
4834 024022 104451
4835 024024
4836 024024 104444
4837 024026 005037 002352
4838 024032
4839 024032
4840 024032 012700 000004

```

.SBTTL          TEST 1 - DMR CSR VERIFICATION
:
:*****
:          TEST 1 - DMR-11
:* VERIFY THAT ADDRESSING THE 4 UNIBUS CSRS DOES NOT CAUSE A NON-
:* EXISTENT MEMORY TRAP.
:
:* THE DMR IS AN NPR DEVICE RESIDING ON A UNIBUS. COMMUNICATION
:* BETWEEN THE MAIN CPU AND THE DMR IS ACCOMPLISHED THROUGH A
:* SET OF FOUR 16-BIT UNIBUS CONTROL AND STATUS REGISTERS (CSRS).
:* THE FOUR REGISTERS ARE ASSIGNED ADDRESSES IN THE I/O PAGE
:* FLOATING ADDRESS SPACE: 76XXX0 - 76XXX6
:
:* NOTE: THIS TEST IS REDUNDANT IN THAT STATIC LOGIC TESTS SHOULD
:* HAVE BEEN RUN BEFORE THESE FREE-RUNNING TESTS WERE STARTED, AND
:* THEY SHOULD HAVE DETECTED ANY CSR ADDRESSING PROBLEMS.
:* BUT JUST IN CASE THOSE STATIC TESTS AREN'T RUN, WE'LL BE SAFE.
:*****
BGNTST
:
:          T1::
:
:          SETVEC #4,#LOCATE,#PRI07 ;SET UP NON -EXISTENT MEMORY TRAP VECTOR.
:
:          MOV #PRI07,-(SP)
:          MOV #LOCATE,-(SP)
:          MOV #4,-(SP)
:          MOV #3,-(SP)
:          TRAP C$SVEC
:          ADD #10,SP
:
:          CLR NXMFLG ;FLAG USED IN THE TRAP ROUTINE.
:          CLR R1 ;USE REGISTER TO REMEMBER WHICH OF THE
: ;4 CSRS WE ARE ADDRESSING.
:
:*****
: IF ADDRESSING ANY ONE OF THE CSRS RESULTS IN A TRAP TO VECTOR 04, THE TRAP
: WILL REPORT THE ERROR (SEE INTERRUPT ROUTINE 'LOCATE'). OTHERWISE THE
: MEMORY REFERENCE IS UNEVENTFUL AND THE DEVICE IS READY FOR FURTHER TESTS
:*****
:
:          TST @SEL0 ;TEST THE CSR AT 76XXX0
:          MOV #2,R1 ;SAVE THE OFFSET OF THE NEXT CSR
:          TST @SEL2 ;TEST THE CSR AT 76XXX2
:          MOV #4,R1 ;SAVE THE OFFSET OF THE NEXT CSR
:          TST @SEL4 ;TEST THE CSR AT 76XXX4
:          MOV #6,R1 ;SAVE THE OFFSET OF THE NEXT CSR
:          TST @SEL6 ;TEST THE CSR AT 76XXX6
:          TST NXMFLG ;WAS THERE A TRAP?
:          BEQ 10$ ;IF NOT - EXIT.
:          DODU LOGDEV ;DROP THE DEVICE
:
:          MOV LOGDEV,R0
:          TRAP C$DODU
:
:          DOCLN ;DO CLEAN UP - FORCE BACK TO INIT CODE.
:          TRAP C$DCLN
:
:          CLR NXMFLG ;RESTORE THE FLAG.
:
:10$:
:          CLRVEC #4 ;RETURN VECTOR 04 TO NORMAL STATE
:          MOV #4,R0
    
```

```

4841 024036 104436 TRAP C$CVEC
4842
4843 024040 ENDTST
4844 024040 L10023:
4845 024040 104401 TRAP C$SETST
4846
4847
4848 024042 BGNSRV LOCATE ;INTERRUPT SERVICE ROUTINE
4849 024042 LOCATE::
4850 024042 010046 MOV RO,-(SP) ;SAVE RO
4851 024044 005737 002352 TST NXMFLG ;HAVE WE HAD AT LEAST 1 PREVIOUS TRAP?
4852 024050 001006 BNE 10$ ;IF YES, DON'T BOTHER DECLARING ANOTHER
4853 ;DEVICE FATAL ERROR
4854 024052 ERRDF 6,EMTO ;NON-EXISTENT DEVICE ERROR
4855 024052 104455 TRAP C$ERDF
4856 024054 000006 .WORD 6
4857 024056 024120 .WORD EMTO
4858 024060 000000 .WORD 0
4859 024062 005237 002352 INC NXMFLG ;SET THE FLAG
4860 024066 10$:
4861 024066 PRINTX #FMT0,R1,CSR(R1) ;PRINT THE CSR THAT DOESN'T RESPOND.
4862 024066 016146 002232 MOV CSR(R1),-(SP)
4863 024072 010146 MOV R1,-(SP)
4864 024074 012746 024147 MOV #FMT0,-(SP)
4865 024100 012746 000003 MOV #3,-(SP)
4866 024104 010600 MOV SP,R0
4867 024106 104415 TRAP C$PNTX
4868 024110 062706 000010 ADD #10,SP
4869 024114 012600 MOV (SP)+,R0 ;RESTORE R0
4870 024116 ENDSRV
4871 024116 L10024:
4872 024116 000002 RTI
4873
4874 024120 042101 051104 051505 EMT0: .ASCIZ /ADDRESS ERROR - TRAP 4/
4875 024126 020123 051105 047522
4876 024134 020122 020055 051124
4877 024142 050101 032040 000
4878 024147 045 031523 040445 FMT0: .ASCIZ /%S3%ACSR (SEL%D1%A) AT %06%A DOES NOT RESPOND%/
4879 024154 051503 020122 051450
4880 024162 046105 042045 022461
4881 024170 024501 040440 020124
4882 024176 047445 022466 020101
4883 024204 047504 051505 047040
4884 024212 052117 051040 051505
4885 024220 047520 042116 047045
4886 024226 000
4887 024230 .EVEN

```

4888
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4919 024230
4920 024230
4921 024230
4922 024230
4923 024230 104402
4924 024232 022737 000001 002270
4925 024240 001061
4926
4927 024242 005004
4928 024244 012705 000001
4929 024250 012737 001774 002412
4930 024256
4931 024256 013746 002370
4932 024262 012746 025204
4933 024266 012746 000002
4934 024272 010600
4935 024274 104414
4936 024276 062706 000006
4937 024302
4938 024302
4939 024306 117737 155726 025540
4940 024314 117737 155730 025542
4941
4942 024322
4943 024322 012746 025542

```
.SBTTL          TEST 2 - ROM CHECK
:*****
:*              TEST 2 - DMR-11
:* ROM CRC/CCITT - CHECK ROM POSITION AND CALCULATE CRC/CCITT. THE
:* LAST 4 BYTES CONTAIN INFORMATION ABOUT THE ROM TO CHECK. THE 1ST
:* OF THESE BYTES CONTAINS THE ASCII VERSION NUMBER. THE 2ND BYTE
:* CONTAINS THE ROM NUMBER. THE 3RD AND 4TH BYTES CONTAIN A NEGATIVE
:* CRC/CCITT WORD FOR THE ROM.
:*              CHIP ADDRESS RANGE
:*      LOCATION  CHIP NO.      BYTE      ADDRESS RANGE
:*      E03       0            LOW       0000 - 1777
:*      E02       1            HIGH      0000 - 1777
:*      E04       2            LOW       2000 - 3777
:*      E01       3            HIGH      2000 - 3777
:*      E05       4            LOW       4000 - 5777
:*      E14       5            HIGH      4000 - 5777
:*
:****** IMPORTANT !!!!!!!!!!!!! *****
:* FOR THIS TEST TO RUN CORRECTLY, ENSURE THAT SWITCH 1 AT LOCATION
:* E85 ON THE M8207 IS ON. IF THIS SWITCH IS OFF, BSEL1 WILL BE
:* LOCKED OUT AND THE MAINTENANCE FEATURES WILL NOT BE ENABLED.
:******
:*
:*      SUBTEST 1 - ON THE FIRST PASS PRINT THE VERSION # IN EACH ROM
:*      SUBTEST 2 - GENERATE THE CRC-CCITT IN EACH ROM AND COMPARE IT
:*                  IT AGAINST THE CRC BLASTED IN THE ROM
:*      SUBTEST 3 - COMPARE THE ROM # BLASTED IN THE ROM AGAINST THE
:*                  EXPECTED ROM #.
:******
BGNTST
BGNSUB
T2::
T2.1:
TRAP CSBSUB
CMP #1,STARES ;IS THIS THE FIRST PASS?
BNE 5$        ;IF NOT - SKIP THIS SUBROUTINE.
CLR R4       ;GET VERSION # FROM EACH ROM AND PRINT IT OUT
MOV #1,R5    ;# OF THE 1ST ROM
MOV #1774,ROMADR ;# OF NEXT ROM
PRINTB #FMT1,LOGDEV ;ADDRESS OF BYTE CONTAINING # IN ROMS 0 & 1
;MICROCODE VERSION
MOV LOGDEV,-(SP)
MOV #FMT1,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C$PNTB
ADD #6,SP
1$:
CALL $ROMO ;GET ROM CONTENTS.
MOVB @BSEL6,REV1 ;SAVE THE ASCII REVISION # OF THE ROM
MOVB @BSEL7,REV2 ;SAVE THE REV. # OF THE NEXT ROM
PRINT ;PRINT
PRINTB #FMT2,R4,#REV1,R5,#REV2
MOV #REV2,-(SP)
```

```

4944 024326 010546
4945 024330 012746 025540
4946 024334 010446
4947 024336 012746 025253
4948 024342 012746 000005
4949 024346 010600
4950 024350 104414
4951 024352 062706 000014
4952
4953 024356 022705 000005      CMP      #5,R5      ;ARE WE DONE?
4954 024362 001410      BEQ      5$      ;IF YES - EXIT
4955 024364 062704 000002      ADD      #2,R4      ;INCR. ROM NUMBERS
4956 024370 062705 000002      ADD      #2,R5      ;
4957 024374 062737 002000 002412      ADD      #2000,ROMADR ;ADDRESS OF BYTES CONTAINING NEXT ROM REV #S.
4958 024402 000737      BR       1$      ;
4959
4960 024404      5$:
4961 024404      ENDSUB
4962 024404
4963 024404 104403      L10026:
4964
4965
4966 024406      BGNSUB
4967 024406
4968 024406 104402      T2.2:
4969 024410 005037 002344      CLR      FLAG      TRAP      C$ESUB
4970
4971 024414 005004      CLR      R4
4972
4973
4974 024416 005037 002412      CLR      ROMADR    ;USE THE FLAG TO MARK WHEN AN ERRDF
4975
4976 024422      10$:      ;HAS BEEN DETECTED IN THIS TEST.
4977 024422 012737 177777 002402      MOV      #-1,LOCRC ;START CRC CHECK WITH ROM 0
4978
4979 024430 012737 177777 002404      MOV      #-1,HICRC ;R4 IS THE ROM #. THE LOCATION FOR THE
4980 024436 012701 001000      MOV      #1000,R1  ;ROM IS CONTAINED IN THE TABLE 'ROMLOC'.
4981
4982
4983
4984
4985
4986
4987
4988
4989 024442      20$:
4990 024442      ; BECAUSE A ROM OUT WILL OUTPUT THE ROM CONTENTS (I.E. 16 BITS)
4991 024446 117737 155566 002406      CALL     $ROMO     ; THIS ROUTINE WILL CALCULATE/CHECK THE CRC 2 ROMS AT A TIME.
4992 024454 117737 155570 002410      MOV     @BSEL6,LOWORD ;GET THE ROM CONTENTS
4993 024462 005237 002412      MOV     @BSEL7,HIWORD ;SAVE THE LOW BYTE OF THE ROM CONTENTS.
4994 024466      INC     ROMADR     ;SAVE THE HIGH BYTE OF THE ROM CONTENTS.
4995 024472 117737 155542 002407      INC     ROMADR     ;INCREMENT THE ROM ADDRESS POINTER
4996 024500 117737 155544 002411      CALL     $ROMO     ;GET THE CONTENTS OF THE NEXT ROM ADDRESS
4997
4998
4999 024506 005237 002412      MOV     @BSEL6,LOWORD+1 ;SAVE THE NEXT LOW BYTE.
      MOV     @BSEL7,HIWORD+1 ;SAVE THE NEXT HIGH BYTE.
      ;NOTE: AT THIS POINT LOWORD IS A WORD WHICH
      ;HAS 2 CONSECUTIVE LOW BYTES OF ROM CONTENTS.
      ;INCREMENT THE ROM ADDRESS POINTER

```



```

5000 024512 005301      DEC      R1      ;ARE WE FINISHED WITH THESE 2 ROMS?
5001 024514 001443      BEQ      40$     ;IF YES, CHECK CRC
5002
5003      ; CRC/CCITT CALCULATION - CONVERT THE WORD (LOWORD & HIWORD) TO
5004      ; A SERIAL STREAM FOR CALCULATION.
5005
5006 024516 012703 000020      MOV      #16.,R3 ;16 BITS TO CONSIDER
5007      25$:
5008 024522 000241      CLC
5009 024524 006037 002402      ROR      LOCRC   ;CLEAR THE CARRY
5010 024530 006037 002406      ROR      LOWORD  ;ROTATE BIT0 INTO THE CARRY BIT
5011      ;ROTATE BIT0 INTO C AND THE OLD C INTO BIT15
5012      ;ARE THE BITS 15 & BITS 0 THE SAME?
5013 024534 102011      BVC      30$     ;IF YES (V IS CLEAR), DON'T DO THE CRC
5014 024536 012702 102010      MOV      #102010,R2 ;NOTE: V IS THE EXCLUSIVE OR OF BIT0 & BIT15.
5015 024542 043702 002402      BIC      LOCRC,R2 ;CRC/CCITT POLYNOMIAL
5016 024546 042737 102010 002402      BIC      #102010,LOCRC
5017 024554 050237 002402      BIS      R2,LOCRC
5018      30$:
5019 024560 000241      CLC
5020 024562 006037 002404      ROR      HICRC   ;CLEAR THE CARRY
5021 024566 006037 002410      ROR      HIWORD  ;ROTATE BIT 0 INTO C
5022      ;ROTATE OLD C INTO BIT15 (SIGN) & BIT0 INTO C
5023 024572 102011      BVC      35$     ;ARE THE BITS 0 OF HICRC & HIWORD THE SAME?
5024      ;IF YES (V IS CLEAR), DON'T DO THE CRC.
5025 024574 012702 102010      MOV      #102010,R2 ;NOTE: V IS THE EXCLUSIVE OR OF BIT0 & BIT15.
5026 024600 043702 002404      BIC      HICRC,R2 ;CRC/CCITT POLYNOMIAL
5027 024604 042737 102010 002404      BIC      #102010,HICRC
5028 024612 050237 002404      BIS      R2,HICRC
5029      35$:
5030 024616 005303      DEC      R3      ;DO ALL 16 BITS
5031 024620 001340      BNE      25$
5032 024622 000707      BR       20$     ;GET THE CONTENTS OF THE NEXT 2 ROM ADDRESSES.
5033      40$:
5034      ;
5035      ; AT THIS POINT WE'VE READ THE CONTENTS AND CALCULATED THE CRC FOR
5036      ; 2 ROM ROMS (ONE LOW BYTE & ONE HIGH BYTE). ALSO WE'VE READ THE
5037      ; CRC BLASTED INTO THE LAST 2 BYTES OF THE ROM (IN LOWORD/HIWORD)
5038      ;
5039 024624 005137 002402      COM      LOCRC   ;COMPLEMENT THE CALCULATED CRC
5040 024630 023737 002402 002406      CMP      LOCRC,LOWORD ;IS THE CRC IN ROM THE SAME AS THE
5041      ;CALCULATED CRC?
5042 024636 001427      BEQ      50$     ;IF YES - CHECK THE HIGH BYTE CRC (NEXT ROM)
5043 024640 005737 002344      TST      FLAG    ;HAS AN ERRDF ALREADY BEEN DECLARED (REMEMBER
5044      ;WE'RE IN A LOOP)
5045 024644 001007      BNE      41$     ;IF YES, DON'T BOTHER WITH ANOTHER ERRDF.
5046 024646 012737 000001 002344      MOV      #1,FLAG ;FLAG THAT ERRDF HAS BEEN DETECTED.
5047 024654      ERRDF 7,EMT1   ;ROM ERROR
5048 024654 104455      TRAP    C$ERDF
5049 024656 000007      .WORD  7
5050 024660 025472      .WORD  EMT1
5051 024662 000000      .WORD  0
5052 024664      41$:
5053 024664      PRINTB #FMT3,R4,LOCRC,LOWORD
5054 024664 013746 002406      MOV      LOWORD,-(SP)
5055 024670 013746 002402      MOV      LOCRC,-(SP)
  
```

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5056 024674 010446
5057 024676 012746 025330
5058 024702 012746 000004
5059 024706 010600
5060 024710 104414
5061 024712 062706 000012
5062 024716
5063 024716 005204
5064 024720 005137 002404
5065 024724 023737 002404 002410
5066 024732 001427
5067 024734 005737 002344
5068
5069 024740 001007
5070 024742 012737 000001 002344
5071 024750
5072 024750 104455
5073 024752 000007
5074 024754 025472
5075 024756 000000
5076 024760
5077 024760
5078 024760 013746 002410
5079 024764 013746 002404
5080 024770 010446
5081 024772 012746 025330
5082 024776 012746 000004
5083 025002 010600
5084 025004 104414
5085 025006 062706 000012
5086 025012
5087 025012 022704 000005
5088 025016 001403
5089 025020 005204
5090 025022 000137 024422
5091 025026
5092
5093 025026
5094 025026
5095 025026 104403
5096
5097 025030
5098 025030
5099 025030 104402
5100 025032 005037 002344
5101 025036 005004
5102 025040 012737 001775 002412
5103 025046
5104 025046
5105 025052 117701 155162
5106
5107 025056 000402
5108 025060
5109 025060 117701 155164
5110
5111 025064

```

```

MOV R4,-(SP)
MOV #FMT3,-(SP)
MOV #4,-(SP)
MOV SP,R0
TRAP C$PNTB
ADD #12,SP

50$:
INC R4 ;INCR ROM #
COM HICRC ;COMPLEMENT THE CALCULATED CRC FOR THE HI BYTE
CMP HICRC,HIWORD ;ROM CRC AND CALCULATED CRC THE SAME?
BEQ 60$ ;IF YES - CHECK THE ROM LOCATIONS.
TST FLAG ;HAS AN ERRDF ALREADY BEEN DECLARED (REMEMBER WE'RE IN A LOOP)
BNE 51$ ;IF YES, DON'T BOTHER WITH ANOTHER ERRDF.
MOV #1,FLAG ;FLAG THAT ERRDF HAS BEEN DETECTED.
ERRDF 7,EMT1 ;ROM ERROR

TRAP C$ERDF
WORD 7
WORD EMT1
WORD 0

51$:
PRINTB #FMT3,R4,HICRC,HIWORD

MOV HIWORD,-(SP)
MOV HICRC,-(SP)
MOV R4,-(SP)
MOV #FMT3,-(SP)
MOV #4,-(SP)
MOV SP,R0
TRAP C$PNTB
ADD #12,SP

60$:
CMP #5,R4 ;IF WE'VE DONE ROMS 0-5, WE'RE DONE.
BEQ 70$ ;EXIT WHEN DONE
INC R4 ;CHECK THE NEXT ROM.
JMP 10$

70$:
ENDSUB

L10027:
TRAP C$ESUB

BGNSUB

T2.3:
TRAP C$BSUB

10$:
CLR FLAG ;CLEAR FLAG
CLR R4 ;BEGIN AT ROM 0
MOV #1775,ROMADR ;ADDRESS OF BYTE CONTAINING ROM #

CALL $ROMO ;GET ROM CONTENTS
MOVB @BSEL6,R1 ;SAVE THE CONTENTS OF THE LOW BYTE
;FOR ROMS 0,2,4

15$:
BR 17$

17$:
MOVB @BSEL7,R1 ;SAVE THE CONTENTS OF THE HIGH BYTE
;FOR ROMS 1,3,5

```

```

5112 025064 042701 177760      BIC    #^C17,R1      ;CONVERT THE ASCII BYTE TO AN OCTAL WORD.
5113 025070 020104              CMP    R1,R4         ;IS THIS THE EXPECTED ROM #
5114 025072 001427              BEQ    20$           ;IF YES - OK.
5115 025074 005737 002344      TST    FLAG          ;HAS AN ERRDF ALREADY BEEN DECLARED (REMEMBER
5116                                ;WE'RE IN A LOOP)
5117 025100 001007              BNE    18$           ;IF YES, DON'T BOTHER WITH ANOTHER ERRDF.
5118 025102 012737 000001 002344  MOV    #1,FLAG       ;FLAG THAT ERRDF HAS BEEN DETECTED.
5119 025110              ERRDF  7,EMT2        ;ROM ERROR
5120 025110 104455                                TRAP   C$ERDF
5121 025112 000007                                .WORD 7
5122 025114 025512                                .WORD EMT2
5123 025116 000000                                .WORD 0
5124 025120
5125 025120      18$:      PRINTB #FMT4,<B,ROMLOC(R4)>,R1,R4
5126 025120 010446                                MOV    R4,-(SP)
5127 025122 010146                                MOV    R1,-(SP)
5128 025124 005046                                CLR    -(SP)
5129 025126 156416 025531                                BISB  ROMLOC(R4),(SP)
5130 025132 012746 025414                                MOV    #FMT4,-(SP)
5131 025136 012746 000004                                MOV    #4,-(SP)
5132 025142 010600                                MOV    SP,R0
5133 025144 104414                                TRAP  C$PNTB
5134 025146 062706 000012                                ADD   #12,SP
5135 025152
5136 025152 022704 000005      20$:      CMP    #5,R4         ;DID WE FINISH THE LAST ROM?
5137 025156 001410              BEQ    30$           ;IF YES - SKIP TO THE END
5138 025160 005204              INC    R4            ;POINT TO THE NEXT ROM #
5139 025162 032704 000001              BIT    #BIT0,R4     ;IS THIS AN ODD #
5140 025166 001334              BNE    15$           ;IF YES GO BACK AND READ THE HIGH BYTE
5141
5142 025170 062737 002000 002412  ADD    #2000,ROMAC  ;INCR. ADDRESS POINTER TO NEXT ROM #.
5143 025176 000723              BR     10$
5144 025200
5145 025200      30$:      ENDSUB
5146 025200
5147 025200 104403                                L10030: TRAP   C$ESUB
5148
5149 025202      ENDTST
5150 025202
5151 025202 104401                                L10025: TRAP   C$ETST
5152 025204 047045 040445 044515  FMT1:  .ASCIZ  /%N%AMICROCODE REVISION IN UNIT%D3%A:%N/
5153 025212 051103 041517 042117
5154 025220 020105 042522 044526
5155 025226 044523 047117 044440
5156 025234 020116 047125 052111
5157 025242 042045 022463 035101
5158 025250 047045 000
5159 025253 045 051101 046517  FMT2:  .ASCIZ  /%AROM%D2%A - REV. %T%N%AROM%D2%A - REV. %T%N/
5160 025260 042045 022462 020101
5161 025266 020055 042522 027126
5162 025274 022440 022524 022516
5163 025302 051101 046517 042045
5164 025310 022462 020101 020055
5165 025316 042522 027126 022440
5166 025324 022524 000116
5167 025330 040445 047522 022515  FMT3:  .ASCIZ  /%AROM%D2%A: CALCUATED CRC -%06%A  CRC IN ROM =%06%N/
    
```

5168	025336	031104	040445	020072		
5169	025344	040503	041514	040525		
5170	025352	042524	020104	051103		
5171	025360	020103	022475	033117		
5172	025366	040445	020040	051103		
5173	025374	020103	047111	051040		
5174	025402	046517	036440	047445		
5175	025410	022466	000116			
5176	025414	040445	022505	031104	FMT4:	.ASCIZ /%AE%D2%A IS ROM %D1%A (SHOULD BE ROM %D1%A)%N/
5177	025422	040445	044440	020123		
5178	025430	047522	020115	042045		
5179	025436	022461	020101	051450		
5180	025444	047510	046125	020104		
5181	025452	042502	051040	046517		
5182	025460	022440	030504	040445		
5183	025466	022451	000116			
5184						
5185	025472	051103	026503	041503	EMT1:	.ASCIZ /CRC-CCITT ERROR/
5186	025500	052111	020124	051105		
5187	025506	047522	000122			
5188	025512	047514	040503	044524	EMT2:	.ASCIZ /LOCATION ERROR/
5189	025520	047117	042440	051122		
5190	025526	051117	000			
5191						
5192	025531	003	002	004	ROMLOC:	.BYTE 3,2,4,1,5,14. ;ROM 0 = ROM LOCATION 3 ETC.
5193	025534	001	005	016		
5194		025540				
5195	025540	000000			REV1:	.EVEN ;ASCII VALUE OF THE REV. NUMBER
5196	025542	000000			REV2:	.WORD 0 ;ASCII VALUE OF THE REV. NUMBER
5197						
5198						
5199						

5200
5201
5202
5203
5204
5205
5206
5207
5208
5209
5210
5211
5212
5213
5214
5215
5216
5217
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5232 025544 004737 011070
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5236 025550 104410
5237 025552 000072
5238 025554 105777 154464
5239 025560 001011
5240 025562
5241 025562 012746 017436
5242 025566 012746 000001
5243 025572 010600
5244 025574 104414
5245 025576 062706 000004
5246 025602 000420
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5248 025604 117701 154434
5249 025610
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5251 025610 004737 011070
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5254 025614
5255 025614 104410

```
.SBTTL          TEST 3 - MASTER CLEAR AND MICROTEST
*****
*          TEST 3 - DMR-11
* MASTER CLEAR
* THIS TEST WILL ISSUE 2 MASTER CLEARS.  EACH CALL TO THE MASTER
* CLEAR ROUTINE WILL ENSURE THAT THE RUN BIT WILL BE SET.  ALSO
* THE MASTER CLEAR WILL CAUSE THE DIAGNOSTIC MICROTESTS TO BE
* RUN WHEN THE MICRODIAGNOSTIC BIT (BIT 13 IN SEL0) IS CORRECTLY
* SET OR CLEARED.  BECAUSE THE RUNNING OF MICROTESTS DEPENDS ON THE
* EXCLUSIVE OR OF THE HARDWARE SWITCH 10 ON E134 OF THE M8203 AND
* THE MICRODIAGNOSTIC BIT, WE CAN'T KNOW WHETHER THE SETTING OR
* CLEARING OF BIT 13 WILL RESULT IN THE RUNNING OF MICROTESTS.
* THEREFORE THE MASTER CLEAR SUBROUTINE WILL TOGGLE (I.E. SET
* BIT 13 ONLY ON EVERY OTHER MASTER CLEAR) THE SOFTWARE BIT.
* THIS WILL ENSURE THAT REGARDLESS OF THE POSITION OF THE
* HARDWARE SWITCH, MICROTESTS WILL BE RUN EVERY OTHER MASTER CLEAR.
* WHEN RUNNING THIS TEST, WE EXPECT TO ADD THE RESULTS OF BSEL3
* AFTER EACH MASTER CLEAR.
* BSEL3 = 100          - MICROTESTS DISABLED
* BSEL3 = 200          - MICROTESTS RUN SUCCESSFULLY
* IF THE RESULT OF THE 2 MASTER CLEARS IS NOT 300, AN ERROR IS
* REPORTED.
*
* ADDITIONALLY THIS ROUTINE WILL REPORT WHENEVER THE RESULT OF
* BSEL3 IS 0.  THIS WILL MEAN THAT THE DEVICE IS NOT A DMR
* (I.E. DMC)
*****
BGNTST
                                T3::
CLEAR                          ;MACRO FOR MASTER CLEAR
                                ;**** MACRO EXPANSION ****
                                ;ISSUE A DMR MASTER CLEAR
                                ;****                          ****
JSR      PC, $MSCLR
                                ;IF ERROR, BR TO TEST END.
ESCAPE  TST                      ;IF ERROR, BR TO TEST END.
                                TRAP      C$ESCAPE
                                .WORD    L10031-.
TSTB    @BSEL3                   ;IS THERE A DMR RESPONSE?
BNE     1$
PRINTB  #FMG19                   ;REPORT DEVICE NOT DMR.
                                MOV       #FMG19,-(SP)
                                MOV       #1,-(SP)
                                MOV       SP,R0
                                TRAP     C$PNTB
                                ADD      #4,SP
1$:    BR      5$
MOV     @BSEL3,R1                 ;SAVE THE RESULT OF THE FIRST MASTER CLEAR.
CLEAR  ;MASTER CLEAR AGAIN.
                                ;**** MACRO EXPANSION ****
                                ;ISSUE A DMR MASTER CLEAR
                                ;****                          ****
JSR      PC, $MSCLR
                                ;IF ERROR, BR TO TEST END.
ESCAPE  TST                      ;IF ERROR, BR TO TEST END.
                                TRAP     C$ESCAPE
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5256 025616 000026
5257 025620 117702 154420      MOVB  @BSEL3,R2      ;SAVE THE RESULTS OF THE SECOND MASTER CLEAR
5258 025624 060102              ADD   R1,R2          ;ADD THE RESULTS OF THE 2 CLEARS
5259                               ;NOTE: ONE SHOULD BE 100 - MICRO TESTS NOT
5260                               ;ENABLED AND ONE SHOULD BE 200 - MICRO TESTS
5261                               ;SUCCESFULLY RUN.
5262 025626 122702 000300      CMPB  #300,R2        ;WAS THE MICROTEST COMPLETED?
5263 025632 001404              BEQ   5$              ;IF YES - OK
5264 025634              ERRDF 3,EMT3,ERRG3 ;MICROTEST NOT COMPLETED
5265 025634 104455              TRAP  C$ERDF
5266 025636 000003              .WORD 3
5267 025640 025646              .WORD EMT3
5268 025642 015240              .WORD ERRG3
5269 025644              5$:
5270 025644              ENDTST
5271 025644
5272 025644 104401              L10031:
5273                               TRAP  C$ETST
5274 025646 044515 051103 052117 EMT3: .ASCIZ /MICROTEST NOT COMPLETED/
5275 025654 051505 020124 047516
5276 025662 020124 047503 050115
5277 025670 042514 042524 000104
5278                               .EVEN
  
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.SBTTL          TEST 4 - BASE IN COMMAND
:*****
:*              TEST 4 - DMR-11
:* BASE IN COMMANDS
:* SUBTEST 1 - ISSUE A BASE IN - DMR MODE.
:*              ENSURE THAT THE DMR MODE BIT (BIT 4) IS SET IN
:*              THE MICROCODE SCRATCH PAD 7 AND THAT THE DDCMP
:*              MESSAGE VARIABLES ARE PROPERLY INITIALIZED.
:* SUBTEST 2 - ISSUE A BASE IN - DMC MODE.
:*              ENSURE THAT THE DMC MODE BIT (BIT 4) IS CLEAR IN
:*              THE MICROCODE SCRATCH PAD 7 AND THAT THE DDCMP
:*              MESSAGE VARIABLES ARE PROPERLY INITIALIZED.
:*****
BGNTST
          T4::
          BGNSUB          T4.1:
          CLEAR          ;MACRO FOR MASTER CLEAR COMMAND
          ;***** MACRO EXPANSION *****
          JSR PC, $MSCLR ;ISSUE A DMR MASTER CLEAR
          ;*****
          ESCAPE TST     ;IF ERROR, BR TO TEST END
          ;*****
          TRAP          C$BSUB
          .WORD          L10032-.
          BASEIN 0,BASE,DMR ;BASE IN COMMAND WITH NO MAINTENANCE,
          ;BASE=BASE TABLE ADDRESS, AND DMR-11 MODE
          ;***** MACRO EXPANSION *****
          JSR PC, $BASEI ;CALL BASE IN ROUTINE
          .WORD 0        ;MAINTENANCE MODE BITS TO SET IN BSEL1
          .WORD BASE     ;BASE TABLE ADDRESS
          .WORD DMR     ;MODE
          ;*****
          ESCAPE TST     ;IF ERROR, BR TO TEST END
          ;*****
          TRAP          C$ESCAPE
          .WORD          L10032-.
          SHUTDN
          ;***** MACRO EXPANSION *****
          JSR PC, $HALT  ;DMR HALT ROUTINE.
          ;*****
          BITB #BIT4,BASE+ISP7 ;SEE IF THE DMR MODE BIT IS SET IN THE
          ;DMR SCRATCH PAD REGISTER 7 (BASE TABLE
          ;LOCATION CONTAINS AN IMAGE OF SP7)
          ;OK IF SET - BR
          BNE 10$
          ERRDF 20,EMT4
          ;*****
          TRAP          C$ERDF
          .WORD          20
          .WORD          EMT4
          .WORD          0
          10$:
          ;CHECK MESSAGE EXCHANGE VALUES
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5335                                     ; IN THE BASE TABLE.
5336 025752 105737 002702          TSTB  BASE+R          ; #R (MESSAGE RECEIVED) = 0?
5337 025756 001015                 BNE    20$           ; ERROR IF NON ZERO
5338 025760 105737 002703          TSTB  BASE+N          ; #N (MESSAGE TRANSMITTED) = 0?
5339 025764 001012                 BNE    20$           ; ERROR IF NON ZERO
5340 025766 105737 002704          TSTB  BASE+A          ; #A (MESSAGE ACKNOWLEDGED) = 0?
5341 025772 001007                 BNE    20$           ; ERROR IF NON ZERO
5342 025774 122737 000001 002705  CMPB  #1,BASE+T      ; #T (NEXT MESSAGE # TRANSMITTED) = 1?
5343 026002 001003                 BNE    20$           ; ERROR IF NOT EQUAL TO 1.
5344 026004 105737 002706          TSTB  BASE+X          ; #X (LAST MESSAGE TRANSMITTED) = 0?
5345 026010 001404                 BEQ    30$
5346 026012                         20$:
5347 026012                         FRRDF  20,EMT5,ERRT1
5348 026012 104455                                     TRAP  C$ERDF
5349 026014 000024                                     .WORD 20
5350 026016 026421                                     .WORD EMT5
5351 026020 026154                                     .WORD ERRT1
5352 026022                         30$:
5353 026022                         ENDSUB
5354 026022                                     L10033:
5355 026022 104403                                     TRAP  C$ESUB
5356
5357 026024                         BGNSUB
5358 026024                                     T4.2:
5359 026024 104402                                     TRAP  C$BSUB
5360 026026          CLEAR          ;MACRO FOR MASTER CLEAR COMMAND
5361                                     ;**** MACRO EXPANSION ****
5362 026026 004737 011070          JSR   PC, $MSCLR    ;ISSUE A DMR MASTER CLEAR
5363                                     ;****
5364
5365 026032          ESCAPE  TST          ;IF ERROR, BR TO TEST END
5366 026032 104410                                     TRAP  C$ESCAPE
5367 026034 000116                                     .WORD L10032-.
5368
5369
5370 026036          BASEIN  0,BASE,0    ;BASE IN COMMAND WITH NO MAINTENANCE
5371                                     ;AND DMC MODE.
5372 026036 004737 011266          JSR   PC, $BASEI    ;**** MACRO EXPANSION ****
5373 026042 000000          .WORD  0    ;CALL BASE IN ROUTINE
5374 026044 002640          .WORD  BASE  ;MAINTENANCE MODE BITS TO SET IN BSEL1
5375 026046 000000          .WORD  0    ;BASE TABLE ADDRESS
5376                                     ;MODE
5377                                     ;****
5378 026050          ESCAPE  TST          ;IF ERROR, BR TO TEST END
5379 026050 104410                                     TRAP  C$ESCAPE
5380 026052 000100                                     .WORD L10032-.
5381 026054          SHUTDN
5382
5383 026054 004737 012562          JSR   PC, $HALT    ;**** MACRO EXPANSION ****
5384                                     ;DMR HALT ROUTINE.
5385 026060 132737 000020 002732  BITB  #BIT4,BASE+ISP7 ;****
5386                                     ;SEE IF THE DMR MODE BIT IS CLEAR IN THE
5387                                     ;DMR SCRATCH PAD REGISTER 7 (BASETABLE
5388 026066 001404          BEQ    10$    ;LOCATION CONTAINS AN IMAGE OF SP7)
5389 026070          ERRDF  20,EMT6    ;OK IF CLEAR - BR
5390 026070 104455                                     TRAP  C$ERDF
  
```


5391	026072	000024							.WORD	20
5392	026074	026467							.WORD	EMT6
5393	026076	000000							.WORD	0
5394	026100		10\$:							
5395										
5396										
5397	026100	105737	002702	TSTB	BASE+R					
5398	026104	001015		BNE	20\$					
5399	026106	105737	002703	TSTB	BASE+N					
5400	026112	001012		BNE	20\$					
5401	026114	105737	002704	TSTB	BASE+A					
5402	026120	001007		BNE	20\$					
5403	026122	122737	000001 002705	CMPB	#1,BASE+T					
5404	026130	001003		BNE	20\$					
5405	026132	105737	002706	TSTB	BASE+X					
5406	026136	001404		BEQ	30\$					
5407	026140		20\$:							
5408	026140			ERRDF	20,EMT5,FRRT1					
5409	026140	104455							TRAP	C\$ERDF
5410	026142	000024							.WORD	20
5411	026144	026421							.WORD	EMT5
5412	026146	026154							.WORD	ERRT1
5413	026150		30\$:							
5414	026150			ENDSUB						
5415	026150									
5416	026150	104403								
5417										
5418	026152			ENDTST						
5419	026152									
5420	026152	104401								
5421										
5422	026154			BGNMSG	ERRT1					
5423	026154									
5424	026154	105737	002702	TSTB	BASE+R					
5425	026160	001413		BEQ	1\$					
5426	026162			PRINTB	#FMT5,<B,BASE+R>					
5427	026162	005046								
5428	026164	153716	002702							
5429	026170	012746	026526							
5430	026174	012746	000002							
5431	026200	010600								
5432	026202	104414								
5433	026204	062706	000006							
5434	026210		1\$:							
5435	026210	105737	002703	TSTB	BASE+N					
5436	026214	001413		BEQ	2\$					
5437	026216			PRINTB	#FMT6,<B,BASE+N>					
5438	026216	005046								
5439	026220	153716	002642							
5440	026224	012746	026557							
5441	026230	012746	000002							
5442	026234	010600								
5443	026236	104414								
5444	026240	062706	000006							
5445	026244		2\$:							
5446										

```

;CHECK MESSAGE EXCHANGE VALUES
;IN THE BASE TABLE.
; #R (MESSAGE RECEIVED) = 0?
;ERROR IF NON ZERO
; #N (MESSAGE TRANSMITTED) = 0?
;ERROR IF NON ZERO
; #A (MESSAGE ACKNOWLEDGED) = 0?
;ERROR IF NON ZERO
; #T (NEXT MESSAGE # TRANSMITTED) = 1?
;ERROR IF NOT EQUAL TO 1.
; #X (LAST MESSAGE TRANSMITTED) = 0?

```

```

L10034: TRAP C$ESUB
L10032: TRAP C$ETST

```

```

ERRT1::
CLR -(SP)
BISB BASE+R,(SP)
MOV #FMT5,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C$PNTB
ADD #6,SP

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```

CLR -(SP)
BISB BASE+2,(SP)
MOV #FMT6,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C$PNTB
ADD #6,SP

```

5447	026244	105737	002704		TSTB	BASE+A		;IS #A = 0?		
5448	026250	001413			BEQ	3\$;OK - IF ZERO		
5449	026252				PRINTB	#FMT7,<B,BASE+A>		;PRINT #A		
5450	026252	005046							CLR	-(SP)
5451	026254	153716	002704						BISB	BASE+A,(SP)
5452	026260	012746	026610						MOV	#FMT7,-(SP)
5453	026264	012746	000002						MOV	#2,-(SP)
5454	026270	010600							MOV	SP,R0
5455	026272	104414							TRAP	C\$PNTB
5456	026274	062706	000006						ADD	#6,SP
5457	026300									
5458	026300	122737	000001	002705	3\$:	CMPB	#1,BASE+T		;IS #T = 1?	
5459	026306	001413				BEQ	4\$;OK - IF ONE	
5460	026310					PRINTB	#FMT8,<B,BASE+T>		;PRINT #T	
5461	026310	005046							CLR	-(SP)
5462	026312	153716	002705						BISB	BASE+T,(SP)
5463	026316	012746	026641						MOV	#FMT8,-(SP)
5464	026322	012746	000002						MOV	#2,-(SP)
5465	026326	010600							MOV	SP,R0
5466	026330	104414							TRAP	C\$PNTB
5467	026332	062706	000006						ADD	#6,SP
5468	026336				4\$:					
5469	026336	105737	002706			TSTB	BASE+X		;IS #X = 0?	
5470	026342	001413				BEQ	5\$;OK - IF ZERO	
5471	026344					PRINTB	#FMT9,<B,BASE+X>		;PRINT #X	
5472	026344	005046							CLR	-(SP)
5473	026346	153716	002706						BISB	BASE+X,(SP)
5474	026352	012746	026672						MOV	#FMT9,-(SP)
5475	026356	012746	000002						MOV	#2,-(SP)
5476	026362	010600							MOV	SP,R0
5477	026364	104414							TRAP	C\$PNTB
5478	026366	062706	000006						ADD	#6,SP
5479	026372				5\$:					
5480	026372				ENDMSG					
5481	026372									
5482	026372	104423							L10035:	TRAP
5483										C\$MSG
5484	026374	046504	020122	047515	EMT4:	.ASCIZ	/DMR MODE BIT NOT SET/			
5485	026402	042504	041040	052111						
5486	026410	047040	052117	051440						
5487	026416	052105	000							
5488	026421	104	041504	050115	EMT5:	.ASCIZ	/DDCMP MESSAGE VARIABLE(S) NOT CORRECT/			
5489	026426	046440	051505	040523						
5490	026434	042507	053040	051101						
5491	026442	040511	046102	024105						
5492	026450	024523	047040	052117						
5493	026456	041440	051117	042522						
5494	026464	052103	000							
5495	026467	104	041515	046440	EMT6:	.ASCIZ	/DMC MODE - DMR BIT NOT CLEARED/			
5496	026474	042117	020105	020055						
5497	026502	046504	020122	044502						
5498	026510	020124	047516	020124						
5499	026516	046103	040505	042522						
5500	026524	000104								
5501										
5502	026526	040445	051043	024040	FMT5:	.ASCIZ	/%A#R (MSG. RCVD) = %D3#N/			

5503	026534	051515	027107	051040		
5504	026542	053103	024504	036440		
5505	026550	022440	031504	047045		
5506	026556	000				
5507	026557	045	021501	020116	FMT6:	.ASCIZ /%A#N (MSG. XMIT) = %D3%N/
5508	026564	046450	043523	020056		
5509	026572	046530	052111	020051		
5510	026600	020075	042045	022463		
5511	026606	000116				
5512	026610	040445	040443	024040	FMT7:	.ASCIZ /%A#A (MSG. ACK) - %D3%N/
5513	026616	051515	027107	040440		
5514	026624	045503	020051	036440		
5515	026632	022440	031504	047045		
5516	026640	000				
5517	026641	045	021501	020124	FMT8:	.ASCIZ /%A#T (NEXT XMIT) = %D3%N/
5518	026646	047050	054105	020124		
5519	026654	046530	052111	020051		
5520	026662	020075	042045	022463		
5521	026670	000116				
5522	026672	040445	054043	024040	FMT9:	.ASCIZ /%A#X (LAST XMIT) = %D3%N/
5523	026700	040514	052123	054040		
5524	026706	044515	024524	036440		
5525	026714	022440	031504	047045		
5526	026722	000				
5527	026724					.EVEN

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5528 .SBTTL TEST 5 - DMR COMMANDS
5529
5530 :*****
5531 :* TEST 5 - DMR-11
5532 :* DMR COMMANDS
5533 :* SUBTEST 1 - ISSUE AN ENABLE EXTENDED ERROR COMMAND AND CHECK THAT
5534 :* THE EXT. ENABLE BIT IS SET IS SCRATCH PAD 13. THEN
5535 :* DISABLE EXTENDED ERROR AND CHECK THAT THE ENABLE BIT
5536 :* IS CLEAR.
5537 :* SUBTEST 2 - SET REP/SEL TIMER VALUE AND SET THE DMR THRESHOLD
5538 :* VALUES. CHECK THAT THE VALUES ARE CORRECT IN
5539 :* THE BASE TABLE AFTER HALTING THE DMR.
5540 :*
5541 :*
5542 :*****
5543 BGNTST
5544 BGNSUB
5545 T5:
5546 T5.1:
5547 TRAP C$BSUB
5548 CLEAR ;MACRO FOR MASTER CLEAR COMMAND
5549 ;**** MACRO EXPANSION ****
5550 JSR PC, $MSCLR ;ISSUE A DMR MASTER CLEAR
5551 ;****
5552
5553 ESCAPE TST ;IF ERROR, BR TO TEST END
5554 TRAP C$ESCAPE
5555 .WORD L10036-.
5556
5557 BASEIN ;BASE IN COMMAND WITH LINE UNIT LOOP,
5558 ;**** MACRO EXPANSION ****
5559 JSR PC, $BASEI ;CALL BASE IN ROUTINE WITH DEFAULTS
5560 .WORD LPLU ;SET LINE UNIT LOOP
5561 .WORD BASE ;BASE TABLE ADDRESS
5562 .WORD DMR ;DMR-11 MODE
5563 ;****
5564
5565 ESCAPE TST ;IF ERROR, BR TO TEST END
5566 TRAP C$ESCAPE
5567 .WORD L10036-.
5568 DMRIN EXERR ;ENABLE EXTENDED ERROR NOTIFICATION
5569 ;**** MACRO EXPANSION ****
5570 JSR PC, $DMRIN ;CALL DMR MODE INPUT ROUTINE
5571 .WORD EXERR ;INPUT COMMAND
5572 .WORD 0 ;NO SEL4
5573 .WORD 0 ;NO SEL6
5574 ;****
5575
5576 ESCAPE TST ;IF ERROR, BR TO TEST END
5577 TRAP C$ESCAPE
5578 .WORD L10036-.
5579 SHJTDN ;HALT THE DMR
5580 ;**** MACRO EXPANSION ****
5581 JSR PC, $HALT ;DMR HALT ROUTINE.
5582 ;****
5583 ESCAPE TST ;IF ERROR, BR TO TEST END

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5584 026776 104410
5585 027000 000264
5586 027002 132737 000001 002736 BITB #BIT0,BASE+ISP13 ;CHECK EXT ENABLE BIT IN THE BASE TABLE.
5587 ;IMAGE OF SCRATCH PAD 13.
5588 027010 001005 BNE 10$ ;BIT SET - OK.
5589 027012 ERRDF 24,EMT7 ;ERROR EXT ENABLE CLEAR
5590 027012 104455 TRAP C$LRDF
5591 027014 000030 .WORD 24
5592 027016 027574 .WORD EMT7
5593 027020 000000 .WORD 0
5594 027022 000430 BR 20$
5595 027024 10$:
5596 027024 BASEIN LPLU,BASE,RES.DMR ;BASE IN COMMAND WITH RESUME SET.
5597 ;**** MACRO EXPANSION ****
5598 027024 004737 011266 JSR PC, $BASEI ;CALL BASE IN ROUTINE
5599 027030 004000 .WORD LPLU ;MAINTENANCE MODE BITS TO SET IN BSEL1
5600 027032 002640 .WORD BASE ;BASE TABLE ADDRESS
5601 027034 010522 .WORD RES!DMR ;MODE
5602 ;****
5603
5604 027036 DMRIN DXERR ;DISABLE EXTENDED ERROR NOTIFICATION.
5605 ;**** MACRO EXPANSION ****
5606 027036 004737 012062 JSR PC, $DMRIN ;CALL DMR MODE INPUT ROUTINE
5607 027042 000007 .WORD DXERR ;INPUT COMMAND
5608 027044 000000 .WORD 0 ;NO SEL4
5609 027046 000000 .WORD 0 ;NO SEL6
5610 ;****
5611
5612 027050 ESCAPE TST ;IF ERROR, BR TO TEST END
5613 027050 104410 TRAP C$ESCAPE
5614 027052 000212 .WORD L10036-.
5615 027054 SHUTDN ;HALT THE DMR
5616 ;**** MACRO EXPANSION ****
5617 027054 004737 012562 JSR PC, $HALT ;DMR HALT ROUTINE.
5618 ;****
5619 027060 ESCAPE TST ;IF ERROR, BR TO TEST END.
5620 027060 104410 TRAP C$ESCAPE
5621 027062 000202 .WORD L10036-.
5622 027064 132737 000001 002736 BITB #BIT0,BASE+ISP13 ;CHECK EXT ENABLE BIT IN THE BASE TABLE.
5623 ;IMAGE OF SCRATCH PAD 13.
5624 027072 001404 BEQ 20$ ;IF CLEAR OK
5625 027074 ERRDF 24,EMT7 ;ERROR EXT ENABLE SET
5626 027074 104455 TRAP C$ERDF
5627 027076 000030 .WORD 24
5628 027100 027574 .WORD EMT7
5629 027102 000000 .WORD 0
5630 027104 20$:
5631 027104 ENDSUB
5632 027104 L10037:
5633 027104 104403 TRAP C$ESUB
5634
5635 027106 BGNSUB
5636 027106 T5.2:
5637 027106 104402 TRAP C$BSUB
5638 027110 CLEAR ;MACRO FOR MASTER CLEAR COMMAND
5639 ;**** MACRO EXPANSION ****

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5640 027110 004737 011070 JSR PC, $MSCLR ;ISSUE A DMR MASTER CLEAR
5641 ;*****
5642
5643 027114 ESCAPE TST ;IF ERROR, BR TO TEST END
5644 027114 104410 TRAP C$ESCAPE
5645 027116 000146 .WORD L10036-.
5646
5647 027120 BASEIN ;BASE IN COMMAND WITH LINE UNIT LOOP,
5648 ;***** MACRO EXPANSION *****
5649 027120 004737 011266 JSR PC, $BASEI ;CALL BASE IN ROUTINE WITH DEFAULTS
5650 027124 004000 .WORD LPLU ;SET LINE UNIT LOOP
5651 027126 002640 .WORD BASE ;BASE TABLE ADDRESS
5652 027130 000522 .WORD DMR ;DMR-11 MODE
5653 ;*****
5654
5655 027132 ESCAPE TST ;IF ERROR, BR TO TEST END
5656 027132 104410 TRAP C$ESCAPE
5657 027134 000130 .WORD L10036-.
5658 027136 DMRIN TIMER,0,54 ;SET REP/SELECT TIMER VALUE
5659 ;***** MACRO EXPANSION *****
5660 027136 004737 012062 JSR PC, $DMRIN ;CALL DMR MODE INPUT ROUTINE
5661 027142 000012 .WORD TIMER ;INPUT COMMAND
5662 027144 000000 .WORD 0 ;SEL4 VALUE (OR BITS TO CLEAR IN BSEL6)
5663 027146 000054 .WORD 54 ;SEL6 VALUE (OR BITS TO SET IN BSEL6)
5664 ;*****
5665
5666 027150 ESCAPE TST ;IF ERROR, BR TO TEST END
5667 027150 104410 TRAP C$ESCAPE
5668 027152 000112 .WORD L10036-.
5669
5670 ;SET THRESHOLD VALUES AS FOLLOWS:
5671 ;BSEL4 = NAKS RECEIVED (3)
5672 ;BSEL5 = NAKS TRANSMITTED (13)
5673 ;BSEL6 = REP/SEL SENT (15)
5674 ;BSEL7 = NO BUFFFER (4)
5675 027154 DMRIN THRESH,5403,2015
5676 ;***** MACRO EXPANSION *****
5677 027154 004737 012062 JSR PC, $DMRIN ;CALL DMR MODE INPUT ROUTINE
5678 027160 000013 .WORD THRESH ;INPUT COMMAND
5679 027162 005403 .WORD 5403 ;SEL4 VALUE (OR BITS TO CLEAR IN BSEL6)
5680 027164 002015 .WORD 2015 ;SEL6 VALUE (OR BITS TO SET IN BSEL6)
5681 ;*****
5682
5683 027166 ESCAPE TST ;IF ERROR, BR TO TEST END
5684 027166 104410 TRAP C$ESCAPE
5685 027170 000074 .WORD L10036-.
5686 027172 SHUTDN ;HALT THE DMR.
5687 ;***** MACRO EXPANSION *****
5688 027172 004737 012562 JSR PC, $HALT ;DMR HALT ROUTINE.
5689 ;*****
5690 027176 ESCAPE TST ;IF ERROR, BR TO TEST END
5691 027176 104410 TRAP C$ESCAPE
5692 027200 000064 .WORD L10036-.
5693 027202 122737 000054 002715 CMPB #54,BASE+PRETIM ;CHECK REP/SEL TIME IN BASE TABLE.
5694 027210 001020 BNE 10$ ;IF NOT 54, BR TO ERROR.
5695 027212 122737 000015 002724 CMPB #15,BASE+TH3L ;CHECK REP. THRESH. IN BASE TABLE.

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5696 027220 001014          BNE      10$          ;IF NOT 15, BR TO ERROR.
5697 027222 122737 000003 002720  CMPB    #3,BASE+TH1L ;CHECK NAK RCVD. THRESH. IN BASE TABLE.
5698 027230 001010          BNE      10$          ;IF NOT 3, BR TO ERROR.
5699 027232 122737 000013 002722  CMPB    #13,BASE+TH2L ;CHECK NAK SENT THRESH. IN BASE TABLE.
5700 027240 001004          BNE      10$          ;IF NOT 13, BR TO ERROR
5701 027242 122737 000004 002726  CMPB    #4,BASE+TH4L ;CHECK NO BUF. THRESH. IN BASE TABLE.
5702 027250 001404          BEQ      20$          ;IF 4, ALL CHECKS OK - EXIT
5703 027252                10$:
5704 027252                ERRDF   24,EMT8,ERRT3
5705 027252 104455
5706 027254 000030
5707 027256 027625
5708 027260 027266
5709 027262                20$:
5710 027262                ENDSUB
5711 027262
5712 027262 104403
5713 027264                ENDTST
5714 027264
5715 027264 104401
5716 027266
5717 027266                BGNMSG  ERRT3
5718 027266
5719 027266                PRINTB #FMG1,@SEL0,@SEL2 ;PRINT SEL0 AND SEL2
5720 027266 017746 152742
5721 027272 017746 152734
5722 027276 012746 016302
5723 027302 012746 000003
5724 027306 010600
5725 027310 104414
5726 027312 062706 000010
5727 027316                PRINTB #FMT11,<B,BASE+ISP13> ;PRINT OUT THE IMAGE OF SCRATCH PAD 13.
5728 027316 005046
5729 027320 153716 002736
5730 027324 012746 027662
5731 027330 012746 000002
5732 027334 010600
5733 027336 104414
5734 027340 062706 000006
5735 027344 122737 000054 002715  CMPB    #54,BASE+PRETIM ;IS REP/SEL TIME OK?
5736 027352 001413          BEQ      1$          ;BR IF OK
5737 027354                PRINTB #FMT12,<B,BASE+PRETIM> ;PRINT IT OUT.
5738 027354 005046
5739 027356 153716 002715
5740 027362 012746 027713
5741 027366 012746 000002
5742 027372 010600
5743 027374 104414
5744 027376 062706 000006
5745 027402                1$:
5746 027402 122737 000003 002720  CMPB    #3,BASE+TH1L ;IS NAK RCVD OK?
5747 027410 001413          BEQ      2$          ;BR IF OK.
5748 027412                PRINTB #FMT13,<B,BASE+TH1L> ;PRINT IT OUT
5749 027412 005046
5750 027414 153716 002720
5751 027420 012746 027750

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TRAP  C$ERDF
.WORD 24
.WORD EMT8
.WORD ERRT3
L10040: TRAP  C$ESUB
L10036: TRAP  C$ETST
ERRT3::
MOV   @SEL2,-(SP)
MOV   @SEL0,-(SP)
MOV   #FMG1,-(SP)
MOV   #3,-(SP)
MOV   SP,R0
TRAP  C$PNTB
ADD   #10,SP
CLR   -(SP)
BISB  BASE+ISP13,(SP)
MOV   #FMT11,-(SP)
MOV   #2,-(SP)
MOV   SP,R0
TRAP  C$PNTB
ADD   #6,SP
CLR   -(SP)
BISB  BASE+PRETIM,(SP)
MOV   #FMT12,-(SP)
MOV   #2,-(SP)
MOV   SP,R0
TRAP  C$PNTB
ADD   #6,SP
CLR   -(SP)
BISB  BASE+TH1L,(SP)
MOV   #FMT13,-(SP)

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5752	027424	012746	000002							MOV	#2,-(SP)
5753	027430	010600								MOV	SP,R0
5754	027432	104414								TRAP	C\$PNTB
5755	027434	062706	000006							ADD	#6,SP
5756	027440				2\$:						
5757	027440	122737	000013	002722		CMPB	#13,BASE+TH2L	;IS NAK SENT OK?			
5758	027446	001413				BEQ	3\$;BR IF OK.			
5759	027450					PRINTB	#FMT14,<B,BASE+TH2L>	;PRINT IT OUT			
5760	027450	005046								CLR	-(SP)
5761	027452	153716	002722							BISB	BASE+TH2L,(SP)
5762	027456	012746	030005							MOV	#FMT14,-(SP)
5763	027462	012746	000002							MOV	#2,-(SP)
5764	027466	010600								MOV	SP,R0
5765	027470	104414								TRAP	C\$PNTB
5766	027472	062706	000006							ADD	#6,SP
5767	027476				3\$:						
5768	027476	122737	000015	002724		CMPB	#15,BASE+TH3L	;IS REP LEVEL OK?			
5769	027504	001413				BEQ	4\$;BR IF OK.			
5770	027506					PRINTB	#FMT15,<B,BASE+TH3L>	;PRINT IT OUT			
5771	027506	005046								CLR	-(SP)
5772	027510	153716	002724							BISB	BASE+TH3L,(SP)
5773	027514	012746	030042							MOV	#FMT15,-(SP)
5774	027520	012746	000002							MOV	#2,-(SP)
5775	027524	010600								MOV	SP,R0
5776	027526	104414								TRAP	C\$PNTB
5777	027530	062706	000006							ADD	#6,SP
5778	027534				4\$:						
5779	027534	122737	000004	002726		CMPB	#4,BASE+TH4L	;IS NO BUFFER LEVEL OK?			
5780	027542	001413				BEQ	5\$;BR IF OK.			
5781	027544					PRINTB	#FMT16,<B,BASE+TH4L>	;PRINT IT OUT			
5782	027544	005046								CLR	-(SP)
5783	027546	153716	002726							BISB	BASE+TH4L,(SP)
5784	027552	012746	030077							MOV	#FMT16,-(SP)
5785	027556	012746	000002							MOV	#2,-(SP)
5786	027562	010600								MOV	SP,R0
5787	027564	104414								TRAP	C\$PNTB
5788	027566	062706	000006							ADD	#6,SP
5789	027572				5\$:						
5790	027572				ENDMSG						
5791	027572										
5792	027572	104423								L10041:	TRAP C\$MSG
5793											
5794											
5795	027574	054105	027124	042440	EMT7:	.ASCIZ	/EXT. ERROR BIT INCORRECT/				
5796	027602	051122	051117	041040							
5797	027610	052111	044440	041516							
5798	027616	051117	042522	052103							
5799	027624	000									
5800	027625	104	051115	046440	EMT8:	.ASCIZ	/DMR MODE INPUT COMMAND ERROR/				
5801	027632	042117	020105	047111							
5802	027640	052520	020124	047503							
5803	027646	046515	047101	020104							
5804	027654	051105	047522	000122							
5805											
5806	027662	040445	046511	043501	FMT11:	.ASCIZ	/%AIMAGE OF SP 13 = %D3%N/				
5807	027670	020105	043117	051440							

5808	027676	020120	031461	036440	
5809	027704	022440	031504	047045	
5810	027712	000			
5811	027713	045	051101	050105	FMT12: .ASCIZ /%AREP-SEL TIME VALUE = %D3%N/
5812	027720	051455	046105	052040	
5813	027726	046511	020105	040526	
5814	027734	052514	020105	020075	
5815	027742	042045	022463	000116	
5816	027750	040445	040516	020113	FMT13: .ASCIZ /%ANAK RCVD THRESHOLD = %D3%N/
5817	027756	041522	042126	052040	
5818	027764	051110	051505	047510	
5819	027772	042114	036440	022440	
5820	030000	031504	047045	000	
5821	030005	045	047101	045501	FMT14: .ASCIZ /%ANAK SENT THRESHOLD = %D3%N/
5822	030012	051440	047105	020124	
5823	030020	044124	042522	044123	
5824	030026	046117	020104	020075	
5825	030034	042045	022463	000116	
5826	030042	040445	042522	020120	FMT15: .ASCIZ /%AREP SENT THRESHOLD = %D3%N/
5827	030050	042523	052116	052040	
5828	030056	051110	051505	047510	
5829	030064	042114	036440	022440	
5830	030072	031504	047045	000	
5831	030077	045	047101	020117	FMT16: .ASCIZ /%ANO BUFFER THRESHOLD = %D3%N/
5832	030104	052502	043106	051105	
5833	030112	052040	051110	051505	
5834	030120	047510	042114	036440	
5835	030126	022440	031504	047045	
5836	030134	000			
5837		030136			.EVEN
5838					
5839					

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030140 004737 011070
030144 104410
030146 000404
030150
030150 004737 011266
030154 004000
030156 002640
030160 000522
030162
030162 104410
030164 000366
030166
030166 004737 011522
030172 000000
030174
030174 104410
030176 000354
030200 052777 000057 152024
030206

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.SBTTL TEST 6 - CONTROL IN COMMAND
*****
* TEST 6 - DMR-11
* CONTROL IN COMMAND TEST -
* SUBTEST 1 - CONTROL IN, FULL DUPLEX, DDCMP MODE. ENSURE THAT
* THE HALF-DUPLEX BIT IS CLEAR IN THE MODEM STATUS WORD,
* ALSO ENSURE THAT DDCMP MODE BIT IS SET IN SCRATCH PAD 7.
* SUBTEST 2 - CONTROL IN, HALF DUPLEX. ENSURE THAT THE HALF DUPLEX
* BIT IS SET.
* SUBTEST 3 - CONTROL IN, MAINTENANCE MODE. ENSURE THAT MAINT. MODE
* BIT IS SET IN SCRATCH PAD 7.
* SUBTEST 4 - CONTROL IN USING SELECTED LOOPBACK. ISSUE A CONTROL IN
* USING THE USER SELECTED LOOPBACK. IF THE LOOPBACK IS
* NOT CORRECT, DMR RUN MODE ACKNOWLEDGE WILL NOT BE
* RECEIVED.
*****
BGNTST
BGNSUB
T6:
T6.1: TRAP C$BSUB
CLEAR ;MACRO FOR MASTER CLEAR
;**** MACRO EXPANSION ****
JSR PC, $MSCLR ;ISSUE A DMR MASTER CLEAR
;****
ESCAPE TST ;IF ERROR, BR TO TEST END.
TRAP C$ESCAPE
;WORD L10042-.
BASEIN ;MACRO FOR BASE IN COMMAND
;**** MACRO EXPANSION ****
JSR PC, $BASEI ;CALL BASE IN ROUTINE WITH DEFAULTS
;WORD LPLU ;SET LINE UNIT LOOP
;WORD BASE ;BASE TABLE ADDRESS
;WORD DMR ;DMR-11 MODE
;****
ESCAPE TST ;IF ERROR, BR TO TEST END.
TRAP C$ESCAPE
;WORD L10042-.
CNTRIN ;MACRO FOR CONTROL IN (FULL DUPLEX)
;**** MACRO EXPANSION ****
JSR PC, $CNTIN ;CALL CONTROL IN ROUTINE WITH DEFAULT
;WORD 0 ;SEL6 - FULL DUPLEX, RUN MODE, 1 SEC START.
;****
ESCAPE TST ;IF ERROR, BR TO TEST END.
TRAP C$ESCAPE
;WORD L10042-.
BIS #RQI!RMODEM,@SELO ;SET RQI AND READ MODEM COMMAND
WAIT RDI ;WAIT FOR RDI TO BE SET
;**** MACRO EXPANSION ****

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5896 030206 004737 010276 JSR PC, $WAIT ;CALL WAIT ROUTINE
5897 030212 000000 .WORD 0 ;FLAG THAT WE'RE WAITING FOR RDI
5898 ;*****
5899 030214 032777 000020 152014 BIT #BIT4,@SEL4 ;IS THE HDX BIT SET IN MODEM STATUS REG?
5900 030222 001404 BEQ 10$ ;OK - IF BIT CLEAR
5901 030224 ERRDF 21,EMT9 ;ERROR HDX BIT SET
5902 030224 104455 TRAP C$ERDF
5903 030226 000025 .WORD 21
5904 030230 030554 .WORD EMT9
5905 030232 000000 .WORD 0
5906 030234 10$:
5907 030234 WAIT RQI ;CLEAR RQI AND WAIT FOR RDI TO CLFAR.
5908 ;***** MACRO EXPANSION *****
5909 030234 004737 010706 JSR PC, $CLRQI ;CLEAR RQI AND WAIT FOR IT TO BE CLEARED.
5910 ;*****
5911 030240 SHUTDN ;HALT DMR
5912 ;***** MACRO EXPANSION *****
5913 030240 004737 012562 JSR PC, $HALT ;DMR HALT ROUTINE.
5914 ;*****
5915 030244 ESCAPE TST ;IF ERROR, EXIT.
5916 030244 104410 TRAP C$ESCAPE
5917 030246 000304 .WORD L10042-.
5918 030250 132737 000020 002732 BITB #BIT4,BASE+ISP7 ;IS THE DDCMP RUN BIT SET IN IMAGE OF SP 7.
5919 030256 001004 BNE 20$
5920 030260 ERRDF 21,EMT10 ;ERROR DDCMP RUN BIT NOT SET
5921 030260 104455 TRAP C$ERDF
5922 030262 000025 .WORD 21
5923 030264 030604 .WORD EMT10
5924 030266 000000 .WORD 0
5925 030270 20$:
5926 030270 ENDSUB
5927 030270 L10043:
5928 030270 104403 TRAP C$ESUB
5929 ;
5930 030272 BGNSUB
5931 030272 T6.2:
5932 030272 104402 TRAP C$BSUB
5933 030274 BASEIN LPLU,BASE,RES!DMR ;BASE IN WITH RESUME.
5934 ;***** MACRO EXPANSION *****
5935 030274 004737 011266 JSR PC, $BASEI ;CALL BASE IN ROUTINE
5936 030300 004000 .WORD LPLU ;MAINTENANCE MODE BITS TO SET IN BSEL1
5937 030302 002640 .WORD BASE ;BASE TABLE ADDRESS
5938 030304 010522 .WORD RES!DMR ;MODE
5939 ;*****
5940 ;
5941 030306 CNTRIN HDX ;CONTROL IN COMMAND WITH HDX.
5942 ;***** MACRO EXPANSION *****
5943 030306 004737 011522 JSR PC, $CNTIN ;CALL CONTROL IN ROUTINE
5944 030312 002000 .WORD HDX ;SEL6 - (DUPLEX, MODE)
5945 ;*****
5946 ;
5947 030314 ESCAPE TST ;IF ERROR, BR TO TEST END.
5948 030314 104410 TRAP C$ESCAPE
5949 030316 000234 .WORD L10042-.
5950 030320 052777 000057 151704 BIS #RQI!RMODEM,@SELO ;SET RQI AND READ MODEM COMMAND
5951 030326 WAIT RDI ;WAIT FOR RDI TO BE SET
  
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5952
5953 030326 004737 010276 JSR PC, $WAIT ;**** MACRO EXPANSION ****
5954 030332 000000 .WORD 0 ;CALL WAIT ROUTINE
5955 ;FLAG THAT WE'RE WAITING FOR RDI
5956 030334 032777 000020 151674 BIT #BIT4,@SEL4 ;****
5957 030342 001004 BNE 10$ ;IS THE HDX BIT SET IN MODEM STATUS REG?
5958 030344 ERRDF 21,EMT11 ;OK - IF BIT SET
5959 030344 104455 ;ERROR HDX BIT CLEAR. TRAP C$ERDF
5960 030346 000025 .WORD 21
5961 030350 030632 .WORD EMT11
5962 030352 000000 .WORD 0
5963 030354
5964 030354 10$: SHUTDN ;HALT THE DMR.
5965 ;**** MACRO EXPANSION ****
5966 030354 004737 012562 JSR PC, $HALT ;DMR HALT ROUTINE.
5967 ;****
5968
5969 030360 ENDSUB
5970 030360 L10044:
5971 030360 104403 TRAP C$ESUB
5972
5973 030362 BGNSUB
5974 030362 T6.3:
5975 030362 104402 TRAP C$BSUB
5976 030364 CLEAR ;MACRO FOR MASTER CLEAR
5977 ;**** MACRO EXPANSION ****
5978 030364 004737 011070 JSR PC, $MSCLR ;ISSUE A DMR MASTER CLEAR
5979 ;****
5980
5981 030370 ESCAPE TST ;IF ERROR, BR TO TEST END.
5982 030370 104410 TRAP C$ESCAPE
5983 030372 000160 .WORD L10042-.
5984 030374 BASEIN ;MACRO FOR BASE IN COMMAND
5985 ;**** MACRO EXPANSION ****
5986 030374 004737 011266 JSR PC, $BASEI ;CALL BASE IN ROUTINE WITH DEFAULTS
5987 030400 004000 .WORD LPLU ;SET LINE UNIT LOOP
5988 030402 002640 .WORD BASE ;BASE TABLE ADDRESS
5989 030404 000522 .WORD DMR ;DMR-11 MODE
5990 ;****
5991
5992 030406 ESCAPE TST ;IF ERROR, BR TO TEST END.
5993 030406 104410 TRAP C$ESCAPE
5994 030410 000142 .WORD L10042-.
5995 030412 CNTRIN MAINT ;MACRO FOR CONTROL IN (MAINT. MODE)
5996 ;**** MACRO EXPANSION ****
5997 030412 004737 011522 JSR PC, $CNTIN ;CALL CONTROL IN ROUTINE
5998 030416 000400 .WORD MAINT ;SEL6 - (DUPLEX, MODE)
5999 ;****
6000
6001 030420 ESCAPE TST ;IF ERROR, BR TO TEST END.
6002 030420 104410 TRAP C$ESCAPE
6003 030422 000130 .WORD L10042-.
6004 030424 SHUTDN ;HALT
6005 ;**** MACRO EXPANSION ****
6006 030424 004737 012562 JSR PC, $HALT ;DMR HALT ROUTINE.
6007 ;****

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CZDMIDO DMR-11 FUNCTIONAL TESTS MACY11 30A(1052) 29-JUL-81 12:42 L 10 PAGE 129 SEQ 0128
CZDMID.P11 29-JUL-81 11:32 TEST 6 - CONTROL IN COMMAND

6008 030430 FESCAPE TST ;IF ERROR, BR TO TEST END.
6009 030430 104410 TRAP C$ESCAPE
6010 030432 000120 .WORD L10042-.
6011 030434 132737 000002 002732 BITB #BIT1,BASE+ISP7 ;IS THE MAINTENANCE BIT SET IN IMAGE OF SP 7.
6012 030442 001004 BNE 10$
6013 030444 ERRDF 21,EMT12 ;ERROR - MAINT. BIT NOT SET.
6014 030444 104455 TRAP C$ERDF
6015 030446 000025 .WORD 21
6016 030450 030666 .WORD EMT12
6017 030452 000000 .WORD 0
6018 030454 10$:
6019 030454 ENDSUB
6020 030454
6021 030454 104403 I10045: TRAP C$ESUB
6022 030454
6023 030456 BGNSUB
6024 030456
6025 030456 104402 T6.4: TRAP C$BSUB
6026 030456
6027 030460 CLEAR ;MACRO FOR MASTER CLEAR
6028 ;**** MACRO EXPANSION ****
6029 030460 004737 011070 JSR PC, $MSCLR ;ISSUE A DMR MASTER CLEAR
6030 ;****
6031
6032 030464 FESCAPE TST ;IF ERROR, BR TO TEST END.
6033 030464 104410 TRAP C$ESCAPE
6034 030466 000064 .WORD L10042-.
6035 030470 005737 002254 TST D%TURN ;IS INTERNAL LOOPBACK REQUESTED?
6036 030474 001004 BNE 1$ ;IF NOT, BR
6037 030476 052737 004000 030520 BIS #LPLU,100$ ;SET LINE UNIT LOOPBACK.
6038 030504 000403 BR 2$
6039 030506 1$:
6040 030506 042737 004000 030520 BIC #LPLU,100$ ;CLEAR LINE UNIT LOOPBACK.
6041 030514 2$:
6042 030514 CALL $BASEI ;BASE IN COMMAND.
6043 030520 000000 100$: .WORD 0 ;MAINTENANCE BITS (L. U. LOOPBACK?)
6044 030522 002640 .WORD BASE ;BASE TABLE ADDRESS.
6045 030524 000522 .WORD DMR ;DMR MODE.
6046 030526 ESCAPE TST ;IF ERROR, BR TO TEST END.
6047 030526 104410 TRAP C$ESCAPE
6048 030530 000022 .WORD L10042-.
6049 030532 CALL $LOOP ;EXTENDED DMR COMMAND TO SET MAINT. BITS
6050 ;IF NEEDED. THIS WILL ALLOW MODEM LOOPBACK
6051 ;IF THE USER REQUESTED IT.
6052 030536 ESCAPE TST ;IF ERROR, BR TO TEST END.
6053 030536 104410 TRAP C$ESCAPE
6054 030540 000012 .WORD L10042-.
6055 030542 CNTRIN ;MACRO FOR CONTROL IN (FULL DUPLEX)
6056 ;**** MACRO EXPANSION ****
6057 030542 004737 011522 JSR PC, $CNTIN ;CALL CONTROL IN ROUTINE WITH DEFAULT
6058 030546 000000 .WORD 0 ;SEL6 - FULL DUPLEX, RUN MODE, 1 SEC START.
6059 ;****
6060 030550 ENDSUB
6061 030550
6062 030550 104403 L10046: TRAP C$FSUB
6063

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6105 030716
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6108 030716
6109 030716
6110 030716 104402
6111 030720
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6113 030720 004737 011070
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6116 030724
6117 030724 104410
6118 030726 000232
6119 030730
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6121 030730 004737 011266
6122 030734 004000
6123 030736 002640
6124 030740 000522
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6126
6127 030742
6128 030742 104410
6129 030744 000214
6130
6131 030746 012701 000005
6132 030752 012702 031162
6133 030756
6134 030756 012237 030772
6135 030762 004737 012062
6136 030766 000005
6137 030770 000377
6138 030772 000000
6139 030774
6140 030774 104410
6141 030776 000162
6142
6143 031000 052777 000057 151224
6144 031006
6145
6146 031006 004737 010276

```
.SBTTL TEST 7 - MODEM WRITE COMMAND
*****
* TEST 7 - DMR-11
* MODEM WRITE COMMAND
* SUBTEST 1 - WRITE DATA PATTERNS INTO THE MODEM WRITE REGISTER.
* ENSURE THAT ON THE NEXT MODEM READ THAT THE
* MICROCODE RETURNS THE PATTERN WRITTEN INTO BSEL6.
* SUBTEST 2 - ATTEMPT TO WRITE BOTH THE HALF-DUPLEX BIT AND THE
* RTS HOLD BIT. THE MICROCODE SHOULD NOT ALLOW THIS
* TO HAPPEN. WHEN READING THE MODEM STATUS, ONLY
* THE HALF-DUPLEX SHOULD BE SET.
*****
BGNTST T7::
BGNSUB T7.1:
CLEAR ;MACRO FOR MASTER CLEAR
;**** MACRO EXPANSION ****
JSR PC, $MSCLR ;ISSUE A DMR MASTER CLEAR
;****
ESCAPE TST ;IF ERROR, BR TO TEST END.
TRAP C$ESCAPE
;WORD L10047-.
BASEIN ;BASE IN COMMAND.
;**** MACRO EXPANSION ****
JSR PC, $BASEI ;CALL BASE IN ROUTINE WITH DEFAULTS
;WORD LPLU ;SET LINE UNIT LOOP
;WORD BASE ;BASE TABLE ADDRESS
;WORD DMR ;DMR-11 MODE
;****
ESCAPE TST ;IF ERROR, BR TO TEST END.
TRAP C$ESCAPE
;WORD L10047-.
MOV #5,R1 ;COUNTER
MOV #MODEM,R2 ;PATTERN TO WRITE INTO MODEM
10$: MOV (R2)+,15$ ;WRITE PATTERN
JSR PC,$DMRIN ;ISSUE DMR MODE COMMAND
;WORD WMODEM ;WRITE MODEM COMMAND
;WORD 377 ;CLEAR ALL BITS IN BSEL6
;WORD 0 ;SET THE BITS IN BSEL6 (FROM PATTERN)
15$: ESCAPE TST ;IF ERROR, BR TO TEST END.
TRAP C$ESCAPE
;WORD L10047-.
BIS #RQI!RMODEM,@SELO ;SET RQI AND READ MODEM COMMAND
WAIT RDI ;WAIT FOR RDI TO BE SET.
;**** MACRO EXPANSION ****
JSR PC, $WAIT ;CALL WAIT ROUTINE
```

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6147 031012 000000          .WORD 0          ;FLAG THAT WE'RE WAITING FOR RDI
6148                                     ;****
6149 031014          ESCAPE TST          ;IF ERROR, EXIT TEST.
6150 031014 104410                                     TRAP C$ESCAPE
6151 031016 000142                                     .WORD L10047-.
6152 031020
6153 031020 127737 151214 030772 20$: CMPB @BSEL6,15$ ;DID THE MICROCODE COPY THE BITS?
6154 031026 001406          BEQ 25$          ;IF YES CONTINUE
6155 031030 013703 030772          MOV 15$,R3      ;SAVE THE PATTERN FOR THE ERROR MESSAGE.
6156 031034          ERRDF 22.FMT13,ERRT2 ;WRITE MODEM ERROR
6157 031034 104455                                     TRAP C$ERDF
6158 031036 000026                                     .WORD 22
6159 031040 031226                                     .WORD EMT13
6160 031042 031174                                     .WORD ERRT2
6161 031044
6162 031044          25$: WAIT RQI          ;CLEAR RQI AND WAIT FOR RDI TO CLEAR.
6163                                     ;**** MACRO EXPANSION ****
6164 031044 004737 010706          JSR PC, $CLRQI ;CLEAR RQI AND WAIT FOR IT TO BE CLEARED.
6165                                     ;****
6166 031050          ESCAPE TST          ;IF ERROR, EXIT TEST.
6167 031050 104410                                     TRAP C$ESCAPE
6168 031052 000106                                     .WORD L10047-.
6169 031054 005301
6170 031056 001337          DEC R1          ;DECREMENT COUNTER
6171 031060          BNE 10$          ;CONTINUE UNTIL ALL 5 PATTERNS TRIED.
6172          30$: ENDSUB
6173 031060
6174 031060
6175 031060 104403                                     L10050: TRAP C$ESUB
6176
6177 031062          BGNSUB
6178 031062
6179 031062 104402                                     T7.2: TRAP C$BSUB
6180
6181 031064          DMRIN WMODEM,377,21 ;ATTEMPT TO WRITE MODEM HDX AND RTS.
6182                                     ;**** MACRO EXPANSION ****
6183 031064 004737 012062          JSR PC, $DMRIN ;CALL DMR MODE INPUT ROUTINE
6184 031070 000005          .WORD WMODEM ;INPUT COMMAND
6185 031072 000377          .WORD 377      ;SEL4 VALUE (OR BITS TO CLEAR IN BSEL6)
6186 031074 000021          .WORD 21      ;SEL6 VALUE (OR BITS TO SET IN BSEL6)
6187                                     ;****
6188
6189 031076          ESCAPE TST          ;IF ERROR, BR TO END.
6190 031076 104410                                     TRAP C$ESCAPE
6191 031100 000060                                     .WORD L10047-.
6192 031102 052777 000057 151122          BIS #RQI!RMODEM,@SELO ;SET RQI AND READ MODEM COMMAND.
6193 031110          WAIT RDI          ;WAIT FOR RDI TO BE SET
6194                                     ;**** MACRO EXPANSION ****
6195 031110 004737 010276          JSR PC, $WAIT ;CALL WAIT ROUTINE
6196 031114 000000          .WORD 0      ;FLAG THAT WE'RE WAITING FOR RDI
6197                                     ;****
6198 031116          ESCAPE TST          ;IF ERROR, EXIT TEST.
6199 031116 104410                                     TRAP C$ESCAPE
6200 031120 000040                                     .WORD L10047-.
6201
6202 031122 122777 000020 151110          CMPB #20,@BSEL6 ;IS ONLY HDX SET?

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6203 031130 001406          BEQ      10$          ;IF YES - OK
6204 031132 012703 000021  MOV     #21,R3       ;SAVE THE PATTERN FOR THE ERROR MESSAGE.
6205 031136                ERRDF   22,EMT13,ERRT2
6206 031136 104455                TRAP   C$ERDF
6207 031140 000026                .WORD  22
6208 031142 031226                .WORD  EMT13
6209 031144 031174                .WORD  ERRT2
6210 031146                10$:
6211 031146                WAIT   RQI           ;CLEAR RQI AND WAIT FOR RDI TO CLEAR.
6212                ;**** MACRO EXPANSION ****
6213 031146 004737 010706        JSR    PC, $CLRQI   ;CLEAR RQI AND WAIT FOR IT TO BE CLEARED.
6214                ;****
6215 031152                SHUTDN
6216                ;**** MACRO EXPANSION ****
6217 031152 004737 012562        JSR    PC, $HALT    ;DMR HALT ROUTINE.
6218                ;****
6219
6220                ENDSUB
6221 031156                L10051:
6222 031156 104403                TRAP   C$ESUB
6223
6224                ENDTST
6225 031160                L10047:
6226 031160 104401                TRAP   C$ETST
6227
6228 031162 000000 000376 000001  MODEM: .WORD  0,376,1,252,357 ;PATTERN TO WRITE INTO MODEM
6229 031170 000252 000357
6230
6231 031174                BGNMSG  ERRT2
6232 031174                ERRT2::
6233 031174                PRINTB #FMT19,R3,<B,@BSEL6>
6234 031174 005046                CLR    -(SP)
6235 031176 157716 151036        BISB  @BSEL6,(SP)
6236 031202 010346                MOV   R3,-(SP)
6237 031204 012746 031252        MOV   #FMT19,-(SP)
6238 031210 012746 000003        MOV   #3,-(SP)
6239 031214 010600                MOV   SP,R0
6240 031216 104414                TRAP  C$PNTB
6241 031220 062706 000010        ADD   #10,SP
6242 031224                ENDMSG
6243 031224                L10052:
6244 031224 104423                TRAP   C$MSG
6245
6246
6247 031226 051127 052111 020105  EMT13: .ASCIZ  /WRITE MODEM ERROR /
6248 031234 047515 042504 020115
6249 031242 051105 047522 020122
6250 031250                000
6251                031252                .EVEN
6252
6253 031252 040445 051127 052117  FMT19: .ASCIZ  /%AWROTE IN BSEL6: %03%A  MODEM FORMAT IN BSEL6: %03%N/
6254 031260 020105 047111 041040
6255 031266 042523 033114 020072
6256 031274 047445 022463 020101
6257 031302 046440 042117 046505
6258 031310 043040 051117 040515

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6259	031316	020124	047111	041040
6260	031324	042523	033114	020072
6261	031332	047445	022463	000116
6262				
6263				
6264				
6265				

.EVEN

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6279 031340
6280 031340
6281 031340
6282 031340
6283 031340 104402
6284 031342
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6286 031342 004737 011070
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6289 031346
6290 031346 104410
6291 031350 000416
6292 031352
6293
6294 031352 004737 011266
6295 031356 004000
6296 031360 002640
6297 031362 000522
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6300 031364
6301 031364 104410
6302 031366 000400
6303 031370
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6305 031370 004737 011522
6306 031374 000400
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6309 031376
6310 031376 104410
6311 031400 000366
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6318 031402
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6320 031402 004737 012062
6321 031406 000013

```
.SBTTL          TEST 8 - NO BUFFER ERROR

*****
*          TEST 8 - DMR-11
* SUBTEST 1 - TRANSMIT A BUFFER THREE TIMES WIHOUT ASSIGNING A
*              RECEIVE BUFFER.  BY ASSIGNING A NO BUFFER THRESHOLD
*              OF THREE, ENSURE THAT A NO BUFFER ERROR IS RECEIVED
*              AFTER THE THIRD THRANSMISSION.
* SUBTEST 2 - TRANSMIT A BUFFER WITHOUT A RECEIVE BUFFER.
*              ASSIGN THE NAKS THRESHOLD OF 3 AND A NO BUFFER
*              THRESHOLD OF 7.  CHECK THAT THE NAKS ERROR COUNT IS
*              THREE AFTER SHUTDOWN.
*****

BGNTST
          T8::
          T8.1:
          TRAP  C$BSUB
          CLEAR          ;MACRO FOR MASTER CLEAR
          ;***** MACRO EXPANSION *****
          JSR    PC, $MSCLR ;ISSUE A DMR MASTER CLEAR
          ;*****

          ESCAPE TST          ;IF LRROR, BR TO TEST END.
          TRAP  C$ESCAPE
          .WORD  L10053-.

          BASEIN          ;MACRO FOR BASE IN COMMAND
          ;***** MACRO EXPANSION *****
          JSR    PC, $BASEI ;CALL BASE IN ROUTINE WITH DEFAULTS
          .WORD  LPLU      ;SET LINE UNIT LOOP
          .WORD  BASE      ;BASE TABLE ADDRESS
          .WORD  DMR       ;DMR-11 MODE
          ;*****

          ESCAPE TST          ;IF ERROR, BR TO TEST END.
          TRAP  C$ESCAPE
          .WORD  L10053-.

          CNTRIN MAINT          ;MACRO FOR CONTROL IN (FULL DUPLEX AND MAINT)
          ;***** MACRO EXPANSION *****
          JSR    PC, $CNTIN ;CALL CONTROL IN ROUTINE
          .WORD  MAINT      ;SEL6 - (DUPLEX, MODE)
          ;*****

          ESCAPE TST          ;IF ERROR, BR TO TEST END.
          TRAP  C$ESCAPE
          .WORD  L10053-.

          ;SET THRESHOLDS:
          ;NAKS RCVD = 377
          ;NAKS SENT = 377
          ;REP SENT = 377
          ;NO BUFFER = 3
          DMRIN THRESH,177777,1777
          ;***** MACRO EXPANSION *****
          JSR    PC, $DMRIN ;CALL DMR MODE INPUT ROUTINE
          .WORD  THRESH ;INPUT COMMAND
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6322 031410 177777 .WORD 177777 ;SEL4 VALUE (OR BITS TO CLEAR IN BSEL6)
6323 031412 001777 .WORD 1777 ;SEL6 VALUE (OR BITS TO SET IN BSEL6)
6324 ;*****
6325 ;*****
6326 031414 ESCAPE TST ;IF ERROR, BR TO TEST END.
6327 031414 104410 TRAP C$ESCAPE
6328 031416 000350 .WORD L10053-.
6329 031420 012700 000003 MOV #3,R0 ;SET UP A COUNTER
6330 031424 1$: BACCIT ;BA/CC IN COMMAND FOR TRANSMIT
6331 031424 ;***** MACRO EXPANSION *****
6332 ;CALL BA/CC IN ROUTINE WITH DEFAULTS
6333 031424 004737 012272 JSR PC, $BACC ;BA/CC IN TRANSMIT COMMAND
6334 031430 000040 .WORD RQI!BACC ;TRANSMIT BUFFER ADDRESS
6335 031432 002522 .WORD TBUF ;TRANSMIT CHARACTER COUNT
6336 031434 000044 .WORD TCOUNT ;*****
6337 ;*****
6338
6339 031436 WAIT RDO ;WAIT FOR RDO TO BE SET
6340 ;***** MACRO EXPANSION *****
6341 031436 004737 010276 JSR PC, $WAIT ;CALL WAIT ROUTINE
6342 031442 000001 .WORD 1 ;FLAG THAT WE'RE WAITING FOR RDO
6343 ;*****
6344 ESCAPE TST ;IF RDO NOT SET, BR TO TEST END.
6345 031444 104410 TRAP C$ESCAPE
6346 031446 000320 .WORD L10053-.
6347 031450 005300 DEC R0 ;DEC COUNTER
6348 031452 001404 BEQ 10$ ;TRANSMIT FOR 3 TIMES.
6349 031454 042777 000207 150552 BIC #RDO!CMD,@SEL2 ;CLEAR BACC OUT TRANSMIT.
6350 031462 000760 BR 1$ ;TRANSMIT AGAIN
6351 031464 10$: BIT #CNTRL,@SEL2 ;IS THIS A CONTROL OUT?
6352 031464 032777 000001 150542 BNE 20$ ;IF YES, PROCEED.
6353 031472 001005 ERRDF 8,EMG8,ERRG2 ;EXPECTED CONTROL OUT NOT RECEIVED.
6354 031474 104455 TRAP C$ERDF
6355 031474 000010 .WORD 8
6356 031476 000010 .WORD EMG8
6357 031500 017774 .WORD ERRG2
6358 031502 015124 BR 30$ ;EXIT
6359 031504 000410 20$: BIT #NOBFR,@SEL6 ;IS THE NO BUFFER FLAG SET?
6360 031506 032777 000004 150524 BNE 30$ ;IF YES - OK, PROCEED.
6361 031514 001004 ERRDF 9,EMG9,ERRG2 ;WE'RE NOT GETTING EXPECTED RESULT
6362 031516 104455 TRAP C$ERDF
6363 031516 000011 .WORD 9
6364 031516 000011 .WORD EMG9
6365 031520 000011 .WORD ERRG2
6366 031522 020040 ;(EITHER CONTROL OUT OR NOBUF/NAKS)
6367 031524 015124 30$: BIC #RDO!CMD,@SEL2 ;CLEAR CONTROL OUT
6368 031526 WAIT RDO ;EXPECT ANOTHER BACC OUT.
6369 031526 042777 000207 150500 ;***** MACRO EXPANSION *****
6370 031534 ;CALL WAIT ROUTINE
6371 ;FLAG THAT WE'RE WAITING FOR RDO
6372 ;*****
6373 031534 004737 010276 JSR PC, $WAIT
6374 031540 000001 .WORD 1
6375 ;*****
6376 031542 ESCAPE TST ;IF ERROR, BR TO END.
6377 031542 104410 TRAP C$ESCAPE

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6378 031544 000222                                     .WORD  L10053-.
6379 031546 042777 000207 150460      BIC      #RDO!CMD,@SEL2 ;CLEAR BACC OUT.
6380 031554                                SHUTDN   ;HALT DMR
6381                                     ;**** MACRO EXPANSION ****
6382 031554 004737 012562      JSR      PC, $HALT    ;DMR HALT ROUTINE.
6383                                     ;****                               ****
6384 031560                                50$:
6385 031560                                ENDSUB
6386 031560                                     L10054:
6387 031560 104403                                     TRAP   C$ESUB
6388
6389 031562                                BGNSUB
6390 031562                                     T8.2:
6391 031562 104402                                     TRAP   C$BSUB
6392 031564
6393      CLEAR                               ;MACRO FOR MASTER CLEAR
6394 031564 004737 011070      JSR      PC, $MSCLR   ;**** MACRO EXPANSION ****
6395                                     ;ISSUE A DMR MASTER CLFAR
6396                                     ;****                               ****
6397 031570      ESCAPE  TST                               ;IF ERROR, BR TO TEST END.
6398 031570 104410                                     TRAP   C$ESCAPE
6399 031572 000174                                     .WORD  L10053-.
6400 031574
6401      BASEIN                               ;MACRO FOR BASE IN COMMAND
6402 031574 004737 011266      JSR      PC, $BASEI   ;**** MACRO EXPANSION ****
6403 031600 004000      .WORD  LPLU       ;CALL BASE IN ROUTINE WITH DEFAULTS
6404 031602 002640      .WORD  BASE        ;SET LINE UNIT LOOP
6405 031604 000522      .WORD  DMR         ;BASE TABLE ADDRESS
6406                                     ;DMR-11 MODE
6407                                     ;****                               ****
6408 031606      ESCAPE  TST                               ;IF ERROR, BR TO TEST END.
6409 031606 104410                                     TRAP   C$ESCAPE
6410 031610 000156                                     .WORD  L10053-.
6411 031612
6412      CNTRIN                               ;MACRO FOR CONTROL IN (FULL DUPLEX)
6413 031612 004737 011522      JSR      PC, $CNTIN   ;**** MACRO EXPANSION ****
6414 031616 000000      .WORD  0          ;CALL CONTROL IN ROUTINE WITH DEFAULT
6415                                     ;SEL6 - FULL DUPLEX, RUN MODE, 1 SEC START.
6416                                     ;****                               ****
6417 031620      ESCAPE  TST                               ;IF ERROR, BR TO TEST END.
6418 031620 104410                                     TRAP   C$ESCAPE
6419 031622 000144                                     .WORD  L10053-.
6420
6421                                     ;SET THRESHOLDS:
6422                                     ;NAKS RCVD = 3
6423                                     ;NAKS SENT = 3
6424                                     ;REP SENT = 377
6425                                     ;NO BUFFER = 7
6426 031624      DMRIN   THRESH,1403,3777
6427
6428 031624 004737 012062      JSR      PC, $DMRIN   ;**** MACRO EXPANSION ****
6429 031630 000013      .WORD  THRESH      ;CALL DMR MODE INPUT ROUTINE
6430 031632 001403      .WORD  1403        ;INPUT COMMAND
6431 031634 003777      .WORD  3777        ;SEL4 VALUE (OR BITS TO CLEAR IN BSEL6)
6432                                     ;SEL6 VALUE (OR BITS TO SET IN BSEL6)
6433                                     ;****                               ****

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6434 031636          ESCAPE TST          ;IF ERROR, BR TO TEST END.
6435 031636 104410          TRAP          C$ESCAPE
6436 031640 000126          .WORD          L10053-.
6437 031642          BACCIT          ;BA/CC IN COMMAND FOR TRANSMIT
6438          ;**** MACRO EXPANSION ****
6439 031642 004737 012272  JSR      PC, $BACC          ;CALL BA/CC IN ROUTINE WITH DEFAULTS
6440 031646 000040          .WORD  RQI!BACC          ;BA/CC IN TRANSMIT COMMAND
6441 031650 002522          .WORD  TBUF          ;TRANSMIT BUFFER ADDRESS
6442 031652 000044          .WORD  TCOUNT          ;TRANSMIT CHARACTER COUNT
6443          ;****          ****
6444 031654          10$:
6445 031654          WAIT      RDO          ;WAIT FOR RDO TO BE SET
6446          ;**** MACRO EXPANSION ****
6447 031654 004737 010276  JSR      PC, $WAIT          ;CALL WAIT ROUTINE
6448 031660 000001          .WORD  1          ;FLAG THAT WE'RE WAITING FOR RDO
6449          ;****          ****
6450          ESCAPE TST          ;IF RDO NOT SET, BR TO TEST END.
6451 031662 104410          TRAP          C$ESCAPE
6452 031664 000102          .WORD          L10053-.
6453 031666 032777 000001 150340  BIT      #CNTRL,@SEL2          ;IS THIS A CONTROL OUT?
6454 031674 001005          BNE      20$          ;IF YES, PROCEED.
6455 031676          ERRDF  8,EMG8,ERRG2          ;EXPECTED CONTROL OUT NOT RECEIVED.
6456 031676 104455          TRAP          C$ERDF
6457 031700 000010          .WORD          8
6458 031702 017774          .WORD          EMG8
6459 031704 015124          .WORD          ERRG2
6460 031706 000410          BR      30$          ;EXIT
6461 031710          20$:
6462 031710 032777 000004 150322  BIT      #NOBFR,@SEL6          ;IS THE NO BUFFER FLAG SET?
6463 031716 001004          BNE      30$          ;IF YES - OK, PROCEED.
6464 031720          ERRDF  9,EMG9,ERRG2          ;WE'RE NOT GETTING EXPECTED RESULT
6465 031720 104455          TRAP          C$ERDF
6466 031722 000011          .WORD          9
6467 031724 020040          .WORD          EMG9
6468 031726 015124          .WORD          ERRG2
6469          ;(EITHER CONTROL OUT OR NOBUF/NAKS)
6470 031730          30$:
6471 031730          SHUTDN
6472          ;**** MACRO EXPANSION ****
6473 031730 004737 012562  JSR      PC, $HALT          ;DMR HALT ROUTINE.
6474          ;****          ****
6475 031734 123727 002643 000003  CMPB    BASE+3,#3          ;NAKS REC. - NO BUFFER = 3?
6476 031742 001004          BNE      35$          ;IF NOT ERROR
6477 031744 123727 002646 000003  CMPB    BASE+6,#3          ;NAKS SENT - NO BUFFER - 3?
6478 031752 001404          BEQ      40$          ;IF OK - SKIP.
6479 031754          35$:
6480 031754          ERRDF  23,EMT20,ERRT4
6481 031754 104455          TRAP          C$ERDF
6482 031756 000027          .WORD          23
6483 031760 032026          .WORD          EMT20
6484 031762 031770          .WORD          ERRT4
6485
6486 031764          40$:
6487 031764          ENDSUB
6488 031764          L10055:
6489 031764 104403          TRAP          C$ESUB
  
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6490	031766				ENDTST			
6491	031766						L10053:	
6492	031766	104401					TRAP	C\$ETST
6493								
6494								
6495	031770				BGNMSG	ERRT4		
6496	031770						ERRT4::	
6497	031770				PPINTB	#FMG7,<B,BASE+3>,<B,BASE+6>		
6498	031770	005046					CLR	-(SP)
6499	031772	153716	002646				BISB	BASE+6,(SP)
6500	031776	005046					CLR	-(SP)
6501	032000	153716	002643				BISB	BASE+3,(SP)
6502	032004	012746	016522				MOV	#FMG7,-(SP)
6503	032010	012746	000003				MOV	#3,-(SP)
6504	032014	010600					MOV	SP,R0
6505	032016	104414					TRAP	C\$PNTB
6506	032020	062706	000010				ADD	#10,SP
6507	032024				ENDMSG			
6508	032024						L10056:	
6509	032024	104423					TRAP	C\$MESSG
6510								
6511	032026	040516	051513	042440	EMT20:	.ASCIZ /NAKS ERROR/		
6512	032034	051122	051117	000				
6513		032042				.EVEN		
6514								

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6530 032042
6531 032042 104402
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6534 032044 004737 011070
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6537 032050
6538 032050 104410
6539 032052 000500
6540 032054
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6542 032054 004737 011266
6543 032060 004000
6544 032062 002640
6545 032064 000522
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6548 032066
6549 032066 104410
6550 032070 000462
6551 032072
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6553 032072 004737 012562
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6555 032076
6556 032076 104410
6557 032100 000452
6558 032102 012737 000001 002366
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6564 032110 004737 011266
6565 032114 000000
6566 032116 160000
6567 032120 150522
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6570 032122

```
.SBTTL TEST 9 - NON-EXISTENT MEMORY ERROR
*****
* TEST 9 - DMR-11
* NON-EXISTENT MEMORY (NXM) ERROR CHECK
* PERFORM DMR COMMANDS USING NXM ADDRESSES; VERIFY THAT NXM ERROR IS
* REPORTED IN EACH OF THE FOLLOWING SUBTESTS:
* SUBTEST 1 - BASE IN RESUME COMMAND - BASE TABLE ADDRESS IS NXM
* SUBTEST 2 - BA/CC IN RECEIVE COMMAND - BA/CC IN ADDRESS IS NXM
* SUBTEST 3 - BA/CC IN TRANSMIT COMMAND - BA/CC IN ADDRESS IS NXM
*****
BGNTST
BGNSUB
T9.:
T9.1:
TRAP C$BSUB
CLEAR ;MASTER CLEAR MACRO
;**** MACRO EXPANSION ****
;ISSUE A DMR MASTER CLEAR
;****
JSR PC, $MSCLR
ESCAPE TST ;IF ERROR, BR TO TEST END
TRAP C$ESCAPE
;WORD L10057-.
BASEIN ;BASE IN COMMAND - DMR MODE
;**** MACRO EXPANSION ****
;CALL BASE IN ROUTINE WITH DEFAULTS
;SET LINE UNIT LOOP
;BASE TABLE ADDRESS
;DMR-11 MODE
;****
ESCAPE TST ;IF ERROR, BR TO TEST END
TRAP C$ESCAPE
;WORD L10057-.
SHUTDN ;HALT
;**** MACRO EXPANSION ****
;DMR HALT ROUTINE.
;****
ESCAPE TST ;IF ERROR, BR TO TEST END.
TRAP C$ESCAPE
;WORD L10057-.
MOV #CNTRL,ERROR ;THIS FLAG WILL INHIBIT CONTROL OUT
;ERROR REPORTING - BECAUSE WE EXPECT ONE.
BASEIN 0,160000,BIT15!BIT14!RES!DMR ;BASE IN RESUME COMMAND WITH NXM BASE TABLE.
;**** MACRO EXPANSION ****
;CALL BASE IN ROUTINE
;MAINTENANCE MODE BITS TO SET IN BSEL1
;BASE TABLE ADDRESS
;MODE
;****
WAIT RDO ;WAIT FOR RDO TO BE SET
```



```

6571
6572 032122 004737 010276      JSR    PC, $WAIT      ;**** MACRO EXPANSION ****
6573 032126 000001              .WORD 1              ;CALL WAIT ROUTINE
6574                                     ;FLAG THAT WE'RE WAITING FOR RDO
6575 032130 032777 000001 150076  BIT    #CNTRL,@SEL2   ;****                ****
6576 032136 001005              BNE    10$           ;IS THERE A CONTROL OUT REPORTED ?
6577 032140                      ERRDF  8,EMG8,ERRG2   ;IF YES, PROCEED.
6578 032140 104455              ;EXPECTED CONTROL OUT
6579 032142 000010              TRAP   C$ERDF
6580 032144 017774              .WORD 8
6581 032146 015124              .WORD  EMG8
6582 032150 000410              .WORD  ERRG2
6583 032152                      BR     20$           ;EXIT
6584 032152 032777 000400 150060 10$:  BIT    #NXM,@SEL6   ;IS THE NXM FLAG SET?
6585 032160 001004              BNE    20$           ;IF YES - ERROR REPORTED CORRECTLY
6586 032162                      ERRDF  9,EMG9,ERRG2   ;UNEXPECTED CONTROL OUT RECEIVED
6587 032162 104455              TRAP   C$ERDF
6588 032164 000011              .WORD 9
6589 032166 020040              .WORD  EMG9
6590 032170 015124              .WORD  ERRG2
6591 032172                      20$:  BIC    #RDO!CMD,@SEL2 ;CLEAR RDO AND THE COMMAND BITS
6592 032172 042777 000207 150034  CLR    ERROR        ;ALLOW ERROR REPORTING
6593 032200 005037 002366      ENDSUB
6594 032204                      L10060:
6595 032204                      TRAP   C$ESUB
6596 032204 104403
6597
6598                      BGNSUB
6599                      T9.2:
6600 032206 104402                      TRAP   C$BSUB
6601 032210
6602 CLEAR                      ;MACRO FOR MASTER CLEAR
6603 032210 004737 011070      JSR    PC, $MSCLR    ;**** MACRO EXPANSION ****
6604                                     ;ISSUE A DMR MASTER CLEAR
6605                                     ;****                ****
6606 032214                      ESCAPE TST          ;IF ERROR, BR TO TEST END.
6607 032214 104410              TRAP   C$ESCAPE
6608 032216 000334              .WORD L10057-.
6609 032220                      BASEIN
6610                                     ;MACRO FOR BASE IN COMMAND
6611 032220 004737 011266      JSR    PC, $BASEI   ;**** MACRO EXPANSION ****
6612 032224 004000              .WORD LPLU         ;CALL BASE IN ROUTINE WITH DEFAULTS
6613 032226 002640              .WORD  BASE        ;SET LINE UNIT LOOP
6614 032230 000522              .WORD  DMR         ;BASE TABLE ADDRESS
6615                                     ;DMR-11 MODE
6616                                     ;****                ****
6617 032232                      ESCAPE TST          ;IF ERROR, BR TO TEST END.
6618 032232 104410              TRAP   C$ESCAPE
6619 032234 000316              .WORD L10057-.
6620 032236                      CNTRIN
6621                                     ;MACRO FOR CONTROL IN (FULL DUPLEX)
6622 032236 004737 011522      JSR    PC, $CNTIN   ;**** MACRO EXPANSION ****
6623 032242 000000              .WORD 0           ;CALL CONTROL IN ROUTINE WITH DEFAULT
6624                                     ;SEL6 - FULL DUPLEX, RUN MODE, 1 SEC START.
6625                                     ;****                ****
6626 032244                      ESCAPE TST          ;IF ERROR, BR TO TEST END.

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6627 032244 104410
6628 032246 000304
6629 032250 012737 000001 002366      MOV    #CNTRL,ERROR      ;INHIBIT CONTROL OUT ERROR REPORTING AGAIN.
6630
6631                                     ;BA/CC IN REC. COMMAND WITH NXM
6632                                     ;ADDR = 760000 AND A CHARACTER COUNT = 3.
6633 032256      BACCIR 160000,BIT15!BIT14!RCOUNT
6634                                     ;**** MACRO EXPANSION ****
6635 032256 004737 012272      JSR    PC,$BACC          ;CALL BA/CC IN ROUTINE
6636 032262 000044      .WORD RQI!BACCR        ;BA/CC IN RECEIVE COMMAND
6637 032264 160000      .WORD 160000          ;BUFFER ADDRESS BITS 0-15
6638 032266 140044      .WORD BIT15!BIT14!RCOUNT ;BA BITS 16/17 AND CHAR. COUNT
6639                                     ;****
6640
6641 032270      BACCIT                                     ;BA/CC IN XMIT
6642                                     ;**** MACRO EXPANSION ****
6643 032270 004737 012272      JSR    PC,$BACC          ;CALL BA/CC IN ROUTINE WITH DEFAULTS
6644 032274 000040      .WORD RQI!BACCT        ;BA/CC IN TRANSMIT COMMAND
6645 032276 002522      .WORD TBUF            ;TRANSMIT BUFFER ADDRESS
6646 032300 000044      .WORD TCOUNT         ;TRANSMIT CHARACTER COUNT
6647                                     ;****
6648
6649 032302      WAIT   RDO                                     ;WAIT FOR RDO
6650                                     ;**** MACRO EXPANSION ****
6651 032302 004737 010276      JSR    PC,$WAIT         ;CALL WAIT ROUTINE
6652 032306 000001      .WORD 1                ;FLAG THAT WE'RE WAITING FOR RDO
6653                                     ;****
6654 032310 032777 000001 147716      BIT    #CNTRL,@SEL2    ;IS THERE A CONTROL OUT REPORTED ?
6655 032316 001005      BNE    10$             ;IF YES, PROCEED.
6656 032320      ERRDF 8,EMG8,ERRG2 ;EXPECTED CONTROL OUT
6657 032320 104455
6658 032322 000010
6659 032324 017774
6660 032326 015124
6661 032330 000410
6662 032332
6663 032332 032777 000400 147700      10$: BIT    #NXM,@SEL6    ;IS THE NXM FLAG SET?
6664 032340 001004      BNE    20$             ;IF YES - ERROR REPORTED CORRECTLY
6665 032342      ERRDF 9,EMG9,ERRG2 ;UNEXPECTED CONTROL OUT RECEIVED
6666 032342 104455
6667 032344 000011
6668 032346 020040
6669 032350 015124
6670
6671 032352
6672 032352 042777 000207 147654      20$: BIC    #RDO.CMD,@SEL2 ;CLEAR RDO AND THE COMMAND BITS.
6673 032360 005037 002366      CLR    ERROR          ;ENABLE ERROR REPORTING
6674 032364      ENDSUB
6675 032364
6676 032364 104403
6677
6678 032366
6679 032366
6680 032366 104402
6681 032370
6682      CLEAR          ;MACRO FOR MASTER CLEAR
                                     ;**** MACRO EXPANSION ****

```

TRAP C\$ESCAPE
.WORD L10057-

TRAP C\$ERDF
.WORD 8
.WORD EMG8
.WORD ERRG2

TRAP C\$ERDF
.WORD 9
.WORD EMG9
.WORD ERRG2

L10061: TRAP C\$ESUB

T9.3: TRAP C\$BSUB

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6683 032370 004737 011070      JSR    PC, $MSCLR      ;ISSUE A DMR MASTER CLEAR
6684                               ;*****
6685                               ;*****
6686 032374                               ESCAPE TST              ;IF ERROR, BR TO TEST END.
6687 032374 104410                               TRAP    C$ESCAPE
6688 032376 000154                               .WORD  L10057-.
6689 032400                               BASEIN                  ;MACRO FOR BASE IN COMMAND
6690                               ;***** MACRO EXPANSION *****
6691 032400 004737 011266      JSR    PC, $BASEI      ;CALL BASE IN ROUTINE WITH DEFAULTS
6692 032404 004000      .WORD  LPLU           ;SET LINE UNIT LOOP
6693 032406 002640      .WORD  BASE           ;BASE TABLE ADDRESS
6694 032410 000522      .WORD  DMR            ;DMR-11 MODE
6695                               ;*****
6696                               ;*****
6697 032412                               ESCAPE TST              ;IF ERROR, BR TO TEST END.
6698 032412 104410                               TRAP    C$ESCAPE
6699 032414 000136                               .WORD  L10057-.
6700 032416                               CNTRIN                  ;MACRO FOR CONTRL IN (FULL DUPLEX)
6701                               ;***** MACRO EXPANSION *****
6702 032416 004737 011522      JSR    PC, $CNTIN      ;CALL CONTROL IN ROUTINE WITH DEFAULT
6703 032422 000000      .WORD  0              ;SEL6 - FULL DUPLEX, RUN MODE, 1 SEC START.
6704                               ;*****
6705                               ;*****
6706 032424                               ESCAPE TST              ;IF ERROR, BR TO TEST END.
6707 032424 104410                               TRAP    C$ESCAPE
6708 032426 000124                               .WORD  L10057-.
6709 032430                               BACCIR                  ;BA/CC IN RCV
6710                               ;***** MACRO EXPANSION *****
6711 032430 004737 012272      JSR    PC, $BACC       ;CALL BA/CC IN ROUTINE WITH DEFAULTS
6712 032434 000044      .WORD  RQI!BACCR      ;BA/CC IN RECEIVE COMMAND
6713 032436 002572      .WORD  RBUF           ;RECEIVE BUFFER
6714 032440 000044      .WORD  RCOUNT        ;RECEIVE CHARACTER COUNT
6715                               ;*****
6716                               ;*****
6717 032442                               ESCAPE TST              ;IF ERROR, BR TO TEST END.
6718 032442 104410                               TRAP    C$ESCAPE
6719 032444 000106                               .WORD  L10057-.
6720 032446 012737 000001 002366      MOV    #CNTRL,ERROR    ;INHIBIT CONTROL OUT ERROR REPORTING AGAIN.
6721                               ;*****
6722                               ;*****
6723                               ;BA/CC IN XMIT COMMAND WITH NXM BUFFER
6724 032454      BACCIT 160000,BIT15!BIT14!1 ;ADDRESS (760000) AND A CHAR. COUNT = 1
6725                               ;***** MACRO EXPANSION *****
6726 032454 004737 012272      JSR    PC, $BACC       ;CALL BA/CC IN ROUTINE
6727 032460 000040      .WORD  RQI!BACCT      ;BA/CC IN TRANSMIT COMMAND
6728 032462 160000      .WORD  160000         ;BUFFER ADDRESS BITS 0-15
6729 032464 140001      .WORD  BIT15!BIT14!1 ;BA BITS 16 & 17 AND CHAR. COUNT
6730                               ;*****
6731                               ;*****
6732 032466      WAIT    RDO           ;WAIT FOR RDO TO BE SET.
6733                               ;***** MACRO EXPANSION *****
6734 032466 004737 010276      JSR    PC, $WAIT       ;CALL WAIT ROUTINE
6735 032472 000001      .WORD  1              ;FLAG THAT WE'RE WAITING FOR RDO
6736                               ;*****
6737 032474 032777 000001 147532      BIT    #CNTRL,@SEL2    ;IS THERE A CONTROL OUT REPORTED ?
6738 032502 001005      BNE    10$            ;IF YES, PROCEED.

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6739 032504 ERRDF 8,EMG8,ERRG2 ;EXPECTED CONTROL OUT
6740 032504 104455 TRAP C$ERDF
6741 032506 000010 .WORD 8
6742 032510 017774 .WORD EMG8
6743 032512 015124 .WORD ERRG2
6744 032514 000410
6745 032516
6746 032516 032777 000400 147514 10$: BR 20$ ;EXIT
6747 032524 001004 BIT #NXM,@SEL6 ;IS THE NXM FLAG SET?
6748 032526 BNE 20$ ;IF YES - ERROR REPORTED CORRECTLY
6749 032526 104455 ERRDF 9,EMG9,ERRG2 ;UNEXPECTED CONTROL OUT RECEIVED
6750 032530 000011 TRAP C$ERDF
6751 032532 020040 .WORD 9
6752 032534 015124 .WORD EMG9
6753 032536 .WORD ERRG2
6754 032536 042777 000207 147470 20$: BIC #RDO!CMD,@SEL2 ;CLEAR RDO AND THE COMMAND BITS.
6755 032544 005037 002366 CLR ERROR ;DON'T INHIBIT CONTROL OUT ERRORS
6756 032550 ENDSUB
6757 032550
6758 032550 104403 L10062: TRAP C$ESUB
6759
6760 032552 ENDTST
6761 032552 L10057:
6762 032552 104401 TRAP C$ETST
6763
6764
6765
6766
6767
6768

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6825 032632 004737 011522 JSR PC, $CNTIN ;CALL CONTROL IN ROUTINE WITH DEFAULT
6826 032636 000000 .WORD 0 ;SEL6 - FULL DUPLEX, RUN MODE, 1 SEC START.
6827 ;**** ;****
6828
6829 032640 ESCAPE TST ;IF ERROR, BR TO TEST END.
6830 032640 104410 TRAP C$ESCAPE
6831 032642 000134 .WORD L10063-.
6832
6833 ;BLIND THE RECEIVER BY GOING INTO HDX.
6834 032644 DMRIN WMODEM,0,BIT4 ;USE WRITE MODEM COMMAND TO SET HALF DUPLEX.
6835 ;**** MACRO EXPANSION ****
6836 032644 004737 012062 JSR PC, $DMRIN ;CALL DMR MODE INPUT ROUTINE
6837 032650 000005 .WORD WMODEM ;INPUT COMMAND
6838 032652 000000 .WORD 0 ;SEL4 VALUE (OR BITS TO CLEAR IN BSEL6)
6839 032654 000020 .WORD BIT4 ;SEL6 VALUE (OR BITS TO SET IN BSEL6)
6840 ;**** ;****
6841
6842 032656 BACCIT ;BA/CC IN XMIT BUFFER
6843 ;**** MACRO EXPANSION ****
6844 032656 004737 012272 JSR PC, $BACC ;CALL BA/CC IN ROUTINE WITH DEFAULTS
6845 032662 000040 .WORD RQI!BACC ;BA/CC IN TRANSMIT COMMAND
6846 032664 002522 .WORD TBUF ;TRANSMIT BUFFER ADDRESS
6847 032666 000044 .WORD TCOUNT ;TRANSMIT CHARACTER COUNT
6848 ;**** ;****
6849
6850 032670 ESCAPE TST ;IF ERROR, EXIT
6851 032670 104410 TRAP C$ESCAPE
6852 032672 000104 .WORD L10063-.
6853 032674
6854 WAIT RDO ;WAIT FOR THE READY OUT.
6855 032674 004737 010276 JSR PC, $WAIT ;**** MACRO EXPANSION ****
6856 032700 000001 .WORD 1 ;CALL WAIT ROUTINE
6857 ;FLAG THAT WE'RE WAITING FOR RDO
6858 ;**** ;****
6859 032702 ESCAPE TST ;IF ERROR, EXIT.
6860 032704 000072 TRAP C$ESCAPE
6861 032706 023727 002416 011610 CMP COUNT,#5000. ;CHECK THE SOFTWARE TIMER COUNT.
6862 ;THE TIMER VALUE WAS DETERMINED
6863 ;EMPIRICALLY ON A 11/04, 11/34, 11/40, 11/70.
6864 032714 003005 BGT 5$ ;IF OK - PROCEED
6865 ;*****
6866 032716 ERRDF 19,EMG19 ;
6867 032716 104455 TRAP C$ERDF
6868 032720 000023 .WORD 19
6869 032722 020402 .WORD EMG19
6870 032724 000000 .WORD 0
6871 ;1MSEC PROGRAM TIMER - OUT OF RANGE.
6872 ;IF THIS ERROR OCCURS, CHECK THE M8207
6873 ;MICROPROCESSOR AS FOLLOWS:
6874 ;RESET THE DMR, SCOPE E-69, PIN 4 TO VERIFY
6875 ;THAT THE 1MSEC TIMER IS OUT OF RANGE.
6876 ;*****
6877 032726 000423 BR 25$
6878 032730 5$:
6879 032730 032777 000001 147276 BIT #CNTRL,@SEL2 ;IS THIS A CONTROL OUT
6880 032736 001005 BNE 10$ ;IF YES, PROCEED.

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6881 032740 ERRDF 8,EMG8,ERRG2 ;EXPECTED A CONTROL OUT.
6882 032740 104455
6883 032742 000010 TRAP C$ERDF
6884 032744 017774 .WORD 8
6885 032746 015124 .WORD EMG8
6886 032750 000410 .WORD ERRG2
6887 032752
6888 032752 032777 000002 147260 10$: BR 20$ ;EXIT
6889 032760 001004 BIT #TOUT,@SEL6 ;WAS THE TIME OUT REPORTED?
6890 032762 ERRDF 9,EMG9,ERRG2 ;IF YES, EXIT
6891 032762 104455 ;UNEXPECTED ERROR.
6892 032764 000011 TRAP C$ERDF
6893 032766 020040 .WORD 9
6894 032770 015124 .WORD EMG9
6895 032772 20$: .WORD ERRG2
6896 032772 SHUTDN
6897 032772
6898 032772 004737 012562 JSR PC, $HALT ;**** MACRO EXPANSION ****
6899 032772 ;DMR HALT ROUTINE.
6900 032776 25$: ;****
6901 032776
6902 032776 ENDTST
6903 032776
6904 032776 104401 L10063: TRAP C$ETST

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6905      .SBTTL          TEST 11 - MESSAGE TOO LONG ERROR
6906
6907      ;*****
6908      ;*          TEST 11 - DMR-11
6909      ;* MESSAGE TOO LONG - TRANSMIT A MESSAGE THAT IS TOO LONG FOR THE
6910      ;* RECEIVE BUFFER AND VERIFY THAT THE 'TOO LONG' ERROR IS RECEIVED.
6911      ;*
6912      ;*****
6913      BGNTST
6914      T11::
6915      CLEAR          ;MACRO FOR MASTER CLEAR
6916      ;**** MACRO EXPANSION ****
6917      JSR      PC, $MSCLR      ;ISSUE A DMR MASTER CLEAR
6918      ;****
6919      ;****
6920      ESCAPE TST          ;IF ERROR, BR TO TEST END.
6921      TRAP      C$ESCAPE
6922      .WORD    L10064-
6923      BASEIN          ;MACRO FOR BASE IN COMMAND
6924      ;**** MACRO EXPANSION ****
6925      JSR      PC, $BASEI      ;CALL BASE IN ROUTINE WITH DEFAULTS
6926      .WORD    LPLU          ;SET LINE UNIT LOOP
6927      .WORD    BASE          ;BASE TABLE ADDRESS
6928      .WORD    DMR          ;DMR-11 MODE
6929      ;****
6930      ;****
6931      ESCAPE TST          ;IF ERROR, BR TO TEST END.
6932      TRAP      C$ESCAPE
6933      .WORD    L10064-
6934      CNTRIN          ;MACRO FOR CONTROL IN (FULL DUPLEX)
6935      ;**** MACRO EXPANSION ****
6936      JSR      PC, $CNTIN      ;CALL CONTROL IN ROUTINE WITH DEFAULT
6937      .WORD    0              ;SEL6 - FULL DUPLEX, RUN MODE, 1 SEC START.
6938      ;****
6939      ;****
6940      ESCAPE TST          ;IF ERROR, BR TO TEST END.
6941      TRAP      C$ESCAPE
6942      .WORD    L10064-
6943      BACCIR RBUF,RCOUNT/2 ;SET UP THE RECEIVE BUFFER WITH 1/2 BUF. SPACE
6944      ;**** MACRO EXPANSION ****
6945      JSR      PC, $BACC        ;CALL BA/CC IN ROUTINE
6946      .WORD    RQI!BACCR      ;BA/CC IN RECEIVE COMMAND
6947      .WORD    RBUF          ;BUFFER ADDRESS BITS 0-15
6948      .WORD    RCOUNT/2      ;BA BITS 16/17 AND CHAR. COUNT
6949      ;****
6950      ;****
6951      MOV      #CNTRL,ERROR    ;THIS FLAG WILL DISABLE ANY CONTROL OUT ERROR
6952      ;REPORTING BECAUSE WE ARE INTENTIONALLY
6953      ;CAUSING ONE IN THIS TEST.
6954      BACCIT          ;BA/CC IN XMIT COMMAND
6955      ;**** MACRO EXPANSION ****
6956      JSR      PC, $BACC        ;CALL BA/CC IN ROUTINE WITH DEFAULTS
6957      .WORD    RQI!BACCT      ;BA/CC IN TRANSMIT COMMAND
6958      .WORD    TBUF          ;TRANSMIT BUFFER ADDRESS
6959      .WORD    TCOUNT      ;TRANSMIT CHARACTER COUNT
6960      ;****

```


TEST 11 - MESSAGE TOO LONG ERROR

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6961 033072          10$:
6962 033072          WAIT    RDO          ;WAIT FOR RDO TO BE SET
6963                                     ;**** MACRO EXPANSION ****
6964 033072 004737 010276 JSR     PC, $WAIT          ;CALL WAIT ROUTINE
6965 033076 000001          .WORD    1          ;FLAG THAT WE'RE WAITING FOR RDO
6966                                     ;****
6967 033100          ESCAPE  TST          ;IF RDO NOT SET, BR TO TEST END.
6968 033100 104410          TRAP    C$ESCAPE
6969 033102 000054          .WORD    L10064-.
6970 033104 032777 000001 147122 BIT    #CNTRL,@SEL2        ;IS THIS A CONTROL OUT?
6971 033112 001005          BNE    20$
6972 033114          ERRDF  8,EMG8,ERRG2 ;IF YES, PROCEED
6973 033114 104455          ;EXPECTED CONTROL OUT.
6974 033116 000010          TRAP    C$ERDF
6975 033120 017774          .WORD    8
6976 033122 015124          .WORD    EMG8
6977 033124 000410          .WORD    ERRG2
6978 033126          BR     40$          ;EXIT
6979 033126 032777 000020 47104 20$: BIT    #TOLONG,@SEL6      ;IS THE TOO LONG BIT SET?
6980 033134 001004          BNE    40$          ;IF YES, TEST OK - FINISH UP.
6981 033136          30$: ERRDF  9,EMG9,ERRG2 ;WE'RE NOT GETTING EXPECTED RESULT
6982 033136          TRAP    C$ERDF
6983 033136 104455          .WORD    9
6984 033140 000011          .WORD    EMG9
6985 033142 020040          .WORD    ERRG2
6986 033144 015124
6987
6988 033146          40$: CLR    ERROR          ;RESTORE ERROR FLAG TO NORMAL STATE.
6989 033146 005037 002366 SHUTDN ;HALT THE DMR.
6990 033152          JSR     PC, $HALT        ;**** MACRO EXPANSION ****
6991
6992 033152 004737 012562          ;DMR HALT ROUTINE.
6993                                     ;****
6994
6995
6996
6997 033156          ENDTST
6998 033156
6999 033156 104401          L10064: TRAP    C$ETST
7000
7001

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033160 104402
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033162 004737 011070
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033166 004737 011266
033172 004000
033174 002640
033176 000522
033200 012737 000001 002366
033206
033206 004737 011266
033212 004000
033214 002640
033216 000522
033220
033220 004737 010276
033224 000001
033226
033226 104410
033230 000632
033232 032777 000001 146774
033240 001005
033242
033242 104455

```
.SBTTL TEST 12 - PROCEDURE ERRORS
*****
* TEST 12 - DMR-11
* PROCEDURE ERRORS -
* THE FOLLOWING SHOULD CAUSE THE DMR-11 TO HALT AND RESPOND WITH
* A PROCEDURE ERROR:
* SUBTEST 1 - A SECOND BASE IN COMMAND
* SUBTEST 2 - A CONTROL IN BEFORE A BASE IN
* SUBTEST 3 - A BA/CC IN BEFORE A BASE IN
* SUBTEST 4 - A BA/CC IN RCV WITH A BUFFER LENGTH OF 0
* SUBTEST 5 - A BA/CC IN XMIT. WITH A BUFFER LENGTH OF 0
*****
BGNTST
BGNSUB
T12::
T12.1: TRAP C$BSUB

CLEAR ;MASTER CLEAR MACRO
;**** MACRO EXPANSION ****
JSR PC, $MSCLR ;ISSUE A DMR MASTER CLEAR
;****

BASEIN ;**** MACRO EXPANSION ****
;CALL BASE IN ROUTINE WITH DEFAULTS
;SET LINE UNIT LOOP
;WORD LPLU ;BASE TABLE ADDRESS
;WORD BASE ;DMR-11 MODE
;WORD DMR ;****

MOV #CNTRL,ERROR ;THIS FLAG WILL DISABLE ANY CONTROL OUT ERROR
;REPORTING BECAUSE WE ARE INTENTIONALLY
;CAUSING ONE IN THIS TEST.
BASEIN ;SECOND BASE IN
;**** MACRO EXPANSION ****
;CALL BASE IN ROUTINE WITH DEFAULTS
;SET LINE UNIT LOOP
;WORD LPLU ;BASE TABLE ADDRESS
;WORD BASE ;DMR-11 MODE
;WORD DMR ;****

WAIT RDO ;WAIT FOR RDO TO BE SET
;**** MACRO EXPANSION ****
JSR PC, $WAIT ;CALL WAIT ROUTINE
;WORD 1 ;FLAG THAT WE'RE WAITING FOR RDO
;****

ESCAPE TST ;IF RDO NOT SET, BR TO TEST END.
TRAP C$ESCAPE
;WORD L10065-

BIT #CNTRL,@SEL2 ;IS THIS A CONTROL OUT?
BNE 10$ ;IF YES, PROCEED.
ERRDF 8,EMGB,ERRG2 ;EXPECTED CONTROL OUT
TRAP C$ERDF
```

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7058 033244 00001C                                     .WORD 8
7059 033246 017774                                     .WORD EMG8
7060 033250 015124                                     .WORD ERRG2
7061 033252 000410
7062 033254
7063 033254 032777 001000 146756 10$: BR 15$ ;EXIT
7064 033262 001004 BIT #HALTC,@SEL6 ;IS THE HALT - PROCEDURE ERROR BIT SET?
7065 033264 ERRDF 15$ ;IF YES - ERROR REPORTED CORRECTLY
7066 033264 104455 ERRDF 9,EMG9,ERRG2 ;UNEXPECTED CONTROL OUT RECEIVED
7067 033266 000011 TRAP C$ERDF
7068 033270 020040 .WORD 9
7069 033272 015124 .WORD EMG9
7070 033274 .WORD ERRG2
7071 033274 042777 000207 146732 15$: BIC #RDO!CMD,@SEL2 ;CLEAR RDO AND THE COMMAND BITS
7072 033302 005037 002366 CLR ERROR ;RESTORE FLAG
7073 033306 ENDSUB
7074 033306 L10066:
7075 033306 104403 TRAP C$ESUB
7076 033310 BGNSUB
7077 033310 T12.2:
7078 033310 TRAP C$BSUB
7079 033310 104402
7080 033312 CLEAR ;MASTER CLEAR MACRO
7081 033312 JSR PC, $MSCLR ;**** MACRO EXPANSION ****
7082 033312 004737 011070 ;ISSUE A DMR MASTER CLEAR
7083 033312 ;****
7084 033316 012737 000001 002366 MOV #CNTRL,ERROR ;THIS FLAG WILL DISABLE ANY CONTROL OUT ERROR
7085 033316 012737 000001 002366 ;REPORTING BECAUSE WE ARE INTENTIONALLY
7086 033316 012737 000001 002366 ;CAUSING ONE IN THIS TEST.
7087 033316 012737 000001 002366 CLR DMRFLG ;CLEAR FLAG THAT IS SET IN BASEIN IN ORDER
7088 033316 012737 000001 002366 ;TO FLAG THAT A CONTROL OUT-DMR RUN MODE
7089 033324 005037 002260 CLR DMRFLG ;COMMAND IS EXPECTED (THIS FLAG WAS SET IN
7090 033324 005037 002260 ;THE PREVIOUS SUBTEST BASEIN)
7091 033324 005037 002260 ;CONTROL IN
7092 033324 005037 002260 ;**** MACRO EXPANSION ****
7093 033330 CNTRIN ;CALL CONTROL IN ROUTINE WITH DEFAULT
7094 033330 004737 011522 JSR PC, $CNTIN ;SEL6 - FULL DUPLEX, RUN MODE, 1 SEC START.
7095 033330 004737 011522 .WORD 0 ;****
7096 033334 000000
7097 033334 000000
7098 033336 WAIT RDO ;WAIT FOR RDO TO BE SET
7099 033336 ;**** MACRO EXPANSION ****
7100 033336 004737 010276 JSR PC, $WAIT ;CALL WAIT ROUTINE
7101 033336 004737 010276 .WORD 1 ;FLAG THAT WE'RE WAITING FOR RDO
7102 033342 000001 ;****
7103 033342 000001 ;****
7104 033344 ESCAPE TST ;IF RDO NOT SET, BR TO TEST END.
7105 033344 104410 TRAP C$ESCAPE
7106 033346 000514 .WORD L10065-.
7107 033350 032777 000001 146656 BIT #CNTRL,@SEL2 ;IS THIS A CONTROL OUT?
7108 033356 001005 BNE 10$ ;IF YES - PROCEED.
7109 033360 001005 ERRDF 8,EMG8,ERRG2 ;EXPECTED CONTROL OUT
7110 033360 104455 TRAP C$ERDF
7111 033362 000010 .WORD 8
7112 033364 017774 .WORD EMG8
7113 033366 015124 .WORD ERRG2

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7114 033370 000410 BR 15$ ;EXIT
7115 033372 10$:
7116 033372 032777 001000 146640 BIT #HALTC,@SEL6 ;IS THE HALT - PROCEDURE ERROR BIT SET?
7117 033400 001004 BNE 15$ ;IF YES - ERROR REPORTED CORRECTLY
7118 033402 ERRDF 9,EMG9,ERRG2 ;UNEXPECTED CONTROL OUT RECEIVED
7119 033402 10'455 TRAP C$ERDF
7120 033404 000011 .WORD 9
7121 033406 020040 .WORD EMG9
7122 033410 015124 .WORD ERRG2
7123 033412 15$:
7124 033412 042777 000207 146614 BIC #RDO.CMD,@SEL2 ;CLEAR RDO AND THE COMMAND BITS.
7125 033420 005037 002366 CLR ERROR ;RESTORE FLAG
7126 033424 ENDSUB
7127 033424 L10067:
7128 033424 104403 TRAP C$ESUB
7129
7130 033426 BGNSUB
7131 033426 T12.3:
7132 033426 104402 TRAP C$BSUB
7133
7134 033430 CLEAR ;MASTER CLEAR MACRO
7135 ;**** MACRO EXPANSION ****
7136 033430 004737 011070 JSR PC, $MSCLR ;ISSUE A DMR MASTER CLEAR
7137 ;****
7138
7139 033434 012737 000001 002366 MOV #CNTRL,ERROR ;THIS FLAG WILL DISABLE ANY CONTROL OUT ERROR
7140 ;REPORTING BECAUSE WE ARE INTENTIONALLY
7141 ;CAUSING ONE IN THIS TEST.
7142 033442 BACCIR ;BA/CC IN RCV. COMMAND
7143 ;**** MACRO EXPANSION ****
7144 033442 004737 012272 JSR PC, $BACC ;CALL BA/CC IN ROUTINE WITH DEFAULTS
7145 033446 000044 .WORD RQI!BACCR ;BA/CC IN RECEIVE COMMAND
7146 033450 002572 .WORD RBUF ;RECEIVE BUFFER
7147 033452 000044 .WORD RCOUNT ;RECEIVE CHARACTER COUNT
7148 ;****
7149
7150 033454 WAIT RDO ;WAIT FOR RDO TO BE SET
7151 ;**** MACRO EXPANSION ****
7152 033454 004737 010276 JSR PC, $WAIT ;CALL WAIT ROUTINE
7153 033460 000001 .WORD 1 ;FLAG THAT WE'RE WAITING FOR RDO
7154 ;****
7155 ESCAPE TST ;IF RDO NOT SET, BR TO TEST END.
7156 033462 104410 TRAP C$ESCAPE
7157 033464 000376 .WORD L10065-.
7158 033466 032777 000001 146540 BIT #CNTRL,@SEL2 ;IS THIS A CONTROL OUT?
7159 033474 001005 BNE 10$ ;IF YES - PROCEED.
7160 033476 ERRDF 8,EMG8,ERRG2 ;EXPECTED CONTROL OUT
7161 033476 104455 TRAP C$ERDF
7162 033500 000010 .WORD 8
7163 033502 017774 .WORD EMG8
7164 033504 015124 .WORD ERRG2
7165 033506 000410 BR 15$ ;EXIT
7166 033510 10$:
7167 033510 032777 001000 146522 BIT #HALTC,@SEL6 ;IS THE HALT - PROCEDURE ERROR BIT SET?
7168 033516 001004 BNE 15$ ;IF YES - ERROR REPORTED CORRECTLY
7169 033520 ERRDF 9,EMG9,ERRG2 ;UNEXPECTED CONTROL OUT RECEIVED

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7170 033520 104455 TRAP C$ERDF
7171 033522 000011 .WORD 9
7172 033524 020040 .WORD EMG9
7173 033526 015124 .WORD ERRG2
7174 033530
7175 033530 042777 000207 146476 15$: BIC #RDO!CMD,@SEL2 ;CLEAR RDO AND THE COMMAND BITS.
7176 033536 005037 002366 CLR ERROR ;RESTORE FLAG
7177 033542 ENDSUB
7178 033542 L10070:
7179 033542 104403 TRAP C$ESUB
7180
7181 033544 BGNSUB
7182 033544 T12.4:
7183 033544 104402 TRAP C$BSUB
7184 033546 CLEAR ;MASTER CLEAR
7185 ;**** MACRO EXPANSION ****
7186 033546 004737 011070 JSR PC, $MSCLR ;ISSUE A DMR MASTER CLEAR
7187 ;****
7188
7189 033552 ESCAPE TST ;IF ERROR, EXIT.
7190 033552 104410 TRAP C$ESCAPE
7191 033554 000306 .WORD L10065-.
7192 033556
7193
7194 033556 004737 011266 JSR PC, $BASEI ;BASE IN COMMAND
7195 033562 004000 .WORD LPLU ;**** MACRO EXPANSION ****
7196 033564 002640 .WORD BASE ;CALL BASE IN ROUTINE WITH DEFAULTS
7197 033566 000522 .WORD DMR ;SET LINE UNIT LOOP
7198 ;BASE TABLE ADDRESS
7199 ;DMR-11 MODE
7200 ;****
7201 033570 ESCAPE TST ;IF ERROR, EXIT.
7202 033570 104410 TRAP C$ESCAPE
7203 033572 000270 .WORD L10065-.
7204 033574 BACCIR ;ASSIGN A BA/CC IN RECEIVE BUFFER
7205 033574 004737 012272 JSR PC, $BACC ;**** MACRO EXPANSION ****
7206 033600 000044 .WORD RQI!BACCR ;CALL BA/CC IN ROUTINE WITH DEFAULTS
7207 033602 002572 .WORD RBUF ;BA/CC IN RECEIVE COMMAND
7208 033604 000044 .WORD RCOUNT ;RECEIVE BUFFER
7209 ;RECEIVE CHARACTER COUNT
7210 ;****
7211 033606 ESCAPE TST ;IF ERROR, EXIT.
7212 033606 104410 TRAP C$ESCAPE
7213 033610 000252 .WORD L10065-.
7214 033612 012737 000001 002366 MOV #CNTRL,ERROR ;THIS FLAG WILL DISABLE ANY CONTROL OUT
7215 ;ERROR REPORTING BECAUSE WE ARE INTENTIONALLY
7216 ;CAUSING ONE.
7217 033620 BACCIT TBUF,0 ;ASSIGN A BA/CC IN XMIT BUFFER LENGTH = 0.
7218 ;**** MACRO EXPANSION ****
7219 033620 004737 012272 JSR PC, $BACC ;CALL BA/CC IN ROUTINE
7220 033624 000040 .WORD RQI!BACCT ;BA/CC IN TRANSMIT COMMAND
7221 033626 002522 .WORD TBUF ;BUFFER ADDRESS BITS 0-15
7222 033630 000000 .WORD 0 ;BA BITS 16 & 17 AND CHAR. COUNT
7223 ;****
7224
7225 033632 WAIT RDO ;WAIT FOR RDO TO BE SET

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7226
7227 033632 004737 010276 JSR PC, $WAIT ;**** MACRO EXPANSION ****
7228 033636 000001 .WORD 1 ;CALL WAIT ROUTINE
7229 ;FLAG THAT WE'RE WAITING FOR RDO
7230 033640 ESCAPE TST ;****
;IF RDO NOT SET, BR TO TEST END.
7231 033640 104410 TRAP C$ESCAPE
7232 033642 000220 .WORD L10065-.
7233 033644 032777 000001 146362 BIT #CNTRL,@SEL2 ;IS THIS A CONTROL OUT?
7234 033652 001005 BNE 10$ ;IF YES - PROCEED.
7235 033654 ERRDF 8,EMG8,ERRG2 ;EXPECTED CONTROL OUT
7236 033654 104455 TRAP C$ERDF
7237 033656 000010 .WORD 8
7238 033660 017774 .WORD EMG8
7239 033662 015124 .WORD ERRG2
7240 033664 000410 BR 15$ ;EXIT
7241 033666 10$:
7242 033666 032777 001000 146344 BIT #HALTC,@SEL6 ;IS THE HALT - PROCEDURE ERROR BIT SET?
7243 033674 001004 BNE 15$ ;IF YES - ERROR REPORTED CORRECTLY
7244 033676 ERRDF 9,EMG9,ERRG2 ;UNEXPECTED CONTROL OUT RECEIVED
7245 033676 104455 TRAP C$ERDF
7246 033700 000011 .WORD 9
7247 033702 020040 .WORD EMG9
7248 033704 015124 .WORD ERRG2
7249 033706 15$:
7250 033706 042777 000207 146320 BIC #RDO!CMD,@SEL2 ;CLEAR RDO AND THE COMMAND BITS.
7251 033714 005037 002366 CLR ERROR ;RESTORE FLAG
7252 033720 ENDSUB
7253 033720 L10071:
7254 033720 104403 TRAP C$ESUB
7255
7256 033722 BGNSUB
7257 033722 T12.5:
7258 033722 104402 TRAP C$BSUB
7259 033724 CLEAR ;MASTER CLEAR
7260 ;**** MACRO EXPANSION ****
7261 033724 004737 011070 JSR PC, $MSCLR ;ISSUE A DMR MASTER CLEAR
7262 ;****
7263
7264 033730 ESCAPE TST ;IF ERROR, EXIT.
7265 033730 104410 TRAP C$ESCAPE
7266 033732 000130 .WORD L10065-.
7267 033734 BASEIN ;BASE IN COMMAND
7268 ;**** MACRO EXPANSION ****
7269 033734 004737 011266 JSR PC, $BASEI ;CALL BASE IN ROUTINE WITH DEFAULTS
7270 033740 004000 .WORD LPLU ;SET LINE UNIT LOOP
7271 033742 002640 .WORD BASE ;BASE TABLE ADDRESS
7272 033744 000522 .WORD DMR ;DMR-11 MODE
7273 ;****
7274
7275 033746 ESCAPE TST ;IF ERROR, EXIT.
7276 033746 104410 TRAP C$ESCAPE
7277 033750 000112 .WORD L10065-.
7278 033752 012737 000001 002366 MOV #CNTRL,ERROR ;THIS FLAG WILL DISABLE ANY CONTROL OUT
7279 ;ERROR REPORTING BECAUSE WE ARE INTENTIONALLY
7280 ;CAUSING ONE.
7281 033760 BACCIR RBUF,0 ;ASSIGN A BA/CC IN REC. BUFFER LENGTH = 0

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7282
7283 033760 004737 012272 JSR PC, $BACC ;**** MACRO EXPANSION ****
7284 033764 000044 .WORD RQI!BACCR ;CALL BA/CC IN ROUTINE
7285 033766 002572 .WORD RBUF ;BA/CC IN RECEIVE COMMAND
7286 033770 000000 .WORD 0 ;BUFFER ADDRESS BITS 0-15
;BA BITS 16/17 AND CHAR. COUNT
7287 ;****
7288 ;****
7289 033772 WAIT RDO ;WAIT FOR RDO TO BE SET
7290 ;**** MACRO EXPANSION ****
7291 033772 004737 010276 JSR PC, $WAIT ;CALL WAIT ROUTINE
7292 033776 000001 .WORD 1 ;FLAG THAT WE'RE WAITING FOR RDO
7293 ;****
7294 034000 ESCAPE TST ;IF RDO NOT SET, BR TO TEST END.
7295 034000 104410 TRAP C$ESCAPE
7296 034002 000060 .WORD L10065-.
7297 034004 032777 000001 146222 BIT #CNTRL,@SEL2 ;IS THIS A CONTROL OUT?
7298 034012 001005 BNE 10$ ;IF YES - PROCEED.
7299 034014 ERRDF 8,EMG8,ERRG2 ;EXPECTED CONTROL OUT
7300 034014 104455 TRAP C$ERDF
7301 034016 000010 .WORD 8
7302 034020 017774 .WORD EMG8
7303 034022 015124 .WORD ERRG2
7304 034024 000410 BR 15$ ;EXIT
7305 034026 10$:
7306 034026 032777 001000 146204 BIT #HALTC,@SEL6 ;IS THE HALT - PROCEDURE ERROR BIT SET?
7307 034034 001004 BNE 15$ ;IF YES - ERROR REPORTED CORRECTLY
7308 034036 ERRDF 9,EMG9,ERRG2 ;UNEXPECTED CONTROL OUT RECEIVED
7309 034036 104455 TRAP C$ERDF
7310 034040 000011 .WORD 9
7311 034042 020040 .WORD EMG9
7312 034044 015124 .WORD ERRG2
7313 034046 15$:
7314 034046 042777 000207 146160 BIC #RDO!CMD,@SEL2 ;CLEAR RDO AND THE COMMAND BITS.
7315 034054 005037 002366 CLR ERROR ;RESTORE FLAG
7316 034060 ENDSUB
7317 034060 L10072:
7318 034060 104403 TRAP C$ESUB
7319
7320 034062 ENDTST
7321 034062 L10065:
7322 034062 104401 TRAP C$ETST

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7323 .SBTTL TEST 13 - DATA TEST
7324
7325 :*****
7326 :* TEST 13 - DMR-11
7327 :* FREE RUNNING FLAG MODE DATA TEST
7328 :* TRANSMIT A MESSAGE AND VERIFY THE RECEIVED DATA IS CORRECT.
7329 :* IN THIS TEST NO INTERRUPTS ARE USED AND THE LINE UNIT IS IN
7330 :* INTERNAL (TTL) LOOPBACK. THIS TEST IS THE FIRST TEST IN WHICH
7331 :* THE DMR IS USED IN A DATA TRANSMISSION MODE.
7332 :*****
7333 BGNTST
7334
7335 T13::
7336 MOV RCOUNT,RO ;BYTE COUNT FOR RECEIVE BUFFER
7337 ADD #2,RO ;2 ADDITIONAL BYTES AT END OF BUFFER ARE
7338 ;USED FOR DELIMITOR
7339 MOV #RBUF,R1 ;ADDRESS OF RECEIVE BUFFER
7340 10$: CLR (R1)+ ;CLEAR A BYTE IN THE BUFFER
7341 DEC RO ;CONTINUE - UNTIL ENTIRE BUFFER DONE
7342 BNE 10$
7343
7344 CLR TFLAG ;CLEAR TRANSMIT FLAG
7345 CLR RFLAG ;CLEAR RECEIVER FLAG
7346 CLEAR ;MACRO FOR MASTER CLEAR
7347 ;**** MACRO EXPANSION ****
7348 JSR PC, $MSCLR ;ISSUE A DMR MASTER CLEAR
7349 ;****
7350
7351 ESCAPE TST ;IF ERROR, BR TO TEST END.
7352 TRAP C$ESCAPE
7353 .WORD L10073-.
7354 TST DMTURN ;IS INTERNAL LOOPBACK DESIRED?
7355 BNE 11$ ;IF NOT, CLEAR INTERNAL LOOPBACK.
7356 BIS #LPLU,100$ ;SET LINE UNIT LOOPBACK.
7357 BR 12$
7358 11$:
7359 BIC #LPLU,100$ ;CLEAR LINE UNIT LOOPBACK.
7360 12$:
7361 CALL $BASEI ;BASE IN COMMAND.
7362 .WORD 0 ;MAINTENANCE BITS (LINE UNIT LOOP)
7363 .WORD BASE ;BASE TABLE ADDRESS
7364 .WORD DMR ;DMR MODE
7365 ESCAPE TST ;IF ERROR, BR TO TEST END.
7366 TRAP C$ESCAPE
7367 .WORD L10073-.
7368
7369 CALL $LOOP ;DMR COMMAND TO SET MAINT. BITS
7370 ESCAPE TST ;IF ERROR, BR TO TEST END.
7371 TRAP C$ESCAPE
7372 .WORD L10073-.
7373
7374 CNTRIN ;MACRO FOR CONTROL IN (FULL DUPLEX)
7375 ;**** MACRO EXPANSION ****
7376 JSR PC, $CNTIN ;CALL CONTROL IN ROUTINE WITH DEFAULT
7377 .WORD 0 ;SEL6 - FULL DUPLEX, RUN MODE, 1 SEC START.
7378 ;****
  
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7379 034206          ESCAPE TST          ;IF ERROR, BR TO TEST END.
7380 034206 104410          TRAP          C$ESCAPE
7381 034210 000402          .WORD          L10073-.
7382
7383 034212          BACCIR          ;BUFFER ADDRESS/CHARACTER COUNT REC. IN
7384          ;**** MACRO EXPANSION ****
7385 034212 004737 012272  JSR      PC, $BACC          ;CALL DA/CC IN ROUTINE WITH DEFAULTS
7386 034216 000044          .WORD      RQI!BACCR          ;BA/CC IN RECEIVE COMMAND
7387 034220 002572          .WORD      RBUF          ;RECEIVE BUFFER
7388 034222 000044          .WORD      RCOUNT          ;RECEIVE CHARACTER COUNT
7389          ;****          ****
7390
7391 034224          ESCAPE TST          ;IF ERROR (I.E. RDI NOT SET), ESCAPE
7392 034224 104410          TRAP          C$ESCAPE
7393 034226 000364          .WORD          L10073-.
7394
7395 034230          BACCIT          ;BUFFER ADDRESS/CHARACTER COUNT XMIT. IN
7396          ;**** MACRO EXPANSION ****
7397 034230 004737 012272  JSR      PC, $BACC          ;CALL BA/CC IN ROUTINE WITH DEFAULTS
7398 034234 000040          .WORD      RQI!BACCT          ;BA/CC IN TRANSMIT COMMAND
7399 034236 002522          .WORD      TBUF          ;TRANSMIT BUFFER ADDRESS
7400 034240 000044          .WORD      TCOUNT          ;TRANSMIT CHARACTER COUNT
7401          ;****          ****
7402
7403 034242          ESCAPE TST          ;IF ERROR (I.E. RDI NOT SET), ESCAPE
7404 034242 104410          TRAP          C$ESCAPE
7405 034244 000346          .WORD          L10073-.
7406
7407 034246          20$:
7408 034246          WAIT      RDO          ;WAIT FOR RDO
7409          ;**** MACRO EXPANSION ****
7410 034246 004737 010276  JSR      PC, $WAIT          ;CALL WAIT ROUTINE
7411 034252 000001          .WORD      1          ;FLAG THAT WE'RE WAITING FOR RDO
7412          ;****          ****
7413 034254          BFRROR 52$          ;IF ERROR - RDO NOT SET, END TEST
7414 034254 103552          BCS          52$
7415 034256 032777 000001 145750  BIT      #CNTRL,@SEL2          ;IS THIS A CONTROL OUT COMMAND ?
7416 034264 001405          BEQ      25$          ;IF NOT - PROCEED
7417 034266          ERRDF 9,EMG9,ERRG2          ;UNEXPECTED CONTROL OUT RECEIVED
7418 034266 104455          TRAP          C$ERDF
7419 034270 000011          .WORD          9
7420 034272 020040          .WORD          EMG9
7421 034274 015124          .WORD          ERRG2
7422 034276 000541          BR      52$
7423 034300          25$:
7424 034300 032777 000004 145726  BIT      #RCV,@SEL2          ;TRANSMIT OR RECEIVE ?
7425 034306 001035          BNE      40$          ;BR FOR RECEIVE
7426
7427          ;CHECK TRANSMIT
7428
7429 034310 005737 002520          TST      TFLAG          ;IS THIS THE FIRST TRANSMIT DONE?
7430 034314 001405          BEQ      30$          ;YES - OK
7431 034316          ERRDF 10,EMG10,ERRG2          ;ERROR MULTIPLE TRANSMITS
7432 034316 104455          TRAP          C$ERDF
7433 034320 000012          .WORD          10
7434 034322 020067          .WORD          EMG10

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7435 034324 015124 .WORD ERRG2
7436 034326 000525
7437 034330
7438 034330 012737 177777 002520 30$: MOV #-1,TFLAG ;FLAG THAT TRANSMIT CHECK IS DONE.
7439 034336 022777 002522 145672 CMP #TBUF,@SEL4 ;TRANSMIT BUFFER ADDRESS CORRECT?
7440 034344 001405 BEQ 32$ ;YES - PROCEED
7441 034346 ERRDF 11,EMG11,ERRG2 ;BUFFER ADDRESS ERROR
7442 034346 104455 TRAP C$ERDF
7443 034350 000013 .WORD 11
7444 034352 020116 .WORD EMG11
7445 034354 015124 .WORD ERRG2
7446 034356 000511
7447 034360
7448 034360 022777 000044 145652 32$: CMP #TCOUNT,@SEL6 ;COUNT CORRECT ?
7449 034366 001470 BEQ 50$ ;YES - PROCEED
7450 034370 ERRDF 12,EMG12,ERRG2 ;CHARACTER COUNT ERROR
7451 034370 104455 TRAP C$ERDF
7452 034372 000014 .WORD 12
7453 034374 020141 .WORD EMG12
7454 034376 015124 .WORD ERRG2
7455 034400 000500 BR 52$
7456
7457 ;CHECK RECEIVE
7458
7459 034402
7460 034402 005737 002570 40$: TST RFLAG ;IS THIS THE FIRST RECEIVE DONE :
7461 034406 001405 BEQ 41$ ;YES - PROCEED
7462 034410 ERRDF 13,EMG13,ERRG2 ;MULTIPLE RECEIVES
7463 034410 104455 TRAP C$ERDF
7464 034412 000015 .WORD 13
7465 034414 020167 .WORD EMG13
7466 034416 015124 .WORD ERRG2
7467 034420 000470
7468 034422
7469 034422 012737 177777 002570 41$: MOV #-1,RFLAG ;FLAG THAT RECEIVE CHECK HAS BEEN DONE.
7470 034430 022777 002572 145600 CMP #RBUF,@SEL4 ;IS THE RECEIVE BUFFER ADDRESS CORRECT?
7471 034436 001405 BEQ 43$ ;YES - PROCEED
7472 034440 ERRDF 11,EMG11,ERRG2 ;BUFFER ADDRESS ERROR
7473 034440 104455 TRAP C$ERDF
7474 034442 000013 .WORD 11
7475 034444 020116 .WORD EMG11
7476 034446 015124 .WORD ERRG2
7477 034450 000454
7478 034452
7479 034452 022777 000044 145560 43$: CMP #RCOUNT,@SEL6 ;IS THE BUFFER COUNT CORRECT?
7480 034460 001405 BEQ 44$ ;YES - PROCEED
7481 034462 ERRDF 12,EMG12,ERRG2 ;CHARACTER COUNT ERROR
7482 034462 104455 TRAP C$ERDF
7483 034464 000014 .WORD 12
7484 034466 020141 .WORD EMG12
7485 034470 015124 .WORD ERRG2
7486 034472 000443
7487 034474
7488 034474 012700 000044 44$: MOV #RCOUNT,R0 ;SET UP FOR DATA CHECK (CHARCATER COUNT)
7489 034500 012701 002522 MOV #TBUF,R1 ;GOOD DATA POINTER
7490 034504 012702 002572 MOV #RBUF,R2 ;RECEIVE DATA POINTER
  
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7491 034510          45$:
7492 034510 122122    CMPB   (R1)+,(R2)+   ;IS THE DATA THE SAME ?
7493 034512 001011    BNE    46$          ;IF NOT, BRANCH TO DATA ERROR MESSAGE
7494 034514 005300    DEC    R0           ;CONTINUE CHECKING UNTIL DONE WITH BUFFER.
7495 034516 001374    BNE    45$          ;
7496 034520 005712    TST    @R2         ;THIS SHOULD BE 0 - REMEMBER WE CLEARED
7497                ;2 EXTRA BYTES DURING BUFFER INIT.
7498 034522 001412    BEQ    50$          ;IF OK - PROCEED
7499 034524                ERRDF  14,EMG14,ERRG2 ;RECEIVED EXTRA DATA
7500 034524 104455                TRAP  C$ERDF
7501 034526 000016                .WORD 14
7502 034530 020215                .WORD EMG14
7503 034532 015124                .WORD ERRG2
7504 034534 000422    BR     52$
7505 034536                46$:
7506 034536                ERRDF  15,EMG15,ERRG2 ;DATA ERROR
7507 034536 104455                TRAP  C$ERDF
7508 034540 000017                .WORD 15
7509 034542 020235                .WORD EMG15
7510 034544 015124                .WORD ERRG2
7511 034546 000415    BR     52$
7512
7513                ; TRANSMIT OR RECEIVE CHECK DONE
7514
7515 034550                50$:
7516 034550 042777 000213 145456 BIC    #RDO+RCV+CMD,@SEL2 ;CLEAR RDO, RCV & COMMAND BITS (0,1)
7517 034556 005737 002570                TST    RFLAG        ;IS THE RECEIVE DONE ? (IF DONE, FLAG = -1)
7518 034562 001002                BNE    51$          ;YES - SEE IF TRANSMIT DONE
7519 034564 000137 034246                JMP    20$          ;NO - GO BACK AND DO IT.
7520 034570                51$:
7521 034570 005737 002520                TST    TFLAG        ;IS THE TRANSMIT DONE ?
7522 034574 001002                BNE    52$          ;YES - BR TO SHUTDOWN
7523 034576 000137 034246                JMP    20$          ;NO - DO IT
7524 034602                52$:
7525 034602                SHUTDN                ;SHUTDOWN DMR
7526                ;**** MACRO EXPANSION ****
7527 034602 004737 012562                JSR    PC, $HALT    ;DMR HALT ROUTINE.
7528                ;****
7529
7530 034606                CALL  $ERROR        ;CHECK BASE TABLE AND REPORT ANY SOFT ERRORS
7531
7532                ENDTST
7533 034612
7534 034612 104401                L10073:
7535                TRAP  C$ETST
7536
7537

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7538 .SBTTL TEST 14 - EXTENDED ADDRESSING DATA TEST
7539
7540 ;*****
7541 ;* TEST 14 - DMR-11
7542 ;* IN THIS TEST - SEE IF WE HAVE MEMORY MANAGEMENT, IF SO SEE IF WE
7543 ;* HAVE THE MEMORY TO CHECK BITS 16 & 17 IN SEL6. THIS WILL ALLOW
7544 ;* US TO TRANSFER DATA USING THOSE EXTENDED ADDRESSING BITS. AS IN
7545 ;* TEST 13 THE TEST IS NON-INTERRUPT AND INTERNAL (TTL) LOOPBACK IS
7546 ;* USED.
7547 ;*
7548 ;*****
7549 034614 BGNST
7550 034614
7551
7552 .ENABL LSB ;ENABLE LOCAL BLOCK - NEEDED BECAUSE OF
7553 SETVEC #4,#NOXMEM,#PRI07 ;USE OF SYMBOLIC LABELS 'RSEL4' ETC.
7554 034614 012746 000340 ;SET UP TRAP VECTOR 4
7555 034620 012746 023630 MOV #PRI07,-(SP)
7556 034624 012746 000004 MOV #NOXMEM,-(SP)
7557 034630 012746 000003 MOV #4,-(SP)
7558 034634 104437 MOV #3,-(SP)
7559 034636 062706 000010 TRAP C$SVEC
7560 034642 005037 002352 ADD #10,SP
7561 034646 005737 177572 CLR NXMFLG ;CLEAR FLAG - SET IF TRAP TO 4.
7562 034652 012700 000004 TST @#177572 ;ADDRESS MEMORY MANAGEMENT REGISTER.
7563 034652 012700 000004 CLRVEC #4 ;RESTORE TRAP VECTOR 4.
7564 034656 104436 MOV #4,R0
7565 034660 005737 002352 TRAP C$CVEC
7566
7567 TSI NXMFLG ;IS THE FLAG STILL CLEARED?
7568 034664 001404 BEQ 10$ ;NOTE: THE FLAG WILL BE SET BY TRAP 4
7569 034666 005037 002352 CLR NXMFLG ;IF THERE IS NO MEMORY MANAGEMENT.
7570 034672 000137 036236 JMP 85$ ;IF FLAG IS CLEARED, PROCEED WITH TEST.
7571 034676 10$: ;RESTORE FLAG
7572 ;EXIT - CAN'T TEST WITHOUT MEM. MANAG.
7573 ;NOTE: L$HIMEM IS SIZE OF TOTAL MEMORY IN
7574 ;PAGE ADDRESS REGISTER FORM - DETERMINED BY
7575 034676 023727 002120 002200 CMP L$HIMEM,#2200 ;BY DIAGNOSTIC SUPERVISOR AT STARTUP.
7576 034704 002002 BGE 15$ ;DO WE HAVE ENOUGH MEMORY TO ADDRESS BIT 16?
7577 034706 000137 036236 JMP 85$ ;IF YES - PROCEED WITH TEST
7578 034712 15$: ;IF NOT - EXIT
7579 034712 SETPRI #PRI07 ;MAKE SURE WE ARE IN KERNEL MODE.
7580 034712 012700 000340 MOV #PRI07,R0
7581 034716 104441 TRAP C$SPRI
7582 ;SETTING PRI SHOULD ALSO CLEAR BITS 14 & 15
7583 ;IN PSW WHICH PLACES PROCESSOR IN KERNEL MODE.
7584 034720 012701 172300 MOV #172300,R1 ;GET ADDRESS OF KERNEL PDR REG 0
7585 034724 012700 000010 MOV #8.,R0 ;GOING TO WRITE PDR REG 0-7
7586 034730 20$:
7587 034730 012721 077406 MOV #77406,(R1)+ ;WRITE BITS FOR THE FOLLOWING PAGE DESCRIPTION
7588 ;READ/WRITE ACCESS, 128. BLOCK PAGE LENGTH.
7589 034734 005300 DEC R0 ;WRITE ALL PDRS
7590 034736 001374 BNE 20$
7591 034740 012701 172340 MOV #172340,R1 ;GET ADDRESS OF KERNAL PAR 0
7592 034744 005011 CLR (R1) ;PAR 0, ADDRS 0 - 17776
7593 034746 012761 000200 000002 MOV #200,2(R1) ;PAR 1, ADDRS 20000 - 37776

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7594 034754 012761 000400 000004      MOV      #400,4(R1)      ;PAR 2,  ADDRS  40000 - 57776
7595 034762 012761 000600 000006      MOV      #600,6(R1)      ;PAR 3,  ADDRS  60000 - 77776
7596 034770 012761 001000 000010      MOV      #1000,10(R1)     ;PAR 4,  ADDRS 100000 - 117776
7597 034776 012761 002000 000012      MOV      #2000,12(R1)    ;PAR 5,  ADDRS 200000 - 217776
7598 035004 012761 004000 000014      MOV      #4000,14(R1)    ;PAR 6,  ADDRS 400000 - 417776
7599 035012 012761 007600 000016      MOV      #7600,16(R1)    ;PAR 7,  ADDRS 160000 - 177776 (I/O PAGE)
7600
7601 035020 012703 000100      MOV      #64.,R3         ;COUNTER FOR OUTER LOOP OF TEST PATTERN GEN.
7602 035024 012704 120000      MOV      #120000,R4      ;USE VIRTUAL ADDRESS TO MAP TO PAR 5
7603                                     ;GENERATE A TEST PATTERN IN THE 1ST 4K
7604                                     ;BYTES OF PAR 5 (VIRTUAL ADDR 120000 - 127776)
7605 035030 005037 002352      CLR      NXMFLG          ;ENSURE FLAG IS CLEARED
7606 035034                                     SETVEC   #4,#NOXMEM,#PRI07 ;SET UP TRAP VECTOR 4 (WILL SET FLAG)
7607 035034 012746 000340      MOV      #PRI07,-(SP)
7608 035040 012746 023630      MOV      #NOXMEM,-(SP)
7609 035044 012746 000004      MOV      #4,-(SP)
7610 035050 012746 000003      MOV      #3,-(SP)
7611 035054 104437                                     TRAP    C$SVEC
7612 035056 062706 000010      ADD      #10,SP
7613 035062 012737 000001 177572      MOV      #1,@#177572    ;ENABLE MEMORY MANAGEMENT
7614 035070                                     21$:
7615 035070 012701 000040      MOV      #32.,R1         ;COUNTER FOR INNER LOOP OF TEST PATTERN GEN.
7616 035074 012702 002420      MOV      #$$CITT,R2      ;ADDRESS FOR 32. WORD TEST PATTERN
7617 035100                                     22$:
7618 035100 012224      MOV      (R2)+,(R4)+     ;WRITE TEST PATTERN INTO 4K BYTES
7619                                     ;(PHYSICAL ADDRESS 200000 - 207776)
7620 035102 005737 002352      TST      NXMFLG          ;NXM TRAP 4?
7621 035106 001014      BNE      24$             ;IF YES - EXIT
7622 035110 005301      DEC      R1              ;DO THE INNER LOOP 32. TIMES
7623 035112 001372      BNE      22$
7624 035114 005303      DEC      R3              ;DO THE OUTER LOOP 128. TIMES
7625 035116 001364      BNE      21$
7626 035120 012701 004000      MOV      #4000,R1        ;COUNTER TO CLEAR THE NEXT 4K BYTES.
7627 035124                                     23$:
7628 035124 005024      CLR      (R4)+           ;CLEAR OUT THE ENTIRE PAR
7629                                     ;(PHYSICAL ADDRESS 210000 - 217776)
7630 035126 005737 002352      TST      NXMFLG          ;NXM TRAP 4?
7631 035132 001002      BNE      24$             ;IF YES - EXIT
7632 035134 005301      DEC      R1
7633 035136 001372      BNE      23$
7634 035140                                     24$:
7635 035140 005037 177572      CLR      @#177572        ;TURN OFF MEMORY MANAGEMENT
7636 035144      CLRVEC   #4             ;RESTORE TRAP 4 TO SUPERVISOR
7637 035144 012700 000004      MOV      #4,R0
7638 035150 104436      TRAP    C$CVEC
7639 035152 005737 002352      TST      NXMFLG          ;WAS THIS AN ERROR EXIT
7640 035156 001417      BEQ     25$             ;IF NOT, PROCEED.
7641 035160      ERRDF   19,EMT22
7642 035160 104455      TRAP    C$ERDF
7643 035162 000023      .WORD   19
7644 035164 036240      .WORD   EMT22
7645 035166 000000      .WORD   0
7646 035170      PRINTB  #FMT25,R4
7647 035170 010446      MOV      R4,-(SP)
7648 035172 012746 036276      MOV      #FMT25,-(SP)
7649 035176 012746 000002      MOV      #2,-(SP)

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7650 035202 01060C
7651 035204 104414
7652 035206 062706 000006
7653 035212 000137 036236
7654 035216
7655 035216
7656
7657 035216 004737 011070
7658
7659
7660 035222
7661 035222 104410
7662 035224 001012
7663
7664 035226 005737 002254
7665 035232 001004
7666 035234 052737 004000 035256
7667 035242 000403
7668 035244
7669 035244 042737 004000 035256
7670 035252
7671 035252
7672 035256 000000
7673 035260 002640
7674 035262 000522
7675 035264
7676 035264 104410
7677 035266 000750
7678
7679 035270
7680 035274
7681 035274 104410
7682 035276 000740
7683
7684 035300
7685
7686 035300 004737 011522
7687 035304 000000
7688
7689 035306
7690 035306 104410
7691 035310 000726
7692
7693 035312 005037 002520
7694 035316 005037 002570
7695 035322 005037 002346
7696
7697
7698 035326 012737 010000 035362
7699 035334 012737 050000 035364
7700
7701 035342 005037 035400
7702 035346 012737 050000 035402
7703
7704 035354
7705 035354

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25\$: JMP 85\$
CLEAR ;MACRO FOR MASTER CLEAR
;**** MACRO EXPANSION ****
JSR PC, \$MSCLR ;ISSUE A DMR MASTER CLEAR
;****
ESCAPE TST ;IF ERROR, BR TO TEST END.
TRAP C\$ESCAPE
.WORD L10074-.
TST DMTURN ;IS INTERNAL LOOPBACK DESIRED?
BNF 30\$;IF NOT, CLEAR INTERNAL LOOPBACK.
BIS #LPLU,100\$;SET LINE UNIT LOOPBACK.
BR 32\$
30\$: BIC #LPLU,100\$;CLEAR LINE UNIT LOOPBACK.
32\$: CALL \$BASEI ;BASE IN COMMAND.
100\$: .WORD 0 ;MAINTENANCE BITS (LINE UNIT LOOP)
.WORD BASE ;BASE TABLE ADDRESS
.WORD DMR ;DMR MODE
ESCAPE TST ;IF ERROR, BR TO TEST END.
TRAP C\$ESCAPE
.WORD L10074-.
CALL \$LOOP ;DMR COMMAND TO SET MAINT. BITS
ESCAPE TST ;IF ERROR, BR TO TEST END.
TRAP C\$ESCAPE
.WORD L10074-.
CNTRIN ;MACRO FOR CONTROL IN (FULL DUPLEX)
;**** MACRO EXPANSION ****
JSR PC, \$CNTIN ;CALL CONTROL IN ROUTINE WITH DEFAULT
.WORD 0 ;SEL6 - FULL DUPLEX, RUN MODE, 1 SEC START.
;****
ESCAPE TST ;IF ERROR, BR TO TEST END.
TRAP C\$ESCAPE
.WORD L10074-.
CLR TFLAG ;CLEAR TRANSMIT FLAG
CLR RFLAG ;CLEAR RECEIVE FLAG
CLR SFLAG ;CLEAR SECOND LOOP FLAG
;IF SFLAG = 0, THEN THIS IS A TEST OF BIT 16
;IF SFLAG = -1, THEN THIS IS A TEST OF BIT 17
MOV #10000,RSEL4 ;RECEIVE BUFFER ADDRESS (BITS 0-15)
MOV #BIT14!10000,RSEL6 ;REC BUFFER ADDR BIT 16 SET AND 4K
;BYTE RECEIVE CHARACTER COUNT
CLR TSEL4 ;TRANSMIT BUFFER ADDRESS (BITS 0-15)
MOV #BIT14!10000,TSEL6 ;XMIT BUFFER ADDR BIT 16 SET AND 4K
;BYTE XMIT CHARACTER COUNT
35\$: CALL \$BACC ;ISSUE THE BUFFER ADDR/ CHAR COUNT COMMAND

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7706 035360 000044          .WORD  RQI:BACCR      ;COMMAND FOR BA/CC IN RECEIVE
7707 035362 000000          .WORD  0              ;BJFFER ADDRESS BITS 0-15
7708 035364 000000          RSEL4: .WORD  0              ;BUFFER ADDR BIT 16 + CHAR. COUNT
7709 035366          RSEL6: .WORD  0              ;IF ERROR, END TEST
7710 035366 104410          ESCAPE TST
7711 035370 000646          TRAP   C$ESCAPE
7712          .WORD   L10074-.
7713 035372          CALL   $BACC          ;ISSUE THE BUFFER ADDR/ CHAR COUNT COMMAND
7714 035376 000040          .WORD  RQI.BACCT      ;COMMAND FOR BA/CC IN TRANSMIT
7715 035400 000000          TSEL4: .WORD  0              ;BUFFER ADDRESS BITS 0-15
7716 035402 000000          TSEL6: .WORD  0              ;BUFFER ADDR BIT 16 + CHAR. COUNT
7717 035404          ESCAPE TST          ;IF ERROR, END TEST
7718 035404 104410          TRAP   C$ESCAPE
7719 035406 000630          .WORD   L10074-.
7720 035410          40$:
7721 035410          WAIT   RDO          ;WAIT FOR RDO TO BE SET
7722          .WORD   ;**** MACRO EXPANSION ****
7723 035410 004737 010276          JSR    PC, $WAIT      ;CALL WAIT ROUTINE
7724 035414 000001          .WORD  1              ;FLAG THAT WE'RE WAITING FOR RDO
7725          .WORD   ;****
7726 035416          ESCAPE TST          ;IF RDO NOT SET BEFORE TIMEOUT, END TEST
7727 035416 104410          TRAP   C$ESCAPE
7728 035420 000616          .WORD   L10074-.
7729
7730 035422 032777 000001 144604          BIT    #CNTRL,@SEL2   ;IS THIS A CONTROL OUT COMMAND?
7731 035430 001406          BEQ    50$           ;NO - PROCEED
7732 035432          ERRDF 9,EMG9,ERRG2 ;UNEXPECTED CONTROL OUT.
7733 035432 104455          TRAP   C$ERDF
7734 035434 000011          .WORD   9
7735 035436 020040          .WORD   EMG9
7736 035440 015124          .WORD   ERRG2
7737 035442 000137 036226          JMP    80$           ;EXIT
7738 035446          50$:
7739 035446 032777 000004 144560          BIT    #RCV,@SEL2    ;IS THIS A TRANSMIT OR RECEIVE?
7740 035454 001040          BNE    60$           ;BR FOR RECEIVE
7741 035456 005737 002520          TST    TFLAG         ;IS THIS THE 1ST TRANSMIT DONE
7742 035462 001406          BEQ    55$           ;IF YES, PROCEED
7743 035464          ERRDF 10,EMG10,ERRG2 ;MULTIPLE TRANSMITS
7744 035464 104455          TRAP   C$ERDF
7745 035466 000012          .WORD   10
7746 035470 020067          .WORD   EMG10
7747 035472 015124          .WORD   ERRG2
7748 035474 000137 036226          JMP    80$           ;EXIT
7749 035500          55$:
7750 035500 012737 177777 002520          MOV    #-1,TFLAG     ;FLAG THAT THE TRANSMIT IS DONE.
7751 035506 023777 035400 144522          CMP    TSEL4,@SEL4   ;IS THE BUFFER ADDRESS CORRECT?
7752 035514 001406          BEQ    56$           ;IF OK, PROCEED WITH CHECK.
7753 035516          ERRDF 11,EMG11,ERRG2 ;BUFFER ADDRESS ERROR
7754 035516 104455          TRAP   C$ERDF
7755 035520 000013          .WORD   11
7756 035522 020116          .WORD   EMG11
7757 035524 015124          .WORD   ERRG2
7758 035526 000137 036226          JMP    80$           ;EXIT
7759 035532          56$:
7760 035532 023777 035402 144500          CMP    TSEL6,@SEL6   ;IS THE CHAR. COUNT CORRECT?
7761 035540 001502          BEQ    70$           ;IF OK, PROCEED

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7762 035542 ERRDF 12,EMG12,ERRG2 ;CHARACTER COUNT ERROR - OR EXT MEM PROBLEM
7763 035542 104455 TRAP C$ERDF
7764 035544 000014 .WORD 12
7765 035546 020141 .WORD EMG12
7766 035550 015124 .WORD ERRG2
7767 035552 000137 036226 JMP 80$ ;EXIT
7768 035556 60$:
7769 035556 005737 002570 TST RFLAG ;IS THIS THE 1ST RECEIVE DONE
7770 035562 001406 BEQ 61$ ;IF YES, PROCEED
7771 035564 ERRDF 13,EMG13,ERRG2 ;MULTIPLE RECEIVES
7772 035564 104455 TRAP C$ERDF
7773 035566 000015 .WORD 13
7774 035570 020167 .WORD EMG13
7775 035572 015124 .WORD E-RG2
7776 035574 000137 036226 JMP 80$ ;EXIT
7777 035600 61$:
7778 035600 012737 177777 002570 MOV #-1,RFLAG ;FLAG THAT THE RECEIVE IS DONE.
7779 035606 023777 035362 144422 CMP RSEL4,@SEL4 ;IS THE BUFFER ADDRESS CORRECT?
7780 035614 001405 BEQ 62$ ;IF OK, PROCEED WITH CHECK.
7781 035616 ERRDF 11,EMG11,ERRG2 ;BUFFER ADDRESS ERROR
7782 035616 104455 TRAP C$ERDF
7783 035620 000013 .WORD 11
7784 035622 020116 .WORD EMG11
7785 035624 015124 .WORD ERRG2
7786 035626 000577 BR 80$ ;EXIT
7787 035630 62$:
7788 035630 023777 035364 144402 CMP RSEL6,@SEL6 ;IS THE CHAR. COUNT CORRECT?
7789 035636 001404 BEQ 63$ ;IF OK, PROCEED
7790 035640 ERRDF 12,EMG12,ERRG2 ;CHARACTER COUNT ERROR
7791 035640 104455 TRAP C$ERDF
7792 035642 000014 .WORD 12
7793 035644 020141 .WORD EMG12
7794 035646 015124 .WORD ERRG2
7795 035650 63$:
7796 035650 005737 002346 TST SFLAG ;WHICH EXTENDED ADDRESS ARE WE CHECKING?
7797 035654 001007 BNE 65$ ;BR FOR BIT 17 CHECK
7798 035656 012700 004000 MOV #4000,R0 ;IN THE TEST OF BIT 16 WE SENT 4K BYTES
7799 035662 012701 120000 MOV #120000,R1 ;THE XMIT BUFFER BEGAN AT THIS VIRTUAL ADDR
7800 ;WHICH WILL MAP TO 200000
7801 035666 012702 130000 MOV #130000,R2 ;THE REC BUF. MAPS TO 210000
7802 035672 000406 BR 66$ ;GO COMPARE THE XMIT AND REC BUFFERS
7803 035674 65$:
7804 035674 012700 010000 MOV #10000,R0 ;IN THE TEST OF BIT 17 WE SENT 8K BYTES
7805 035700 012701 120000 MOV #120000,R1 ;THE XMIT BUFFER MAPS TO 200000
7806 035704 012702 140000 MOV #140000,R2 ;THE REC BUF. MAPS TO 400000
7807 035710 66$:
7808 035710 012737 000001 177572 MOV #1,@#177572 ;TURN MEMORY MANAGEMENT BACK ON.
7809 035716 67$:
7810 035716 022122 CMP (R1)+,(R2)+ ;IS THE DATA THE SAME?
7811 035720 001003 BNE 68$ ;IF NOT THERE IS A DATA ERROR.
7812 035722 005300 DEC R0 ;CHECK ENTIRE BUFFER
7813 035724 001374 BNE 67$
7814 035726 000407 BR 70$ ;IF DATA OK, PROCEED WITH TEST.
7815 035730 68$:
7816 035730 005037 177572 CLR @#177572 ;TURN MEMORY MANAGEMENT OFF.
7817 035734 ERRDF 15,EMG15,ERRG2 ;DATA ERROR

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7818 035734 104455
7819 035736 000017
7820 035740 020235
7821 035742 015124
7822 035744 000530
7823 035746
7824 035746 005037 177572
7825 035752 042777 000213 144254
7826 035760 005737 002570
7827 035764 001002
7828 035766 000137 035410
7829 035772
7830 035772 005737 002520
7831 035776 001002
7832 036000 000137 035410
7833 036004
7834 036004 005737 002346
7835 036010 001106
7836
7837 036012 012737 177777 002346
7838 036020 023727 002120 004200
7839 036026 002477
7840 036030 005037 002520
7841 036034 005037 002570
7842
7843
7844
7845
7846
7847
7848 036040 005037 035362
7849 036044 012737 120000 035364
7850
7851 036052 005037 035400
7852 036056 012737 060000 035402
7853
7854 036064 012701 010000
7855 036070 012704 140000
7856
7857 036074 005037 002352
7858 036100
7859 036100 012746 000340
7860 036104 012746 023630
7861 036110 012746 000004
7862 036114 012746 000003
7863 036120 104437
7864 036122 062706 000010
7865 036126 012737 000001 177572
7866 036134
7867 036134 005024
7868 036136 005737 002352
7869 036142 001002
7870 036144 005300
7871 036146 001372
7872 036150
7873 036150 005037 177572

70$: BR 80$ ;EXIT
CLR @#177572 ;TURN MEMORY MANAGEMENT OFF.
BIC #RDO+RCV+CMD,@SEL2 ;CLEAR RDO, RCV & COMMAND BITS (0,1)
TST RFLAG ;IS THE RECEIVE DONE ? (IF DONE, FLAG = -1)
BNE 71$ ;YES - SEE IF TRANSMIT DONE
JMP 40$ ;NO - GO BACK AND DO IT.

71$: TST TFLAG ;IS THE TRANSMIT DONE ?
BNE 72$ ;YES - SEE IF THERE IS MORE
JMP 40$ ;NO - DO IT

72$: TST SFLAG ;HAVE WE ALREADY TESTED BIT 17
BNE 80$ ;IF SO - END OF TEST

MOV #-1,SFLAG ;FLAG SO WE DON'T COME THIS WAY AGAIN.
CMP L$HIMEM,#4200 ;IS THERE ENOUGH MEMORY TO TEST BIT 17?
BLT 80$ ;IF NOT - END OF TEST.
CLR TFLAG ;CLEAR FLAGS FOR NEXT TEST
CLR RFLAG

;SET UP TO TEST BIT 17, IF THERE IS ENOUGH MEMORY.
;THIS TEST WILL TRANSMIT 8K BYTES STARTING AT PHYSICAL ADDRESS 20000
;TO PHYSICAL ADDRESS 40000. THE TRANSMITTED BUFFER STILL CONTAINS
;THE TEST PATTERN GENERATED IN THE BIT 16 TEST.

CLR RSEL4 ;RECEIVE BUFFER ADDRESS (BITS 0-15)
MOV #BIT15!20000,RSEL6 ;REC BUFFER ADDR BIT 17 SET AND 8K
;BYTE RECEIVE CHARACTER COUNT
CLR TSEL4 ;TRANSMIT BUFFER ADDRESS (BITS 0-15)
MOV #BIT14!20000,TSEL6 ;XMIT BUFFER ADDR BIT 16 SET AND 8K
;BYTE XMIT CHARACTER COUNT
MOV #10000,R1 ;COUNTER TO CLEAR 8K BYTES
MOV #140000,R4 ;VIRTUAL ADDRESS THAT WILL MAP INTO PAR 6
;WITH THE PHYSICAL ADDRESS 40000
CLR NXMFLG ;ENSURE FLAG IS CLEAR
SETVEC #4,#NOXMEM,#PRI07 ;SET UP TRAP TO VECTOR 4 (WILL SET FLAG)
MOV #PRI07,-(SP)
MOV #NOXMEM,-(SP)
MOV #4,-(SP)
MOV #3,-(SP)
TRAP C$SVEC
ADD #10,SP

74$: MOV #1,@#177572 ;TURN ON MEMORY MANAGEMENT
CLR (R4)+ ;CLEAR 40000 - 417776
TST NXMFLG ;DOES A NXM TRAP 4 OCCUR?
BNE 75$ ;IF YES, EXIT
DEC R0
BNE 74$

75$: CLR @#177572 ;TURN OFF MEMORY MANAGEMENT

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7874 036154          CLRVEC #4          ;RESTORE TRAP 4
7875 036154 012700 000004          MOV #4,R0
7876 036160 104436          TRAP C$CVEC
7877 036162 005737 002352          TST NXMFLG          ;WAS THIS AN ERROR EXIT?
7878 036166 001002          BNE 76$            ;IF YES - REPORT ERROR
7879 036170 000137 035354          JMP 35$            ;START THE SECOND TEST
7880 036174          76$:
7881 036174          ERRDF 19,EMT22
7882 036174 104455          TRAP C$ERDF
7883 036176 000023          .WORD 19
7884 036200 036240          .WORD EMT22
7885 036202 000000          .WORD 0
7886 036204          PRINTB #FMT25,R4
7887 036204 010446          MOV R4,-(SP)
7888 036206 012746 036276          MOV #FMT25,-(SP)
7889 036212 012746 000002          MOV #2,-(SP)
7890 036216 010600          MOV SP,R0
7891 036220 104414          TRAP C$PNTB
7892 036222 062706 000006          ADD #6,SP
7893 036226          80$:
7894 036226          SHUTDN          ;SHUTDOWN DMR
7895          ;**** MACRO EXPANSION ****
7896 036226 004737 012562          JSR PC,$HALT      ;DMR HALT ROUTINE.
7897          ;****
7898 036232          CALL $ERROR      ;CHECK BASE TABLE AND REPORT ANY SOFT ERRORS
7899
7900          85$:
7901          .DSABL LSB          ;DISABLE LOCAL SYMBOL BLOCK
7902          ENDTST
7903          L10074:
7904 036236 104401          TRAP C$ETST
7905
7906
7907 036240 040503 023516 020124          EMT22: .ASCIZ /CAN'T ADDRESS EXTENDED MEMORY/
7908 036246 042101 051104 051505
7909 036254 020123 054105 042524
7910 036262 042116 042105 046440
7911 036270 046505 051117 000131
7912 036276 040445 042515 047515          FMT25: .ASCIZ /%AMEMORY ADDRESS %06% DOES NOT RESPOND - TRAP 4%/
7913 036304 054522 040440 042104
7914 036312 042522 051523 022440
7915 036320 033117 020045 047504
7916 036326 051505 047040 052117
7917 036334 051040 051505 047520
7918 036342 042116 026440 052040
7919 036350 040522 020120 022464
7920 036356 000116
7921          .EVEN

```

.SBTTL TEST 15 - DMC MODE TEST

TEST 15 - DMR-11

* DMC MODE
* IN THIS TEST THE DMR WILL TRANSMIT AND RECEIVE 7 BUFFERS.

* ALL BA/CC OUTS RECEIVES AND TRANSMITS WILL BE ACCOUNTED FOR AND
* THE CHARACTER COUNTS AND BUFFER ADDRESSES WILL BE CHECKED AGAINST
* THE RECEIVE/TRANSMIT TABLE.

* THE BUFFERS ARE DETERMINED IN THE SUBROUTINE \$BUFFS. THIS
* SUBROUTINE WILL DETERMINE THE ADDRESS AND CHARACTER COUNT OF
* SEVEN RECEIVE AND SEVEN TRANSMIT BUFFERS. THE ROUTINE WILL
* ATTEMPT TO USE AS LARGE BUFFERS AS POSSIBLE IN THE FOLLOWING
* HIERARCHY:

- A. IF THERE IS MEMORY MANAGEMENT, USE A PAGE ABOVE 32K.
- B. IF THERE IS FREE MEMORY ABOVE THE SUPERVISOR GREATER THAN 2K BYTES, USE THAT MEMORY
- C. IF NEITHER OF THE PRECEEDING TWO ARE POSSIBLE, USE THE 2K BYTE DEFAULT BUFFER WITHIN THIS DIAGNOSTIC.

BGNTST

T15::

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036360
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036360 005737 002306
036364 001035
036366 012737 000007 002324
036374 005037 002274
036400 012737 000001 002276
036406 005037 002300
036412
036416
036416 004737 011070
036422
036422 104410
036424 000034
036426
036426 004737 011266
036432 004000
036434 002640
036436 000522
036440
036440 104410

TST WMAINT
BNE 40\$
MOV #7, BUFNUM
CLR RESUME
MOV #1, DMCMDR
CLR MNTMDE
CALL \$BUFFS
CLEAR
JSR PC, \$MSCLR
ESCAPE TST
BASEIN
JSR PC, \$BASEI
.WORD LPLU
.WORD BASE
.WORD DMR

;DO WE NEED TO WRITE MODEM
;MAINTENANCE 1 OR 2 ?
;IF YES, WE CAN'T RUN THIS TEST.
;# OF RCV & XMIT BUFFERS.
;FLAG NOT TO USE RESUME.
;FLAG SET TO REQUEST DMC MODE.
;FLAG NOT TO REQUEST MAINTENANCE MODE.
;DETERMINE 7 RCV & 7 XMIT PUFFERS
;MASTER CLEAR
;**** MACRO EXPANSION ****
;ISSUE A DMR MASTER CLEAR
;****
;IF ERROR, EXIT TEST
;ISSUE A DMR MODE BASEIN
;IN DMR MODE, IF A INTERFACE IS REQUIRED
;TO BE WRITTEN - IT WILL BE DONE.
;**** MACRO EXPANSION ****
;CALL BASE IN ROUTINE WITH DEFAULTS
;SET LINE UNIT LOOP
;BASE TABLE ADDRESS
;DMR-11 MODE
;****

TRAP C\$ESCAPE
.WORD L10075-

TRAP C\$ESCAPE

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036462 013737 002254 002336
036470 005037 002254
036474 012737 000007 002324
036502 012737 000001 002274
036510 005037 002276
036514 005037 002300
036520

```
.SBTTL TEST 16 - DMR MODE (RESUME) INTERRUPT TEST
*****
* TEST 16 - DMR-11
* RESUME BASE IN - DMR MODE
* IN THIS TEST THE DMR WILL TRANSMIT AND RECEIVE 7 BUFFERS. DURING THE
* TEST THE DMR WILL BE HALTED AND RESTARTED BY A BASE-IN RESUME IN THE
* FOLLOWING MANNER:
* BASE IN
* CONTROL IN
* HALT MASTER CLEAR BASE IN RESUME
* 2 BA/CC IN RECEIVE
* HALT MASTER CLEAR BASE IN RESUME
* 2 BA/CC IN RECEIVE
* HALT MASTER CLEAR BASE IN RESUME
* 2 BA/CC IN RECEIVE
* HALT MASTER CLEAR BASE IN RESUME
* 1 BA/CC IN RECEIVE
* HALT MASTER CLEAR BASE IN RESUME
* 2 BA/CC IN TRANSMIT
* HALT MASTER CLEAR BASE IN RESUME
* 2 BA/CC IN TRANSMIT
* HALT MASTER CLEAR BASE IN RESUME
* 2 BA/CC IN TRANSMIT
* HALT MASTER CLEAR BASE IN RESUME
* 1 BA/CC IN TRANSMIT
* HALT MASTER CLEAR BASE IN RESUME
*
* ALL BA/CC OUTS RECEIVES AND TRANSMITS WILL BE ACCOUNTED FOR AND
* THE CHARACTER COUNTS AND BUFFER ADDRESSES WILL BE CHECKED AGAINST
* THE RECEIVE/TRANSMIT TABLE.
*
* THE BUFFERS ARE DETERMINED IN THE SUBROUTINE $BUFFS. THIS
* SUBROUTINE WILL DETERMINE THE ADDRESS AND CHARACTER COUNT OF
* SEVEN RECEIVE AND SEVEN TRANSMIT BUFFERS. THE ROUTINE WILL
* ATTEMPT TO USE AS LARGE BUFFERS AS POSSIBLE IN THE FOLLOWING
* HIERARCHY:
* A. IF THERE IS MEMORY MANAGEMENT, USE A PAGE ABOVE 32K.
* B. IF THERE IS FREE MEMORY ABOVE THE SUPERVISOR GREATER
* THAN 2K BYTES, USE THAT MEMORY
* C. IF NEITHER OF THE PRECEEDING TWO ARE POSSIBLE, USE
* THE 2K BYTE DEFAULT BUFFER WITHIN THIS DIAGNOSTIC.
*****
BGNTST
MOV DMTURN,TEMP1 ;SAVE OFF LOOP TYPE
CLR DMTURN ;FORCE INTERNAL LOOP
MOV #7,BUFNUM ;# OF RCV & XMIT BUFFERS.
MOV #1,RESUME ;FLAG SET TO REQUEST USE OF RESUME.
CLR DMCMDL ;FLAG CLEARED - DMR MODE.
CLR MNTMDE ;FLAG NOT TO REQUEST MAINTENANCE MODE.
CALL $BUFFS ;DETERMINE 7 RCV & 7 XMIT BUFFERS
T16::
```



```

8085 .SBTTL TEST 17 - DMR MODE INTERRUPT EXERCISE
8086
8087
8088 *****
8089 * TEST 17 - DMR-11
8090 * INTERRUPT DRIVEN EXERCISE
8091 * IN THIS TEST 64 BUFFERS WILL BE TRANSMITTED AND RECEIVED
8092 *
8093 * ALL BA/CC OUTS RECEIVES AND TRANSMITS WILL BE ACCOUNTED FOR AND
8094 * THE CHARACTER COUNTS AND BUFFER ADDRESSES WILL BE CHECKED AGAINST
8095 * THE RECEIVE/TRANSMIT TABLE.
8096 *
8097 * THE BUFFERS ARE DETERMINED IN THE SUBROUTINE $BUFFS. THIS
8098 * SUBROUTINE WILL DETERMINE THE ADDRESS AND CHARACTER COUNT OF
8099 * 64 RECEIVE AND 64 TRANSMIT BUFFERS. THE ROUTINE WILL
8100 * ATTEMPT TO USE AS LARGE BUFFERS AS POSSIBLE IN THE FOLLOWING
8101 * HIERARCHY:
8102 * A. IF THERE IS MEMORY MANAGEMENT, USE A PAGE ABOVE 32K.
8103 * B. IF THERE IS FREE MEMORY ABOVE THE SUPERVISOR GREATER
8104 * THAN 2K BYTES, USE THAT MEMORY
8105 * C. IF NEITHER OF THE PRECEEDING TWO ARE POSSIBLE, USE
8106 * THE 2K BYTE DEFAULT BUFFER WITHIN THIS DIAGNOSTIC.
8107 *
8108 *****
8109 BGNST
8110 036550 012737 000100 002324 MOV #64, BUFNUM ;# OF RCV & XMIT BUFFERS. T17::
8111
8112 036556 005037 002274 CLR RESUME ;FLAG CLEARED IN ORDER NOT TO USE RESUME.
8113 036562 005037 002276 CLR DMCMD E ;FLAG CLEARED TO ALLOW DMR MODE.
8114 036566 005037 002300 CLR MNTMDE ;FLAG NOT TO REQUEST MAINTENANCE MODE.
8115
8116 036572 CALL $BUFFS ;DETERMINE 64 RCV & 64 XMIT BUFFERS
8117
8118 036576 CLEAR ;MASTER CLEAR
8119 ;**** MACRO EXPANSION ****
8120 036576 004737 011070 JSR PC, $MSCLR ;ISSUE A DMR MASTER CLEAR
8121 ;**** ****
8122
8123 036602 ESCAPE TST ;IF ERROR, EXIT TEST
8124 036602 104410 TRAP C$ESCAPE
8125 036604 000012 .WORD L10077-
8126
8127 036606 CALL $INOUT ;THIS ROUTINE WILL MANAGE ALL THE DMR
8128 ;COMMANDS ISSUED IN THE INTERRUPT ROUTINES
8129 ;(FROM BASE IN UNTIL SHUT DOWN). BESIDES
8130 ;CONTROLLING THE SOFTWARE TIMEOUT, THIS
8131 ;ROUTINE WILL ALSO CHECK THAT BUFFER
8132 ;CHARACTER COUNTS AND ADDRESSES ARE CORRECT
8133 ;AND THAT THE DATA IS CORRECT IN THOSE BUFFERS
8134
8135 036612 CALL $ERROR ;CHECK BASE TABLE FOR SOFT ERRORS
8136
8137
8138 036616 ENDTST
8139 036616 L10077:
8140 036616 104401 TRAP C$ETST
    
```

8141


```

8142 .SBTTL TEST 18 - DMR MODE LARGE MESSAGE
8143
8144 :*****
8145 :* TEST 18 - DMR-11
8146 :* LARGE MESSAGE
8147 :* IN THIS MODE TRANSMIT AND RECEIVE 1 LARGE BUFFER
8148 :*
8149 :* THE BA/CC OUT RECEIVE AND TRANSMIT WILL BE ACCOUNTED FOR AND
8150 :* THE CHARACTER COUNTS AND BUFFER ADDRESSES WILL BE CHECKED AGAINST
8151 :* THE RECEIVE/TRANSMIT TABLE.
8152 :*
8153 :* THE BUFFERS ARE DETERMINED IN THE SUBROUTINE $BUFFS. THIS
8154 :* SUBROUTINE WILL DETERMINE THE ADDRESS AND CHARACTER COUNT OF
8155 :* ONE RECEIVE AND ONE TRANSMIT BUFFER. THE ROUTINE WILL
8156 :* ATTEMPT TO USE AS LARGE BUFFERS AS POSSIBLE IN THE FOLLOWING
8157 :* HIERARCHY:
8158 :* A. IF THERE IS MEMORY MANAGEMENT, USE A PAGE ABOVE 32K.
8159 :* B. IF THERE IS FREE MEMORY ABOVE THE SUPERVISOR GREATER
8160 :* THAN 2K BYTES, USE THAT MEMORY
8161 :* C. IF NEITHER OF THE PRECEEDING TWO ARE POSSIBLE, USE
8162 :* THE 2K BYTE DEFAULT BUFFER WITHIN THIS DIAGNOSTIC.
8163 :*
8164 :*****
8165 036620 BGNST
8166 036620 T18::
8167
8168 036620 012737 000001 002324 MOV #1,BUFNUM ;# OF RCV & XMIT BUFFERS.
8169
8170 036626 005037 002274 CLR RESUME ;FLAG CLEARED IN ORDER NOT TO USE RESUME.
8171 036632 005037 002276 CLR DMCMDE ;FLAG CLEARED TO ALLOW DMR MODE.
8172 036636 005037 002300 CLR MNTIDE ;FLAG NOT TO REQUEST MAINTENANCE MODE.
8173
8174 036642 CALL $BUFFS ;DETERMINE 1 RCV & 1 XMIT BUFFER
8175
8176 036646 CLEAR ;MASTER CLEAR
8177 ;**** MACRO EXPANSION ****
8178 036646 004737 011070 JSR PC, $MSCLR ;ISSUE A DMR MASTER CLEAR
8179 ;****
8180
8181 036652 ESCAPE TST ;IF ERROR, EXIT TEST
8182 036652 104410 TRAP C$ESCAPE
8183 036654 000012 .WORD L10100-.
8184
8185 036656 CALL $INOUT ;THIS ROUTINE WILL MANAGE ALL THE DMR
8186 ;COMMANDS ISSUED IN THE INTERRUPT ROUTINES
8187 ;(FROM BASE IN UNTIL SHUT DOWN). BESIDES
8188 ;CONTROLLING THE SOFTWARE TIMEOUT, THIS
8189 ;ROUTINE WILL ALSO CHECK THAT BUFFER
8190 ;CHARACTER COUNTS AND ADDRESSES ARE CORRECT
8191 ;AND THAT THE DATA IS CORRECT IN THOSE BUFFERS
8192
8193 036662 CALL $ERROR ;CHECK BASE TABLE FOR SOFT ERRORS
8194 ;NOTE: NORMALLY ANY NON-ZERO ERROR COUNT IS
8195 ;REPORTED; HOWEVER IN THIS TEST A REP COUNT
8196 ;OF 1 IS ALLOWED, BECAUSE AT LOW BAUD RATES
8197 ;WE WOULD EXPECT 1 REP.

```

CZDMIDO DMR-11 FUNCTIONAL TESTS MACY11 30A(1052) 29-JUL-81 12:42 E 14 PAGE 174
CZDMID.P11 29-JUL-81 11:32 TEST 18 - DMR MODE LARGE MESSAGE

SEQ 0173

8198
8199
8200 036666
8201 036666
8202 036666 104401
8203

ENDTST

L10100:
TRAP CSETST

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 8226 036670
 8227 036670
 8228 036670 012737 000001 002324
 8229
 8230 036676 005037 002274
 8231 036702 005037 002276
 8232 036706 012737 000001 002300
 8233
 8234 036714
 8235
 8236 036720
 8237
 8238 036720 004737 011070
 8239
 8240
 8241 036724
 8242 036724 104410
 8243 036726 000012
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 8245 036730
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 8253 036734
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 8256 036740
 8257 036740
 8258 036740 104401

```

.SBTTL          TEST 19 - DMR MAINTENANCE MODE MESSAGE
:*****
:*              TEST 19 - DMR-11
:* MAINTENANCE MODE OPERATION
:*
:* THE BA/CC OUT RECEIVE AND TRANSMIT WILL BE ACCOUNTED FOR AND
:* THE CHARACTER COUNTS AND BUFFER ADDRESSES WILL BE CHECKED AGAINST
:* THE RECEIVE/TRANSMIT TABLE.
:*
:* THE BUFFERS ARE DETERMINED IN THE SUBROUTINE $BUFFS. THIS
:* SUBROUTINE WILL DETERMINE THE ADDRESS AND CHARACTER COUNT OF
:* ONE RECEIVE AND ONE TRANSMIT BUFFER. THE ROUTINE WILL
:* ATTEMPT TO USE AS LARGE BUFFERS AS POSSIBLE IN THE FOLLOWING
:* HIERARCHY:
:*   A. IF THERE IS MEMORY MANAGEMENT, USE A PAGE ABOVE 32K.
:*   B. IF THERE IS FREE MEMORY ABOVE THE SUPERVISOR GREATER
:*      THAN 2K BYTES, USE THAT MEMORY
:*   C. IF NEITHER OF THE PRECEEDING TWO ARE POSSIBLE, USE
:*      THE 2K BYTE DEFAULT BUFFER WITHIN THIS DIAGNOSTIC.
:*****
BGNTST
:*****
:              T19::
MOV          #1,BUFNUM          ;# OF RCV & XMIT BUFFERS.
CLR          RESUME             ;DON'T ALLOW RESUME
CLR          DMCMD             ;FLAG CLEARED TO ALLOW DMR MODE.
MOV          #1,MNTMDE         ;FLAG SET TO REQUEST MAINTENANCE MODE.
CALL         $BUFFS            ;DETERMINE 1 RCV & 1 XMIT BUFFER
CLEAR        ;MASTER CLEAR
:***** MACRO EXPANSION ****
JSR         PC, $MSCLR         ;ISSUE A DMR MASTER CLEAR
:*****
ESCAPE      TST                ;IF ERROR, EXIT TEST
:              TRAP          C$ESCAPE
:              .WORD        L10101-.
CALL         $INOUT            ;THIS ROUTINE WILL MANAGE ALL THE DMR
:              ;COMMANDS ISSUED IN THE INTERRUPT ROUTINES
:              ;(FROM BASE IN UNTIL SHUT DOWN). BESIDES
:              ;CONTROLLING THE SOFTWARE TIMEOUT, THIS
:              ;ROUTINE WILL ALSO CHECK THAT BUFFER
:              ;CHARACTER COUNTS AND ADDRESSES ARE CORRECT
:              ;AND THAT THE DATA IS CORRECT IN THOSE BUFFERS
CALL         $ERROR            ;CHECK BASE TABLE FOR SOFT ERRORS
:*****
ENDTST
:              L10101:
:              TRAP          C$FTST
  
```

.SBTTL HARDWARE PARAMETER CODING SECTION

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8271 036742
8272 036742 000015
8273 036744
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8275 036744
8276 036744 001031
8277 036746 036776
8278 036750 160000
8279 036752 177776
8280 036754
8281 036754 002031
8282 036756 037014
8283 036760 000000
8284 036762 000776
8285 036764
8286 036764 010032
8287 036766 037035
8288 036770 000007
8289 036772 000000
8290 036774 000007
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8292 036776
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8294 036776
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8296 036776 051503 020122 042101 P1:
8297 037004 051104 051505 035123
8298 037012 000040
8299 037014 042526 052103 051117 P2:
8300 037022 040440 042104 042522
8301 037030 051523 020072 000
8302 037035 124 051505 020124 P3:
8303 037042 047503 043116 043511
8304 037050 051125 052101 047511
8305 037056 020116 006455 012
8306 037063 040 030040 036440
8307 037070 044440 052116 051105
8308 037076 040516 020114 047050
8309 037104 020117 047503 047116
8310 037112 041505 047524 024522
8311 037120 005015
8312 037122 020040 020061 020075
8313 037130 031510 032462 020064
8314 037136 020055 027126 032463

: THE HARDWARE PARAMETER CODING SECTION CONTAINS MACROS
: THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES. THE
: MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE
: INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES. THE
: MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS
: WITH THE OPERATOR.
:*****

BGNHRD

.WORD L10102-L\$HARD/2
L\$HARD::

GPRMA P1,2,0,160000,177776,YES

.WORD T\$CODE
.WORD P1
.WORD T\$LOLIM
.WORD T\$HILIM

GPRMA P2,4,0,0,776,YES

.WORD T\$CODE
.WORD P2
.WORD T\$LOLIM
.WORD T\$HILIM

GPRMD P3,20,0,7,0,7,YES

.WORD T\$CODE
.WORD P3
.WORD 7
.WORD T\$LOLIM
.WORD T\$HILIM

ENDHRD

.EVEN
L10102:

.ASCIZ /CSR ADDRESS: /

.ASCIZ /VECTOR ADDRESS: /

.ASCII /TEST CONFIGURATION -/<CR><LF>

.ASCII / 0 = INTERNAL (NO CONNECTOR)/<CR><LF>

.ASCII / 1 - H3254 - V.35 (NOTE: MODE 1-4 ALLOWS/<CR><LF>

8315	037144	020040	020040	020040
8316	037152	047050	052117	035105
8317	037160	020040	047515	042504
8318	037166	030440	032055	040440
8319	037174	046114	053517	006523
8320	037202	012		
8321	037203	040	031040	036440
8322	037210	044040	031063	032065
8323	037216	026440	044440	052116
8324	037224	043505	040522	020114
8325	037232	020040	051120	043517
8326	037240	040522	020115	047111
8327	037246	042524	043122	041501
8328	037254	020105	042523	042514
8329	037262	052103	047511	024516
8330	037270	005015	020040	020063
8331	037276	020075	031510	032462
8332	037304	020065	020055	051522
8333	037312	031462	041462	032057
8334	037320	031462	005015	
8335				
8336	037324	020040	020064	020075
8337	037332	031510	032462	020065
8338	037340	020055	051522	031064
8339	037346	006462	012	
8340	037351	040	032440	036440
8341	037356	041440	041101	042514
8342	037364	040440	042116	051440
8343	037372	020127	040520	045503
8344	037400	044440	052116	051105
8345	037406	040506	042503	051440
8346	037414	046105	041505	042524
8347	037422	006504	012	
8348	037425	040	020040	020040
8349	037432	024040	027126	032463
8350	037440	044055	031063	030065
8351	037446	020054	047111	042524
8352	037454	051107	046101	041055
8353	037462	032503	040465	030455
8354	037470	026060		
8355	037472	051040	031123	031063
8356	037500	026503	031510	032462
8357	037506	020054	051522	031064
8358	037514	027463	031064	026462
8359	037522	031510	032462	024461
8360	037530	005015		
8361	037532	020052	042523	042514
8362	037540	052103	052040	042510
8363	037546	043040	046117	047514
8364	037554	044527	043516	047440
8365	037562	046116	020131	043111
8366	037570	052040	042510	046440
8367	037576	042117	046505	051440
8368	037604	050125	047520	052122
8369	037612	020123	047514	050117
8370	037620	040502	045503	025040

.ASCII / 2 = H3254 - INTEGRAL PROGRAM INTERFACE SELECTION)/

.ASCII <CR><LF>/ 3 = H3255 - RS232C/<57>/423/<CR><LF>

.ASCII / 4 = H3255 - RS422/<CR><LF>

.ASCII / 5 = CABLE AND SW PACK INTERFACE SELECTED/<CR><LF>

.ASCII / (V.35-H3250, INTEGRAL-BC55A-10, /

.ASCII / RS232C-H325, RS423/<57>/422-H3251)/<CR><LF>

.ASCII /* SELECT THE FOLLOWING ONLY IF THE MODEM SUPPORTS LOOPBACK */

8371	037626	005015	020040	020066	.ASCII <CR><LF>/ 6 = LOCAL LOOP/<CR><LF>
8372	037634	020075	047514	040503	
8373	037642	020114	047514	050117	
8374	037650	005015			
8375	037652	020040	020067	020075	.ASCIZ / 7 = REMOTE LOOP/<CR><LF>
8376	037660	042522	047515	042524	
8377	037666	046040	047517	006520	
8378	037674	000012			
8379					.EVEN
8380					

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8394 037676 000005
8395 037700
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8397 037700
8398 037700 000032
8399 037702 037712
8400 037704 000007
8401 037706 000001
8402 037710 000005
8403
8404 037712
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8406 037712
8407
8408 037712 042523 042514 052103
8409 037720 041101 042514 050040
8410 037726 047522 051107 046501
8411 037734 046040 047517 020120
8412 037742 044524 042515 047455
8413 037750 052125 053040 051101
8414 037756 040511 046102 006505
8415 037764 012
8416 037765 133 042522 042506
8417 037772 020122 047524 046040
8418 040000 051511 044524 043516
8419 040006 033040 031456 030456
8420 040014 056463 020040 024040
8421 040022 040515 036530 035465
8422 040030 046440 047111 030475
8423 040036 020051 000
8424 040042
8425
8426
8427 040042
8428 040066
8429 040066 000240
8430 040070 000240
8431 040072 000240
8432
8433 040074
8434
8435 040074
8436

.SBTTL SOFTWARE PARAMETER CODING SECTION

: THE SOFTWARE PARAMETER CODING SECTION CONTAINS MACROS
: THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES. THE
: MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE
: INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES. THE
: MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS
: WITH THE OPERATOR.

BGNSFT

.WORD L10103-L\$SOFT/2
L\$SOFT::

GPRMD S1,0,0,7,1,5,YES

.WORD T\$CODE
.WORD S1
.WORD 7
.WORD T\$LOLIM
.WORD T\$HILIM

ENDSFT

.EVEN
L10103:

S1: .ASCII /SELECTABLE PROGRAM LOOP TIME-OUT VARIABLE/<CR><LF>

.ASCIIZ /[REFER TO LISTING 6.3.13] (MAX=5; MIN=1) /

.EVEN

***** PATCH AREA *****

PATCH:
. = +20.
NOP
NOP
NOP

ENDMOD

LASTAD

.EVEN

8437 040074 000000
8438 040076 000000
8439 040100
8440 000001

LSLAST::
.END

.JORD 0
.WORD 0

FMDROP	023670	4769	4780#																
FMG1	016302	3443	3514	3546	3782#	5722													
FMG10	016715	3665	3830#																
FMG11	016753	3707	3836#																
FMG12	017005	3680	3841#																
FMG13	017036	3688	3715	3846#															
FMG14	017112	3723	3854#																
FMG15	017166	3695	3862#																
FMG16	017215	3738	3866#																
FMG17	017274	3752	3874#																
FMG18	017347	3768	3882#																
FMG19	017436	3584	3892#	5241															
FMG2	016334	3451	3522	3787#															
FMG21	017461	3458	3896#																
FMG22	017527	3465	3903#																
FMG23	017534	3481	3907#																
FMG24	017635	3489	3916#																
FMG3	016366	3435	3505	3537	3601	3792#													
FMG4	016440	3556	3799#																
FMG5	016471	3566	3574	3804#															
FMG7	016522	3617	3809#	6502															
FMG8	016573	3633	3816#																
FMG9	016644	3649	3823#																
FMS1	010620	2219	2255#																
FMS2	010653	2237	2260#																
FMS3	011746	2614	2645#																
FMT0	024147	4864	4878#																
FMT1	025204	4932	5152#																
FMT11	027662	5730	5806#																
FMT12	027713	5740	5811#																
FMT13	027750	5751	5816#																
FMT14	030005	5762	5821#																
FMT15	030042	5773	5826#																
FMT16	030077	5784	5831#																
FMT19	031252	6237	6253#																
FMT2	025253	4947	5159#																
FMT25	036276	7648	7888	7912#															
FMT3	025330	5057	5081	5167#															
FMT4	025414	5130	5176#																
FMT5	026526	5429	5502#																
FMT6	026557	5440	5507#																
FMT7	026610	5452	5512#																
FMT8	026641	5463	5517#																
FMT9	026672	5474	5522#																
FRSPAS	002266	1712#	4034*	4068*															
FRSTIM	002264	1711#	4031	4033*															
F\$AU =	000015	1231#																	
F\$AUTO=	000020	1231#	4273	4305															
F\$BGN =	000040	1231#	1234	3432	3500	3532	3596	3678	3705	3734	3748	3762	3998	4018					
		4273	4323	4345	4607	4746	4763	4804	4844	4849	4920	4922	4962	4967					
		5094	5098	5146	5150	5229	5236	5255	5271	5296	5298	5306	5318	5354					
		5358	5366	5379	5415	5419	5423	5544	5546	5554	5566	5577	5584	5613					
		5620	5632	5636	5644	5656	5667	5684	5691	5711	5714	5718	5860	5862					
		5870	5881	5890	5916	5927	5931	5948	5970	5974	5982	5993	6002	6009					
		6020	6024	6033	6047	6053	6061	6067	6106	6109	6117	6128	6140	6150					
		6167	6174	6178	6190	6199	6221	6225	6232	6280	6282	6290	6301	6310					

GSEXCP=	000400	1231#																		
GSHILI=	000002	1231#																		
GSLOLI=	000001	1231#																		
GSNO =	000000	1231#																		
GSOFFS=	000400	1231#	8276	8281	8286	8398														
GSOFSI=	000376	1231#	8276	8281	8286	8398														
GSPRMA=	000001	1231#	8276	8281																
GSPRMD=	000002	1231#	8286	8398																
GSPRML=	000000	1231#																		
GSRADA=	000140	1231#																		
GSRADB=	000000	1231#																		
GSRADD=	000040	1231#																		
GSRADL=	000120	1231#																		
GSRADO=	000020	1231#	8276	8281	8286	8398														
G\$XFER=	000004	1231#																		
G\$YES =	000010	1231#	8276	8281	8286	8398														
HALTC =	001000	1585#	2932	4621	7063	7116	7167	7242	7306											
HDX =	002000	1583#	5944																	
HELP =	000000	1224#	1251	1351	1392	1904														
HICRC	002404	1797#	4979*	5020*	5026	5027*	5028*	5064*	5065	5079										
HIWORD	002410	1799#	4992*	4996*	5021*	5065	5078													
HLT =	000002	1610#	2900	4371	4516	4565	4585													
HOE =	100000	G 1527#																		
IBE =	010000	G 1524#																		
IDU =	000040	G 1517#																		
IECLR =	000020	1566#	3382	4502	4508															
IEO =	000100	1573#	3383	4429	4644	4733														
IER =	020000	G 1525#																		
ICSET =	000100	1563#	3300	3382	4541	4546	4550	4565	4570	4585	4590	4646								
INFACE	002262	1709#	2517	4198*	4201*	4539														
INFLAG	002354	1772#	3287*	3309	4520	4593*	4634													
INISR	022064	G 4139	4345#																	
INRCV	002326	1756#	3283*	3470*	3474*	3480	4450*	4553	4557											
INTER =	000015	1625#	2525	4369	4533	4541														
INXMIT	002330	1757#	3284*	3471*	3475*	3479	4458*	4573	4577											
ISP13 =	000076	1649#	5586	5622	5729															
ISP7 =	000072	1648#	5324	5385	5918	6011														
ISR =	000100	G 1518#																		
IXE =	004000	G 1523#																		
I\$AU =	000041	1231#																		
I\$AUTO=	000041	1231#	4273#	4307#																
I\$CLN =	000041	1231#	4323#	4333#																
I\$DU =	000041	1231#	4763#	4778#																
I\$HRD =	000041	8272#	8295#																	
I\$INIT=	000041	1231#	4018#	4235#																
I\$MOD =	000041	1231#	1234#	8434#																
I\$MSG =	000041	1231#	3432#	3497#	3500#	3530#	3532#	3593#	3596#	3674#	3678#	3703#	3705#	3731#						
			3734#	3746#	3748#	3760#	3762#	3776#	5423#	5483#	5718#	5793#	6232#	6245#	6496#					
			6510#																	
I\$PROT=	000040	1231#	3998#																	
I\$PTAB=	000041	1231#																		
I\$PWR =	000041	1231#																		
I\$RPT =	000041	1231#																		
I\$SEG =	000041	1231#	4804	4920	4922	4967	5098	5229	5296	5298	5358	5544	5546	5636						
			5860	5862	5931	5974	6024	6106	6178	6280	6282	6390	6528	6530						
			6599	6679	6779	6914	7017	7019	7078	7131	7182	7257	7334	7550	7946					

L\$EF	002052	G	1311#			
L\$ENVI	002044	G	1304#			
L\$ETP	002102	G	1334#			
L\$EXP1	002046	G	1306#			
L\$EXP4	002064	G	1320#			
L\$EXP5	002066	G	1322#			
L\$SHARD	036744	G	1283	8272	8273#	
L\$HIME	002120	G	1348#	3089	7575	7838
L\$HPCP	002016	G	1282#			
L\$HPTP	002022	G	1286#			
L\$HW	002174	G	1287	1407	1408#	
L\$ICP	002104	G	1336#			
L\$INIT	020452	G	1337	4018#		
L\$LADP	002026	G	1290#			
L\$LAST	040100	G	1291	8439#		
L\$LOAD	002100	G	1332#			
L\$LUN	002074	G	1328#			
L\$MREV	002050	G	1308#			
L\$NAME	002000	G	1265#			
L\$PRIO	002042	G	1302#			
L\$PROT	020444	G	1343	3998#		
L\$PRT	002112	G	1342#			
L\$REPP	002062	G	1318#			
L\$REV	002010	G	1274#			
L\$SOFT	037700	G	1285	8394	8395#	
L\$SPC	002056	G	1314#			
L\$SPCP	002020	G	1284#			
L\$SPTP	002024	G	1288#			
L\$STA	002030	G	1292#			
L\$SW	002224	G	1289	1439	1440#	
L\$TEST	002114	G	1344#	2849	2851	3384
L\$TIML	002014	G	1280#			
L\$UNIT	002012	G	1278#	4072		
L10000	002222		1407	1424#		
L10001	002226		1439	1446#		
L10002	015122		3495#			
L10003	015236		3528#			
L10004	015466		3591#			
L10005	015762		3672#			
L10006	016060		3701#			
L10007	016162		3729#			
L10010	016214		3744#			
L10011	016246		3758#			
L10012	016300		3774#			
L10014	021510		4233#			
L10015	022044		4305#			
L10016	022062		4331#			
L10017	023140		4600#			
L10020	023626		4739#			
L10021	023636		4751#			
L10022	023666		4776#			
L10023	024040		4844#			
L10024	024116		4871#			
L10025	025202		5150#			
L10026	024404		4962#			
L10027	025026		5094#			

RMODEM=	000017	1626#	5893	5950	6143	6192								
ROMADR	002412	1800#	2967	2976	4929*	4957*	4974*	4993*	4999*	5102*	5142*			
ROMI =	001000	1559#	2968	2974	2977	2983								
ROMLOC	025531	5129	5192#											
ROMO =	002000	1558#	2984											
RQI =	000040	1565#	2298	2483	2525	2581	2705	2900	3300	4503	4541	4546	4550	4565
		4570	4585	4590	4646	5893	5950	6143	6192	6334	6440	6636	6644	6712
		6727	6845	6946	6957	7145	7206	7220	7284	7386	7398	7706	7714	
RRAM =	000014	1624#												
RSEL4	035362	7698*	7707#	7779	7848*									
RSEL6	035364	7699*	7708#	7788	7849*									
RUN =	100000	1553#	2415	3551										
SAVE	002342	1765#	2702*	2720	2744*									
SECN =	004000	1581#												
SELO =	002232	1684#	2187	2298*	2311	2398*	2401*	2415	2495*	2522	2705*	2900*	2966*	2968*
		2973*	2974*	2977*	2981*	2983*	2984*	3382*	3442	3513	3545	3551	4327*	4348
		4350	4439*	4442*	4502*	4503*	4508*	4641*	4646*	4823	5721	5893*	5950*	6143*
		6192*												
SEL2	002234	1681#	1687	2185	2224	2621	2639*	2901*	2923	2940*	2967*	3383*	3441	3512
		3544	4097*	4098*	4429*	4609	4619	4644*	4692	4733*	4736*	4825	5720	6349*
		6352	6370*	6379*	6453	6575	6592*	6654	6672*	6737	6754*	6879	6970	7054
		7071*	7107	7124*	7158	7175*	7233	7250*	7297	7314*	7415	7424	7516*	7730
		7739	7825*											
SEL4	002236	1682#	1689	2497*	2722*	2794*	3450	3521	3687	3714	4101*	4102*	4418*	4451*
		4459*	4468	4469*	4476*	4482*	4486*	4671	4673*	4677*	4682*	4687*	4690*	4695
		4715	4827	5899	5956	7439	7470	7751	7779					
SEL6	002240	1683#	1691	2499*	2510	2593*	2595	2630	2725*	2730*	2732*	2796*	2932	2969*
		2978*	3449	3520	3686	3713	4105*	4106*	4396*	4397*	4400*	4401*	4409*	4412*
		4422*	4425*	4432*	4452*	4460*	4465*	4621	4623	4659	4670*	4705	4725	4829
		6361	6462	6584	6663	6746	6888	6979	7063	7116	7167	7242	7306	7448
		7479	7760	7788										
SFLAG	002346	1767#	7695*	7796	7834	7937*								
SFPTBL	002224 G	1441#												
SKIP	002350	1768#												
SPEED	002224	1443#	4156											
STARES	002270	1713#	4064*	4069*	4217	4924								
STARST	020606	4045	4050	4063#										
START	002272	1717#	3289*	4427	4527	4549*								
STLU =	010000	1556#												
STREC =	000200	1591#												
STUP -	000400	1560#	2973	2974	2981	2983								
SUBRPC	002374	1788#	2172*	2173*	2245*	2301*	2302*	2337*	2384*	2385*	2440*	2480*	2481*	2541*
		2579*	2580*	2642*	2699*	2700*	2742*	2779*	2780*	2805*	2834*	2835*	2870*	2898*
		2899*	2942*	3016*	3017*	3037*	3074*	3075*	3256*	3280*	3281*	3414*	3434	3501
		3504	3533	3536	3597	3600	4024*							
SVCGBL -	000000	1231#	1241#	1265	1274	1276	1278	1280	1282	1284	1286	1288	1290	1292
		1294	1296	1298	1300	1302	1304	1306	1308	1311	1314	1316	1318	1320
		1322	1324	1326	1328	1330	1332	1334	1336	1338	1340	1342	1344	1346
		1348	1371	1408	1409	1440	1441	1883	1891	3432	3500	3532	3596	3678
		3705	3734	3748	3762	3998	4018	4273	4323	4345	4607	4746	4763	4849
		5423	5718	6232	6496	8273	8395	8439#	8440					
SVCINS=	000001	1231#	1238#	1266	1267	1268	1269	1270	1271	1272	1273	1275	1277	1279
		1281	1283	1285	1287	1289	1291	1293	1295	1297	1299	1301	1303	1305
		1307	1309	1310	1312	1313	1315	1317	1319	1321	1323	1325	1327	1329
		1331	1333	1335	1337	1339	1341	1343	1345	1347	1349	1370	1372	1373
		1374	1375	1376	1377	1378	1379	1380	1381	1382	1383	1384	1385	1386

1387	1388	1389	1390	1407	1430	1884	1885	1892	1896	2190	2195	2196
2197	2198	2199	2200	2201	2202	2207	2208	2209	2210	2219	2220	2221
2222	2223	2227	2228	2229	2230	2237	2238	2239	2240	2241	2314	2318
2319	2320	2321	2322	2323	2324	2325	2329	2330	2331	2332	2418	2422
2423	2424	2425	2426	2427	2428	2429	2433	2434	2435	2436	2491	2507
2532	2589	2612	2614	2615	2616	2617	2618	2624	2625	2626	2627	2633
2634	2635	2636	2716	2790	2863	2864	2865	2866	2909	2915	2922	2926
2927	2928	2929	2935	2936	2937	2938	3078	3079	3080	3081	3082	3083
3094	3095	3161	3162	3164	3165	3296	3297	3305	3315	3316	3317	3318
3319	3320	3321	3322	3328	3329	3330	3331	3354	3355	3356	3357	3373
3374	3375	3376	3411	3412	3434	3435	3436	3437	3438	3439	3441	3442
3443	3444	3445	3446	3447	3449	3450	3451	3452	3453	3454	3455	3457
3458	3459	3460	3461	3462	3464	3465	3466	3467	3468	3469	3479	3480
3481	3482	3483	3484	3485	3487	3488	3489	3490	3491	3492	3493	3496
3504	3505	3506	3507	3508	3509	3512	3513	3514	3515	3516	3517	3518
3520	3521	3522	3523	3524	3525	3526	3529	3536	3537	3538	3539	3540
3541	3544	3545	3546	3547	3548	3549	3550	3556	3557	3558	3559	3560
3566	3567	3568	3569	3570	3574	3575	3576	3577	3578	3584	3585	3586
3587	3588	3592	3600	3601	3602	3603	3604	3605	3613	3614	3615	3616
3617	3618	3619	3620	3621	3629	3630	3631	3632	3633	3634	3635	3636
3637	3645	3646	3647	3648	3649	3650	3651	3652	3653	3661	3662	3663
3664	3665	3666	3667	3668	3669	3673	3680	3681	3682	3683	3684	3686
3687	3688	3689	3690	3691	3692	3694	3695	3696	3697	3698	3699	3702
3707	3708	3709	3710	3711	3713	3714	3715	3716	3717	3718	3719	3721
3722	3723	3724	3725	3726	3727	3730	3736	3737	3738	3739	3740	3741
3742	3745	3750	3751	3752	3753	3754	3755	3756	3759	3766	3767	3768
3769	3770	3771	3772	3775	4021	4022	4038	4039	4042	4043	4045	4047
4048	4050	4052	4053	4055	4057	4058	4060	4075	4076	4077	4079	4086
4087	4088	4089	4090	4091	4092	4114	4115	4116	4117	4118	4119	4120
4138	4139	4140	4141	4142	4143	4145	4146	4147	4148	4149	4150	4220
4221	4222	4223	4224	4234	4276	4277	4278	4279	4280	4281	4293	4294
4298	4299	4301	4306	4332	4374	4375	4376	4377	4601	4613	4614	4615
4616	4628	4629	4630	4631	4651	4652	4653	4654	4664	4665	4666	4667
4699	4700	4701	4702	4708	4709	4710	4711	4718	4719	4720	4721	4740
4752	4766	4768	4769	4770	4771	4772	4773	4777	4807	4808	4809	4810
4811	4812	4833	4834	4836	4840	4841	4845	4855	4856	4857	4858	4862
4863	4864	4865	4866	4867	4868	4872	4923	4931	4932	4933	4934	4935
4936	4943	4944	4945	4946	4947	4948	4949	4950	4951	4963	4968	5048
5049	5050	5051	5054	5055	5056	5057	5058	5059	5060	5061	5072	5073
5074	5075	5078	5079	5080	5081	5082	5083	5084	5085	5095	5099	5120
5121	5122	5123	5126	5127	5128	5129	5130	5131	5132	5133	5134	5147
5151	5236	5237	5241	5242	5243	5244	5245	5255	5256	5265	5266	5267
5268	5272	5299	5306	5307	5318	5319	5329	5330	5331	5332	5348	5349
5350	5351	5355	5359	5366	5367	5379	5380	5390	5391	5392	5393	5409
5410	5411	5412	5416	5420	5427	5428	5429	5430	543	5432	5433	5438
5439	5440	5441	5442	5443	5444	5450	5451	5452	5453	5454	5455	5456
5461	5462	5463	5464	5465	5466	5467	5472	5473	5474	5475	5476	5477
5478	5482	5547	5554	5555	5566	5567	5577	5578	5584	5585	5590	5591
5592	5593	5613	5614	5620	5621	5626	5627	5628	5629	5633	5637	5644
5645	5656	5657	5667	5668	5684	5685	5691	5692	5705	5706	5707	5708
5712	5715	5720	5721	5722	5723	5724	5725	5726	5728	5729	5730	5731
5732	5733	5734	5738	5739	5740	5741	5742	5743	5744	5749	5750	5751
5752	5753	5754	5755	5760	5761	5762	5763	5764	5765	5766	5771	5772
5773	5774	5775	5776	5777	5782	5783	5784	5785	5786	5787	5788	5792
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	5355	5416	5420	5482	5633	5712	5715	5792	5928	5971	6021	6062	6068	
	6175	6222	6226	6244	6387	6489	6492	6509	6596	6676	6758	6762	6904	
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T\$SAVL- 177777	1231#													
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	4022	4039	4043	4048	4053	4058	4076	4091	4119	4142	4149	4223	4234
	4280	4294	4299	4301	4306	4332	4374	4613	4628	4651	4664	4699	4708
	4718	4766	4772	4777	4811	4834	4836	4841	4845	4855	4867	4923	4935
	4950	4963	4968	5048	5060	5072	5084	5095	5099	5120	5133	5147	5151
	5236	5244	5255	5265	5272	5299	5306	5318	5329	5348	5355	5359	5366
	5379	5390	5409	5416	5420	5432	5443	5455	5466	5477	5482	5547	5554
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	6128	6140	6150	6157	6167	6175	6179	6190	6199	6206	6222	6226	6240
	6244	6283	6290	6301	6310	6327	6345	6355	6364	6377	6387	6391	6398
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	7179	7183	7190	7201	7212	7231	7236	7245	7254	7258	7265	7276	7295
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	8083	8124	8140	8182	8202	8242	8258						
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T\$\$HAR= 010102	8272#	8294											
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T\$\$INI= 010014	4018#	4233											
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	7891#	7904#	7963#	7977#	7984#	8002#	8068#	8083#	8124#	8140#	8182#	8202#	8242#	8258#	
MSWORD	1#	1231#	1304#	1313	1370#	1372	1373	1374	1375	1376	1377	1378	1379	1380	1381
	1382	1383	1384	1385	1386	1387	1388	1389	1390	2207#	2208	2209	2210	2227#	2228
	2229	2230	2329#	2330	2331	2332	2433#	2434	2435	2436	2624#	2625	2626	2627	2633#
	2634	2635	2636	2863#	2864	2865	2866	2926#	2927	2928	2929	2935#	2936	2937	2938
	3328#	3329	3330	3331	3354#	3355	3356	3357	3373#	3374	3375	3376	4374#	4375	4376
	4377	4613#	4614	4615	4616	4628#	4629	4630	4631	4651#	4652	4653	4654	4664#	4665
	4666	4667	4699#	4700	4701	4702	4708#	4709	4710	4711	4718#	4719	4720	4721	4855#
	4856	4857	4858	5048#	5049	5050	5051	5072#	5073	5074	5075	5120#	5121	5122	5123
	5265#	5266	5267	5268	5329#	5330	5331	5332	5348#	5349	5350	5351	5390#	5391	5392
	5393	5409#	5410	5411	5412	5590#	5591	5592	5593	5626#	5627	5628	5629	5705#	5706
	5707	5708	5902#	5903	5904	5905	5921#	5922	5923	5924	5959#	5960	5961	5962	6014#
	6015	6016	6017	6157#	6158	6159	6160	6206#	6207	6208	6209	6355#	6356	6357	6358
	6364#	6365	6366	6367	6456#	6457	6458	6459	6465#	6466	6467	6468	6481#	6482	6483
	6484	6578#	6579	6580	6581	6587#	6588	6589	6590	6657#	6658	6659	6660	6666#	6667
	6668	6669	6740#	6741	6742	6743	6749#	6750	6751	6752	6867#	6868	6869	6870	6882#
	6883	6884	6885	6891#	6892	6893	6894	6973#	6974	6975	6976	6983#	6984	6985	6986
	7057#	7058	7059	7060	7066#	7067	7068	7069	7110#	7111	7112	7113	7119#	7120	7121
	7122	7161#	7162	7163	7164	7170#	7171	7172	7173	7236#	7237	7238	7239	7245#	7246
	7247	7248	7300#	7301	7302	7303	7309#	7310	7311	7312	7418#	7419	7420	7421	7432#
	7433	7434	7435	7442#	7443	7444	7445	7451#	7452	7453	7454	7463#	7464	7465	7466
	7473#	7474	7475	7476	7482#	7483	7484	7485	7500#	7501	7502	7503	7507#	7508	7509
	7510	7642#	7643	7644	7645	7733#	7734	7735	7736	7744#	7745	7746	7747	7754#	7755
	7756	7757	7763#	7764	7765	7766	7772#	7773	7774	7775	7782#	7783	7784	7785	7791#
	7792	7793	7794	7818#	7819	7820	7821	7882#	7883	7884	7885	8276#	8281#	8286#	8398#
	8437	8438													
MSXFER	1#	1231#													
OPEN	1#	1231#													
POINTE	1#	1231#	1249												
PRINTB	1#	1231#	2218	2236	2613	3433	3440	3448	3456	3463	3478	3486	3503	3511	3519
	3535	3543	3555	3565	3573	3583	3599	3612	3628	3644	3660	3679	3685	3693	3706
	3712	3720	3735	3749	3765	4085	4113	4219	4930	4942	5053	5077	5125	5240	5426
	5437	5449	5460	5471	5719	5727	5737	5748	5759	5770	5781	6233	6497	7646	7886
PRINTF	1#	1231#	4767												
PRINTS	1#	1231#													
PRINTX	1#	1231#	4861												
READBU	1#	1231#													
READEF	1#	1231#	4041	4046	4051	4056									
RFLAGS	1#	1231#													
SETPRI	1#	1231#	3093	3295	3410	4020	7579								
SETVEC	1#	1231#	3077	4137	4144	4275	4806	7553	7606	7858					

SHUTDN	2071#	3405	5320	5381	5579	5615	5686	5911	5964	6004	6215	6380	6471	6551	6896
	6990	7525	7894	7979											
SLASH	1#	1231#													
STARS	1#	1231#													
SVC	1#	1229#	1230												
WAIT	1947#	2485	2502	2526	2535	2583	2599	2606	2709	2735	2784	2799	2903	2910	2916
	3398	5894	5907	5951	6144	6162	6193	6211	6339	6371	6445	6570	6649	6732	6853
	6962	7046	7099	7150	7225	7289	7408	7721							
XFER	1#	1231#													
XFERF	1#	1231#													
XFERT	1#	1231#													

. ABS. 040100 000

ERRORS DETECTED: 0

CZDMID/I,CZDMID.SEQ/DOC/CRF/NL: OC/SOL=SVC34R.MLB.CZDMID.P11
 RUN-TIME: 34 42 4 SECONDS
 RUN-TIME RATIO: 140/81=1.7
 CORE USED: 19K (37 PAGES)

DOCUMENT PAGES: 208