

DLV-11F

DLV-11F OFFLINE TST
CVDVCCO

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The main body of the document consists of a grid of 150 small, illegible data tables or charts, arranged in 10 columns and 15 rows. Each cell in the grid contains a small, dense block of text or data, which is too small to read. The overall appearance is that of a technical manual or a data reference sheet.



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IDENTIFICATION

PRODUCT CODE: AC-E006C-MC
PRODUCT NAME: CVOVCCO DLV11-F OFFLINE TEST
PRODUCT DATE: AUGUST 1984
AUTHOR: ODES CHOATE
MAINTAINER: DIAGNOSTIC ENGINEERING GROUP

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 79 CONTENTS OF 'USER'. THE DIAGNOSTIC IS DESIGNED TO TEST AND
 80 DETECT FAULTS TO THE LOGIC LEVEL (NOT TO THE CHIP LEVEL).
 81 THIS TEST OPERATES ON UP TO SIXTEEN(16) IDENTICALLY CONFIGURED
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 85 175610 -FIRST SERIAL LINE ADDRESS OF 15 CONSECUTIVE
 86 SERIAL LINE DEVICES.
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 89 60 - VECTOR FOR CONSOLE DEVICE INTERFACE.
 90 300 - VECTOR FOR FIRST OF 15 DEVICES.
 91
 92 THIS PROGRAM IS DESIGNED TO RUN ON ANY PDP-11 WITH 4K OF
 93 MEMORY AND A DLV11-F (LSI-BUS) MODULE. IT CAN RUN UNDER XXDP,
 94 APT, AND ACT MONITORS, AND ON PROCESSORS WITH NO HARDWARE
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 119 DIAGNOSTIC ENGINEERING STANDARDS AND CONVENTIONS 175-003-009-02
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 121 ACT AUTOCAT-11-QZAUB
 122 SYSMAC MD-11-DZQAC
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 124
 125 1.4 DIAGNOSTIC HIERARCHY PREREQUISITES.

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NO SPECIAL DIAGNOSTICS ARE REQUIRED TO RUN BEFORE THIS, BUT THE PROCESSOR, MEMORY, AND BUS ARE ASSUMED TO BE FULLY OPERATIONAL.

1.5 ASSUMPTIONS.

THIS DIAGNOSTIC ASSUMES THAT THE OPERATOR HAS INITIALIZED LOCATION 'IUSMR' AND 'IDEVH' TO THE PROPER VALUES.

2.0 OPERATING INSTRUCTIONS.

2.1 LOADING AND STARTING PROCEDURES.

USE STANDARD PROCEDURE FOR PDP-11 ABSOLUTE BINARY FORMATTED MEDIA.

THIS DIAGNOSTIC HAS ONLY ONE (1) STARTING ADDRESS. 200 FOR START AND RESTART.

THE USER CAN SELECT A SPECIFIC TEST TO BE EXECUTED BY SETTING SWITCH 8 IN THE SWITCH REGISTER AND THE TEST NUMBER (IN OCTAL) IN THE LOWER BYTE. (NOTE: ALL TESTS PREVIOUS TO THE SELECTED ONE ARE EXECUTED WITHOUT ITERATIONS.)

2.2 SPECIAL ENVIRONMENTS.

THIS DIAGNOSTIC FOLLOWS THE STANDARD PROCEDURE FOR RUNNING UDER APT,ACT,XXDP MONITORS, AS DESCRIBED IN THEIR RESPECTIVE PROCEDURES MANUAL AND SYSMAC PACKAGE.

2.3 OPERATIONAL SWITCH SETTINGS

IF THE DIAGNOSTIC IS RUN ON A CPU WITHOUT A SWITCH REGISTER THEN A SOFTWARE SWITCH REGISTER IS USED WHICH ALLOWS THE USER THE SAME SWITCH OPTIONS AS THE HARDWARE SWITCH REGISTER. IF THE HARDWARE SWITCH REGISTER DOES NOT EXIST OR IF ONE DOES AND IT CONTAINS ALL ONES (177777) THEN THE SOFTWARE SWITCH REGISTER (LOC. 176) IS USED.

CONTROL:

THIS PROGRAM ALSO SUPPORTS THE DYNAMIC LOADING OF THE SOFTWARE SWITCH REGISTER (LOC. 176) FROM THE TTY. THIS CAN BE ACCOMPLISHED BY DOING THE FOLLOWING:

- 1) TYPE CONTROL G <↑G>; THIS WILL ALLOW THE TTY TO ENTER DATA INTO LOC. 176 AT SELECTED POINTS WITHIN THE

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PROGRAM.

- 2) THE MACHINE WILL THEN TYPE: ' SWR=XXXXXX NEW=' (XXXXXX IS THE OCTAL CONTENTS OF THE SOFTWARE SWITCH REGISTER.)
- 3) AFTER THE 'NEW=' HAS BEEN TYPED THEN THE OPERATOR CAN DO ONE OF THE FOLLOWING AT THE TTY:
 - A) TYPE A NUMBER TO BE LOADED INTO LOC. 176 FOLLOWED BY A <CR>. (ONLY NUMBERS BETWEEN 0-7 WILL BE ACCEPTED). LEADING ZEROS NEED NOT BE TYPED, AND IF MORE THAN 6 DIGITS ARE TYPED THE LAST 6 WILL BE USED. IF A <CR> IS THE FIRST KEY DEPRESSED THE SOFTWARE SWITCH REGISTER CONTENTS WILL NOT BE CHANGED.
 - B) IF A CONTROL U <+U> IS DEPRESSED THEN THE PROGRAM WILL SEND YOU BACK TO STEP 3.
 - C) IF THE INPUT CHARACTER IS NOT ONE OF THE CHARACTERS MENTIONED ABOVE THEN A QUESTION MARK (?) WILL BE TYPED FOLLOWED BY A CARRAGE RETURN AND A LINE FEED SEQUENCE THEN PROCEED FROM STEP 3 (ERASING ALL PREVIOUS INPUT).

DYNAMIC SWITCH REGISTER

- BIT 15 - HALT ON ERROR
 14 - LOOP ON TEST
 13 - INHIBIT ERROR TIMEOUTS
 12 - (UNUSED)
 11 - INHIBIT ITERATIONS
 10 - BELL ON ERROR
 9 - LOOP ON ERROR
 8 - LOOP ON TEST IN SWR<7:0>
 7:0 - TEST NUMBER TO LOOP ON (USED WITH BIT 8)

2.4 PROGRAM OPTIONS.

THIS PROGRAM WILL SUPPORT TESTING OF MULTIPLE DLV11-F'S. IT REQUIRES THE ADDRESS OF THE FIRST RCSR (STORED AT '#BASE'), AND ITS INTERRUPT VECTOR (STORED AT '#VECT1'); AND WILL BE ABLE TO ADDRESS ANY DLV11-F STARTING AT THE SPECIFIED BASE ADDRESS UP TO 16 CONSECUTIVE DEVICES.

EXAMPLES: #BASE: 175610
 #VECT1: 300

THE PROGRAM WILL BE ABLE TO TEST ANY DLV11-F WITHIN THE ADDRESS RANGE 175610 --> 176000

#BASE AND #VECT1 DEFAULT TO 175610 AND 300 RESPECTIVELY.

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THE PROGRAM ASSOCIATES UNIT NUMBERS AS FOLLOWS: (NUMBERS IN PARENTHESIS ARE OCTAL)

UNIT#0 -- BASE ADDRESS STORED AT '#BASE'
ASSOCIATED BASE VECTOR STORED AT '#VECT1'
UNIT#1 -- BASE ADDRESS * (10)
BASE VECTOR * (10)

⋮
UP TO

UNIT#14 -- BASE ADDRESS * (160)
BASE VECTOR * (160)

LOCATION '#DEVH' IS USED AS A BIT MAP TO INDICATE WHICH UNIT NUMBERS ARE PRESENT AND WILL BE TESTED.

BIT 15	BIT 14	-	-	-	BIT 1	BIT 0
! CON-	! UNIT !				! UNIT !	! UNIT !
! SOLE !	! 14 !				! 01 !	! 00 !

A BIT MAP CAN BE ENTERED AT '#DEVH' PRIOR TO STARTING THE PROGRAM.

EXAMPLE:

#BASE: 175610
#VECTOR: 300
#DEVH: 100013

THE PROGRAM WILL TEST-

UNIT#0	175610	300
UNIT#1	175620	310
UNIT#3	175640	330
CONSOLE	177560	60

OPTIONS

LOCATION #USMR CONTAINS ALL THE USER SELECTABLE OPTIONS. THE VALUES IN THIS WORD MUST CONFORM TO THE ACTUAL BOARD CONFIGURATION.

THE DEFAULT VALUE OF #USMR IS AS FOLLOWS:

BIT POSITION	DEFINITION	DEFAULT VALUE
0-3	# OF DATA BITS	10(8) = 8
4	PARITY ENABLED -(SEE	0 = NO
5	EVEN ODD PARITY-/ NOTE)	0 = ODD
6	COMMON SPEED	1 = YES
7	PROGRAMMABLE BAUD RATE	0 = NO
8-11	BAUD RATE OFFSET (SEE FOLLOWING NOTE)	02(8) = 110 BAUD
12	BREAK GENERATION ENABLED	1 = YES
13	WRAP CONNECTOR INSTALLED	0 = NO
14	MAINT JUMPER (SEE NOTE)	0 = NO
15	ERROR BITS ENABLED	0 = NO

NOTE ON BITS <4;5>
THIS DIAGNOSTIC DOES NOT TEST THE PARITY LOGIC.

NOTE ON BITS <7:11>
WHEN THE PROGRAMMABLE BAUD RATE OPTION IS ENABLED THE PROGRAMMABLE BAUD RATE TEST WILL EXIT WITH THE BAUD RATE SET TO THE SELECTED VALUE. TO CHANGE THE DEFAULT VALUE OF 110 BAUD REPLACE BITS <11:8> WITH THE OFFSET INDICATED IN THE TABLE AT THE END OF THE PBR TEST.(TEST #16)

NOTE ON BIT 14
THIS SWITCH WHEN ON WILL ALLOW THE DIAGNOSTIC TO TEST IN MAINTAINCE MODE. IT IS ASSUMED THAT THE MAINTAINCE JUMPER IS INSTALLED ON ALL OF THE DLV11-F MODULES WHEN THIS BIT IS SET.

DLV11-F INDIVIDUAL TEST REQUIREMENTS TABLE

TEST #	1	2	3	4	5	6	7	10	11	12	13	14	15	16	17	20	21	22	23	24
CONSOLE DEVICE
APT ENVIRONMENT
(MAINT) BIT SET
(WRAP CON) BIT SET
(ERROR BITS) BIT SET
(COM SPD) BIT SET
(BREAK) BIT SET
(PROG BAUD RATE) BIT SET

.. TEST WILL NOT RUN IF THIS CONDITION IS TRUE.
-- TEST WILL NOT RUN IF THIS CONDITION IS FALSE.
** TEST WILL NOT RUN IF ALL OF THE CONDITIONS IN THIS COLUMN ARE FALSE.

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2.5 EXECUTION TIMES.

EXECUTION TIMES ARE FOR AN LSI-11 PROCESSOR WITH ALL OPTIONS ENABLED ON THE DLV11-F (EXCEPT FOR PROGRAMMABLE BAUD RATE), AT 110 BAUD, AND NOT AT THE CONSOLE ADDRESS.

FIRST PASS- 90 SECONDS
 ADDITIONAL PASSES 95 SECONDS
 ADDITIONAL DEVICES 95 SECONDS

THE TEST TIME IS BAUD RATE DEPENDANT; HIGHER BAUD GIVES SHORTER PASS TIMES.

IF THE DIAGNOSTIC IS RUN AT THE CONSOLE ADDRESS THE RUNNING TIME IS 5 SECONDS PER PASS.

3.0 ERROR INFORMATION.

3.1 ERROR REPORTING PROCEDURE.

SINCE THIS DIAGNOSTIC WAS DESIGNED TO FIT IN 4-K OF MEMORY THE ERROR TYPEOUT IS VERY BRIEF. THE FORMAT OF THE ERROR TYPEOUT IS AS FOLLOWS:

TEST@_____,ERROR@_____,PC=_____,ADDRESS=_____,VECTOR=_____

WHERE ALL VALUES TYPED ARE OCTAL.
 THE ADDRESS AND VECTOR REFER TO THE FAILING SLU'S.
 FOR FURTHER INFORMATION THE LISTING MUST BE CONSULTED.
 BITS 15,13,10 AND 9 OF THE SWITCH REGISTER CONTROL THE SEQUENCE OF EVENTS AFTER AN ERROR IS CAUGHT.

BIT 15 - CAUSES THE PROGRAM TO HALT IN THE ERROR ROUTINE. CONTINUEING THE PROGRAM CAUSES IT TO PROCEED.

BIT 13 - DISABLES THE PRINTING OF THE ERROR MESSAGE.

BIT 10 - CAUSES THE BELL TO RING ON ERROR.

BIT 9 - CAUSES THE DIAGNOSTIC TO LOOP FROM BEGINNING OF TEST TO ERROR.

THE ERROR ROUTINE SUPPORTS THE CONTROL G FUNCTION.

3.2 ERROR HALTS.

THE ONLY HALT IN THIS DIAGNOSTIC IS IN THE ERROR ROUTINE, AND IS EXECUTED ONLY IF BIT 15 OF THE SWITCH REGISTER IS A ONE WHEN AN ERROR OCCURS.

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4 0 PERFORMANCE AND PROGRESS REPORTS.

4 1 PERFORMANCE REPORTS.

AS EACH DEVICE COMPLETES ONE PASS OF THE DIAGNOSTIC THE FOLLOWING WILL BE TYPED:

CSR:-----,VECTOR:-----,ERRORS:-----

WHERE: 'CSR:-----' IS THE DEVICE CSR UNDER TEST
 'VECTOR:-----' IS THE ASSOCIATED VECTOR
 AND 'ERRORS:-----' IS THE TOTAL NUMBER OF ERRORS ON THIS DEVICE ON THIS PASS.

NOTE

THIS IS TYPED AFTER THE DEVICE HAS COMPLETED ITS PASS.

AFTER ALL DEVICES HAVE BEEN EXERCISED AN END PASS STATEMENT IS TYPED:

..0 "ENDPASS@-TABLES."
 DEVICE INFORMATION TABLES.

	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
RCSR:					!RCVR!				!RCVR!	!RCVR!						!RDR!
					!ACT!				!DONE!	!IE!						!ENB!
RBUF:	!ERRO!	!OR!	!FR!	!P!												RECEIVED DATA BUFFER
	!R!	!ERR!	!ERR!	!ERR!												
TCSR:	PROGRAMMABLE BAUD				!PBR!				!XMIT!	!XMIT!				!MAIN!		!BREA!
	RATE SELECT			!ENAB!				!RDY!	!IE!					!T!		!K!
TBUF:																TRANSMITTER DATA BUFFER

NOTE

BLANK BOXFS INDICATE UNUSED AND RESERVED BIT POSITIONS. SEE THE LISTING FOR AN EXPLANATION OF THE BITS.

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6.0 SUMMARY OF TESTS AND SPECIAL SUBROUTINES.

TEST 1 ADDRESSABILITY

THIS TEST VERIFIES THAT THE ADDRESS AS PLACED IN THE
 HARDWARE P-TABLE TO BE CORRECT AND THE DLV11-F
 RESPONDS TO THAT ADDRESS SPACE.

TEST 2 BREAK - TCSR0 SET, CLEAR, RESET

TEST 3 MAINT - TCSR2 SET, CLEAR, RESET

TEST 4 XMITIE - TCSR6 SET, CLEAR, RESET

TEST 5 RCVRIE - RCSR6 SET, CLEAR, RESET

THE FOLLOWING 4 TESTS VERIFY THAT RESET (INIT) INITIALIZES
 READ ONLY BITS.

TEST 6 RCVRDONE - RCSR 7 - IS CLEARED BY INIT

TEST 7 RCVRACT - RCSR 11 - 15 CLEARED BY INIT

TEST 10 XMITRDY - TCSR 7 - IS SET BY INIT

TEST 11 XMIT RDY - TCSR 7 - CLEARS WHEN TBUF IS LOADED

WITH A CHARACTER AND THAT IT SETS WITHIN A
 REASONABLE AMOUNT OF TIME.

TEST 12 OUTPUTTING A CHAR FROM TBUF (WITH MAINT SET)

RESULTS IN RCVRDONE SETTING WITHIN A
 REASONABLE AMOUNT OF TIME AND THAT RESET
 CLEARS THE BIT.

492 TEST 13 RCVRDONE IS CLEARED BY READING RBUF
 493 ---- --

494 TEST 14 RCVRACT - RCSR 11 - SETS WHEN A START BIT IS
 495 ---- --
 496 RECEIVED AND CLEARS WHEN RCVRDONE - RCSR 7 -
 497 SETS
 498
 499

500 TEST 15 OVERRUN BIT - RBUF 14
 501 ---- --

502 TEST 16 PROGRAMMABLE BAUD RATE TEST TEST AT ALL SPEEDS
 503 ---- --
 504 AVAILABLE A COMPARISON WILL BE MADE TO SEE IF
 505 NEW TIME IS LESS THAN PREVIOUS.
 506
 507

508 TEST 17 TRANSMITTER INTERRUPT LOGIC TEST
 509 ---- --

510 LOGICALLY THIS IS 4 SEPARATE TESTS
 511 A) DOES TRANSMITTER INTERRUPT LOGIC WORK
 512 B) AT PRIORITY OF 0
 513 C) AND ONLY ONCE
 514 D) BUT NOT WITH INTERRUPT ENABLE CLEAR
 515
 516
 517

518 TEST 20 RECEIVER INTERRUPT LOGIC TEST THIS TEST COVERS ALL
 519 ---- --
 520 OF THE RECEIVER SIDE OF THE INTERRUPT LOGIC IN
 521 CHARACTER MODE.
 522
 523

524 TEST 21 TEST ACTUAL DATA TRANSFERED NON-INTERRUPT
 525 ---- --
 526 MAINTENANCE BIT SET
 527
 528

529 TEST 22 TEST DATA THROUGH WRAP
 530 ---- --

531 TEST 23 FULL DATA TRANSFER WITH INTERRUPTS AND MAINTENANCE
 532 ---- --
 533 MODE.
 534
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536 TEST 24 TEST BREAK GENERATION LOGIC TRANSMIT KNOWN CHAR
 537 ---- --
 538 WITH BREAK SET AND COMPARE RECEIVED WITH 0.
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541 TEST 25 NOT A TEST - SEND BACK TO LOOP
 542 ---- --
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NOTE

FOR ALL OF THE FOLLOWING ROUTINES THE USE OF (R5) IS PART OF THE LINKAGE MECHANISM BETWEEN THE CALLER AND THE CALLED.

ROUTINE:TIMER

THIS ROUTINE IS USED TO TEST THE STATUS OF ANY BIT IN ANY REGISTER.

INPUTS:

HOWLONG THE MAXIMUM AMOUNT OF TIME TO SPEND IN THIS ROUTINE.
 WHICHBIT A MASK WITH THE BIT(S) SET THAT ARE TO BE CHECKED
 REG A POINTER TO THE REGISTER TO BE CHECKED
 SETCLR THE DESIRED RESULTS -- EITHER SET OR CLEAR

OUTPUT:

THE 'C' BIT IS SET TO INDICATE AN ERROR BUT IT IS TESTED BY THE IF.ERROR STATEMENT.

ROUTINE:DATLNG

THIS ROUTINE SETS UP A MASK FOR DATA, WITH -

INPUT: NOTHING IS PASSED TO THIS ROUTINE BUT GLOBAL INFORMATION IS ASSUMED TO EXIST:
 #USWR-- THE WORD FOR SOFTWARE PARAMETERS
 DATA-- A MASK FOR THE LOCATION OF THE OCTAL NUMBER OF DATA BITS

OUTPUT----

MASK-- A MASK OF BINARY ZEROS RIGHT-JUSTIFIED THE NUMBER OF WHICH IS DEFINED IN #USWR WORD.

ROUTINE:WAIT

THIS ROUTINE IS USED TO DELAY EXECUTION OF THE MAIN PROGRAM FOR A SPECIFIED AMOUNT OF TIME. THIS IS ACCOMPLISHED BY INCREMENTING A REGISTER UP TO A LIMIT. THE INNER LOOP IS SET TO APPROXIMATE 1 MICRO SEC.

SERVICE ROUTINE: INTSRV

THIS GLOBAL ROUTINE DOES NOTHING BUT INCREMENT

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MAINDEC-11-DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 14
CVDVCC.P11 12-SEP-84 08:55

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'INTFLAG' EACH TIME IT IS CALLED. IT ASSUMES
THAT THE MAIN CALLING ROUTINE WILL KNOW WHAT
TO LOOK FOR.

ROUTINE:CYCLE

THIS ROUTINE CAUSES ADRS TO POINT TO THE
ADDRESS OF DLV11-F UNDER TEST, ADRS +2 TO
POINT TO THE VECTOR OF THE DLV11-F UNDER TEST.
IT KEEPS TRACK OF THE CURRENT DEVICE AND BIT
MASKS.

```

616      @
617      .TITLE MAINDEC-11-DVDVC-C
618      ;*COPYRIGHT (C) 1977
619      ;*DIGITAL EQUIPMENT CORP.
620      ;*MAYNARD, MASS. 01754
621      ;*
622      ;*PROGRAM BY ODES CHOATE
623      ;*
624      ;*THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
625      ;*PACKAGE (MAINDEC-11-DZGAC-C3), JAN 19, 1977.
626      ;*
627      .SBTTL OPERATIONAL SWITCH SETTINGS
628      ;*
629      ;*      SWITCH      USE
630      ;*      -----
631      ;*      15          HALT ON ERROR
632      ;*      14          LOOP ON TEST
633      ;*      13          INHIBIT ERROR TYPEOUTS
634      ;*      11          INHIBIT ITERATIONS
635      ;*      10          BELL ON ERROR
636      ;*      9          LOOP ON ERROR
637      ;*      8          LOOP ON TEST IN SHR<7:0>
638
639      .SBTTL BASIC DEFINITIONS
640
641      ;*INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***
642      001100 STACK= 1100
643      .EQUIV EMT,ERROR      ;:BASIC DEFINITION OF ERROR CALL
644      .EQUIV IOT,SCOPE     ;:BASIC DEFINITION OF SCOPE CALL
645
646      ;*MISCELLANEOUS DEFINITIONS
647      000011 MT= 11      ;:CODE FOR HORIZONTAL TAB
648      000012 LF= 12      ;:CODE FOR LINE FEED
649      000015 CR= 15      ;:CODE FOR CARRIAGE RETURN
650      000200 CRLF= 200   ;:CODE FOR CARRIAGE RETURN-LINE FEED
651      177776 PS= 177776 ;:PROCESSOR STATUS WORD
652      .EQUIV PS,PSW
653      177774 STGLMT= 177774 ;:STACK LIMIT REGISTER
654      177772 PIRQ= 177772 ;:PROGRAM INTERRUPT REQUEST REGISTER
655      177570 DSMR= 177570 ;:HARDWARE SWITCH REGISTER
656      177570 DDISP= 177570 ;:HARDWARE DISPLAY REGISTER
657
658      ;*GENERAL PURPOSE REGISTER DEFINITIONS
659      000000 R0= #0      ;:GENERAL REGISTER
660      000001 R1= #1      ;:GENERAL REGISTER
661      000002 R2= #2      ;:GENERAL REGISTER
662      000003 R3= #3      ;:GENERAL REGISTER
663      000004 R4= #4      ;:GENERAL REGISTER
664      000005 R5= #5      ;:GENERAL REGISTER
665      000006 R6= #6      ;:GENERAL REGISTER
666      000007 R7= #7      ;:GENERAL REGISTER
667      000006 SP= #6      ;:STACK POINTER
668      000007 PC= #7      ;:PROGRAM COUNTER
669
670      ;*PRIORITY LEVEL DEFINITIONS
671      000000 PRO= 0      ;:PRIORITY LEVEL 0

```

MAINDEC-11-DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 16
 CVDVCC.P11 12-SEP-84 08:55 BASIC DEFINITIONS

672	000040	PR1-	40	::PRIORITY LEVEL 1
673	000100	PR2-	100	::PRIORITY LEVEL 2
674	000140	PR3-	140	::PRIORITY LEVEL 3
675	000200	PR4-	200	::PRIORITY LEVEL 4
676	000240	PR5-	240	::PRIORITY LEVEL 5
677	000300	PR6-	300	::PRIORITY LEVEL 6
678	000340	PR7-	340	::PRIORITY LEVEL 7

679
680 ;="SWITCH REGISTER" SWITCH DEFINITIONS

681	100000	SW15-	100000
682	040000	SW14-	40000
683	020000	SW13-	20000
684	010000	SW12-	10000
685	004000	SW11-	4000
686	002000	SW10-	2000
687	001000	SW09-	1000
688	000400	SW08-	400
689	000200	SW07-	200
690	000100	SW06-	100
691	000040	SW05-	40
692	000020	SW04-	20
693	000010	SW03-	10
694	000004	SW02-	4
695	000002	SW01-	2
696	000001	SW00-	1
697		.EQUIV	SW09,SW9
698		.EQUIV	SW08,SW8
699		.EQUIV	SW07,SW7
700		.EQUIV	SW06,SW6
701		.EQUIV	SW05,SW5
702		.EQUIV	SW04,SW4
703		.EQUIV	SW03,SW3
704		.EQUIV	SW02,SW2
705		.EQUIV	SW01,SW1
706		.EQUIV	SW00,SW0

707
708 ;=DATA BIT DEFINITIONS (BIT00 TO BIT15)

709	100000	BIT15-	100000
710	040000	BIT14-	40000
711	020000	BIT13-	20000
712	010000	BIT12-	10000
713	004000	BIT11-	4000
714	002000	BIT10-	2000
715	001000	BIT09-	1000
716	000400	BIT08-	400
717	000200	BIT07-	200
718	000100	BIT06-	100
719	000040	BIT05-	40
720	000020	BIT04-	20
721	000010	BIT03-	10
722	000004	BIT02-	4
723	000002	BIT01-	2
724	000001	BIT00-	1
725		.EQUIV	BIT09,BIT9
726		.EQUIV	BIT08,BIT8
727		.EQUIV	BIT07,BIT7

MAINDEC-11-DVDVC-C
CVDVCC.P11

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BASIC DEFINITIONS

```

728 .EQUIV BIT06,BIT6
729 .EQUIV BIT05,BIT5
730 .EQUIV BIT04,BIT4
731 .EQUIV BIT03,BIT3
732 .EQUIV BIT02,BIT2
733 .EQUIV BIT01,BIT1
734 .EQUIV BIT00,BIT0

```

;BASIC "CPU" TRAP VECTOR ADDRESSES

```

737 000004 ERRVEC= 4 ; TIME OUT AND OTHER ERRORS
738 000010 RESVEC= 10 ; RESERVED AND ILLEGAL INSTRUCTIONS
739 000014 TBITVEC=14 ; "T" BIT
740 000014 TRTVEC= 14 ; TRACE TRAP
741 000014 BPTVEC= 14 ; BREAKPOINT TRAP (BPT)
742 000020 IOTVEC= 20 ; INPUT/OUTPUT TRAP (IOT) **SCOPE**
743 000024 PMRVEC= 24 ; POWER FAIL
744 000030 EMTVEC= 30 ; EMULATOR TRAP (EMT) **ERROR**
745 000034 TRAPVEC=34 ; "TRAP" TRAP
746 000060 TKVEC= 60 ; TTY KEYBOARD VECTOR
747 000064 TPVEC= 64 ; TTY PRINTER VECTOR
748 000240 PIRQVEC=240 ; PROGRAM INTERRUPT REQUEST VECTOR

```

```

ILLMEM= 4
ADRS= R1
GOOD= R2
BAD= R3
REGISTER=R1
BIT= R2
FUNCT= R3
LEAD= R2
FOLLOW= R4
DLADDR= 175610

```

```

; THE FOLLOWING DEFINITIONS APPLY TO THE GLOBAL SUBS
SET= -1
CLR= 0

```

```

761 177777
762 000000

```

```

;*****
; RCSR REGISTER BIT NAMES
;*****

```

```

768 ; UNUSED BIT15
769 ; UNUSED BIT14
770 ; UNUSED BIT13
771 ; UNUSED BIT12
772 004000 RCVRACT= BIT11 ; RECEIVER ACTIVE INDICATOR
773 ; UNUSED BIT10
774 ; UNUSED BIT09
775 ; UNUSED BIT08
776 000200 RCVRDONE= BIT07 ; RECEIVER DONE
777 000100 RCVRIE= BIT06 ; RECEIVER INTERRUPT ENABLE
778 ; UNUSED BIT05
779 ; UNUSED BIT04
780 ; UNUSED BIT03
781 ; UNUSED BIT02
782 ; UNUSED BIT01
783 000001 RDRRUN= BIT00 ; READER RUN

```

784
785
786
787
788 100000
789 040000
790 020000
791 010000
792
793
794
795
796 000200
797 000100
798 000040
799 000020
800 000010
801 000004
802 000002
803 000001
804
805
806
807
808 100000
809 040000
810 020000
811 010000
812 004000
813
814
815
816
817 000200
818 000100
819
820
821
822 000004
823
824 000001
825
826
827
828
829
830
831
832
833
834
835
836
837
838 000200
839 000100

```

;*****
; RBUF REGISTER BIT NAMES
;*****

```

```

ERROR-      BIT15      ; ERROR INDICATOR
ORERR-      BIT14      ; OVERRUN ERROR
FRERR-      BIT13      ; FRAMING ERROR
PERR-       BIT12      ; PARITY ERROR
; UNUSED    BIT11
; UNUSED    BIT10
; UNUSED    BIT09
; UNUSED    BIT08
RDATA7-     BIT07      ; \
RDATA6-     BIT06      ; |
RDATA5-     BIT05      ; |
RDATA4-     BIT04      ; |
RDATA3-     BIT03      ; |
RDATA2-     BIT02      ; |
RDATA1-     BIT01      ; |
RDATA0-     BIT00      ; /

```

RECEIVED DATA BITS

```

;*****
; TCSR REGISTER BIT NAMES
;*****

```

```

PBAUD3-     BIT15      ; \
PBAUD2-     BIT14      ; | PROGRAMMABLE BAUD
PBAUD1-     BIT13      ; | RATE BITS
PBAUD0-     BIT12      ; /
PBAUDSET-   BIT11      ; ENABLE SETTING OF
; UNUSED    BIT10      ; PROGRAMMABLE BAUDE RATE
; UNUSED    BIT09
; UNUSED    BIT08
XMITRDY-    BIT07      ; TRANSMITTER READY
XMITIE-     BIT06      ; TRANSMITTER INTERRUPT ENABLE
; UNUSED    BIT05
; UNUSED    BIT04
; UNUSED    BIT03
MAINT-      BIT02      ; MAINTENANCE SET BIT
; UNUSED    BIT01
BREAK-      BIT00      ; SEND BREAK (CONTINUOUS SPACE)

```

```

;*****
; TBUF REGISTER BIT NAMES
;*****

```

```

; UNUSED    BIT15
; UNUSED    BIT14
; UNUSED    BIT13
; UNUSED    BIT12
; UNUSED    BIT11
; UNUSED    BIT10
; UNUSED    BIT09
; UNUSED    BIT08
TDATA7-     BIT07      ; \
TDATA6-     BIT06      ; |

```

MAINDEC-11 DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 19
 CVDVCC.P11 12-SEP-84 08:55 BASIC DEFINITIONS

840	000040	TDATA5=	BIT05	! !	
841	000020	TDATA4=	BIT04	! !	TRANSMITTER DATA BUFFER
842	000010	TDATA3=	BIT03	! !	/
843	000004	TDATA2=	BIT02	! !	
844	000002	TDATA1=	BIT01	! !	
845	000001	TDATA0=	BIT00	! !	/

 ; FLAG BITS TO BE USE OR CLEARED IN \$USMR.

851	000017	DATA =	17
852	000020	PARITY =	20
853	000040	EVENODD =	40
854	000100	COMSPD =	100
855	000200	PBR =	200

; BAUDE MUST BE ON THE UPPER
 ; BYTE BOUNDRY OF \$USMR.--4 BITS
 BAUD = 7400
 BRK = 10000
 WRAP = 20000
 MAINTJUMP = 40000
 ERBITS = 100000

 .SBTTL TRAP CATCHER

867	000000	.=0	
868		; *ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A ".=2,HALT"	
869		; *SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS	
870		; *LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS	
871	000174	.=174	
872	000174	DISPREG: .WORD 0	;;SOFTWARE DISPLAY REGISTER
873	000176	SWREG: .WORD 0	;;SOFTWARE SWITCH REGISTER
874		.SBTTL	STARTING ADDRESS(ES)
875	000200	JMP	\$START ;;JUMP TO STARTING ADDRESS OF PROGRAM

00137 001336

MAINDEC 11 DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 20
 DVDVCC.P11 12-SEP-84 08:55 ACT11 HOOKS

```

876      .SBTTL ACT11 HOOKS
877
878      ;;*****
879      ;HOOKS REQUIRED BY ACT11
880          ;SVPC=          ;SAVE PC
881          .-46
882      000046 011410      ;1)SET LOC.46 TO ADDRESS OF ;ENDAD IN .;EOP
883          .-52
884      000052 000000      ;2)SET LOC.52 TO ZERO
885          .-;SVPC      ;; RESTORE PC
886          -1000
887      .SBTTL APT PARAMETER BLOCK
888
889      ;;*****
890      ;SET LOCATIONS 24 AND 44 AS REQUIRED FOR APT
891      ;;*****
892          .;X=          ;;SAVE CURRENT LOCATION
893          .-24          ;;SET POWER FAIL TO POINT TO START OF PROGRAM
894      000024 000200      200      ;;FOR APT START UP
895          .-44          ;;POINT TO APT INDIRECT ADDRESS PNTR.
896      000044 001000      ;APTHDR ;;POINT TO APT HEADER BLOCK
897          .-.;X      ;;RESET LOCATION COUNTER
898      ;;*****
899      ;SETUP APT PARAMETER BLOCK AS DEFINED IN THE APT-PDP11 DIAGNOSTIC
900      ;INTERFACE SPEC.
901
902      ;APTHD:
903      ;HIBTS: .WORD 0      ;;TWO HIGH BITS OF 18 BIT MAILBOX ADDR.
904      ;MADR: .WORD ;MAIL  ;;ADDRESS OF APT MAILBOX (BITS 0-15)
905      ;TSTM: .WORD 5      ;;RUN TIM OF LONGEST TEST
906      ;PASTH: .WORD 45.   ;;RUN TIME IN SECS. OF 1ST PASS ON 1 UNIT (QUICK VERIFY)
907      ;UNITH: .WORD 30.   ;;ADDITIONAL RUN TIME (SECS) OF A PASS FOR EACH ADDITIONAL UNIT
908      .WORD ;ETEND-;MAIL/2 ;;LENGTH MAILBOX-ETABLE(WORDS)

```

```

909      .SBTTL  COMMON TAGS
910
911      ;*****
912      ;*THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
913      ;*USED IN THE PROGRAM.
914
915      001100      .=-1100
916      001100      $CHTAG:      .WORD      0      ;:START OF COMMON TAGS
917      001100      000000      $TSTN:      .BYTE      0      ;:CONTAINS THE TEST NUMBER
918      001102      000      $ERFLG:      .BYTE      0      ;:CONTAINS ERROR FLAG
919      001103      000      $ICNT:      .WORD      0      ;:CONTAINS SUBTEST ITERATION COUNT
920      001104      000000      $LPADR:      .WORD      0      ;:CONTAINS SCOPE LOOP ADDRESS
921      001106      000000      $LPERR:      .WORD      0      ;:CONTAINS SCOPE RETURN FOR ERRORS
922      001110      000000      $ERTTL:      .WORD      0      ;:CONTAINS TOTAL ERRORS DETECTED
923      001112      000000      $ITEMB:      .BYTE      0      ;:CONTAINS ITEM CONTROL BYTE
924      001114      000      $ERMAX:      .BYTE      1      ;:CONTAINS MAX. ERRORS PER TEST
925      001115      001      $ERRPC:      .WORD      0      ;:CONTAINS PC OF LAST ERROR INSTRUCTION
926      001116      000000      $GDADR:      .WORD      0      ;:CONTAINS ADDRESS OF 'GOOD' DATA
927      001120      000000      $BDADR:      .WORD      0      ;:CONTAINS ADDRESS OF 'BAD' DATA
928      001122      000000      $GDDAT:      .WORD      0      ;:CONTAINS 'GOOD' DATA
929      001124      000000      $BDDAT:      .WORD      0      ;:CONTAINS 'BAD' DATA
930      001126      000000      .WORD      0      ;:RESERVED--NOT TO BE USED
931      001130      000000      .WORD      0
932      001132      000000      .WORD      0
933      001134      000      $AUTOB:      .BYTE      0      ;:AUTOMATIC MODE INDICATOR
934      001135      000      $INTAG:      .BYTE      0      ;:INTERRUPT MODE INDICATOR
935      001136      000000      .WORD      0
936      001140      177570      $SMR:      .WORD      DSMR      ;:ADDRESS OF SWITCH REGISTER
937      001142      177570      $DISPLAY:      .WORD      DDISP      ;:ADDRESS OF DISPLAY REGISTER
938      001144      177560      $TKS:      177560      ;:TTY KBD STATUS
939      001146      177562      $TKB:      177562      ;:TTY KBD BUFFER
940      001150      177564      $TPS:      177564      ;:TTY PRINTER STATUS REG. ADDRESS
941      001152      177566      $TPB:      177566      ;:TTY PRINTER BUFFER REG. ADDRESS
942      001154      000      $NULL:      .BYTE      0      ;:CONTAINS NULL CHARACTER FOR FILLS
943      001155      002      $FILLS:      .BYTE      2      ;:CONTAINS # OF FILLER CHARACTERS REQUIRED
944      001156      012      $FILLC:      .BYTE      12      ;:INSERT FILL CHARS. AFTER A "LINE FEED"
945      001157      000      $TPFLG:      .BYTE      0      ;:"TERMINAL AVAILABLE" FLAG (BIT<07>=0=YES)
946      001160      000000      $TIMES:      0      ;:MAX. NUMBER OF ITERATIONS
947      001162      000000      $ESCAPE:      0      ;:ESCAPE ON ERROR ADDRESS
948      001164      177607      000377      $BELL:      .ASCIZ      <207><377><377> ;:CODE FOR BELL
949      001170      077      $QUES:      .ASCII      /?/      ;:QUESTION MARK
950      001171      015      $CRLF:      .ASCII      <15>      ;:CARRIAGE RETURN
951      001172      000012      $LF:      .ASCIZ      <12>      ;:LINE FEED
952      ;*****
953      .SBTTL  APT MAILBOX-ETABLE
954
955      ;*****
956      .EVEN
957      001174      $MAIL:      .WORD      MSGTY      ;:APT MAILBOX
958      001174      000C00      $MSGTY:      .WORD      MSGTY      ;:MESSAGE TYPE CODE
959      001176      000000      $FATAL:      .WORD      AFATAL      ;:FATAL ERROR NUMBER
960      001200      000000      $TESTN:      .WORD      ATESTN      ;:TEST NUMBER
961      001202      000000      $PASS:      .WORD      APASS      ;:PASS COUNT
962      001204      000000      $DEVCT:      .WORD      ADEVCT      ;:DEVICE COUNT
963      001206      000000      $UNIT:      .WORD      AUNIT      ;:I/O UNIT NUMBER
964      001210      000000      $MSGAD:      .WORD      AMSGAD      ;:MESSAGE ADDRESS

```

MAINDEC-11-DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 22
 CVDVCC.P11 12-SEP-84 08:55 APT MAILBOX-ETABLE

965	001212	000000	\$MSGLG: .WORD	AMSGLG	::MESSAGE LENGTH
966	001214		\$ETABLE:		::APT ENVIRONMENT TABLE
967	001214	000	\$ENV: .BYTE	AENV	::ENVIRONMENT BYTE
968	001215	000	\$ENVM: .BYTE	AENVM	::ENVIRONMENT MODE BITS
969	001216	000000	\$SWREG: .WORD	ASWREG	::APT SWITCH REGISTER
970	001220	011110	\$USMR: .WORD	AUSMR	::USER SWITCHES
971	001222	000000	\$CPUOP: .WORD	ACPUOP	::CPU TYPE,OPTIONS
972			;		BITS 15-11-CPU TYPE
973			;		11/04=01,11/05=02,11/20=03,11/40=04,11/45=05
974			;		11/70=06,PDQ=07,Q=10
975			;		BIT 10=REAL TIME CLOCK
976			;		BIT 9=FLOATING POINT PROCESSOR
977			;		BIT 8=MEMORY MANAGEMENT
978	001224	000	\$HAMS1: .BYTE	AHAMS1	::HIGH ADDRESS,H.S. BYTE
979	001225	000	\$HTYP1: .BYTE	AHTYP1	::MEM. TYPE,BLK#1
980			;		MEM.TYPE BYTE -- (HIGH BYTE)
981			;		900 NSEC CORE=001
982			;		300 NSEC BIPOLAR=002
983			;		500 NSEC MOS=003
984	001226	000000	\$HADR1: .WORD	AHADR1	::HIGH ADDRESS,BLK#1
985			;		MEM.LAST ADDR.=3 BYTES,THIS WORD AND LOW OF "TYPE" ABOVE
986	001230	000	\$HAMS2: .BYTE	AHAMS2	::HIGH ADDRESS,H.S. BYTE
987	001231	000	\$HTYP2: .BYTE	AHTYP2	::MEM. TYPE,BLK#2
988	001232	000000	\$HADR2: .WORD	AHADR2	::MEM.LAST ADDRESS,BLK#2
989	001234	000	\$HAMS3: .BYTE	AHAMS3	::HIGH ADDRESS,H.S.BYTE
990	001235	000	\$HTYP3: .BYTE	AHTYP3	::MEM. TYPE,BLK#3
991	001236	000000	\$HADR3: .WORD	AHADR3	::MEM.LAST ADDRESS,BLK#3
992	001240	000	\$HAMS4: .BYTE	AHAMS4	::HIGH ADDRESS,H.S.BYTE
993	001241	000	\$HTYP4: .BYTE	AHTYP4	::MEM. TYPE,BLK#4
994	001242	000000	\$HADR4: .WORD	AHADR4	::MEM.LAST ADDRESS,BLK#4
995	001244	000300	\$VECT1: .WORD	AVECT1	::INTERRUPT VECTOR#1,BUS PRIORITY#1
996	001246	000000	\$VECT2: .WORD	AVECT2	::INTERRUPT VECTOR#2BUS PRIORITY#2
997	001250	175610	\$BASE: .WORD	ABASE	::BASE ADDRESS OF EQUIPMENT UNDER TEST
998	001252	100000	\$DEVN: .WORD	ADEVN	::DEVICE MAP
999	001254		\$ETEND:		
1000			.MEXIT		

MAINDEC-11-DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 23
 CVDVCC.P11 12-SEP-84 08:55 ERROR POINTER TABLE

.SBTTL ERROR POINTER TABLE

;*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
 ;*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
 ;*LOCATION ;ITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
 ;*NOTE1: IF ;ITEMB IS 0 THE ONLY PERTINENT DATA IS ;ERRPC).
 ;*NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

;* EM ;:POINTS TO THE ERROR MESSAGE
 ;* DM ;:POINTS TO THE DATA HEADER
 ;* DT ;:POINTS TO THE DATA
 ;* DF ;:POINTS TO THE DATA FORMAT

ERRTB:

```

;: GLOBAL DATA
DLADD: DLADDR
DLVEC: 300
RCSR: DLADDR * 0
RBUF: DLADDR * 2
TCSR: DLADDR * 4
TCSRHI: DLADDR * 5
TBUF: DLADDR * 6
I: 0
.BLKW 20 ;FOR R5 STACK
RSSTACK: .WORD 0
    
```

1001
 1002
 1003
 1004
 1005
 1006
 1007
 1008
 1009
 1010
 1011
 1012
 1013
 1014
 1015 001254
 1016
 1017 001254 175610
 1018 001256 000300
 1019 001260 175610
 1020 001262 175612
 1021 001264 175614
 1022 001266 175615
 1023 001270 175616
 1024 001272 000000
 1025 001274 000020
 1026 001334 000000

```

1027 001336 START:
1028 .SBTTL INITIALIZE THE COMMON TAGS
1029 ;;CLEAR THE COMMON TAGS (%CHTAG) AREA
1030 001336 012706 001100 MOV @%CHTAG,%R6 ;;FIRST LOCATION TO BE CLEARED
1031 001342 005026 CLR (%R6)+ ;;CLEAR MEMORY LOCATION
1032 001344 022706 001140 CMP @%SMR,%R6 ;;DONE?
1033 001350 001374 BNE -6 ;;LOOP BACK IF NO
1034 001352 012706 001100 MOV @%STACK,%SP ;;SETUP THE STACK POINTER
1035 ;;INITIALIZE A FEW VECTORS
1036 001356 012737 013404 000020 MOV @%SCOPE,@%IOTVEC ;;IOT VECTOR FOR SCOPE ROUTINE
1037 001364 012737 000340 000022 MOV @%340,@%IOTVEC+2 ;;LEVEL 7
1038 001372 012737 013204 000030 MOV @%ERROR,@%EHTVEC ;;EHT VECTOR FOR ERROR ROUTINE
1039 001400 012737 000340 000032 MOV @%340,@%EHTVEC+2 ;;LEVEL 7
1040 001406 012737 014336 000034 MOV @%TRAP,@%TRAPVEC ;;TRAP VECTOR FOR TRAP CALLS
1041 001414 012737 000340 000036 MOV @%340,@%TRAPVEC+2 ;;LEVEL 7
1042 001422 012737 011444 000024 MOV @%PWRDN,@%PWRVEC ;;POWER FAILURE VECTOR
1043 001430 012737 000340 000026 MOV @%340,@%PWRVEC+2 ;;LEVEL 7
1044 001436 016767 007714 007704 MOV %ENDCT,%EOPCT ;;SETUP END-OF-PROGRAM COUNTER
1045 001444 005067 177510 CLR %TIMES ;;INITIALIZE NUMBER OF ITERATIONS
1046 001450 005067 177506 CLR %ESCAPE ;;CLEAR THE ESCAPE ON ERROR ADDRESS
1047 001454 112767 000001 177433 MOVB @1,%ERMAX ;;ALLOW ONE ERROR PER TEST
1048 001462 012767 001462 177416 MOV @,%%LPADR ;;INITIALIZE THE LOOP ADDRESS FOR SCOPE
1049 001470 012767 001470 177412 MOV @,%%LPERR ;;SETUP THE ERROR LOOP ADDRESS
1050 ;;SIZE FOR A HARDWARE SWITCH REGISTER. IF NOT FOUND OR IT IS
1051 ;;EQUAL TO A "-1", SETUP FOR A SOFTWARE SWITCH REGISTER.
1052 001476 013746 000004 MOV @%ERRVEC,-(%SP) ;;SAVE ERROR VECTOR
1053 001502 012737 001536 000004 MOV @%64,@%ERRVEC ;;SET UP ERROR VECTOR
1054 001510 012767 177570 177422 MOV @%DSMR,%SMR ;;SETUP FOR A HARDWARE SWICH REGISTER
1055 001516 012767 177570 177416 MCV @%DDISP,%DISPLAY ;;AND A HARDWARE DISPLAY REGISTER
1056 001524 022777 177777 177406 CMP @-1,%SMR ;;TRY TO REFERENCE HARDWARE SMR
1057 001532 001012 BNE 664 ;;BRANCH IF NO TIMEOUT TRAP OCCURRED
1058 ;;AND THE HARDWARE SMR IS NOT - 1
1059 001534 000403 BR 654 ;;BRANCH IF NO TIMEOUT
1060 001536 012716 001544 644: MOV @%654,(%SP) ;;SET UP FOR TRAP RETURN
1061 001542 000002 RTI
1062 001544 012767 000176 177366 654: MOV @%SMREG,%SMR ;;POINT TO SOFTWARE SMR
1063 001552 012767 000174 177362 MOV @%DISPREG,%DISPLAY
1064 001560 012637 000004 664: MOV (%SP)+,%ERRVEC ;;RESTORE ERROR VECTOR
1065
1066 001564 005067 177412 CLR %PASS ;;CLEAR PASS COUNT
1067 001570 132767 000200 177417 BITB @%APTSIZE,%ENVM ;;TEST USER SIZE UNDER APT
1068 001576 001403 BEQ 674 ;;YES,USE NON-APT SWITCH
1069 001600 012767 001216 177332 MOV @%SMREG,%SMR ;;NO,USE APT SWITCH REGISTER
1070
1071 674:
1072 .SBTTL TYPE PROGRAM NAME
1073 ;;TYPE THE NAME OF THE PROGRAM IF FIRST PASS
1074 001606 005227 177777 INC @-1 ;;FIRST TIME?
1075 001612 001037 BNE 684 ;;BRANCH IF NO
1076 001614 022737 011410 000042 CMP @%ENDAD,@%42 ;;ACT-11?
1077 001622 001433 BEQ 684 ;;BRANCH IF YES
1078 001624 104401 001672 TYPE ,694 ;;TYPE ASCIZ STRING
1079 .SBTTL GET VALUE FOR SOFTWARE SWITCH REGISTER
1080 001630 005737 000042 TST @%42 ;;ARE WE RUNNING UNDER XXDP/ACT?
1081 001634 001012 BNE 704 ;;BRANCH IF YES
1082 001636 126727 177352 000001 CMPB %ENV,@1 ;;ARE WE RUNNING UNDER APT?
1083 001644 001406 BEQ 704 ;;BRANCH IF YES

```


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CVDVCC.P11 12-SEP-84 08:55 GET VALUE FOR SOFTWARE SWITCH REGISTER

1083	001646	026727	177266	000176		CMP	SWR,#SWREG	;;SOFTWARE SWITCH REG SELECTED?
1084	001654	001005				BNE	71#	;;BRANCH IF NO
1085	001656	104406				GTSWR		;;GET SOFT-SWR SETTINGS
1086	001660	000403				BR	71#	
1087	001662	112767	000001	177244	70#:	MOVB	#1,#AUTOB	;;SET AUTO-MODE INDICATOR
1088	001670				71#:			
1089	001670	000410				BR	68#	;;GET OVER THE ASCIZ
1090					;;69#:	.ASCIZ	<CRLF>*MD-11-DVDVC-C*<CRLF>	
1091	001712				68#:			

MAINDEC-11-DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 26
CVDVCC.P11 12-SEP-84 08:55 GET VALUE FOR SOFTWARE SWITCH REGISTER

```

1092 001712                               WHILE #DEVH EQ #0 DO
1093 001712                               500004:
1094 001712 005767 177334                   TST   #DEVH
1095 001716 001101                           BNE   500014
1096 001720                               TYPTXT <<CRLF>! I HAVE NO DEVICE TO TEST.!>
1097 001762                               TYPTXT <<CRLF>! SET UP #DEVH TO INDICATE ACTUAL CONFIGURATION.!>
1098 002050                               TYPTXT <<CRLF>! TYPE PROCEED (P) TO CONTINUE.!>
1099 002116 000000                           HALT
1100 002120                               ENDDO
1101 002120 000674                           BR    500004
1102 002122                               500014:
1103 002122                               LET  INITFLAG := #1
1104 002122 012767 000001 006760               MOV  #1,INITFLAG
1105 002130                               LET  BITMASK := #BIT15 ; START AT CONSOLE
1106 002130 012767 100000 006750               MOV  #BIT15,BITMASK
1107 002136                               LOOP:
1108 002136                               CALL CYCLE ; NO ARGUMENTS--ADDRS -> NEXT ADDRESS
1109 002136 004767 006524                       JSR  PC,CYCLE
1110 002142                               ;
1111 002142                               ; ADDR+2 -> NEXT VECTOR
1112 002142                               ; GET UNIT ADDRESS
1113 002142 012167 177106                       MOV  (ADRS)+,DLADD
1114 002146                               ; GET UNIT VECTOR
1115 002146                               LET  DLVEC := (ADRS)
1116 002146 011167 177104                       MOV  (ADRS),DLVEC
1117 002152                               LET  ADRS := DLADD
1118 002152 016701 177076                       MOV  DLADD,ADRS
1119 002156                               ; RCSR = DLADD + 0
1120 002156 016767 177072 177074               LET  RCSR := DLADD
1121 002164                               LET  RBUF := DLADD + #2
1122 002164 016767 177064 177070               MOV  DLADD,RBUF
1123 002172 062767 000002 177062               ADD  #2,RBUF
1124 002200                               LET  TCSR := DLADD + #4
1125 002200 016767 177050 177056               MOV  DLADD,TCSR
1126 002206 062767 000004 177050               ADD  #4,TCSR
1127 002214                               LET  TCSRHI := DLADD + #5
1128 002214 016767 177034 177044               MOV  DLADD,TCSRHI
1129 002222 062767 000005 177036               ADD  #5,TCSRHI
1130 002230                               LET  TBUF := DLADD + #6
1131 002230 016767 177020 177032               MOV  DLADD,TBUF
1132 002236 062767 000006 177024               ADD  #6,TBUF
1133 002244                               LET  R5 := #R5STACK
1134 002244 012705 001334                       MOV  #R5STACK,R5
1135 002250                               ; BRESET
1136 002250 000005                               RESET
1137

```

MAINDEC-11-DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 27
 CVDVCC.P11 12-SEP-84 08:55 T1 ADDRESSABILITY

```

1138 ;*****
1139 ;*TEST 1 ADDRESSABILITY
1140 ;* THIS TEST VERIFIES THAT THE ADDRESS AS PLACED IN
1141 ;* THE HARDWARE P-TABLE TO BE CORRECT AND THE DLV11-F RESPONDS
1142 ;* TO THAT ADDRESS SPACE
1143 ;*****
1144 002252 000004 TST1: SCOPE
1145 002254 012767 000002 176676 MOV #2,#TIMES ;DO 2 ITERATIONS
1146 002262 012767 000001 176710 MOV #1,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
1147 002270 LET ADRS := DLADD
1148 002270 016701 176760 MOV DLADD,ADRS
1149 ; SET UP INTERRUPT
1150 002274 SETVEC @ILLMEM,@INTSRV,@PR7
1151 002274 010146 MOV R1,-(SP)
1152 002276 012701 000004 MOV @ILLMEM,R1
1153 002302 012721 010656 MOV @INTSRV,(R1)+
1154 002306 012711 000340 MOV @PR7,(R1)
1155 002312 012601 MOV (SP)+,R1
1156 002314 LET I := #0
1157 002314 005067 176752 CLR I
1158 002320 REPEAT
1159 002320 50002: BGNSUB
1160 002320 MOV #64, #LPERR ;CLEAR FLAG
1161 002320 012767 002326 176562 ;LET INTFLAG := #0
1162 ;READ FLAG
1163 002326 CLR INTF 'G
1164 002326 005067 006332 ;IF INTFLAG NE #0 THEN
1165 ; FATAL ERROR
1166 ;ERRDF 1,,NOOL
1167 002332 005711 TST @ADRS
1168 002334 IF INTFLAG NE #0 THEN
1169 002334 005767 006324 TST INTFLAG
1170 002340 001401 BEQ 50003:
1171 ; FATAL ERROR
1172 002342 ERROR 1 ;ERRDF 1,,NOOL
1173 002342 104001
1174 002344 ENDSUB
1175 002344 50003: LET I := I + #2
1176 002344 LET ADRS := DLADD + I
1177 002344
1178 002344 062767 000002 176720 ADD #2,I
1179 002352 UNTIL I EQ #8.
1180 002352 016701 176676 MOV DLADD,ADRS
1181 002356 066701 176710 ADD I,ADRS
1182 002362
1183 002362 026727 176704 000010 CMP I,#8.
1184 002370 001353 BNE 50002:
1185 002372 CLRVEC ILLMEM
1186 002372 010146 MOV R1,-(SP) ;PUSH R1 ON STACK
    
```

MAINDEC 11 DVDVC C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 28
CVDVCC.P11 12 SEP-84 08:55 T1 ADDRESSABILITY

```
1187 002374 010246      MOV      R2, -(SP)      ; PUSH R2 ON STACK
1188 002376 012701 000004  MOV      @ILLMEM, R1
1189 002402 010102      MOV      R1, R2
1190 002404 062702 000002  ADD      @2, R2
1191 002410 010221      MOV      R2, (R1)+
1192 002412 005011      CLR      (R1)
1193 002414 012602      MOV      (SP)+, R2      ; POP STACK INTO R2
1194 002416 012601      MOV      (SP)+, R1      ; POP STACK INTO R1
1195
1196 002420      ENDTST                ; END OF TEST
```

MAINDEC-11-DVDC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 29
CVDC.C.P11 12-SEP-84 08:55 T1 ADDRESSABILITY

1197
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1250
1251
1252

002420 000004
002422 012767 000010 176530
002430 012767 000002 176542
002436
002436 032767 010000 176554
002444 001404
002446 032767 000001 176540
002454 001404
002456
002456
002456 012767 000001 176474
002464 000452
002466
002466
002466 012767 002474 176414
002474
002474 032777 000001 176562
002502 001401
002504
002504 104002
002506
002506
002506
002506
002506
002506
002506 012767 002514 176374
002514
002514 052777 000001 176542
002522
002522 032777 000001 176534
002530 001001
002532
002532 104003
002534

```

;*****
; THE FOLLOWING 8 TESTS TEST ALL 'READ WRITE' BITS
;*****

;*****
;TEST 2 BREAK - TCSR0 SET, CLEAR, RESET
; THE BREAK BIT IS USUALLY USED ON THE CONSOLE
; DEVICE. IF ADDITIONAL DLV OPTIONS ARE USED
; IT IS RECOMMENDED TO REMOVE THE 'BG' JUMPER AND
; CLEAR BIT 12 IN #USMR WHICH WILL CAUSE THIS
; TEST TO BE SKIPPED.
;*****

TST2: SCOPE
MOV #10,#TIMES ;DO 10 ITERATIONS
MOV #2,#TESTN ;SET TEST NUMBER IN APT MAIL BOX

; IF #BRK NOTSETIN #USMR OR #APTEMV SETIN #ENV THE
BIT #BRK,#USMR
BEQ 50004;
BIT #APTEMV,#ENV
BEQ 50005;

50004:
EXIT TEST ; BREAK NOT INSTALLED

MOV #1,#TIMES
BR TST3 ;EXIT THIS TEST.
50005:
ENDIF

; SEE IF IT IS CLEAR
BGNSUB
MOV #64,#LPERR

IF #BREAK SETIN #TCSR THEN
BIT #BREAK,#TCSR
BEQ 50006;

; BREAK DID NOT RESET IN TCSR
ERRHRD 2.,DIDNOT

50006:
ENDIF

ENDSUB

; TRY TO SET BREAK BIT
BGNSUB
MOV #65,#LPERR
LET #TCSR := #TCSR SET.BY #BREAK

IF #BREAK NOTSETIN #TCSR THEN
BIT #BREAK,#TCSR
BNE 50007;

; BREAK DID NOT SET IN TCSR
ERRHRD 3.,DIDNOT

50007:
ENDIF
```

MAINDEC-11-DVDC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 30
 CVDVCC.P11 12-SEP-84 08:55 T2 BREAK - TCSRO SET, CLEAR, RESET

```

1253 002534          500071:
1254 002534
1255
1256                                ; TRY TO CLEAR A SET BIT
1257 002534          BGNSUB
1258 002534 012767 002542 176346      MOV    #661, $LPERR
1259
1260                                LET    @TCSR := @TCSR CLR.BY @BRE *
1261 002542 042777 000001 176514      BIC    @BREAK, @TCSR
1262                                ; SHOULD HAVE CLEARED
1263 002550                                IF    @BREAK SETIN @TCSR THEN
1264 002550 032777 000001 176506      BIT    @BREAK, @TCSR
1265 002556 001401                    BEQ    500101
1266                                ; BREAK DID NOT CLEAR IN TCSR
1267 002560                                ERRMRD 4., DIDNOT
1268 002560 104004                    ERROR  4
1269 002562
1270 002562          500101:
1271 002562          ENDSUB
1272
1273                                ; NOW SEE IF RESET CLEARS IT
1274 002562          BGNSUB
1275 002562 012767 002570 176320      MOV    #671, $LPERR
1276
1277                                LET    @TCSR := @TCSR SET.BY @BREAK
1278 002570 052777 000001 176466      BIS    @BREAK, @TCSR
1279                                ; ISSUE BUS RESET
1280 002576                                BRESET
1281 002576 000005
1282 002600                                IF    @BREAK SETIN @TCSR THEN
1283 002600 032777 000001 176456      BIT    @BREAK, @TCSR
1284 002606 001401                    BEQ    500111
1285                                ; BREAK DID NOT RESET IN TCSR
1286 002610                                ERRMRD 5., DIDNOT
1287 002610 104005                    ERROR  5
1288 002612
1289 002612          500111:
1290 002612          ENDSUB
1291 002612          ENDTST
1292
1293

```

MAINDEC-11-DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 31
 CVDVCC.P11 12-SEP-84 08:55 12 BREAK - TCSR0 SET, CLEAR, RESET

```

1294
1295
1296
1297
1298 002612 000004
1299 002614 012767 000010 176336
1300 002622 012767 000003 176350
1301
1302 002630
1303 002630 032767 040000 176362
1304 002636 001404
1305 002640 126727 006261 000001
1306 002646 001004
1307 002650
1308 002650
1309 002650 012767 000001 176302
1310 002656 000452
1311 002660
1312 002660
1313
1314
1315 002660
1316 002660 012767 002666 176222
1317
1318 002666
1319 002666 032777 000004 176370
1320 002674 001401
1321
1322 002676
1323 002676 104006
1324 002700
1325 002700
1326 002700
1327
1328
1329 002700
1330 002700 012767 002706 176202
1331 002706
1332 002706 052777 000004 176350
1333
1334 002714
1335 002714 032777 000004 176342
1336 002722 001001
1337
1338 002724
1339 002724 104007
1340 002726
1341 002726
1342 002726
1343
1344
1345 002726
1346 002726 012767 002734 176154
1347
1348 002734
1349 002734 042777 000004 176322

```

```

;*****
;*****
; *TEST 3      MAINT - TCSR2 SET, CLEAR, RESET
;*****
TST3:  SCOPE
      MOV     #10,#TIMES      ;DO 10 ITERATIONS
      MOV     #3,#TESTN      ;SET TEST NUMBER IN APT MAIL BOX
                                IF #MAINTJUMP NOTSETIN #USMR ORB CONSOLE EQ #TRU
      BIT     #MAINTJUMP,#USMR
      BEQ     50012#
      CMPB   CONSOLE,#TRUE
      BNE     50013#
50012#:
                                EXIT TEST
      MOV     #1,#TIMES
      BR      TST4            ;EXIT THIS TEST
                                ENDF
50013#:
                                ; SEE IF IT IS CLEAR
                                BGNSUB
      MOV     #64#,#LPERR
                                IF #MAINT SETIN #TCSR THEN
      BIT     #MAINT,#TCSR
      BEQ     50014#
                                ; MAINT DID NOT RESET IN TCSR
                                ERRHRD 6.,DIDNOT
                                ENDF
50014#:
                                ENDSUB
                                ; TRY TO SET MAINT BIT
                                BGNSUB
      MOV     #65#,#LPERR
                                LET #TCSR := #TCSR SET.BY #MAINT
      BIS     #MAINT,#TCSR
                                ; STUCK TO 0
                                IF #MAINT NOTSETIN #TCSR THEN
                                ; MAINT DID NOT SET IN TCSR
                                ERRHRD 7.,DIDNOT
                                ENDF
50015#:
                                ENDSUB
                                ; TRY TO CLEAR A SET BIT
                                BGNSUB
      MOV     #66#,#LPERR
                                LET #TCSR := #TCSR CLR.BY #MAINT
      BIC     #MAINT,#TCSR

```

MAINDEC-11-DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 32
 CVDVCC.P11 12-SEP-84 08:55 T3 MAIN7 - TCSR2 SET, CLEAR, RESET

```

1350
1351 002742
1352 002742 032777 000004 176314 BIT #MAINT,@TCSR
1353 002750 001401 BEQ 500164
1354
1355 002752
1356 002752 104010 ERROR 10
1357 002754
1358 002754 500164:
1359 002754
1360
1361
1362 002754
1363 002754 012767 002762 176126 MOV #674,#LPERR
1364
1365 002762
1366 002762 052777 000004 176274 BIS #MAINT,@TCSR
1367
1368 002770
1369 002770 000005 RESET
1370 002772
1371 002772 032777 000004 176264 BIT #MAINT,@TCSR
1372 003000 001401 BEQ 500174
1373
1374 003002
1375 003002 104011 ERROR 11
1376 003004
1377 003004 500174:
1378 003004
1379 003004
1380
1381
1382

```

; SHOULD HAVE CLEARED
 #MAINT SETIN @TCSR THEN
 ; MAINT DID NOT CLEAR INTCSR
 ERRHRD 10.,DIDNOT
 ENDIF
 ENDSUB
 ; NOW SEE IF RESET CLEARS IT
 BGNSUB
 LET @TCSR := @TCSR SET.BY #MAINT
 ; ISSUE BUS RESET
 BRESET
 IF #MAINT SETIN @TCSR THEN
 ; MAINT DID NOT RESET IN TCSR
 ERRHRD 11.,DIDNOT
 ENDIF
 ENDSUB
 ENDTST

MAINDEC-11-DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 33
 CVDVCC.P11 12-SEP-84 08:55 T3 MAINT - TCSR2 SET, CLEAR, RESET

```

1383 ;*****
1384 ;*****
1385 ;*TEST 4 XMITIE - TCSR6 SET, CLEAR, RESET
1386 ;*****
1387 003004 000004 TST4: SCOPE
1388 003006 012767 000010 176144 MOV #10,#TIMES ;DO 10 ITERATIONS
1389 003014 012767 000004 176156 MOV #4,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
1390 ; USE PRIORITY OF 7
1391 003022 012746 000340 MOV #PR7,-(SP) ;PUT NEW PS ON STACK
1392 003026 012746 003034 MOV #64,-(SP) ;PUT NEW PC ON STACK
1393 003032 000002 RTI ;POP NEW PC AND PS
1394 003034 644:
1395
1396 ; SEE IF IT IS CLEAR
1397 003034 BGNSUB
1398 003034 012767 003042 176046 MOV #65,#LPERR
1399
1400 IF #XMITIE SETIN #TCSR THEN
1401 003042 032777 000100 176214 BIT #XMITIE,#TCSR
1402 003050 001401 BEQ 50020#
1403 ; XMITIE DID NOT RESET IN TCSR
1404 003052 ERRHRD 12.,DIDNOT
1405 003052 104012 ERROR 12
1406 003054 ENDF
1407 003054 50020#:
1408 003054 ENDSUB
1409
1410 ; TRY TO SET XMITIE BIT
1411 003054 BGNSUB
1412 003054 012767 003062 176026 MOV #66,#LPERR
1413 003062 LET #TCSR := #TCSR SET.BY #XMITIE
1414 003062 052777 000100 176174 BIS #XMITIE,#TCSR
1415 ; STUCK TO 0
1416 003070 IF #XMITIE NOTSETIN #TCSR THEN
1417 003070 032777 000100 176166 BIT #XMITIE,#TCSR
1418 003076 001001 BNE 50021#
1419 ; XMIT DID NOT RESET IN TCSR
1420 003100 ERRHRD 13.,DIDNOT
1421 003100 104013 ERROR 13
1422 003102 ENDF
1423 003102 50021#:
1424 003102 ENDSUB
1425
1426 ; TRY TO CLEAR A SET BIT
1427 003102 BGNSUB
1428 003102 012767 003110 176000 MOV #67,#LPERR
1429
1430 LET #TCSR := #TCSR CLR.BY #XMITIE
1431 003110 042777 000100 176146 BIC #XMITIE,#TCSR
1432 ; SHOULD HAVE CLEARED
1433 003116 IF #XMITIE SETIN #TCSR THEN
1434 003116 032777 000100 176140 BIT #XMITIE,#TCSR
1435 003124 001401 BEQ 50022#
1436 ; XMIT DID NOT CLEAR IN TCSR
1437 003126 ERRHRD 14.,DIDNOT
1438 003126 104014 ERROR 14

```


MAINDEC-11-DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 35
CVDVCC.P11 12-SEP-84 08:55 T4 XMITIE - TCSR6 SET, CLEAR, RESET

```

1465
1466
1467
1468
1469 003160 000004
1470 003162 012767 000010 175770
1471 003170 012767 000005 176002
1472
1473 003176
1474 003176 012767 003204 175704
1475
1476 003204
1477 003204 032777 000100 176046
1478 003212 001401
1479
1480 003214
1481 003214 104035
1482 003216
1483 003216
1484 003216
1485
1486
1487 003216
1488 003216 012767 003224 175664
1489 003224
1490 003224 052777 000100 176026
1491
1492 003232
1493 003232 032777 000100 176020
1494 003240 001001
1495
1496 003242
1497 003242 104036
1498 003244
1499 003244
1500 003244
1501
1502
1503 003244
1504 003244 012767 003252 175636
1505
1506 003252
1507 003252 042777 000100 176000
1508
1509 003260
1510 003260 032777 000100 175772
1511 003266 001401
1512
1513 003270
1514 003270 104037
1515 003272
1516 003272
1517 003272
1518
1519
1520 003272

```

```

;*****
;*****
;TEST 5 RCVRIE - RCSR6 SET, CLEAR, RESET
;*****
TST5: SCOPE
MOV #10,#TIMES ;DO 10 ITERATIONS
MOV #5,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
; SEE IF IT IS CLEAR
BGNSUB
IF #RCVRIE SETIN #RCSR THEN
BIT #RCVRIE,#RCSR
BEQ 50024#
; RCVRIE DID NOT RESET IN RCSR
ERRHRD 35,,DIDNOT
ERROR 35
ENDIF
50024#:
ENDSUB
; TRY TO SET RCVRIE BIT
BGNSUB
MOV #65#,#LPERR
LET #RCSR := #RCSR SET.BY #RCVRIE
BIS #RCVRIE,#RCSR
; STUCK TO 0
IF #RCVRIE NOTSETIN #RCSR THEN
; RCVRIE DID NOT SET IN RCSR
ERRHRD 36,,DIDNOT
ERROR 36
ENDIF
50025#:
ENDSUB
; TRY TO CLEAR A SET BIT
BGNSUB
MOV #66#,#LPERR
LET #RCSR := #RCSR CLR.BY #RCVRIE
BIC #RCVRIE,#RCSR
; SHOULD HAVE CLEARED
IF #RCVRIE SETIN #RCSR THEN
; RCVRIE DID NOT CLEAR IN RCSR
ERRHRD 37,,DIDNOT
ERROR 37
ENDIF
50026#:
ENDSUB
; NOW SEE IF RESET CLEARS IT
BGNSUB

```

MAINDEC-11-DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 36
CVDVCC P:1 12-SEP-84 08:55 75 RCVRIE - RCSR6 SET, CLEAR, RESET

1521 003272 012767 003300 175610
1522
1523 003300
1524 003300 052777 000100 175752
1525
1526 003306
1527 003306 000005
1528 003310
1529 003310 032777 000100 175742
1530 003316 001401
1531
1532 003320
1533 003320 104040
1534 003322
1535 003322
1536 003322
1537 003322
1538 003322
1539
1540
1541
1542

500274:

MOV #674, #LPERR
BIS #RCVRIE, #RCSR
RESET
BIT #RCVRIE, #RCSR
BEQ 500274

ERROR 40

LET #RCSR := #RCSR SET BY #RCVRIE
; ISSUE BUS RESET
BRESET
IF #RCVRIE SET IN #RCSR THEN
; RCVRIE DID NOT RESET IN RCSR
ERRHRD 40, .DIDNOT

ENDIF

CKLOOP

ENDSUB

ENDTST

MAINDEC-11-DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 37
CVDVCC.P11 12-SEP-84 08:55 TS RCVRIE - RCSR6 SET, CLEAR, RESET

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1543
1544
1545
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1547
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1549
1550 003322 000004
1551 003324 012767 000010 175626
1552 003332 012767 000006 175640
1553
1554
1555
1556
1557 003340
1558 003340 012767 003346 175542
1559 003346
1560 003346 032777 000200 175704
1561 003354 001402
1562
1563
1564
1565 003356
1566 003356 104041
1567
1568 003360
1569 003360 000005
1570 003362
1571 003362
1572
1573 003362
1574 003362
1575 003362
1576
1577
1578

```

```

;*****
;* THE FOLLOWING 4 TESTS VERIFY
;* THAT RESET (INIT) INITIALIZES READ ONLY BITS.
;*****
;*TEST 6          TEST THAT RCVRDONE - RCSR 7 - IS CLEARED BY INIT
;*****
TST6:  SCOPE
      MOV    #10,#TIMES      ;;DO 10 ITERATIONS
      MOV    #6,#TESTN      ;;SET TEST NUMBER IN APT MAIL BOX

      BGNSUB
      MOV    #64#,#LPERR
      IF    #RCVRDONE SETIN #RCSR THEN
      BIT    #RCVRDONE,#RCSR
      BEQ    50030#

      ;RCVRDONE SHOULD HAVE CLEARED BY INIT
      ; RCVRDONE DID NOT CLEAR IN RCSR
      ERRORD 41,#RESET, DIDNOT

      ;REISSUE RESET
      BRESET

      ENDIF
      ;ALLOW LOOPING AFTER ERROR
      CKLOOP
      ENDSUB
      ENDTST
50030#:

```

MAINDEC-11-DVDC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 38
CVDVCC.P11 12-SEP-84 08:55 T6 TEST THAT RCVRDONE - RCSR 7 - IS CLEARED BY INIT

```

1579 ;*****
1580 ;*****
1581 ;*TEST 7 TEST THAT RCVRACT - RCSR 11 - IS CLEARED BY INIT
1582 ;*****
1583 003362 000004 TST7: SCOPE
1584 003364 012767 000010 175566 MOV #10,#TIMES ;DO 10 ITERATIONS
1585 003372 012767 000007 175600 MOV #7,#TSTN ;SET TEST NUMBER IN APT MAIL BOX
1586
1587
1588
1589
1590 003400 IFB CONSOLE EQ #TRUE THEN
1591 003400 126727 005521 000001 CMPB CONSOLE,#TRUE
1592 003406 001001 BNE 50031#
1593 ; EXECUTE TEST
1594 003410 ELSE
1595 003410 000416 BR 50032#
1596 003412 50031#:
1597 003412 IF #WRAP SETIN #USMR THEN
1598 003412 032767 020000 175600 BIT #WRAP,#USMR
1599 003420 001401 BEQ 50033#
1600 ; EXECUTE TEST
1601 003422 ELSE
1602 003422 000411 BR 50034#
1603 003424 50033#:
1604 003424 IF #MAINT SETIN #USMR THEN
1605 003424 032767 000004 175566 BIT #MAINT,#USMR
1606 003432 001401 BEQ 50035#
1607 ;EXECUTE TEST
1608 003434 ELSE
1609 003434 000404 BR 50036#
1610 003436 50035#:
1611 003436 EXIT TEST ; LINE MUST BE TERMINATED
1612 003436 012767 000001 175514 MOV #1,#TIMES
1613 003444 000414 BR TST10 ;EXIT THIS TEST
1614 003446 50036#:
1615 003446 50034#:
1616 003446 50034#:
1617 003446 50034#:
1618 003446 50032#:
1619 003446 50032#:
1620
1621 003446 ;*****
1622 003446 012767 003454 175434 MOV #64,#LPERR
1623
1624 ;*****
1625 003454 IF #RCVRACT SETIN #RCSR THEN
1626 003462 032777 004000 175576 BIT #RCVRACT,#RCSR
1627 003462 001405 BEQ 50037#
1628 ;RESET SHOULD HAVE CLEARED RCVRACT
1629 003464 LET #RCSR := #RCSR CLR.BY #MAINT
1630 003464 042777 000004 175572 BIC #MAINT,#RCSR
1631 003472 ERRHRD 44, #RESET, DIDNOT
1632 003472 104044 ERROR 44
1633 ;TESTING EFFECT OF RESET ON BIT
1634

```

MAINDEC-11-DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 39
CVDVCC.P11 12-SEP-84 08:55 T7 TEST THAT RCVRACT - RCSR 11 - IS CLEARED BY INIT

1635
1636
1637
1638
1639 003474
1640 003474 000005
1641 003476
1642 003476
1643
1644 003476
1645 003476
1646 003476
1647

RESET
500374:

;RCVRACT DID NOT CLEAR IN RCSR
;ALLOW ANOTHER TRY
BRESET
ENDIF
;ALLOW LOOPING ON ERROR
CKLOOP
ENDSUB
ENDTST

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CYDVCC.P11

MACY11
12-SEP-84 08:55

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T7

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TEST THAT RCVRCT - RCSR 11 - IS CLEARED BY INIT

1648
 1649
 1650
 1651
 1652 003476 000004
 1653 003500 012767 000010 175452
 1654 003506 012767 000010 175464
 1655
 1656
 1657
 1658
 1659 003514
 1660 003514 012767 003522 175366
 1661
 1662 003522
 1663 003522 032777 000200 175534
 1664 003530 001002
 166
 1666
 1667
 1668 003532
 1669 003532 104042
 1670
 1671 003534
 1672 003534 000005
 1673 003536
 1674 003536
 1675
 1676 003536
 1677 003536
 1678 003536
 1679
 1680
 1681

```

;*****
;*****
;TEST 10      TEST THAT XMITRDY - TCSR 7 - IS SET BY INIT
;*****
TST10:  SCOPE
        MOV     @10,#TIMES      ;DO 10 ITERATIONS
        MOV     @10,#TESTN     ;SET TEST NUMBER IN APT MAIL BOX

                                BGNSUB
        MOV     @64#,#LPERR

                                IF     @XMITRDY NOTSETIN @TCSR THEN
        BIT     @XMITRDY,@TCSR
        BNE     50040#

                                ;RESET SHOULD HAVE SET BIT.
                                ;XMITRDY DID NOT SET IN TCSR (AFTER RESE
                                ERRARD 42,HRESET,DIDNOT
                                ;ISSUE ANOTHER RESET
                                BRESET

                                ENDIF
        ERROR  42
                                ;ALLOW LOOPING ON ERROR
                                CKLOOP
        RESET
                                ENDSUB
        50040# :
                                ENDTST

```


MAINDEC-11 DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 42
 CVDVCC.P11 12-SEP-84 08:55 T11 TEST THAT XMIT RDY TCSR 7 CLEARS

```

1738 003664 104066          ERROR 66
1739 003666                      ENDIF
1740 003666          500451:
1741
1742                      ; LOAD TBUF WITH A SECOND CHARACTER
1743                      ; CHECK IMMEDIATELY THAT XMITRDY IS CLEAR
1744                      ; AND THEN WAIT FOR IT TO SET
1745
1746                      ; SEND SECOND CHARACTER
1747 003666                      LET @TBUF :B= @0
1748 003666 105077 175376      CLRB  @TBUF
1749 003672 000240                      NOP
1750
1751                      ; GIVE IT TIME TO CLEAR
1752                      ; XMITRDY SHOULD HAVE CLEARED UPON
1753                      ; RECEIPT OF A CHARACTER
1754                      IF @XMITRDY SET IN @TCSR THEN
1755
1756                      ; XMITRDY DID NOT CLEAR IN TCSR
1757 003704 012767 177777 000114      MOV  @SET,ERRORFLAG
1758
1759                      ; DEFER ERROR TIMEOUT
1760
1761                      ELSE
1762 003714          500461:
1763
1764                      ; WAIT A MAXIMUM
1765                      ; OF 500 MSEC FOR
1766                      ; XMIT RDY TO SET IN TCSR
1767 003714 010546          MOV  R5, -(SP)
1768 003716 012745 177777      MOV  @SET, -(R5)
1769 003722 016745 175336      MOV  TCSR, -(R5)
1770 003726 012745 000200      MOV  @XMITRDY, -(R5)
1771 003732 012745 000500      MOV  @500, -(R5)
1772 003736 004767 004356      JSR  PC, TIMER
1773 003742 012605          MOV  (SP)+, R5
1774 003744
1775 003744 103001          BCC  500501
1776
1777                      ; XMIT RDY DID NOT SET IN TCSR
1778 003746 104070          ERROR 70
1779 003750                      ENDIF
1780 003750          500501:
1781 003750          500471:
1782 003750
1783 003750
1784 003750 026727 000052 177777      CMP  ERRORFLAG, @SET
1785 003756 001011          BNE  500511
1786 003760
1787 003760 026727 000040 000001      CMP  PASS, @1
1788 003766 003404          BLE  500521
1789
1790                      ; CALL ERROR IF 2ND TRY
1791 003770 104067          ERROR 67
1792 003772                      LET EXITFLAG := @SET
1793 003772 012767 177777 000030      MOV  @SET,EXITFLAG

```

MAINDEC-11-DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 43
CVDVCC.P11 12-SEP-84 08:55 T11 TEST THAT XMIT RDY - TCSR 7 - CLEARS

```

1794 004000
1795 004000
1796 004000
1797 004000 000403
1798 004002
1799 004002
1800 004002 012767 177777 000020
1801 004010
1802 004010
1803 004010
1804 004010 026727 000014 177777
1805 004016 001401
1806 004020
1807 004020 000674
1808 004022
1809 004022
1810 004022 000403
1811 004024 000000
1812 004026 000000
1813 004030 000000
1814 004032

                    500521:
BR 500531
                    500511:
MOV #SET,EXITFLAG
                    500531:
CMP EXITFLAG,#SET
BEQ 500441
BR 500431
                    500441:
BR TST12
PASS: 0
ERRORFLAG: 0
EXITFLAG: 0

ENDIF
ELSE ; NO ERROR
LET EXITFLAG := #SET
ENDIF
EXIF EXITFLAG EQ #SET
ENDLOOP
EXIT ; SKIP AROUND FLAG WORDS
EXIT THIS TEST

ENDTST

```

MAINDEC-11 DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 44
CVDVCC.P11 12-SEP-84 08:55 T11 TEST THAT XMIT RDY - TCSR 7 CLEARS

```

1815
1816
1817
1818
1819
1820
1821 004032 000004
1822 004034 012767 000001 175116
1823 004042 012767 000012 175130
1824
1825 004050
1826 004050 032767 040000 175142
1827 004056 001404
1828 004060 126727 005041 000001
1829 004066 001004
1830 004070
1831 004070
1832 004070 012767 000001 175062
1833 004076 000442
1834 004100
1835 004100
1836
1837
1838 004100
1839 004100 052777 000004 175156
1840
1841 004106
1842 004106 012767 004114 174774
1843
1844
1845 004114
1846 004114 105077 175150
1847
1848
1849
1850
1851 004120
1852 004120 010546
1853 004122 012745 177777
1854 004126 016745 175126
1855 004132 012745 000200
1856 004136 012745 000500
1857 004142 004767 004152
1858 004146 012605
1859
1860
1861 004150
1862 004150 103004
1863
1864
1865 004152
1866 004152 042777 000004 175104
1867 004160
1868 004160 104071
1869 004162
1870 004162

```

```

;*****
;*****
;TEST 12      TEST THAT OUTPUTTING A CHAR FROM TBUF (WITH MAINT SET)
;             RESULTS IN RCVRDONE SETTING WITHIN A REASONABLE AMOUNT OF TIME
;             AND THAT RESET CLEARS THE BIT.
;*****
TST12:  SCOPE
        MOV     #1,#TIMES      ;DO 1 ITERATION
        MOV     #12,#TESTN    ;SET TEST NUMBER IN APT MAIL BOX
                                IF #MAINTJMP NOTSET IN #USMR ORB CONSOLE EQ #TRU
        BIT     #MAINTJMP,#USMR
        BEQ     50054$
        CMPB   CONSOLE,#TRUE
        BNE     50055$
50054$:
                                EXIT TEST
        MOV     #1,#TIMES
        BR     TST13          ;EXIT THIS TEST
50055$:
                                ; SET THE MAINTENANCE BIT
                                LET #TCSR := #TCSR SET.BY #MAINT
                                BGNSUB
        MOV     #64,#LPERR
                                ; SEND A CHARACTER AND LET IT WRAP AROUND
                                LET #TBUF :B= #0
                                ; WAIT A MAXIMUM OF 50 MSEC
                                ; FOR RCVR DONE TO SET IN
                                ; RCSR
                                CALL TIMER IN <#500,#RCVRDONE,#RCSR,#SET>
        MOV     R5,-(SP)
        MOV     #SET,-(R5)
        MOV     RCSR,-(R5)
        MOV     #RCVRDONE,-(R5)
        MOV     #500,-(R5)
        JSR    PC,TIMER
        MOV     (SP),R5
                                ;DIDN'T SET IN TIME
                                IF.ERROR THEN
        BCC     50056$
                                ; RCVRDONE DID NOT SET IN RCSR
                                ; CAN NOT LEAVE WITH MAINT SET
        LET     #TCSR := #TCSR CLR.BY #MAINT
                                ERRMRD 71,,DIDNOT
        ENDIF
50056$:

```


MAINDEC-11-DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 46
 CVDVCC.P11 12-SEP-84 08:55 T12 TEST THAT OUTPUTTING A CHAR FROM TBUF (WITH MAINT SET)

```

1891 ;*****
1892 ;*****
1893 ;*TEST 13 TEST THAT RCVRDONE IS CLEARED BY READING RBUF
1894 ;*****
1895 004204 000004 TST13: SCOPE
1896 004206 012767 000010 174744 MOV #10,#TIMES ;DO 10 ITERATIONS
1897 004214 012767 000013 174756 MOV #13,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
1898
1899 IF #MAINTJUMP NOTSETIN #USMR ORB CONSOLE EQ #TRU
1900 004222 032767 040000 174770 BIT #MAINTJUMP,#USMR
1901 004230 001404 BEQ 50060#
1902 004232 126727 004667 000001 CMPB CONSOLE,#TRUE
1903 004240 001004 BNE 50061#
1904 004242 50060#:
1905 004242 EXIT TEST
1906 004242 012767 000001 174710 MOV #1,#TIMES
1907 004250 000440 BR TST14 ;EXIT THIS TEST
1908 004252 ENDF
1909 004252 50061#:
1910
1911 ; SET MAINT. BIT
1912 004252 LET #TCSR := #TCSR SET.BY #MAINT
1913 004252 052777 000004 175004 BIS #MAINT,#TCSR
1914 004260 BGNSUB
1915 004260 012767 004266 174622 MOV #64#,#LPERR
1916 ; OUTPUT A CHARACTER WITH MAINTENANCE
1917 ; SET, AND WAIT FOR XMITRDY TO SET.
1918
1919 ; OUTPUT A CHARACTER
1920 004266 LET #TBUF :B= #0
1921 004266 105077 174776 CLRB #TBUF
1922 ; WAIT MAXIMUM OF 500 MSEC
1923 ; FOR RCVRDONE TO SET IN
1924 ; RCSR
1925 004272 CALL TIMER IN <#500,#RCVRDONE,RCSR,#SET>
1926 004272 010546 MOV R5,-(SP)
1927 004274 012745 177777 MOV #SET,-(R5)
1928 004300 016745 174754 MOV RCSR,-(R5)
1929 004304 012745 000200 MOV #RCVRDONE,-(R5)
1930 004310 012745 000500 MOV #500,-(R5)
1931 004314 004767 004000 JSR PC,TIMER
1932 004320 012605 MOV (SP),R5
1933 004322 LET #TCSR := #TCSR CLR.BY #MAINT
1934 004322 042777 000004 174734 BIC #MAINT,#TCSR
1935 ; DID IT BECAME READY?
1936 004330 IF.ERROR THEN
1937 004330 103001 BCC 50062#
1938 ;RCVRDONE DID NOT SET IN RCSR
1939 004332 ERRNRD 73., DIDNOT
1940 004332 104073 ERROR 73
1941 004334 ENDF
1942 004334 50062#:
1943 004334 ENDSUB
1944
1945 ; NOW THAT IT IS SET LETS SEE IF READING THE
1946 ; BUFFER CLEARS RCVRDONE.

```

MAINDEC 11-DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 47
CVDVCC.P11 12-SEP 84 08:55 T13 TEST THAT RCVRDONE IS CLEARED BY READING RBUF

```

1947
1948
1949 004334
1950 004334 117700 174722      MOVB   BRBUF,RO
1951
1952 004340
1953 004340 032777 000200 174712  BIT   @RCVRDONE,BRCSR
1954 004346 001401                BEQ   500631
1955
1956 004350
1957 004350 104074                ERROR  74
1958 004352
1959 004352
1960 004352

```

;READ BUFFER
LET RO ;B= BRBUF

IF @RCVRDONE SETIN BRCSR THEN

;RCVRDONE DID NOT CLEAR IN RCSR
ERRMRD 74,,DIDNOT

ENDIF

500631:

ENDTST

MAINDEC-11-DVDVC-C
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12-SEP-84
T13

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TEST THAT RCVRDONE IS CLEARED BY READING RBUF

```

1961
1962
1963
1964
1965
1966
1967 004352 000004
1968 004354 012767 000010 174576
1969 004362 012767 000014 174610
1970 004370
1971 004370 126727 004531 000001
1972 004376 001404
1973 004400 032767 040000 174612
1974 004406 001004
1975 004410
1976 004410
1977 004410 012767 000001 174542
1978 004416 000526
1979 004420
1980 004420
1981 004420
1982 004420 032767 000001 174566
1983 004426 001404
1984 004430
1985 004430 012767 000001 174522
1986 004436 000516
1987 004440
1988 004440
1989
1990
1991
1992 004440
1993 004440 052777 000004 174616
1994 004446
1995 004446 012700 000000
1996 004452
1997 004452 005001
1998
1999
2000
2001
2002 004454
2003 004454 105077 174610
2004 004460
2005 004460
2006 004460
2007 004460 032777 004000 174572
2008 004466 001403
2009 004470
2010 004470 012700 177777
2011 004474
2012 004474 000401
2013 004476
2014 004476
2015 004476 005201
2016 004500

||*****
||*****
|*TEST 14 TEST THAT RCVRACT - RCSR 11 - SETS
|* WHEN A START BIT IS RECEIVED AND
|* CLEARS WHEN RCVRDONE - RCSR 7 - SETS
||*****
TST14: SCOPE
MOV #10,#TIMES ;;DO 10 ITERATIONS
MOV #14,#TESTN ;;SET TEST NUMBER IN APT MAIL BOX
IFB CONSOLE EQ #TRUE OR #MAINTJUMP NOTSETIN #USM
CMPB CONSOLE,#TRUE
BEQ 500644
BIT #MAINTJUMP,#USM
BNE 500654
500644:
EXIT TEST
MOV #1,#TIMES
BR TST15 ;;EXIT THIS TEST
ENDIF
500654:
IF #APTENV SETIN #ENV THEN
BIT #APTENV,#ENV
BEQ 500664
EXIT TEST
MOV #1,#TIMES
BR TST15 ;;EXIT THIS TEST
ENDIF
500664:
LET #TCSR := #TCSR SET BY #MAINT
BIS #MAINT,#TCSR
LET R0 := #CLR
MOV #CLR,R0
LET R1 := #0
CIR R1
;LOAD A CHARACTER INTO TBUF
;WAIT FOR RCVRACT TO SET
;SEND A CHARACTER
LET #TBUF :B= #0
REPEAT
IF #RCVRACT SETIN #RCSR THEN
LET R0 := #SET
ELSE
500704:
LET R1 := R1 + #1
INC R1
ENDIF

```


MAINDEC-11-DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 49
 CVDVCC.P11 12-SEP-84 08:55 T14 TEST THAT RCVRACT - RCSR 11 - SETS

```

2017 004500          500714:
2018 004500
2019 004500 020027 177777          CMP      RC,#SET
2020 004504 001403          BEQ      500724
2021 004506 020167 000160          CMP      R1,MAX
2022 004512 101762          BLOS     500674
2023 004514          500724:
2024 004514
2025 004514 020167 000152          CMP      R1,MAX
2026 004520 101410          BLOS     500734
2027
2028
2029
2030 004522
2031 004522 042777 000004 174534          BIC      #MAINT,#TCSR
2032 004530
2033 004530 104075          ERROR    75
2034 004532
2035 004532 012767 000001 174420          MOV      #1,#TIMES
2036 004540 000455          BR       TST15
2037 004542
2038 004542          500734:
2039
2040
2041
2042
2043
2044
2045 004542
2046 004542          500744:
2047 004542 032777 004000 174510          BIT      #RCVRACT,#RCSR
2048 004550 001421          BEQ      500754
2049
2050 004552
2051 004552 032777 000200 174500          BIT      #RCVRDONE,#RCSR
2052 004560 001414          BEQ      500764
2053 004562
2054 004562 032777 004000 174470          BIT      #RCVRACT,#RCSR
2055 004570 001410          BEQ      500774
2056
2057
2058
2059 004572
2060 004572 042777 000004 174464          BIC      #MAINT,#TCSR
2061 004600
2062 004600 104076          ERROR    76
2063
2064 004602
2065 004602 012767 000001 174350          MOV      #1,#TIMES
2066 004610 000431          BR       TST15
2067 004612
2068 004612          500774:
2069 004612          500764:
2070 004612
2071 004612
2072 004612 000753          BR       500744

;UNTIL R0 EQ #SET OR R1 HI MAX
;IT NEVER SET
;RCVRACT DID NOT SET IN RCSR.
;CAN NOT LEAVE WITH MAINT SET
LET #TCSR := #TCSR CLR.BY #MAINT
ERRHRD 75,, DIDNOT
EXIT TEST
;EXIT THIS TEST
ENDIF

;CHECK FOR TIMING OF RCVRACT. CLEARING
;VS RCVRDONE SETTING

WHILE #RCVRACT SETIN #RCSR DO
IF #RCVRDONE SETIN #RCSR THEN
IF #RCVRACT SETIN #RCSR THEN
;RCVRDONE AND RCVRACT
;BOTH SET
;CAN NOT LEAVE WITH MAINT SET
LET #TCSR := #TCSR CLR.BY #MAINT
ERRHRD 76, DONEACT
;NO USE CONTINUING
EXIT TST
;EXIT THIS TEST
ENDIF
ENDIF
ENDDO
    
```

MAINDEC-11-DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 50
 CVDVCC.P11 12-SEP-84 08:55 T14 TEST THAT RCVRACT - RCSR 11 - SETS

```

2073 004614          500751:
2074
2075
2076 004614          ;RCVRACT = 0 NOW.
                IF #RCVRDONE NOTSETIN #RCSR THEN
2077 004614 032777 000200 174436      BIT      #RCVRDONE,#RCSR
2078 004622 001010          BNE      501001
2079
2080
2081 004624          ;RCVRDONE DID NOT SET IN RCSR
                ; CAN NOT LEAVE WITH MAINT SET
                LET      #TCSR := #TCSR CLR.BY #MAINT
2082 004624 042777 000004 174432      BIC      #MAINT,#TCSR
2083 004632          ERRHRD 77,,DIDNOT
2084 004632 104077          ERROR    77
2085 004634          EXIT TEST
2086 004634 012767 000001 174316      MOV      #1,#TIMES
2087 004642 000414          BR      TST15          ;;;EXIT THIS TEST
2088 004644          ENDIF
2089 004644          501001:
2090
2091          ;TEST THAT READING THE RECEIVER
                ;BUFFER CLEARS RCVRDONE
2092
2093
2094
2095 004644          ;READ CHAR.
                LET RO := #RBUF
2096 004644 017700 174412          MOV      #RBUF,RO
2097
2098
2099 004650          IF #RCVRDONE SETIN #RCSR THEN
2100 004650 032777 000200 174402      BIT      #RCVRDONE,#RCSR
2101 004656 001404          BEQ      501011
2102
2103          ;RCVRDONE DID NOT CLEAR IN RCSR
                ; CAN NOT LEAVE WITH MAINT SET
                LET      #TCSR := #TCSR CLR.BY #MAINT
2104 004660          ERRHRD 100,,DIDNOT
2105 004666          EXIT
2106 004666 104100          ERROR    100
2107 004670          ENDIF
2108 004670          501011:
2109
2110          EXIT
2111 004670 000401          BR      TST15          ;;;EXIT THIS TEST
2112 004672 070000          MAX:70000
2113
2114 004674          ENDTST
2115

```

MAINDEC-11-DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 51
CVDVCC.P11 12-SEP-84 08:55 T14 TEST THAT RCVRCT - RCSR 11 - SETS

```

2116
2117
2118
2119
2120
2121 004674 000004
2122 004676 012767 000010 174254
2123 004704 012767 000015 174266
2124
2125 004712
2126 004712 032767 100000 174300
2127 004720 001404
2128 004722 126727 004177 000001
2129 004730 001004
2130 004732
2131 004732
2132 004732 012767 000001 174220
2133 004740 000547
2134 004742
2135 004742
2136 004742
2137 004742 032767 040000 174250
2138 004750 001004
2139 004752
2140 004752 012767 000001 174200
2141 004760 000537
2142 004762
2143 004762
2144
2145 004762
2146 004762 052777 000004 174274
2147
2148
2149
2150 004770
2151 004770 012767 004776 174112
2152
2153
2154
2155
2156
2157 004776
2158 004776 105077 174266
2159
2160 005002
2161 005002 010546
2162 005004 012745 000310
2163 005010 004767 003562
2164 005014 012605
2165
2166
2167 005016
2168 005016 105077 174246
2169
2170 005022
2171 005022 010546

```

```

;*****
;*****
;TEST 15 TEST THE OVERRUN BIT - RBUF 14
;*****
TST15: SCOPE
MOV #10,#TIMES ;DO 10 ITERATIONS
MOV #15,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
;*****
IF #ERRBITS NOTSETIN #USMR ORB CONSOLE EQ #TRUE
BIT #ERRBITS,#USMR
BEQ 50102#
CMPB CONSOLE,#TRUE
BNE 50103#
50102#:
EXIT TEST
MOV #1,#TIMES
BR TST16 ;;;EXIT THIS TEST
ENDIF
50103#:
IF #MAINTJMP NOTSETIN #USMR THEN
EXIT TEST
MOV #1,#TIMES
BR TST16 ;;;EXIT THIS TEST
ENDIF
50104#:
LET #ICSR := #TCSR SET.BY #MAINT
BIS #MAINT,#TCSR
;*****
BGNSUB
MOV #64,#LPERR
;OUTPUT 2 CHARACTERS WITH
;AMPLE DELAYS BETWEEN FOR RECEPTION.
;THIS SHOULD AN CAUSE OVERRUN ERROR.
;OUTPUT 1 CHARACTER
LET #TBUF :B= #0
;GO AWAY FOR 200 M SEC
WAITMS 200.
MOV R5,-(SP)
MOV #200,-(R5)
JSR PC,WAIT
MOV (SP),R5
;OUTPUT 2ND CHARACTER
LET #TBUF :B= #0
;LET OVERRUN HAPPEN
WAITMS 200.
MOV R5,-(SP)

```

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```

2172 005024 012745 000310      MOV      @200.,-(R5)
2173 005030 004767 003542      JSR      PC,WAIT
2174 005034 012605      MOV      (SP)+,R5
2175
2176                                ;READ BUFFER AND ERROR BITS
2177 005036                                LET R4 := BRBUF
2178 005036 017704 174220      MOV      BRBUF,R4
2179
2180                                ;IT DIDN'T SET
2181 005042                                IF @ORERR NOTSETIN R4 THEN
2182 005042 032704 040000      BIT      @ORERR,R4
2183 005046 001010      BNE      50105$
2184
2185                                ;ORERR DID NOT SET IN RBUF
2186 005050                                ; CAN NOT LEAVE WITH MAINT SET
2187 005050 042777 000004 174206      BIC      @MAINT,@TCSR
2188 005056                                ERRARD 101.,DIDNOT
2189 005056 104101      ERROR    101
2190
2191                                ;NO USE COMPOUNDING ERRORS
2192 005060                                EXIT TST
2193 005060 012767 000001 174072      MOV      @1,@TIMES
2194 005066 000474      BR
2195 005070                                ;;;EXIT THIS TEST
2196 005070                                ENDIF
2197 005070                                ENDSUB
2198
2199                                ;NOW SEE IF ERROR BIT SET WITH OVERRUN ERROR:
2200 005070                                BGNSUB
2201 005070 012767 005076 174012      MOV      @65@,@LPERR
2202 005076                                IF @ERROR NOTSETIN R4 THEN
2203 005076 032704 100000      BIT      @ERROR,R4
2204 005102 001010      BNE      50106$
2205
2206                                ;ERROR DID NOT SET IN RBUF
2207 005104                                ; CAN NOT LEAVE WITH MAINT SET
2208 005104 042777 000004 174152      BIC      @MAINT,@TCSR
2209 005112                                ERRARD 102.,DIDNOT
2210 005112 104102      ERROR    102
2211
2212                                ;-WHEN ORERR SET.
2213 005114                                ;GET OUT NOW.
2214 005114                                EXIT TST
2215 005114 012767 000001 174036      MOV      @1,@TIMES
2216 005122 000456      BR
2217 005124                                ;;;EXIT THIS TEST
2218 005124                                ENDIF
2219 005124                                ENDSUB
2220 005124                                BGNSUB
2221
2222                                ;CHECK REAL RBUF TO SEE IF ORERR IS STILL SET.
2223 005124 012767 005132 173756      MOV      @66@,@LPERR
2224
2225                                IF @ORERR NOTSETIN BRBUF THEN
2226 005132 032777 040000 174122      BIT      @ORERR,BRBUF
2227

```

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 (VDVCC.P11 12-SEP-84 08:55 T15 TEST THE OVERRUN BIT - RBUF 14

```

2228 005140 001010      BNE      501074
2229
2230
2231
2232 005142
2233 005142 042777 000004 174114      BIC      @MAINT,@TCSR
2234 005150
2235 005150 104103      ERROR    103
2236
2237 005152
2238 005152 012767 000001 174000      MOV      @1,@TIMES
2239 005160 000437      BR       TST16
2240 005162
2241 005162
2242 005162
2243
2244 005162
2245 005162 012767 005170 173720      MOV      @674,@LPERR
2246
2247
2248
2249 005170
2250 005170 105077 174074      CLRB    @RBUF
2251
2252 005174
2253 005174 010546      MOV      R5,-(SP)
2254 005176 012745 000310      MOV      @200,-(R5)
2255 005202 004767 003370      JSR     PC,WAIT
2256 005206 012605      MOV      (SP)+,R5
2257
2258 005210
2259 005210 032777 040000 174044      BIT     @ORERR,@RBUF
2260 005216 001410      BEQ     501104
2261
2262
2263 005220
2264 005220 042777 000004 174036      BIC      @MAINT,@TCSR
2265 005226
2266 005226 104104      ERROR    104
2267
2268
2269
2270 005230
2271 005230 012767 000001 173722      MOV      @1,@TIMES
2272 005236 000410      BR       TST16
2273 005240
2274 005240
2275

```

501074:

501104:

```

;READING RBUF CLEARED ORERR.
; CAN NOT LEAVE WITH MAINT SET
LET @TCSR := @TCSR CLR.BY @MAINT
ERRMRD 103,ITCLRED
;SKIP REST OF TEST
EXIT TEST
;;;EXIT THIS TEST
ENDIF
ENDSUB
BGNSUB
;NOW SEE IF THEY CLEAR WHEN ANOTHER CHAR. IS RECEIVED
;SEND A CHARACTER AROUND.
LET @RBUF :B= @0
;LET IT CIRCULATE
WAITMS 200.
IF @ORERR SET IN @RBUF THEN
;ORERR DID NOT CLEAR IN RBUF
; CAN NOT LEAVE WITH MAINT SET
LET @TCSR := @TCSR CLR.BY @MAINT
ERRMRD 104,,DIDNOT
;-AFTER RECEIVING ANOTHER CHAR
;SKIP AROUND REST
EXIT TST
;;;EXIT THIS TEST
ENDIF

```

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2276	005240					
2277	005240	032777	100000	174014	BIT	@ERROR,@RBUF
2278	005246	001404			BEG	501114
2279						
2280						
2281	005250					
2282	005250	042777	000004	174006	BIC	@MAINT,@TCSR
2283	005256					
2284	005256	104105			ERROR	105
2285						
2286	005260					
2287	005260					
2288	005260					
2289	005260					
2290						

IF @ERROR SET IN @RBUF THEN

;ERROR DID NOT CLEAR IN RBUF
; CAN NOT LEAVE WITH MAINT SET
LET @TCSR := @TCSR CLR.BY @MAINT

ERRORD 105..DIDNOT

ENDIF

ENDSUB
ENDTST

501114:

```

2291
2292
2293
2294
2295
2296
2297
2298
2299 005260 000004
2300 005262 012767 000010 173670
2301 005270 012767 000016 173702
2302
2303
2304
2305 005276
2306 005276 032767 000200 173714
2307 005304 001404
2308 005306 032767 040000 173704
2309 005314 001004
2310 005316
2311 005316
2312 005316 012767 000001 173634
2313 005324 000553
2314 005326
2315 005326
2316
2317 005326
2318 005326 132767 000001 173660
2319 005334 001404
2320 005336
2321 005336 012767 000001 173614
2322 005344 000543
2323 005346
2324 005346
2325
2326 005346
2327 005346 005067 002620
2328 005352
2329 005352 012767 177777 000270
2330 005360
2331 005360 012767 177777 000264
2332 005366
2333 005366 052777 000004 173670
2334
2335 005374
2336 005374 005003
2337 005376 000401
2338 005400
2339 005400 005203
2340 005402
2341 005402 020327 000017
2342 005406 003062
2343 005410
2344 005410 017700 173646
2345
2346 005414

```

```

;*****
;*****
;TEST 16 PROGRAMMABLE BAUD RATE TEST
; TEST AT ALL SPEEDS AVAILABLE
; A COMPARISON WILL BE MADE TO SEE
; IF NEW TIME IS LESS THAN PREVIOUS.
;*****
TST16: SCOPE
MOV @10,@TIMES ;DO 10 ITERATIONS
MOV @16,@TESTN ;SET TEST NUMBER IN APT MAIL BOX

IF @PDR NOTSET IN @USMR OR @MAINTJMP NOTSET IN @U
BIT @PDR,@USMR
BEQ 501120
BIT @MAINTJMP,@USMR
BNE 501130
501120:
EXIT TEST
MOV @1,@TIMES
BR TST17 ;EXIT THIS TEST
ENDIF
501130:
IFB @PTENV SET IN @ENV THEN
BITB @PTENV,@ENV
BEQ 501140
EXIT TST
MOV @1,@TIMES
BR TST17 ;EXIT THIS TEST
ENDIF
501140:
LET ENRCHK := @0 ; CLEAR ERROR WORD
LET OLD := @-1
LET OLD*2 := @ 1
LET @TCSR := @TCSR SET.BY @MAINT
;EACH BAUD RATE
INCR R3 FROM @0 TO @15. BY @1
501160:
CLR R3
BR 501150
501150:
INC R3
CMP R3,@15.
BGT 501170
LET R0 := @RBUF
;CHANGE BAUDE RATE
LET @TCSRHI := @ RATES(R3)

```

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2347	005414	116377	005624	173644	MOVB	RATES(R3),@TCSAMI	
2348							;FLAG
2349	005422						LET BIT := @0
2350	005422	005002			CLR	BIT	
2351							;OUTPUT THE CHARACTER
2352	005424						LET @TBUF := @0
2353	005424	005077	173640		CLR	@TBUF	
2354							;INITIALIZE COUNTER
2355	005430						LET NEW := @0
2356	005430	005067	000210		CLR	NEW	
2357	005434						LET NEW*2 := @0
2358	005434	005067	000206		CLR	NEW*2	
2359	005440						WHILE BIT EQ @0 DO
2360	005440						
2361	005440	005702			501201:	TST	BIT
2362	005442	001014				BNE	501211
2363	005444						IF @RCVROONE SETIN @RCSR THEN
2364	005444	032777	000200	173606	BIT	@RCVROONE,@RCSR	
2365	005452	001403			BEQ	501221	
2366							;DONE - ITS READY
2367	005454						LET BIT := @1
2368	005454	012702	000001		MOV	@1,BIT	
2369	005460						ELSE
2370	005460	000404			BR	501231	
2371	005462				501221:		
2372							;OTHERWISE-INCREMENT TIME
2373	005462						LET NEW := NEW + @1
2374	005462	005267	000156		INC	NEW	
2375	005466						LET NEW*2 := NEW*2 + CARRY
2376	005466	005567	000154		ADC	NEW*2	
2377	005472						ENDIF
2378	005472				501231:		
2379							;SIGNALS DONE
2380	005472						ENDDO
2381	005472	000762			BR	501201	
2382	005474				501211:		
2383							IF NEW*2 LO OLD*2 THEN
2384	005474						
2385	005474	026767	000146	000150	CMP	NEW*2,OLD*2	
2386	005502	103001			BHIS	501241	
2387							; OK
2388	005504						ELSE
2389	005504	000414			BR	501251	
2390	005506				501241:		
2391							; NEW*2 >= OLD*2
2392	005506						IF NEW*2 EQ OLD*2 AND NEW LO OLD THEN
2393	005506	026767	000134	000136	CMP	NEW*2,OLD*2	
2394	005514	001005			BNE	501261	
2395	005516	026767	000122	000124	CMP	NEW,OLD	
2396	005524	103001			BHIS	501261	
2397							;OK
2398	005526						ELSE
2399	005526	000403			BR	501271	
2400	005530				501261:		
2401							;NEW*2 > OLD*2 OR
2402							;(NEW*2 = OLD*2 AND

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```

2403                                     ; NEW >= OLD)
2404                                     ;BAUD RATE DIDN'T CHANGE
2405 005530 012767 000004 002434      MOV  #BIT2,ERRCHK      LET ERRCHK := #BIT2 ; SET ERROR INDICATOR
2406 005530 012767 000004 002434      MOV  #BIT2,ERRCHK
2407 005536                               ENDIF
2408 005536 501274:                               ENDIF
2409 005536                               ENDIF
2410 005536 501254:
2411                                     ;UPDATE OLD TIME
2412 005536                               LET OLD := NEW
2413 005536 016767 000102 000104      MOV  NEW,OLD
2414 005544                               LET OLD*2 := NEW*2
2415 005544 016767 000076 000100      MOV  NEW*2,OLD*2
2416                                     ENDINC ;BAUD RATE
2417 005552                               BR 501164
2418 005552 000712                               BR 501174:
2419 005554                               BR 501174:
2420 005554                               LET R3 := #USMR+1 AND #17 ; PUT BAUD BACK
2421 005554 116703 173441      MOVB #USMR+1,R3
2422 005560 110346      MOVB R3,-(SP)
2423 005562 142716 000017      BICB #17,(SP)
2424 005566 142603      BICB (SP)+,R3
2425 005570                               LET R3 := R3 CLR.BY #177400
2426 005570 042703 177400      BIC #177400,R3
2427 005574                               LET @TCSRMI := RATES(R3) ; LIKE HE WANTED IT
2428 005574 116377 005624 173464      MOVB RATES(R3),@TCSRMI
2429
2430                                     ; CAN NOT LEAVE WITH MAINT SET
2431 005602                               LET @TCSR := @TCSR CLR.BY @MAINT
2432 005602 042777 000004 173454      BIC @MAINT,@TCSR
2433 005610                               IF #BIT2 SET IN ERRCHK THEN
2434 005610 032767 000004 002354      BIT #BIT2,ERRCHK
2435 005616 001401      BEQ 501304
2436                                     ; REPORT DEFERED ERROR
2437 005620                               ERRHRD 126
2438 005620 104126      ERROR 126
2439 005622                               ENDIF
2440 005622 501304:
2441 005622                               EXIT ;SKIP TABLE
2442 005622 000414      BR TST17 ;EXIT THIS TEST
2443
2444 005624      RATES: ;A TABLE OF THE ACTUAL BYTES TO MOVE INTO THE
2445      ;UPPER BYTE OF XCSR FOR EACH BAUD RATE
2446      ;** NOTE:: THE VALUE INDICATED IN THE COLUMN 'OFFSET
2447      ;** INTO TABLE' CAN BE PLACED INTO BITS<11:8>
2448      ;** OF LOCATION '#USMR' TO CAUSE THE CORRESPONDING
2449      ;** BAUD TO BE SELECTED IN THE DLV11-F UPON
2450      ;** COMPLETION OF THIS TEST.
2451
2452                                     BAUD  OFFSET INTO TABLE
2453 005624 010      R0050: .BYTE 010 ; 50 0
2454 005625 030      R0070: .BYTE 030 ; 70 1
2455 005626 050      R0110: .BYTE 050 ; 110 2
2456 005627 070      R0135: .BYTE 070 ; 135 3
2457 005630 110      R0150: .BYTE 110 ; 150 4
2458 005631 130      R0300: .BYTE 130 ; 300 5

```

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2459	005632	150	R0600:	.BYTE	150		600	6
2460	005633	170	R0200:	.BYTE	170		1200	7
2461	005634	210	R1800:	.BYTE	210		1800	10
2462	005635	230	R2000:	.BYTE	230		2000	11
2463	005636	250	R2400:	.BYTE	250		2400	12
2464	005637	270	R3600:	.BYTE	270		3600	13
2465	005640	310	R4800:	.BYTE	310		4800	14
2466	005641	330	R7200:	.BYTE	330		7200	15
2467	005642	350	R9600:	.BYTE	350		9600	16
2468	005643	370	R10000:	.BYTE	370		19200	17

2469
2470 005644 000000 000000 NEW: 0.0
2471 005650 000000 000000 OLD: 0.0
2472 005654
2473
2474
2475

ENDTST

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```

2476
2477
2478
2479
2480
2481
2482
2483
2484
2485 005654 000004
2486 005656 012767 000010 173274
2487 005664 012767 000017 173306
2488
2489
2490
2491 005672
2492 005672 032767 000001 173314
2493 005700 001404
2494 005702
2495 005702 012767 000001 173250
2496 005710 000534
2497 005712
2498 005712
2499
2500
2501
2502 005712
2503 005712 005067 002746
2504
2505
2506 005716
2507 005716 016703 173334
2508
2509 005722
2510 005722 062703 000004
2511
2512 005726
2513 005726 010146
2514 005730 010301
2515 005732 012721 010656
2516 005736 012711 000340
2517 005742 012601
2518 005744
2519 005744 012767 005752 173136
2520
2521 005752
2522 005752 010546
2523 005754 012745 177777
2524 005760 016745 173300
2525 005764 012745 000200
2526 005770 012745 000500
2527 005774 004767 002320
2528 006000 012605
2529
2530
2531 006002
    
```

```

;*****
;*****
;TEST 17 TRANSMITTER INTERRUPT LOGIC TEST
; LOGICALLY THIS IS 4 SEPARATE TESTS
; A) DOES TRANSMITTER INTERRUPT LOGIC WORK
; B) AT PRIORITY OF 0
; C) AND ONLY ONCE
; D) BUT NOT WITH INTERRUPT ENABLE CLEAR
;*****
TST17: SCOPE
MOV #10,#TIMES ;DO 10 ITERATIONS
MOV #17,#TESTN ;SET TEST NUMBER IN APT MAIL BOX

;*****
IF #APTENV SETIN #ENV THEN
BIT #APTENV,#ENV
BEQ 50131#
EXIT TEST
MOV #1,#TIMES
BR TST20 ;EXIT THIS TEST
ENDIF
50131#:
;CLEAR 'INTERRUPT OCCURED' FLAG
LET INTFLAG := #0
;GET VECTOR ADDRESS
LET R3 := DLVEC
;FOR THE TRANSMITTER
LET R3 := R3 + #4
;SET VECTOR TO POINT TO TRANS.SRV AT PRI
SETVEC R3, #INTSRV, #PR7
MOV R1,-(SP)
MOV R3,R1
MOV #INTSRV,(R1)+
MOV #PR7,(R1)
MOV (SP)+,R1
BGNSUB
; MAKE SURE THAT TRANSMITTER READY IS SET
CALL TIMER IN <#500,#XMITRDY,TCSR,#SET>
;CLEAR INTERRUPT ENABLE
LET #TCSR := #TCSR CLR.BY #XMITIE
    
```

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```

2532 006002 042777 000100 173254      BIC      @XMITIE,@TCSR
2533
2534                                     ;SET IT TO 0
2535 006010 012746 000000      MOV      @PRO,-(SP)      ;;PUT NEW PS ON STACK
2536 006014 012746 006022      MOV      @65@,-(SP)     ;;PUT NEW PC ON STACK
2537 006020 000002                                     ;;POP NEW PC AND PS
2538 006022                                     65@:
2539
2540                                     ;NOW SET I.E. BIT
2541 006022                                     LET @TCSR := @TCSR SET.BY @XMITIE
2542 006022 052777 000100 173234      BIS      @XMITIE,@TCSR
2543
2544                                     ;LET INTERRUPT HAVE TIME TO OCCUR
2545 006030                                     WAITMS 200.
2546 006030 010546      MOV      R5,-(SP)
2547 006032 012745 000310      MOV      @200,-(R5)
2548 006035 004767 002534      JSR      PC,WAIT
2549 006042 012605      MOV      (SP)+,R5
2550
2551                                     ;DID EXACTLY 1 INTERRUPT OCCUR
2552 006044                                     IF INTFLAG NE #1 THEN
2553 006044 026727 002614 000001      CMP      INTFLAG,#1
2554 006052 0C1406      BEQ      50132@
2555
2556                                     ;NO - WAS IT 0 OR MORE THAN ONCE
2557 006054                                     IF INTFLAG EQ #0 THEN
2558 006060 001002      TST      INTFLAG
2559                                     BNE      50133@
2560 006062                                     ;TRANSMITTER DID NOT INTERRUPT IN TIME
2561 006062 104106      ERROR    106
2562 006064                                     ERRHRD 106,,DIDNOT
2563 006064 000401      BR       50134@
2564 006066                                     ELSE
2565                                     ;TWICE
2566                                     ;TRANSMITTER INTERRUPTED TWICE
2567 006066 104107      ERROR    107
2568 006066                                     ERRHRD 107,,TWICE
2569 006070      BR       50134@
2570 006070      BR       50134@
2571 006070      BR       50134@
2572 006070      BR       50132@
2573 006070
2574                                     ;INTERRUPT WITHOUT INTERRUPT ENABLE SET
2575 006070      BR       50134@
2576 006070 012767 006076 173012      MOV      @66@,@LPERR
2577                                     ;CLEAR 'INTERRUPT OCCURED' FLAG
2578 006076 005067 002562      CLR      INTFLAG
2579 006076 005067 002562      CLR      INTFLAG
2580                                     ;CLEAR INTERRUPT ENABLE
2581 006102 042777 000100 173154      BIC      @XMITIE,@TCSR
2582 006102 042777 000100 173154      BIC      @XMITIE,@TCSR
2583                                     ;NO INTERRUPTS SHOULD OCCUR.
2584 006110 012746 000000      MOV      @PRO,-(SP)      ;;PUT NEW PS ON STACK
2585 006114 012746 006122      MOV      @67@,-(SP)     ;;PUT NEW PC ON STACK
2586 006120 000002                                     ;;POP NEW PC AND PS
2587 006122                                     67@:

```

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```

2588
2589 006122
2590 006122 010546
2591 006124 012745 000002
2592 006130 004767 002442
2593 006134 012605
2594 006136
2595 006136 005767 002522
2596 006142 001401
2597
2598 006144
2599 006144 104110
2600 006146
2601 006146
2602 006146
2603 006146 000005
2604 006150
2605
2606 006150
2607 006150 017704 173102
2608 006154
2609 006154 010146
2610 006156 010246
2611 006160 012701 000004
2612 006164 010102
2613 006166 062702 000002
2614 006172 010221
2615 006174 005011
2616 006176 012602
2617 006200 012601
2618 006202
2619
2620
2621
2622
2623
2624

```

```

;DARE IT TO HAPPEN
WAITMS 2

MOV R5,-(SP)
MOV #2,-(R5)
JSR PC,WAIT
MOV (SP),R5

IF INTFLAG NE #0 THEN

TST INTFLAG
BEQ 501354

;INTERRUPT OCCURED WITH I E CLEARED
ERRNRD 110,NOTENAB

ERROR 110

ENDIF

BRESET

ENDSUB

;RESTORE VECTOR AREA
LET R4 := @DLVEC

MOV @DLVEC,R4

CLRVEC R4
;;PUSH R1 ON STACK
;;PUSH R2 ON STACK

MOV R1,-(SP)
MOV R2,-(SP)
MOV @R4,R1
MOV R1,R2
ADD #2,R2
MOV R2,(R1)+
CLR (R1)
MOV (SP),R2
MOV (SP),R1

;;POP STACK INTO R2
;;POP STACK INTO R1
ENDTST

```

501354:

MAINDEC-11 DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 62
 CVDVCC.P11 12-SEP-84 08:55 T17 TRANSMITTER INTERRUPT LOGIC TEST

```

2625 ;*****
2626 ;*****
2627 ;TEST 20 RECEIVER INTERRUPT LOGIC TEST
2628 ;* THIS TEST COVERS ALL OF THE RECEIVER
2629 ;* SIDE OF THE INTERRUPT LOGIC IN
2630 ;* CHARACTER MODE.
2631 ;*****
2632 TST20: SCOPE
2633 006202 000004 MOV #10,#TIMES ;DO 10 ITERATIONS
2634 006204 012767 000010 172746 MOV #20,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
2635 006212 012767 000020 172760 ;IF #MAINTJUMP NOTSET IN #USMR ORB CONSOLE EQ #TRU
2636 006220 032767 040000 172772 BIT #MAINTJUMP,#USMR
2637 006226 001404 BEQ 50136#
2638 006230 126727 002671 000001 CMPB CONSOLE,#TRUE
2639 006236 001002 BNE 50137#
2640 006240 50136# JMP TST21 ; EXIT TEST
2641 006240 000167 000242 ;
2642 006244 50137# ENDF
2643 006244
2644
2645 ;CLEAR INTERRUPT OCCURED FLAG
2646 ;SET UP RECEIVER INTER.VECTOR
2647 006244 SETVEC DLVEC,#INTSRV,#PR7
2648 006244 010146 MOV R1,-(SP)
2649 006246 016701 173004 MOV DLVEC,R1
2650 006252 012721 010656 MOV #INTSRV,(R1)
2651 006256 012711 000340 MOV #PR7,(R1)
2652 006262 012601 MOV (SP),R1
2653 ;PRIORITY 0 AND MULTIPLE INTERRUPT TEST.-RCVRIE
2654 006264 BGN SUB
2655 006264 012767 006272 172616 MOV #64,#LPERR
2656 006272 LET INTFLAG := #0
2657 006272 005067 002366 CLR INTFLAG
2658 ;SET MAINT. BIT
2659 006276 LET #TCSR := #TCSR SET.BY #MAINT
2660 006276 052777 000004 172760 BIS #MAINT,#TCSR
2661 ;CLEAR INTERRUPTS
2662 006304 LET #RCSR := #RCSR CLR.BY #RCVRIE
2663 006304 042777 000100 172746 BIC #RCVRIE,#RCSR
2664 ;CHANGE PRIORITY
2665 ;..TO 0
2666 006312 012746 000000 MOV #PRO,-(SP) ;PUT NEW PS ON STACK
2667 006316 012746 006324 MOV #65,-(SP) ;PUT NEW PC ON STACK
2668 006322 000002 RTI ;POP NEW PC AND PS
2669 006324 65#
2670
2671 ;SEND A CHARACTER
2672 006324 LET #TBUF := #0
2673 006324 105077 172740 CLRB #TBUF
2674 ;WAIT A MAXIMUM
2675 ;OF 500 MSEC FOR
2676 ;RCVR DONE TO SET IN RCSR
2677 006330 CALL TIMER IN (<#500,#RCVRDONE,RCSR,#SET>)
2678 006330 010546 MOV R5,-(SP)
2679 006332 012745 177777 MOV #SET,-(R5)
2680 006336 016745 172716 MOV RCSR,-(R5)
    
```

MAINDEC-11 DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 63
 CVDVCC.P11 12-SEP-84 08:55 T20 RECEIVER INTERRUPT LOGIC TEST

2681	006342	012745	000200		MOV	#RCVRODNE,-(R5)	
2682	006346	012745	000500		MOV	#500,-(R5)	
2683	006352	004767	001742		JSR	PC,TIMER	
2684	006356	012605			MOV	(SF)+,R5	
2685							;SET INTERRUPT ENABLE
2686	006360						LET #RCSR := #RCSR SET.BY #RCVRIE
2687	006360	052777	000100	172672	BIS	#RCVRIE,#RCSR	
2688							;LET IT COME IN.
2689	006366						WAITMS 1
2690	006366	010546			MOV	R5,-(SP)	
2691	006370	012745	000001		MOV	#1,-(R5)	
2692	006374	004767	002176		JSR	PC,WAIT	
2693	006400	012605			MOV	(SP)+,R5	
2694							
2695	006402						LET R0 := #RBUF ; CLEAR RCVRODNE
2696	006402	017700	172654		MOV	#RBUF,R0	
2697							;DID HE DO IT RIGHT?
2698	006406						IF INTFLAG NE #1 THEN
2699	006406	026727	002252	000001	CMR	INTFLAG,#1	
2700	006414	001411			BEQ	501401	
2701							;NONE OCCURED
2702							; CAN NOT LEAVE WITH MAINT SET
2703	006416						LET #RCSR := #RCSR CLR.BY #MAINT
2704	006416	042777	000004	172640	BIC	#MAINT,#RCSR	
2705	006424						IF INTFLAG EQ #0 THEN
2706	006424	005767	002234		TST	INTFLAG	
2707	006430	001002			BNE	501411	
2708							;RECEIVER DID NOT INTERRUPT IN TIME
2709	006432						ERRMRD 111,,DIDNOT
2710	006432	104111			ERROR	111	
2711							;TWICE OR MORE
2712	006434						ELSE
2713	006434	000401			BR	501421	
2714	006436						
2715							;RECEIVER INTERRUPTED TWICE
2716	006436						ERRMRD 112,,TWICE
2717	006436	104112			ERROR	112	
2718	006440						ENDIF
2719	006440						
2720	006440						ENDIF
2721	006440						
2722	006440						ENDSUB
2723							
2724							
2725							; CLEAR THE WORLD
2726	006440						LET #RCSR := #RCSR CLR.BY #RCVRIE
2727	006440	042777	000100	172612	BIC	#RCVRIE,#RCSR	
2728							
2729							
2730							;RESET MAINT. BIT.
2731	006446						LET #RCSR := #RCSR CLR.BY #MAINT
2732	006446	042777	000004	172610	BIC	#MAINT,#RCSR	
2733							
2734	006454						LET R4 := #DLVEC
2735	006454	017704	172576		MOV	#DLVEC,R4	
2736	006460						CLRVEC R4

MAINDEC-11 DVOVC-C
CVOVCC.P11 12-SEP-8

NY11 30A(1052) 12-SEP-84 15:41 PAGE 64
4 55 T20 RECEIVER INTERRUPT LOGIC TEST

2737	006460	010146		MOV	R1,-(SP)	::PUSH R1 ON STACK
2738	006462	010246		MOV	R2,-(SP)	::PUSH R2 ON STACK
2739	006464	012701	000004	MOV	#R4,R1	
2740	006470	010102		MOV	R1,R2	
2741	006472	062702	000002	ADD	#2,R2	
2742	006476	010221		MOV	R2,(R1)+	
2743	006500	005011		CLR	(R1)	
2744	006502	012602		MOV	(SP)+,R2	::POP STACK INTO R2
2745	006504	012601		MOV	(SP)+,R1	::POP STACK INTO R1
2746	006506					ENDTST

MAINDEC-11-DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 65
 CVDVCC.P11 12-SEP-84 08:55 T20 RECEIVER INTERRUPT LOGIC TEST

```

2747
2748
2749
2750
2751
2752 006506 000004
2753 006510 012767 000001 172442
2754 006516 012767 000021 172454
2755 006524
2756 006524 032767 040000 172466
2757 006532 001404
2758 006534 126727 002365 000001
2759 006542 001004
2760 006544
2761 006544
2762 006544 012767 000001 172406
2763 006552 000526
2764 006554
2765 006554
2766
2767 006554
2768 006554 005067 001412
2769
2770 006560
2771 006560 052777 000004 172476
2772
2773
2774
2775 006566 012746 000000
2776 006572 012746 006600
2777 006576 000002
2778 006600
2779
2780 006600
2781 006600 162705 000002
2782 006604 004767 001666
2783 006610 012501
2784 006612
2785 006612 017700 172444
2786
2787
2788 006616
2789 006616 005002
2790 006620 000401
2791 006622
2792 006622 005202
2793 006624
2794 006624 020227 000377
2795 006630 003062
2796
2797
2798
2799
2800 006632
2801 006632 010546
2802 006634 012745 177777
    
```

```

;*****
;*****
;TEST 21 TEST ACTUAL DATA TRANSFERED
;* NON-INTERRUPT MAINTENANCE BIT SET
;*****
TST21: SCOPE
        MOV #1,#TIMES ;DO 1 ITERATION
        MOV #21,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
                                IF #MAINTJMP NOTSETIN #USMR ORB CONSOLE EQ #TRU
        BIT #MAINTJMP,#USMR
        BEQ 501436
        CMPB CONSOLE,#TRUE
        BNE 501446
501436:
                                EXIT TEST
        MOV #1,#TIMES
        BR TST22 ;EXIT THIS TEST
                                ENDF
501446:
                                LET ERRCHK := #0
                                ;SET MAINT. BIT
                                LET #TCSR := #TCSR SET.BY #MAINT
                                ;CHANGE PRIORITY
                                ;..TO 0
        MOV #PRO,-(SP) ;PUT NEW PS ON STACK
        MOV #64,-(SP) ;PUT NEW PC ON STACK
        RTI ;POP NEW PC AND PS
64:
                                ;GET DATA MASK.
                                CALL DATLNG OUT <R1>
        SUB #1*2,R5
        JSR PC,DATLNG
        MOV (R5),R1
                                LET R0 := #RBUF ; START CLEAR
                                ;ALL BINARY CHAR.
                                INCR R2 FROM #0 TO #377 BY #1
501466:
        CLR R2
        BR 501456
501456:
        INC R2
501456:
        CMP R2,#377
        BGT 501476
                                ;TRANSMIT CHAR IN R2
                                CALL TIMER IN <#500,#XMITRDY,TCSR,#SET>
        MOV R5,-(SP)
        MOV #SET,-(R5)
    
```

```

MAINDEC 11-DVQVC-C      MACY11 30A(1052) 12-SEP-84 15:41 PAGE 66
CVDVCC.P11 12-SEP-84 08:55      T21      TEST ACTUAL DATA TRANSFERED

```

Address	OpCode	Op1	Op2	Op3	Op4	Op5	Op6	Op7	Op8
2803	006640	016745	172420						
2804	006644	012745	000200						
2805	006650	012745	000500						
2806	006654	004767	001440						
2807	006660	012605							
2808	006662								
2809	006662	103003							
2810	006664								
2811	006664	052767	000010	001300					
2812	006672								
2813	006672				501504:				
2814									
2815									
2816	006672								
2817	006672	110277	172372						
2818									
2819	006676								
2820	006676	010546							
2821	006700	012745	177777						
2822	006704	016745	172350						
2823	006710	012745	000200						
2824	006714	012745	000500						
2825	006720	004767	001374						
2826	006724	012605							
2827	006726								
2828	006726	103003							
2829	006730								
2830	006730	052767	000020	001234					
2831	006736								
2832	006736				501514:				
2833									
2834	006736								
2835	006736	017703	172320						
2836									
2837									
2838									
2839									
2840									
2841									
2842	006742								
2843	006742	010204							
2844	006744	040104							
2845	006746								
2846	006746	040103							
2847									
2848									
2849	006750								
2850	006750	020403							
2851	006752	001410							
2852									
2853									
2854	006754								
2855	006754	042777	000004	172302					
2856	006762								
2857	006762	104116							
2858	006764								

```

MOV TCSR, -(R5)
MOV @XMITRDY, -(R5)
MOV @500, -(R5)
JSR PC, TIMER
MOV (SP)+, R5

IF .ERROR THEN
    LET ERRCHK := ERRCHK SET.BY @BIT3
ENDIF

; TRANSMIT IT
LET @TBUF :B= R2

CALL TIMER IN <@500, @RCVROONE, RCSR, @SET>

MOV R5, -(SP)
MOV @SET, -(R5)
MOV RCSR, -(R5)
MOV @RCVROONE, -(R5)
MOV @500, -(R5)
JSR PC, TIMER
MOV (SP)+, R5

IF .ERROR THEN
    LET ERRCHK := ERRCHK SET.BY @BIT4
ENDIF

; AND SAVE IT
LET R3 := @RBUF

; COMPARE TO SEE IF WE RECEIVED IT ALL

; CLEAN OFF NON-DATA BITS
; ON BOTH TRANSMITTED AND
LET R4 := R2 CLR.BY R1

LET R3 := R3 CLR.BY R1

; RECEIVED DATA
IF R4 NE R3 THEN

; DATA COMPARE ERROR
; CAN NOT LEAVE WITH MAINT SET
LET @TCSR := @TCSR CLR.BY @MAINT

ERRHRD 116, COMP, SBWAS

EXIT TEST ; ON ERROR

```

MAINDEC 11 DVDVC C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 67
 CVDVCC.P11 12 SEP 84 08:55 T21 TEST ACTUAL DATA TRANSFERED

2859	006764	012767	000001	172166	MOV	#1, #TIMES	
2860	006772	000416			BR	TST22	;;;EXIT THIS TEST
2861	006774						ENDIF
2862	006774				501520:		
2863	006774						ENDINC 1 R2
2864	006774	000712			BR	501460	
2865	006776				501470:		
2866							
2867							RESET MAINT. BIT.
2868	006776						LET #TCSR := #TCSR CLR.BY #MAINT
2869	006776	042777	000004	172260	BIC	#MAINT, #TCSR	
2870	007004						IF #BIT3 SETIN ERRCHK THEN
2871	007004	032767	000010	001160	BIT	#BIT3, ERRCHK	
2872	007012	001401			BEQ	501530	
2873	007014						ERRWRD 130
2874	007014	104130			ERROR	130	
2875	007016						ENDIF
2876	007016				501530:		
2877	007016						IF #BIT4 SETIN ERRCHK THEN
2878	007016	032767	000020	001146	BIT	#BIT4, ERRCHK	
2879	007024	001401			BEQ	501540	
2880	007026						ERRWRD 131
2881	007026	104131			ERROR	131	
2882	007030						ENDIF
2883	007030				501540:		
2884	007030						ENDTST
2885							
2886							
2887							

MAINDEC-11-DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 68
CYDVCC.P11 12-SEP-84 08:55 121 TEST ACTUAL DATA TRANSFERED

```

2888
2889
2890
2891
2892 007030 000004
2893 007032 012767 000001 172120
2894 007040 012767 000022 172132
2895 007046
2896 007046 032767 020000 172144
2897 007054 001404
2898 007056 032767 000100 172134
2899 007064 001004
2900 007066
2901
2902 007066
2903 007066 012767 000001 172064
2904 007074 000516
2905 007076
2906 007076
2907
2908 007076
2909 007076 042777 000004 172160
2910
2911
2912
2913
2914
2915
2916
2917
2918
2919 007104
2920 007104 052777 000011 172146
2921
2922
2923 007112 012746 000000
2924 007116 012746 007124
2925 007122 000002
2926 007124
2927
2928 007124
2929 007124 162705 000002
2930 007130 004767 001342
2931 007134 012501
2932 007136
2933 007136 017700 172120
2934
2935 007142
2936 007142 005002
2937 007144 000401
2938 007146
2939 007146 005202
2940 007150
2941 007150 020227 000377
2942 007154 003063

```

```

.....
TEST 22 TEST DATA THROUGH WRAP
.....
TST22: SCOPE
      MOV #1,#TIMES      ;DO 1 ITERATION
      MOV #22,#TESTN    ;SET TEST NUMBER IN APT MAIL BOX
                          IF @WRAP NOTSET IN @USMR OR @CONSPD NOTSET IN @USM
      BIT @WRAP,#USMR
      BEQ 501550
      BIT @CONSPD,#USMR
      BNE 501560
501550:
                          ;CAN'T TEST WITHOUT A WRAP
                          EXIT TST
      MOV #1,#TIMES
      BR TST23           ;EXIT THIS TEST
                          ENDF
501560:
                          ;DON'T USE MAINT.
      LET @TCSR := @TCSR CLR.BY @MAINT
                          ; IF A SPECIAL TURN AROUND CARD IS
                          ; CONNECTED IN PLACE OF THE WRAP
                          ; SETTING READER RUN WILL ENABLE IT.
                          ; THIS MODULE IS ONLY USED IN MANUFACTUR
                          ; AND ONLY ON THE CONSOLE OLV11-F.
                          ; IF NO SPECIAL MODULE IS AVAILABLE.
                          ; AND THE WRAP BIT IS SET IN @USMR
                          ; THEN THIS TEST WILL ERROR ON THE CONSO
      LET @RCSR := @RCSR SET.BY #11
                          ;CHANGE PRIORITY
                          ;..TO 0
      MOV @PRO,-(SP)     ;PUT NEW PS ON STACK
      MOV @64,-(SP)     ;PUT NEW PC ON STACK
      RTI                ;POP NEW PC AND PS
                          ;GET DATA MASK
      CALL DATLNG OUT <R1>
      LET R0 := @RBUF ; START CLEAN
                          ;BINARY COUNT PATTERN
      INCR R2 FROM #0 TO #377 BY #1
501600:
      CLR R2
      BR 501570
501570:
      INC R2
501570:
      CMP R2,#377
      BGT 501610

```

MAINDEC-11-DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 69
 CVDVCC.P11 12-SEP-84 08:55 T22 TEST DATA THROUGH WRAP

```

2944
2945
2946
2947
2948 007156
2949 007156 010545
2950 007160 012745 177777
2951 007164 016745 172074
2952 007170 012745 000200
2953 007174 012745 000500
2954 007200 004767 001114
2955 007204 012605
2956 007206
2957 007206 103005
2958 007210 104123
2959 007212
2960 007212 012767 000001 171740
2961 007220 000444
2962 007222
2963 007222
2964
2965
2966 007222
2967 007222 110277 172042
2968
2969 007226
2970 007226 010546
2971 007230 012745 177777
2972 007234 016745 172020
2973 007240 012745 000200
2974 007244 012745 000500
2975 007250 004767 001044
2976 007254 012605
2977 007256
2978 007256 103005
2979 007260
2980 007260 104124
2981
2982 007262
2983 007262 012767 000001 171670
2984 007270 000420
2985 007272
2986 007272
2987
2988
2989 007272
2990 007272 017703 171764
2991
2992
2993 007276
2994 007276 010204
2995 007300 040104
2996 007302
2997 007302 040103
2998
2999

```

; TRANSMIT THE CHAR. IN R2.
 ; MAKE SURE IT'S READY
 CALL TIMER IN (<#500,<#XMITRDY,<#TCSR,<#SET>)
 MOV R5,-(SP)
 MOV #SET,-(R5)
 MOV TCSR,-(R5)
 MOV #XMITRDY,-(R5)
 MOV #500,-(R5)
 JSR PC,TIMER
 MOV (SP),R5
 IF.ERROR THEN
 BCC 501624
 ERROR 123
 ; TRANSMITTER NEVER BECAME READY
 EXIT TEST
 ;;;EXIT THIS TEST
 ENDF
 501624:
 ; START IT ON ITS WAY
 LET #TBUF := R2
 MOV# R2,#TBUF
 ; NOW WAIT FOR RECIEVER DONE
 CALL TIMER IN (<#500,<#RCVROONE,<#RCSR,<#SET>)
 MOV R5,-(SP)
 MOV #SET,-(R5)
 MOV RCSR,-(R5)
 MOV #RCVROONE,-(R5)
 MOV #500,-(R5)
 JSR PC,TIMER
 MOV (SP),R5
 IF.ERROR THEN
 BCC 501634
 ERROR 124
 ERRORD 124
 ; RECIEVER NEVER BECAME READY
 EXIT TEST
 ;;;EXIT THIS TEST
 ENDF
 501634:
 ; RETRIEVE
 LET R3 := #RBUF
 MOV #RBUF,R3
 ; STRIP OFF JUNK ON BOTH
 LET R4 := R2 CLR.BY R1
 MOV R2,R4
 BIC R1,R4
 LET R3 := R3 CLR.BY R1
 BIC R1,R3
 ; WE HAVE TROUBLE

MAINDEC-11 DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 71
 CVDVCC.P11 12 SEP-84 08:55 T22 TEST DATA THROUGH WRAP

```

3028 ; ;
3029 ; ;
3030 ; *TEST 23 FULL DATA TRANSFER WITH INTERRUPTS
3031 ; * AND MAINTENANCE MODE.
3032 ; ;
3033 007332 000004 TST23: SCOPE
3034 007334 012767 000001 171616 MOV #1,#TIMES ;DO 1 ITERATION
3035 007342 012767 000023 171630 MOV #23,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
3036
3037 007350 IF #MAINTJMP NOTSETIN #USMR ORB CONSOLE EQ #TRU
3038 007350 032767 040000 171642 BIT #MAINTJMP,#USMR
3039 007356 001404 BEQ 50165:
3040 007360 126727 001541 000001 CMPB CONSOLE,#TRUE
3041 007366 001004 BNE 50166:
3042 007370 50165:
3043 007370 EXIT TEST
3044 007370 012767 000001 171562 MOV #1,#TIMES
3045 007376 000553 BR TST24 ;EXIT THIS TEST
3046 007400 ENDF
3047 007400 50166:
3048
3049 ;GET DATA MASK
3050 007400 CALL DATLNG OUT <R3>
3051 007400 162705 000002 SUB #1*2,R5
3052 007404 004767 001066 JSR PC,DATLNG
3053 007410 012503 MOV (R5),R3
3054
3055 ; THIS TEST WILL RUN BOTH TRANSMITTER AND
3056 ; RECIEVER AT FULL SPEED TESTING
3057 ; THE ABILITY OF THE MODULE
3058 ; TO HANDLE INTERRUPTS FROM BOTH SIDES
3059 ; AT ONCE. ALSO, THE DOUBLE BUFFERING LOGIC
3060 ; OF THE UART WILL BE FULLY TESTED.
3061 ; THIS TEST WILL TRANSFER A MAXIMUM OF 400(8)
3062 ; CHARACTERS THROUGH THE MODULE, BUT IF AN ERROR
3063 ; IS DETECTED BY THE TEST A PREMATURE SHUTDOWN OCCURS.
3064
3065 ;CHANGE PRIORITY
3066 ;...TO 0
3067
3068 007412 012746 000000 MOV #PRO,-(SP) ;PUT NEW PS ON STACK
3069 007416 012746 007424 MOV #64,-(SP) ;PUT NEW PC ON STACK
3070 007422 000002 RTI ;POP NEW PC AND PS
3071 007424 64:
3072 ;GET VECTOR ADDRESS
3073 007424 LET R1 := DLVEC
3074 007424 016701 171626 MOV DLVEC,R1
3075 ;RCVR VECTOR
3076 007430 LET (R1)+ := #REC
3077 007430 012721 007632 MOV #REC,(R1)+
3078 007434 LET (R1)+ := #PR7
3079 007434 012721 000340 MOV #PR7,(R1)+
3080 ;POINT TO TRANSMITTER VECTOR
3081 ;AND SET IT UP ALSO
3082 007440 LET (R1)+ := #TRAN
3083 007440 012721 007570 MOV #TRAN,(R1)+
    
```

MAINDEC 11 DVDVC C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 72
 CVDVCC.P11 12-SEP-84 08:55 T23 FULL DATA TRANSFER WITH INTERRUPTS

```

3084 007444          MOV      @PR7,(R1)          LET (R1) := @PR7
3085 007444 012711 000340
3086
3087          , CLEAR ERROR COUNTER
3088 007450          LET ERRCNT := #0
3089 007450 005067 000106          CLR      ERRCNT
3090
3091 007454          , INITIALIZE COUNTERS
3092 007454 012701 177777          MOV      @-1,R1          LET R1 := @-1
3093
3094 007460          ,RECEIVER STORAGE
3095 007460 005002          CLR      R2          LET R2 := #0
3096
3097 007462          ,# OF RECEIVED CHAR. COUNT.
3098 007462 012704 177777          MOV      @-1,R4          LET R4 := @-1
3099
3100 007466          BRESET ,SET UP ALL REGISTERS
3101 007466 000005          RESET
3102
3103 007470          ,SET UP MAINTENANCE
3104 007470 052777 000004 171566          BIS      @MAINT,@TCSR          LET @TCSR := @TCSR SET.BY @MAINT
3105
3106          ,SET I.E. IN TRANSMITTER
3107 007476          LET @TCSR := @TCSR SET.BY @XMITIE
3108 007476 052777 000100 171560          BIS      @XMITIE,@TCSR
3109
3110 007504          ,AND RECEIVER
3111 007504 052777 000100 171546          BIS      @RCVRIE,@RCSR          LET @RCSR := @RCSR SET.BY @RCVRIE
3112
3113
3114          ,NOW WE WAIT UNTIL R4 COUNT (RECEIVED) IS EQUAL
3115 007512          REPEAT
3116 007512          501674:
3117 007512          UNTIL R4 EQ NUMBER OR ERRCNT GT #0
3118 007512 020467 000046          CMP      R4,NUMBER
3119 007516 001403          BEQ      501704
3120 007520 005767 000036          TST      ERRCNT
3121 007524 003772          BLE      501674
3122 007526          501704:
3123
3124 007526          LET @TCSR := @TCSR CLR.BY @MAINT
3125 007526 042777 000004 171530          BIC      @MAINT,@TCSR
3126
3127 007534          , CHECK FOR DATA COMPARE ERRORS.
3128 007534 005767 000022          IF ERRCNT NE #0 THEN
3129 007540 001401          TST      ERRCNT
3130
3131 007542          ,DATA COMPARE ERROR
3132 007542 104120          ERROR    120          ERRMRD 120,COMP.FIRST
3133 007544          ENDIF
3134 007544          501714:
3135
3136 007544          LET @TCSR := @TCSR CLR.BY @XMITIE
3137 007544 042777 000100 171512          BIC      @XMITIE,@TCSR
3138 007552          LET @RCSR := @RCSR CLR.BY @RCVRIE
3139 007552 042777 000100 171500          BIC      @RCVRIE,@RCSR

```


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 DVDVCC.P11 12-SEP-84 08:55 T23 FULL DATA TRANSFER WITH INTERRUPTS

```

3140 007560                                EXIT ;SKIP OVER SUPPORT ROUTINES & STORAGE
3141 007560 000462                          BR    TST24                ;;;EXIT THIS TEST
3142
3143 007562 000000                          ERRCNT: 0
3144 007564 001000                          NUMBER: 1000
3145 007566 000                                SB: .BYTE 0
3146 007567 000                                WAS: .BYTE 0
3147
3148
3149
3150
3151 007570                                ;*****
3152 007570                                ;TRANSMIT INTERRUPT HANDLER
3153                                BGNSRV TRAN
3154
3155                                ;*****
3156 007570                                ;INCREMENT CHAR COUNT
3157 007570 005201                          INC    R1                LET R1 := R1 + #1
3158
3159 007572                                ;SET UP FOR TRANSFER
3160 007572 010167 000030                    MOV    R1,HOLD          LET HOLD := R1 CLR.BY R3
3161 007576 040367 000024                    BIC    R3,HOLD
3162
3163 007602                                ;AND SEND.
3164 007602 016777 000020 171460            MOV    HOLD,@YBUF      LET @YBUF := HOLD
3165
3166 007610                                ;ALL DONE
3167 007610 020167 177750                    CMP    R1,NUMBER       IF R1 EQ NUMBER THEN
3168 007614 001003                          BNE    50172#
3169
3170 007616                                ;STOP INTERRUPT PROCESSING
3171 007616 042777 000100 171440            BIC    @XMITIE,@TCSR  LET @TCSR := @TCSR CLR.BY @XMITIE
3172 007624                                ENDIF
3173 007624 50172#
3174
3175 007624 000401                          BR    ZZZ                ; EXIT SRV
3176
3177 007626 000000                          HOLD:0
3178
3179 007630  ZZZ:                                ENDSRV
3180 007630 000002                          RTI
3181
3182
3183
3184
3185                                ;*****
3186 007632                                ;RECEIVER INTERRUPT HANDLER
3187 007632                                BGNSRV REC
3188
3189                                ;*****
3190
3191 007632                                ;COUNT THIS CHAR.
3192 007632 005204                          INC    R4                LET R4 := R4 + #1
3193
3194 007634                                ;GET CHAR IN & MASK IT
3195 007634 017702 171422                    MOV    @RBUF,R2       LET R2 := @RBUF CLR.BY R3
    
```

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```

3196 007640 040302          BIC      R3,R2
3197
3198 007642          ;RMLD WILL CONTAIN EXPECTED INPUT
3199 007642 010467 000054    MOV      R4,RMLD
3200 007646 040367 000050    BIC      R3,RMLD
3201
3202          ;DO THEY COMPARE
3203 007652          IF R2 NE RMLD THEN
3204 007652 020267 000044    CMP      R2,RMLD
3205 007656 001412          BEQ      501734
3206
3207 007660          ;FIRST ERROR
3208 007660 005767 177676    TST      ERRCNT
3209 007664 001005          BNE      501744
3210
3211 007666          ;SAVE RECORD OF FIRST MISS
3212 007666 116767 000030 177672  MOVB     RMLD,SB
3213 007674          LET SB :B= RMLD
3214 007674 110267 177667    MOVB     R2,WAS
3215 007700          LET WAS :B= R2
3216 007700          501744:
3217          ;COUNT IT.
3218 007700          LET ERRCNT := ERRCNT + #1
3219 007700 005267 177656    INC      ERRCNT
3220 007704          501734:
3221 007704
3222
3223          ;ALL DONE?
3224 007704          IF R4 EQ NUMBER THEN
3225 007704 020467 177654    CMP      R4,NUMBER
3226 007710 001003          BNE      501754
3227
3228 007712          ;STOP RECEIVER INTERRUPTS
3229 007712 042777 000100 171340  BIC      @RCVRIE,@RCSR
3230
3231          ;INDICATE ALL DONE TO TIMER
3232          ;MAIN REPEAT LOOP IS CHECKING
3233          ;FOR 'R4 = NUMBER' ALSO
3234 007720          501754:
3235
3236 007720 000401          BR       ZZZZ
3237
3238 007722 000000          ; EXIT SRV
3239 007724          RMLD:0
3240 007724          ZZZZ:
3241 007724 000002          ENDSRV
3242          RTI
3243 007726          ENDTST
3244
3245
3246

```

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CVDVCC.P11 12-SEP-84 08:55 T23 FULL DATA TRANSFER WITH INTERRUPTS

```

3247
3248
3249
3250
3251
3252
3253
3254
3255 007726 000004
3256 007730 012767 000010 171222
3257 007736 012767 000024 171234
3258 007744
3259 007744 032767 040000 171246
3260 007752 001404
3261 007754 032767 010000 171236
3262 007762 001004
3263 007764
3264 007764
3265 007764 012767 000001 171166
3266 007772 000500
3267 007774
3268 007774
3269 007774
3270 007774 126727 001125 000001
3271 010002 001004
3272 010004
3273 010004 012767 000001 171146
3274 010012 000470
3275 010014
3276 010014
3277
3278 010014
3279 010014 005067 000152
3280
3281 010020
3282 010020 052777 000004 171236
3283
3284 010026
3285 010026 052777 000001 171230
3286
3287 010034
3288 010034 012777 000252 171226
3289
3290 010042
3291 010042 010546
3292 010044 012745 177777
3293 010050 016745 171204
3294 010054 012745 000200
3295 010060 012745 000500
3296 010064 004767 000230
3297 010070 012605
3298 010072
3299 010072 103001
3300
3301 010074
3302 010074 104115

```

```

;*****
;*****
;*TEST 24 TEST BREAK GENERATION LOGIC
;* TRANSMIT KNOWN CHAR WITH BREAK SET
;* AND COMPARE RECEIVED WITH 0.
;* FRAMING ERROR WILL ALSO BE CHECKED
;* IF ERROR BITS ARE ENABLED.
;*****
TST24: SCOPE
MOV #10,#TIMES ;DO 10 ITERATIONS
MOV #24,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
; IF #MAINTJMP NOTSETIN #USMR OR #BRK NOTSETIN #U
BIT #MAINTJMP,#USMR
BEQ 50176#
BIT #BRK,#USMR
BNE 50177#
50176#:
EXIT TEST
MOV #1,#TIMES
BR TST25 ;;;EXIT THIS TEST
ENDIF
50177#:
IFB CONSOLE EQ #TRUE THEN
CMPB CONSOLE,#TRUE
BNE 50200#
EXIT TEST
MOV #1,#TIMES
BR TS125 ;;;EXIT THIS TEST
ENDIF
50200#:
LET ERRCHK := #0 ; CLEAR ERROR WORD
;SET MAINTENANCE BIT
LET #TCSR := #TCSR SET.BY #MAINT
;SET BREAK BIT
LET #TCSR := #TCSR SET.BY #BREAK
;NON-ZERO CHAR. '*'
LET #TBUF := #252
;WAIT FOR DONE
CALL TIMER IN (<#500,#RCVRDONE,#RCSR,#SET>)
MOV R5, -(SP)
MOV #SET, -(R5)
MOV RCSR, -(R5)
MOV #RCVRDONE, -(R5)
MOV #500, -(R5)
MOV PC, TIMER
MOV (SP), R5
IF .ERROR THEN
; RECIEVER DONE DID NOT SET
ERRHRU 115
ERROR 115

```

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```

3303 010076
3304 010076          502014:
3305
3306 010076          TSTB  BRBUF
3307 010076 105777 171160      BEQ  502024
3308 010102 001404
3309
3310 010104          , BREAK DID NOT EQUAL 0
3311 010104 052767 000001 000060      BIS  #BIT0,ERRCHK
3312 010112
3313 010112 000413          ELSE
3314 010114          502024:      BR  502034
3315 010114
3316 010114 032767 100000 171076      BIT  #ERRBITS,#USMR
3317 010122 001407      BEQ  502044
3318 010124
3319 010124 032777 020000 171130      BIT  #FRERR, BRBUF
3320 010132 001603      BNE  502054
3321 010134
3322 010134 052767 000002 000030      BIS  #BIT1,ERRCHK
3323 010142
3324 010142          502054:
3325 010142          502044:
3326 010142          502034:
3327 010142
3328 010142
3329
3330 010142          BRESET ;CLEAN UP
3331 010142 000005          RESET
3332
3333 010144
3334 010144 032767 000001 000020      BIT  #BIT0,ERRCHK
3335 010152 001401      BEQ  502064
3336 010154
3337 010154 104121          ERROR 121
3338 010156
3339 010156          502064:
3340 010156
3341 010156 032767 000002 000006      BIT  #BIT1,ERRCHK
3342 010164 001401      BEQ  502074
3343 010166
3344 010166 104122          ERROR 122
3345 010170
3346 010170          502074:
3347 010170
3348 010170 000401          BR  TST25
3349 010172 000000      ERRCHK: .WORD 0
3350 010174
3351
  
```

ENDTF

IFB BRBUF NE #0 THEN

LET ERRCHK := ERRCHK SET.BY #BIT0

ELSE

IF #ERRBITS SETIN #USMR THEN

IF #FRERR NOTSETIN BRBUF THEN

LET ERRCHK := ERRCHK SET.BY #BIT1

ENDIF

ENDIF

ENDIF

BRESET ;CLEAN UP

IF #BIT0 SETIN ERRCHK THEN

ERRHRD 121 ;BREAK ERROR

ENDIF

IF #BIT1 SETIN ERRCHK THEN

ERRHRD 122 ;FRAMING ERROR

ENDIF

EXIT

;;;EXIT THIS TEST

ENDTST

MAI,NDDEC-11-DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 77
 CVDVCC.P11 12-SEP-84 08:55 T24 TEST BREAK GENERATION LOGIC

```

3352
3353
3354
3355
3356 010174 000004
3357 010176 012767 000001 170754
3358 010204 104401 010212
3359 010210 000404
3360
3361 010222
3362 010222 016746 171026
3363 010226 104402
3364 010230 104401 010236
3365 010234 000405
3366
3367 010250
3368 010250 016746 171002
3369 010254 104402
3370 010256 104401 010264
3371 010262 000405
3372
3373 010276
3374 010276 016746 170610
3375 010302 104405
3376 010304 005067 170602
3377 010310 104401 001171
3378 010314 000167 171616

;*****
;TEST 25 NOT A TEST - SEND BACK TO LOOP
;*****
TST25: SCOPE
MOV 01,ITIMES ;DO 1 ITERATION
TYPE .65; ;TYPE ASCIZ STRING
BR 64; ;GET OVER THE ASCIZ
;65; .ASCIZ <CRLF>*CSR: *
64;
MOV DLADD,-(SP) ;SAVE DLADD FOR TYPEOUT
TYPOC ;GO TYPE--OCTAL ASCII(ALL DIGITS)
TYPE .67; ;TYPE ASCIZ STRING
BR 66; ;GET OVER THE ASCIZ
;67; .ASCIZ *.VECTOR: *
66;
MOV DLVEC,-(SP) ;SAVE DLVEC FOR TYPEOUT
TYPOC ;GO TYPE--OCTAL ASCII(ALL DIGITS)
TYPE .69; ;TYPE ASCIZ STRING
BR 68; ;GET OVER THE ASCIZ
;69; .ASCIZ *.ERRORS: *
68;
MOV %ERTTL,-(SP) ;SAVE %ERTTL FOR TYPEOUT
TYPOS ;GO TYPE--DECIMAL ASCII WITH SIGN
CLR %ERTTL ; RESET FOR NEXT DEVICE/PASS
TYPE .%CRLF
JMP LOOP ; BACK UP TO THE BEGINNING

```

MAINDEC-11 DVOVC C ACY11 30A(1052) 12-SEP-84 15:41 PAGE 78
 CVOVCC.P11 12-SEP-84 08:55 T25 NOT A TEST - SEND BACK TO LOOP

```

3379
3380          ;:BGMMOD          SUBS
3381          ;:*****
3382 010320    ROUTINE TIMER <HOWLONG,WHICHBIT,REG,SETCLR>
3383 010320    TIMER:
3384          * ROUTINE:TIMER
3385          * THIS ROUTINE IS USED TO TEST THE STATUS OF ANY BIT
3386          * IN ANY REGISTER.
3387          * INPUTS:
3388          * HOWLONG    THE MAXIMUM AMOUNT OF TIME TO SPEND IN
3389          *              THIS ROUTINE.
3390          * WHICHBIT   A MASK WITH THE BIT(S) SET THAT ARE
3391          *              TO BE CHECKED.
3392          * REG        A POINTER TO THE REGISTER TO BE CHECKED
3393          * SETCLR     THE DESIRED RESULTS
3394          *              EITHER #SET OR #CLEAR
3395          * OUTPUT:
3396          * THE 'C' BIT IS SET TO INDICATE AN ERROR
3397          * BUT IT IS TESTED BY THE IF.ERROR STATEMENT
3398          *
3399          *
3400          * NOTE:: THE USE OF (RS) IS PART OF THE LINKAGE
3401          *          MECHANISM BETWEEN THE CALLER AND THE CALLED
3402          *:*****
3403          .ENABL LSB
3404          000001
3405          000000
3406
3407 010320    LET      REGSAV := REG(R5) ; GET POINTER TO REGIST
3408 010320 016567 000004 000136    MOV      REG(R5),REGSAV
3409 010326    LET      TMSAV := HOWLONG(R5) ; SAVE HOWLONG FOR
3410 010326 016567 000000 000132    MOV      HOWLONG(R5),TMSAV
3411 010334    LET      FLAG :B= #FALSE ; INITIALIZE THE EXIT FLA
3412 010334 112767 000000 000126    MOVB    #FALSE,FLAG
3413
3414
3415          ; START OF AN INFINITE LOOP
3416
3417 010342    LOOP
3418 010342    50002:
3419
3420 010342    IF      ; TEST TO SEE IF WHICHBIT IS SET
3421 010342 036577 000002 000114    BIT     WHICHBIT(R5),BREGSAV
3422 010350 001004    BNE     50004:
3423 010352    LET      HOLDSC :B= #CLR
3424 010352 112767 000000 000111    MOVB    #CLR,HOLDSC
3425 010360    ELSE
3426 010360 000403    BR      50005:
3427 010362    50004:
3428 010362    LET      HOLDSC :B= #SET ; REMEMBER THIS
3429 010362 112767 177777 000101    MOVB    #SET,HOLDSC
3430 010370    ENDF
3431 010370    50005:
3432
3433          ; NOW SEE IF THAT WAS WHAT WE WANTED
3434 010370    IFB     HOLDSC EQ SETCLR(R5) THEN
    
```

```

MAINDEC-11-DVDVC-C      MACY11 30A(1052) 12-SEP-84 15:41 PAGE 79
CYDVCC.P11      12-SEP-84 08:55      T25      NOT A TEST - SEND BACK TO LOOP
3435 010370 126765 000075 000006      CMPB  HOLDSC,SETCLR(R5)
3436 010376 001003      BNE   500061
3437      ; JUST THE THING WE NEEDED
3438 010400      LET   FLAG :B= @TRUE
3439 010400 112767 000001 000062      MOVB  @TRUE,FLAG
3440 010406      ENDIF
3441 010406      500061:
3442
3443 010406      EXIFB FLAG EQ @TRUE OR TMSAV LE #0
3444 010406 126727 000056 000001      CMPB  FLAG,@TRUE
3445 010414 001414      BEQ   500031
3446 010416 005767 000044      TST   TMSAV
3447 010422 003411      BLE   500031
3448      ; ONE WAY OR THE OTHER, WE ARE DONE
3449      ; IF WE ARE STILL HERE THEN HANG AROUND A WHILE
3450
3451 010424      WAITMS 1      ;WAIT FOR 1 MILLI-SECONDS
3452 010424 010546      MOV   R5,-(SP)
3453 010426 012745 000001      MOV   @1,-(R5)
3454 010432 004767 000140      JSR   PC,WAIT
3455 010436 012605      MOV   (SP)+,R5
3456 010440      LET   TMSAV := TMSAV - @1 ; COUNTING DOWN
3457 010440 005367 000022      DEC   TMSAV
3458 010444      ENDL00P      ; CONTINUED AT THE TOP
3459 010444 000736      BR    500021
3460 010446      500031:
3461
3462      ; ONLY 2 WAYS TO GET HERE
3463      ; 1). WE RAN OUT OF TIME---ERROR !!
3464      ; 2). THE BIT IS IN THE CORRECT CONDITION--GOOD !!
3465
3466 010446      IFB   FLAG EQ @TRUE THEN
3467 010446 126727 000016 000001      CMPB  FLAG,@TRUE
3468 010454 001001      BNE   500071
3469 010456      RETURN NO.ERROR      ; GOOD
3470 010456 000405      BR    500001
3471 010460      ENDIF
3472 010460      500071:
3473 010460      RETURN ERROR      ; BAD
3474 010460 000261      SEC
3475 010462 000404      BR    500011
3476
3477 010464 000000      REGSAV: .WORD 0
3478 010466 000000      TMSAV: .WORD 0
3479 010470      000      FLAG: .BYTE 0
3480 010471      000      HOLDSC: .BYTE 0
3481      ; WE ARE DONE GO BACK HOME
3482      ENDRTN
3483 010472      500001:
3484 010472 000241      CLC
3485 010474      500011:
3486 010474 000207      RTS   PC
3487      .DSABL LSB

```

MAINDEC-11 DVDVC-C MACY11 30A(1052) 12-SEP 84 15:41 PAGE 80
CVDVCC.P11 12-SEP-84 08:55 T25 NOT A TEST - SEND BACK TO LOOP

```

3488
3489
3490 010476
3491 010476
3492
3493
3494
3495
3496
3497
3498
3499
3500
3501
3502
3503
3504
3505 010476
3506 010476 005065 000000
3507 010502
3508 010502 016767 170512 000062
3509 010510 016746 000056
3510 010514 042716 000017
3511 010520 042667 000046
3512
3513 010524
3514 010524 012767 000001 170540
3515 010532 000402
3516 010534
3517 010534 005267 170532
3518 010540
3519 010540 026767 170526 000024
3520 010546 003006
3521 010550
3522 010550 006365 000000
3523 010554
3524 010554 052765 000001 000000
3525 010562
3526 010562 000764
3527 010564
3528 010564
3529 010564 005165 000000
3530 010570
3531 010570 000401
3532 010572 000000
3533 010574
3534 010574
3535 010574
3536 010574 000207
3537

```

```

;*****
ROUTINE DATLNG <MASK>
DATLNG:
; ROUTINE:DATLNG
; THIS ROUTINE SETS UP A MASK FOR DATA, WITH
; INPUT - NOTHING IS PASSED TO THIS ROUTINE
; BUT GLOBAL INFORMATION IS ASSUMED TO EXIST:
; $USMR-- THE WORD FOR SOFTWARE PARAMETERS
; DATA-- A MASK FOR THE LOCATION OF THE OCTAL
; NUMBER OF DATA BITS
; OUTPUT----
; MASK-- A MASK OF BINARY ONES RIGHT-JUSTIFIED
; THE NUMBER OF WHICH IS DEFINED IN $USMR WORD.
;*****
.ENABL LSB
          CLR      MASK(R5)
          LET MASK(R5) := #0 ; START
          LET NUMBR := $USMR AND #DATA
          MOV      $USMR,NUMBR
          MOV      NUMBR,-(SP)
          BIC     #DATA,(SP)
          BIC     (SP),NUMBR
          INCR I FROM #1 TO NUMBR BY #1
          MOV      #1,I
          BR      50002#
          50003#: INC      I
          50002#: CMP      I,NUMBR
          BGT     50004#
          LET MASK(R5) := MASK(R5) SHIFT 1
          LET MASK(R5) := MASK(R5) SET.BY #1
          ENDINC
          BR      50003#
          50004#:
          LET MASK(R5) := COMP MASK(R5)
          RETURN
          BR      50000#
          NUMBR:0
          50000#:
          50001#:
          50000#:
          RTS     PC
          .DSABL LSB

```



```

3538
3539
3540 010576
3541 010576
3542
3543
3544
3545
3546
3547
3548
3549 010576 010146
3550 010600 010246
3551 010602 010346
3552 010604
3553 010604 016501 000000
3554 010610
3555 010610 012702 000001
3556 010614 000402
3557 010616
3558 010616 062702 000001
3559 010622
3560 010622 020201
3561 010624 101010
3562 010626
3563 010626 005003
3564 010630 000401
3565 010632
3566 010632 005203
3567 010634
3568 010634 020327 000100
3569 010640 003001
3570 010642
3571 010642 000773
3572 010644
3573 010644
3574 010644 000764
3575 010646
3576 010646 012603
3577 010650 012602
3578 010652 012601
3579 010654
3580 010654
3581 010654
3582 010654 000207

```

```

;*****
ROUTINE WAIT <TIME>
WAIT:
; ROUTINE:WAIT
; THIS ROUTINE IS USED TO DELAY EXECUTION OF THE
; MAIN PROGRAM FOR A SPECIFIED AMOUNT OF TIME.
; THIS IS ACCOMPLISHED BY INCREMENTING A
; REGISTER UP TO A LIMIT. THE INNER LOOP IS SET
; TO APPROXIMATE 1 MILLI SEC.
;*****
MOV R1,-(SP) ;PUSH R1 ON STACK
MOV R2,-(SP) ;PUSH R2 ON STACK
MOV R3,-(SP) ;PUSH R3 ON STACK
LET R1 := TIME(R5)
MOV TIME(R5),R1
INCRU R2 FROM #1 TO R1 BY #1
MOV #1,R2
BR 50002:
50003: ADD #01,R2
50002: CMP R2,R1
BHI 50004:
INCR R3 FROM #0 TO #100 BY #1
CLR R3
BR 50005:
50006: INC R3
50005: CMP R3,#100
BGT 50007:
ENDINC
BR 50006:
50007: ENDINC
BR 50003:
50004: MOV (SP),R3 ;POP STACK INTO R3
MOV (SP),R2 ;POP STACK INTO R2
MOV (SP),R1 ;POP STACK INTO R1
ENDRTN
50000:
50001:
RTS PC

```

MAINDEC-11 DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 82
CVDVCC.P11 12-SEP-84 08:55 T25 NOT A TEST - SEND BACK TO LOOP

```

3583
3584
3585
3586 010656
3587
3588
3589
3590
3591
3592
3593
3594
3595 010656
3596 010656 005267 000002
3597 010662
3598 010662 000002
3599 010664 000000

.SBTTL INTSRV INTERRUPT SERVICE ROUTINE
;*****
INTSRV:
; * SERVICE ROUTINE; INTSRV
; * THIS GLOBAL ROUTINE DOES NOTHING BUT INCREMENT
; * 'INTFLAG' EACH TIME IT IS CALLED. IT ASSUMES
; * THAT THE MAIN CALLING ROUTINE WILL KNOW WHAT
; * TO LOOK FOR.
;*****
;ADD 1 TO 'INTERRUPT OCCURED' FLAG
LET INTFLAG := INTFLAG + #1
INC INTFLAG
ENDSRV
RTI
;THAT'S ALL
INTFLAG: 0

```

MAINDEC-11-DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 83
 CVDVCC.P11 12-SEP-84 08:55 INTSRV INTERRUPT SERVICE ROUTINE

```

3600 010666 ROUTINE CYCLE
3601 010666 CYCLE:
3602 ;*****
3603 ;* ROUTINE: CYCLE
3604 ;* THIS ROUTINE CAUSES ADRS TO POINT TO THE
3605 ;* ADDRESS OF DLV11-F UNDER TEST, ADRS +2 TO
3606 ;* POINT TO THE VECTOR OF THE DLV11-F UNDER TEST.
3607 ;* IT KEEPS TRACK OF THE CURRENT DEVICE AND BIT
3608 ;* MASKS. THE CONSOLE IS TREATED SPECIAL BY THIS ROUTINE.
3609 ;* IT IS ONLY TESTED ONCE IF UNDER APT. IF NOT UNDER APT
3610 ;* ALL TESTS THAT REQUIRE THE MAINT BIT ARE NOT RUN.
3611 ;*****
3612 .ENABL LSB
3613 010666 LET APTCON :B= #FALSE ; SET DEFAULT VALUE
3614 010666 112767 000000 000230 MOVB #FALSE,APTCON
3615 010674 LET CONSOLE :B= #FALSE
3616 010674 112767 000000 000223 MOVB #FALSE,CONSOLE
3617 010702 REPEAT ; UNTIL BITMASK SETIN #DEVN
3618 010702 500024: IF BITMASK EQ #0 THEN
3619 010702 TST BITMASK
3620 010702 005767 000200 BNE 500034
3621 010706 001027 IF INITFLAG EQ #1 THEN
3622 010710 CMP INITFLAG,#1
3623 010710 026727 000174 000001 BNE 500044
3624 010716 001003 LET INITFLAG := #0
3625 010720 CLR INITFLAG
3626 010720 005067 000164 ELSE
3627 010724 BR 500054
3628 010724 000403 500044: CALL #EOP ; AS A SUBROUTINE
3629 010726 JSR PC,#EOP
3630 010726 004767 000370 SPECIALADDRESS: ; BECAUSE #EOP RETURNS AS A JUMP
3631 010726 004767 000370 MOV (SP),R0 LET R0 := POP
3632 010732
3633 010732 012600 ENDIF
3634 010732 500054: LET BITMASK := #1
3635 010732 012767 000001 000144 MOV #1,BITMASK
3636 010734 LET #DEVCT := #1
3637 010734 012767 000001 170234 MOV #1,#DEVCT
3638 010734 LET ADDRESS := #BASE
3639 010734 012767 000001 170234 MOV #BASE,ADDRESS
3640 010742 LET VECTOR := #VECT1
3641 010742 012767 000001 170234 MOV #VECT1,VECTOR
3642 010750 ELSE
3643 010750 016767 170274 000134 BR 500064
3644 010756 500064: LET R4 := #10
3645 010756 016767 170262 000130 MOV #10,R4
3646 010764 LET BITMASK := BITMASK ROTATE 1
3647 010764 000410 ROL BITMASK
3648 010766 LET ADDRESS := ADDRESS + R4
3649 010766 012704 000010 ADD R4,ADDRESS
3650 010766 012704 000010 LET VECTOR := VECTOR + R4
3651 010772
3652 010772 006167 000110
3653 010776
3654 010776 060467 000110
3655 011002

```

MAINDEC-11 DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 84
 DVDVCC.P11 12-SEP-84 08:55 INTSRV INTERRUPT SERVICE ROUTINE

```

3656 011002 060467 000106          ADD      R4,VECTOR
3657 011006                      ENDIF
3658 011006          500061:
3659 011006                      UNTIL  BITMASK SETIN #DEVH
3660 011006 036767 000074 170236    BIT      BITMASK,#DEVH
3661 011014 001732                    BEQ      500021
3662 011016                      IF BITMASK EQ #BIT15 THEN
3663 011016 026727 000064 100000    CMP      BITMASK,#BIT15
3664 011024 001023                    BNE      500071
3665 011026                      LET CONSOLE :B= #TRUE
3666 011026 112767 000001 000071    MOVB     #TRUE,CONSOLE
3667 011034                      LET ADDRESS := CONADR
3668 011034 016767 000060 000050    MOV      CONADR,ADDRESS
3669 011042                      LET VECTOR := CONVECT
3670 011042 016767 000054 000044    MOV      CONVECT,VECTOR
3671                      ;;;;
3672                      ;;;;
3673                      ;;;;
3674 011050                      IF #CONMAINT NOTSETIN #USMR THEN
3675 011050 032767 000001 170136    BIT      #APTENV,#ENV
3676 011056 001406                    BEQ      500101
3677 011060                      IF #PASS NE #0 THEN ; NOT FIRST PASS
3678 011060 005767 170116    TST      #PASS
3679 011064 001403                    BEQ      500111
3680                      ; DEFINE DEVICE AS APT CONSOLE
3681 011066                      LET APTCON :B= #TRUE
3682 011066 112767 000001 000030    MOVB     #TRUE,APTCON
3683 011074                      ENDIF ; FIRST PASS
3684 011074          500111:
3685 011074                      ENDIF ; APT
3686 011074          500101:
3687 011074                      ENDIF ; BITMASK
3688 011074          500071:
3689
3690 011074                      LET ADRS := #ADDRESS
3691 011074 012701 011112    MOV      #ADDRESS,ADRS
3692 011100                      LET #DEVCT := #DEVCT + #1
3693 011100 005267 170100    INC      #DEVCT
3694 011104                      RETURN
3695 011104 000411                    BR      500001
3696 011106 100000          BITMASK: 100000 ; CONSOLE FIRST
3697 011110 000001          INITFLAG: 1
3698 011112 000000          ADDRESS: 0
3699 011114 000000          VECTOR: 0
3700 011116 000000          OK: 0
3701 011120 177560          CONADR: 177560 ; CONSOLE ADDRESS
3702 011122 000060          CONVECT: 60 ; CONSOLE VECTOR

```

MAINDEC-11 DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 85
CVDVCC.P11 12-SEP-84 08:55 INTSRV INTERRUPT SERVICE ROUTINE

3703 011124 000
3704 011125 000
3705 011126 000
3706 011130
3707
3708 011130
3709 011130
3710 011130
3711 011130 000207
3712
3713

APTCOM: .BYTE 0
CONSOLE: .BYTE 0
NOCONPANT: .BYTE 0
.EVEN

ENDRTN

500004:
500014:
RTS PC
.DSABL LSB

MAINDEC-11-DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 86
 CVDVCC.P11 12-SEP-84 08:55 INTSRV INTERRUPT SERVICE ROUTINE

```

3714
3715 011132          ROUTINE MYTYPE
3716 011132          MYTYPE:
3717                ;*****
3718 011132 104401 011140          TYPE      ,65;          ;TYPE ASCIZ STRING
3719 011136 000405          BR          64;          ;GET OVER THE ASCIZ
3720                ;65; .ASCIZ <CRLF>*TEST *
3721 011152 64;
3722 011152 016746 170022          MOV      #TESTN,-(SP)      ;SAVE #TESTN FOR TYPEOUT
3723 011156 104402          TYPOC          ;GO TYPE--OCTAL ASCII(ALL DIGITS)
3724 011160 104401 011166          TYPE      ,67;          ;TYPE ASCIZ STRING
3725 011164 000405          BR          66;          ;GET OVER THE ASCIZ
3726                ;67; .ASCIZ *,ERROR *
3727 011200 66;
3728 011200 116767 167710 167770          MOV     #ITEMB,#FATAL      ;APT FATAL ERROR NUMBER
3729 011206 016746 167764          MOV     #FATAL,-(SP)      ;SAVE #FATAL FOR TYPEOUT
3730 011212 104403          TYPOS          ;GO TYPE--OCTAL ASCII
3731 011214 006          .BYTE      6          ;TYPE 6 DIGITS
3732 011215 000          .BYTE      0          ;SUPPRESS LEADING ZEROS
3733 011216 104401 011224          TYPE      ,69;          ;TYPE ASCIZ STRING
3734 011222 000404          BR          68;          ;GET OVER THE ASCIZ
3735                ;69; .ASCIZ *,PC *
3736 011234 68;
3737 011234 016746 167656          MOV     #ERRPC,-(SP)      ;SAVE #ERRPC FOR TYPEOUT
3738 011240 104402          TYPOC          ;GO TYPE--OCTAL ASCII(ALL DIGITS)
3739 011242 104401 011250          TYPE      ,71;          ;TYPE ASCIZ STRING
3740 011246 000404          BR          70;          ;GET OVER THE ASCIZ
3741                ;71; .ASCIZ *,CSR *
3742 011260 70;
3743 011260 016746 167770          MOV     DLADD,-(SP)      ;SAVE DLADD FOR TYPEOUT
3744 011264 104402          TYPOC          ;GO TYPE--OCTAL ASCII(ALL DIGITS)
3745 011266 104401 011274          TYPE      ,73;          ;TYPE ASCIZ STRING
3746 011272 000405          BR          72;          ;GET OVER THE ASCIZ
3747                ;73; .ASCIZ *,VECTOR *
3748 011306 72;
3749 011306 016746 167744          MOV     DLVEC,-(SP)      ;SAVE DLVEC FOR TYPEOUT
3750 011312 104402          TYPOC          ;GO TYPE--OCTAL ASCII(ALL DIGITS)
3751 011314 104401 001171          TYPE      ,CRLF
3752 011320          ENDRTN
3753 011320          50000;
3754 011320          50001;
3755 011320 000207          RTS      PC
    
```

```

3756          .SBTTL  END OF PASS ROUTINE
3757
3758          ;*****
3759          ;*INCREMENT THE PASS NUMBER (#PASS)
3760          ;*INDICATE END-OF-PROGRAM AFTER 1 PASSES THRU THE PROGRAM
3761          ;*TYPE "END PASS #XXXXX" (WHERE XXXXX IS A DECIMAL NUMBER)
3762          ;*IF THERES A MONITOR GO TO IT
3763          ;*IF THERE ISN'T JUMP TO SPECIALADDRESS
3764
3765          #EUP:
3766          011322 000004          SCOPE
3767          011324 005067 167552  CLR          #TSTNM          ;;ZERO THE TEST NUMBER
3768          011330 005067 167624  CLR          #TIMES          ;;ZERO THE NUMBER OF ITERATIONS
3769          011334 005267 167642  INC          #PASS          ;;INCREMENT THE PASS NUMBER
3770          011340 042767 100000 167634 BIC          #100000,#PASS  ;;DON'T ALLOW A NEG. NUMBER
3771          011346 005327          DEC          (PC)+          ;;LOOP?
3772          011350 000001          #EOPCT: .WORD 1
3773          011352 003022          BGT          #DOAGN          ;;YES
3774          011354 012737          MOV          (PC)+,#(PC)+  ;;RESTORE COUNTER
3775          011356 000001          #ENDCT: .WORD 1
3776          011360 011350          #EOPCT
3777          011362 104401 011427  TYPE          #,#ENDMG          ;;TYPE "END PASS #"
3778          011366 016746 167610  MOV          #PASS,-(SP)      ;;SAVE #PASS FOR TYPEOUT
3779          011372 104405          TYPDS          ;;GO TYPE--DECIMAL ASCII WITH SIGN
3780          011374 104401 011424  TYPE          #,#ENULL          ;;TYPE A NULL CHARACTER
3781          011400 013700 000042  #GET42: MOV          #M42,R0      ;;GET MONITOR ADDRESS
3782          011404 001405          BEQ          #DOAGN          ;;BRANCH IF NO MONITOR
3783          011406 000005          RESET          ;;CLEAR THE WORLD
3784          011410 004710          #ENDAD: JSR          PC,(R0)    ;;GO TO MONITOR
3785          011412 000240          NOP          ;;SAVE ROOM
3786          011414 000240          NOP          ;;FOR
3787          011416 000240          NOP          ;;ACT11
3788          011420          #DOAGN:
3789          011420 000137          JMP          #(PC)+          ;;RETURN
3790          011422 010732          #RTNAD: .WORD  SPECIALADDRESS
3791          011424          377          377          000  #ENULL: .BYTE  -1,-1,0          ;;NULL CHARACTER STRING
3792          011427          015 042412 042116  #ENDMG: .ASCIZ  <15><12>/END PASS #/
3793          011434 050040 051501 020123
3794          011442 000043
  
```

MAINDEC-11-DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 88
CVDVCC.P11 12-SEP-84 08:55 POWER DOWN AND UP ROUTINES

.SBTTL POWER DOWN AND UP ROUTINES

3795
3796
3797
3798
3799
3800
3801
3802
3803
3804
3805
3806
3807
3808
3809
3810
3811
3812
3813
3814
3815
3816
3817
3818
3819
3820
3821
3822
3823
3824
3825
3826
3827
3828
3829
3830
3831
3832
3833
3834
3835
3836
3837
3838
3839

011444 012737 011610 000024
011452 012737 000340 000026
011460 010046
011462 010146
011464 010246
011466 010346
011470 010446
011472 010546
011474 017746 167440
011500 010667 000110
011504 012737 011516 000024
011512 000000
011514 000776

011516 012737 011610 000024
011524 016706 000064
011530 005067 000060
011534 005267 000054
011540 001375
011542 012677 167372
011546 012605
011550 012604
011552 012603
011554 012602
011556 012601
011560 012600
011562 012737 011444 000024
011370 012737 000340 000026
011576 104401
011600 011616
011602 012716
011604 001336
011606 000002
011610 000000
011612 000776
011614 000000
011616 005015 047520 042527
011624 C00122

;;*****

POWER DOWN ROUTINE

```
#PWRDN: MOV @#ILLUP, @#PWRVEC ;;SET FOR FAST UP
MOV @340, @#PWRVEC+2 ;;PRIO:7
MOV R0, -(SP) ;;PUSH R0 ON STACK
MOV R1, -(SP) ;;PUSH R1 ON STACK
MOV R2, -(SP) ;;PUSH R2 ON STACK
MOV R3, -(SP) ;;PUSH R3 ON STACK
MOV R4, -(SP) ;;PUSH R4 ON STACK
MOV R5, -(SP) ;;PUSH R5 ON STACK
MOV BSMR, -(SP) ;;PUSH BSMR ON STACK
MOV SP, #SAVR6 ;;SAVE SP
MOV @#PWRUP, @#PWRVEC ;;SET UP VECTOR
HALT
BR -2 ;;HANG UP
```

;;*****

POWER UP ROUTINE

```
#PWRUP: MOV @#ILLUP, @#PWRVEC ;;SET FOR FAST DOWN
MOV #SAVR6, SP ;;GET SP
CLR #SAVR6 ;;WAIT LOOP FOR THE TTY
11: INC #SAVR6 ;;WAIT FOR THE INC
BNE 11 ;;OF WORD
MOV (SP)+, BSMR ;;POP STACK INTO BSMR
MOV (SP)+, R5 ;;POP STACK INTO R5
MOV (SP)+, R4 ;;POP STACK INTO R4
MOV (SP)+, R3 ;;POP STACK INTO R3
MOV (SP)+, R2 ;;POP STACK INTO R2
MOV (SP)+, R1 ;;POP STACK INTO R1
MOV (SP)+, R0 ;;POP STACK INTO R0
MOV @#PWRDN, @#PWRVEC ;;SET UP THE POWER DOWN VECTOR
MOV @340, @#PWRVEC+2 ;;PRIO:7
TYPE #POWER ;;REPORT THE POWER FAILURE
#PWRMG: .WORD #POWER ;;POWER FAIL MESSAGE POINTER
MOV (PC)+, (SP) ;;RESTART AT START
#PWRAD: .WORD START ;;RESTART ADDRESS
RTI
```

```
#ILLUP: HALT ;;THE POWER UP SEQUENCE WAS STARTED
BR -2 ;;BEFORE THE POWER DOWN WAS COMPLETE
#SAVR6: 0 ;;PUT THE SP HERE
#POWER: .ASCIZ <15><12>"POWER"
```

.EVEN


```
.SBTTL TYPE ROUTINE

;*****
;ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE
;THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
;NOTE1:      #NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
;NOTE2:      #FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
;NOTES:      #FILLC CONTAINS THE CHARACTER TO FILL AFTER.
;
;CALL:
;1) USING A TRAP INSTRUCTION
;# TYPE      #MESADR      ;MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
;OR
;# TYPE      #MESADR
;
3857 011626 105767 167325 #TYPE: TSTB #TPFLG      ;IS THERE A TERMINAL?
3858 011632 100002      BPL 1#              ;BR IF YES
3859 011634 000000      HALT              ;HALT HERE IF NO TERMINAL
3860 011636 000430      BR 3#              ;LEAVE
3861 011640 010046      1#: MOV RO,-(SP)      ;SAVE RO
3862 011642 017600 000002 MOV #2(SP),RO      ;GET ADDRESS OF ASCIZ STRING
3863 011646 122767 000001 167340 CMPB #APTENV,#ENV  ;RUNNING IN APT MODE
3864 011654 001011      BNE 62#           ;NO,GO CHECK FOR APT CONSOLE
3865 011656 132767 000100 167331 BITB #APTSPOOL,#ENVM ;SPOOL MESSAGE TO APT
3866 011664 001405      BEQ 62#           ;NO,GO CHECK FOR CONSOLE
3867 011666 010067 000004 MOV RO,61#         ;SETUP MESSAGE ADDRESS FOR APT
3868 011672 004767 001046 JSR PC,#ATY3      ;SPOOL MESSAGE TO APT
3869 011676 000000      61#: .WORD 0      ;MESSAGE ADDRESS
3870 011700 132767 000040 167307 62#: BITB #APTCSUP,#ENVM ;APT CONSOLE SUPPRESSED
3871 011706 001003      BNE 60#           ;YES,SKIP TYPE OUT
3872 011710 112046      2#: MOVB (RO),-(SP)  ;PUSH CHARACTER TO BE TYPED ONTO STACK
3873 011712 001005      BNE 4#              ;BR IF IT ISN'T THE TERMINATOR
3874 011714 005726      TST (SP),#        ;IF TERMINATOR POP IT OFF THE STACK
3875 011716 012600      60#: MOV (SP),RO      ;RESTORE RO
3876 011720 062716 000002 3#: ADD #2,(SP)      ;ADJUST RETURN PC
3877 011724 000002      RTI              ;RETURN
3878 011726 122716 000011 4#: CMPB #HT,(SP)    ;BRANCH IF <HT>
3879 011732 001430      BEQ 8#              ;
3880 011734 122716 000200 6#: CMPB #CRLF,(SP)  ;BRANCH IF NOT <CRLF>
3881 011740 001006      BNE 5#              ;
3882 011742 005726      TST (SP),#        ;POP <CR><LF> EQUIV
3883 011744 104401      TYPE              ;TYPE A CR AND LF
3884 011746 001171      #CRLF
3885 011750 105067 000202 CLRB #CHARCNT     ;CLEAR CHARACTER COUNT
3886 011754 000755      BR 2#              ;GET NEXT CHARACTER
3887 011756 004767 000056 5#: JSR PC,#TYPEC     ;GO TYPE THIS CHARACTER
3888 011762 126726 167170 6#: CMPB #FILLC,(SP) ;IS IT TIME FOR FILLER CHARS?
3889 011766 001350      BNE 2#              ;IF NO GO GET NEXT CHAR.
3890 011770 016746 67160 MOV #NULL,-(SP)   ;GET # OF FILLER CHARS. NEEDED
3891                                ;AND THE NULL CHAR.
3892 011774 105366 000001 7#: DECB 1(SP)        ;DOES A NULL NEED TO BE TYPED?
3893 012000 002770      BLT 6#              ;BR IF NO--GO POP THE NULL OFF OF STACK
3894 012002 004767 000032 JSR PC,#TYPEC     ;GO TYPE A NULL
3895 012006 105367 000144 DECB #CHARCNT     ;DO NOT COUNT AS A COUNT
```

L7

```

3896 012012 000770          BR      74          ;;LOOP
3897
3898          ;HORIZONTAL TAB PROCESSOR
3899
3900 012014 112716 000040      84:     MOVB     #' ,(SP)          ;;REPLACE TAB WITH SPACE
3901 012020 004767 000014      94:     JSR      PC,#TYPEC          ;;TYPE A SPACE
3902 012024 132767 000007 000124      BITB     @7,#CHARCNT          ;;BRANCH IF NOT AT
3903 012032 001372              BNE      94                    ;;TAB STOP
3904 012034 005726              TST      (SP)+                ;;POP SPACE OFF STACK
3905 012036 000724              BR       24                    ;;GET NEXT CHARACTER
3906 012040
3907 012040 105777 167100      #TYPEC: TSTB     @TKS              ;;CHAR IN KYBD BUFFER?
3908 012044 100022              BPL      104                  ;;BR IF NOT
3909 012046 017746 167074      MOV      @TKB,-(SP)          ;;GET CHAR
3910 012052 042716 177600      BIC      @177600,(SP)       ;;STRIP EXTRANEIOUS BITS
3911 012056 122716 000023      CMPB     @XOFF,(SP)         ;;WAS CHAR XOFF
3912 012062 001012              BNE      1024                ;;BR IF NOT
3913 012064
3914 012064 105777 167054      1014:  TSTB     @TKS              ;;WAIT FOR CHAR
3915 012070 100375              BPL      1014                ;;BR IF NOT
3916 012072 117716 167050      MOVB     @TKB,(SP)          ;;GET CHAR
3917 012076 042716 177600      BIC      @177600,(SP)       ;;STRIP IT
3918 012102 122716 000021      CMPB     @XON,(SP)          ;;WAS IT XON?
3919 012106 001366              BNE      1014                ;;BR IF NOT
3920 012110
3921 012110 005726              1024: TST      (SP)+                ;;FIX STACK
3922 012112
3923 012112 105777 167032      104:  TSTB     @TPS              ;;WAIT UNTIL PRINTER IS READY
3924 012116 100375              BPL      104                  ;;BR IF NOT
3925 012120 116677 000002 167024      MOVB     2(SP),@TPB          ;;LOAD CHAR TO BE TYPED INTO DATA REG.
3926 012126 122766 000015 000002      CMPB     @CR,2(SP)          ;;IS CHARACTER A CARRIAGE RETURN?
3927 012134 001003              BNE      14                    ;;BRANCH IF NO
3928 012136 105067 000014      CLRB     #CHARCNT          ;;YES--CLEAR CHARACTER COUNT
3929 012142 000406              BR       #TYPEX              ;;EXIT
3930 012144 122766 000012 000002      14:   CMPB     @LF,2(SP)          ;;IS CHARACTER A LINE FEED?
3931 012152 001402              BEQ      #TYPEX              ;;BRANCH IF YES
3932 012154 105227              INCB     (PC)+                ;;COUNT THE CHARACTER
3933 012156 000000      #CHARCNT: .WORD 0          ;;CHARACTER COUNT STORAGE
3934 012160 000207      #TYPEX: RTS      PC
3935

```

MAINDEC-11-DVDC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 91
 CYDVCC.P11 12-SEP-84 08:55 TTY INPUT ROUTINE

```

3936 .SBTTL TTY INPUT ROUTINE
3937
3938 ;|*****
3939 .ENABL LSB
3940
3941 ;|*****
3942 ;|SOFTWARE SWITCH REGISTER CHANGE ROUTINE.
3943 ;|ROUTINE IS ENTERED FROM THE TRAP HANDLER, AND WILL
3944 ;|SERVICE THE TEST FOR CHANGE IN SOFTWARE SWITCH REGISTER TRAP CALL
3945 ;|WHEN OPERATING IN TTY FLAG MODE.
3946 012162 022767 000176 166750 ;CKSMR: CMP @SMREG,SMR ;|IS THE SOFT-SMR SELECTED?
3947 012170 001074 ;|BNE 15; ;|BRANCH IF NO
3948 012172 105777 166746 ;|TSTB @TKS ;|CHAR THERE?
3949 012176 100071 ;|BPL 15; ;|IF NO, DON'T WAIT AROUND
3950 012200 117746 166742 ;|MOVB @TKB,-(SP) ;|SAVE THE CHAR
3951 012204 042716 177600 ;|BIC @C177,(SP) ;|STRIP-OFF THE ASCII
3952 012210 022726 000007 ;|CMP @7,(SP); ;|IS IT A CONTROL G?
3953 012214 001062 ;|BNE 15; ;|NO, RETURN TO USER
3954 012216 126727 166712 000001 ;|CMPB @AUTOB,#1 ;|ARE WE RUNNING IN AUTO-MODE?
3955 012224 001456 ;|BEQ 15; ;|BRANCH IF YES
3956
3957 012226 104401 012707 ;|GTSMR: TYPE ;|CNTLG ;|ECHO THE CONTROL-G (+G)
3958 012232 104401 012714 ;|TYPE ;|MSMR ;|TYPE CURRENT CONTENTS
3959 012236 016746 165734 ;|MOV SMREG,-(SP) ;|SAVE SMREG FOR TYPEOUT
3960 012242 104402 ;|TYPC ;|GO TYPE--OCTAL ASCII(ALL DIGITS)
3961 012244 104401 012725 ;|TYPE ;|MNEW ;|PROMPT FOR NEW SMR
3962 012250 005046 ;|CLR -(SP) ;|CLEAR COUNTER
3963 012252 005046 ;|CLR -(SP) ;|THE NEW SMR
3964 012254 105777 166664 ;|TSTB @TKS ;|CHAR THERE?
3965 012260 100375 ;|BPL 7; ;|IF NOT TRY AGAIN
3966
3967 012262 117746 166660 ;|MOVB @TKB,-(SP) ;|PICK UP CHAR
3968 012266 042716 177600 ;|BIC @C177,(SP) ;|MAKE IT 7-BIT ASCII
3969
3970
3971
3972 012272 021627 000025 ;|9;: CMP (SP),#25 ;|IS IT A CONTROL-U?
3973 012276 001005 ;|BNE 10; ;|BRANCH IF NOT
3974 012300 104401 012702 ;|TYPE ;|CNTLU ;|YES, ECHO CONTROL-U (+U)
3975 012304 062716 000006 ;|20;: ADD @6,SP ;|IGNORE PREVIOUS INPUT
3976 012310 000757 ;|BR 19; ;|LET'S TRY IT AGAIN
3977
3978
3979 012312 021627 000015 ;|10;: CMP (SP),#15 ;|IS IT A <CR>?
3980 012316 001022 ;|BNE 16; ;|BRANCH IF NO
3981 012320 005766 000004 ;|TST 4(SP) ;|YES, IS IT THE FIRST CHAR?
3982 012324 001403 ;|BEQ 11; ;|BRANCH IF YES
3983 012326 016677 000002 166604 ;|MOV 2(SP),@SMR ;|SAVE NEW SMR
3984 012334 062706 000006 ;|11;: ADD @6,SP ;|CLEAR UP STACK
3985 012340 104401 001171 ;|14;: TYPE ;|CRLF ;|ECHO <CR> AND <LF>
3986 012344 126727 166565 000001 ;|CMPB @INTAG,#1 ;|RE-ENABLE TTY KBD INTERRUPTS?
3987 012352 001003 ;|BNE 15; ;|BRANCH IF NOT
3988 012354 012777 000100 166562 ;|MOV @100,@TKS ;|RE-ENABLE TTY KBD INTERRUPTS
3989 012362 000002 ;|15;: RTI ;|RETURN
3990 012364 004767 177450 ;|16;: JSR PC,@TYPEC ;|ECHO CHAR
3991 012370 021627 000060 ;|CMP (SP),#60 ;|CHAR < 0?

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MAINDEC-11 DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 92
CVDVCC.P11 12-SEP 84 08:55 TTY INPUT ROUTINE

3992 012374 002420
3993 012376 021627 000067
3994 012402 003015
3995 012404 042726 000040
3996 012410 005766 000002
3997 012414 001403
3998 012416 006316
3999 012420 006316
4000 012422 006316
4001 012424 005266 000002
4002 012430 056616 177776
4003 012434 000707
4004 012436 104401 001170
4005 012442 000720
4006
4007
4008
4009
4010
4011
4012
4013
4014
4015
4016
4017 012444 011646
4018 012446 016666 000004 000002
4019 012454 105777 166464
4020 012460 100375
4021 012462 117766 166460 000004
4022 012470 042766 177600 0000C1
4023 012476 026627 000004 000023
4024 012504 001013
4025 012506 105777 166432
4026 012512 100375
4027 012514 117746 166426
4028 012520 042716 177600
4029 012524 022627 000021
4030 012530 001366
4031 012532 000750
4032 012534 026627 000004 000140
4033 012542 002407
4034 012544 026627 000004 000175
4035 012552 003003
4036 012554 042766 000040 000004
4037 012562 000002
4038
4039
4040
4041
4042
4043
4044
4045 012564 010346
4046 012566 012703 012672
4047 012572 022703 012702

BLT 18#
CMP (SP),#67
BGT 18#
BIC #60,(SP)
TST 2(SP)
BEQ 17#
ASL (SP)
ASL (SP)
ASL (SP)
17# : INC 2(SP)
BIS -2(SP),(SP)
BR 7#
18# : TYPE ,#QUES
BR 20#
.DSABL LSB

;; BRANCH IF YES
;; CHAR > 7?
;; BRANCH IF YES
;; STRIP-OFF ASCII
;; IS THIS THE FIRST CHAR
;; BRANCH IF YES
;; NO, SHIFT PRESENT
;; CHAR OVER TO MAKE
;; ROOM FOR NEW ONE.
;; KEEP COUNT OF CHAR
;; SET IN NEW CHAR
;; GET THE NEXT ONE
;; TYPE ?<CR><LF>
;; SIMULATE CONTROL-U

;; *****
;; THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY

;; CALL:
;; * RDCHR
;; * RETURN HERE
;; * INPUT A SINGLE CHARACTER FROM THE TTY
;; * CHARACTER IS ON THE STACK
;; * WITH PARITY BIT STRIPPED OFF
;; *

RDCHR: MOV (SP),-(SP) ;; PUSH DOWN THE PC
MOV 4(SP),2(SP) ;; SAVE THE PS
1# : TSTB #0TKS ;; WAIT FOR
BPL 1# ;; A CHARACTER
MOVB #0TKB,4(SP) ;; READ THE TTY
BIC #+C<177>,4(SP) ;; GET RID OF JUNK IF ANY
CMP 4(SP),#23 ;; IS IT A CONTROL-S?
BNE 3# ;; BRANCH IF NO
2# : TSTB #0TKS ;; WAIT FOR A CHARACTER
BPL 2# ;; LOOP UNTIL ITS THERE
MOVB #0TKB,-(SP) ;; GET CHARACTER
BIC #+C177,(SP) ;; MAKE IT 7-BIT ASCII
CMP (SP),#21 ;; IS IT A CONTROL-Q?
BNE 2# ;; IF NOT DISCARD IT
BR 1# ;; YES, RESUME
3# : CMP 4(SP),#140 ;; IS IT UPPER CASE?
BLT 4# ;; BRANCH IF YES
CMP 4(SP),#175 ;; IS IT A SPECIAL CHAR?
BGT 4# ;; BRANCH IF YES
BIC #40,4(SP) ;; MAKE IT UPPER CASE
4# : RTI ;; GO BACK TO USER

;; *****
;; THIS ROUTINE WILL INPUT A STRING FROM THE TTY

;; CALL:
;; * RDLIN
;; * RETURN HERE
;; * INPUT A STRING FROM THE TTY
;; * ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK
;; * TERMINATOR WILL BE A BYTE OF ALL 0'S
;; *

RDLIN: MOV R3,-(SP) ;; SAVE R3
1# : MOV #TTYIN,R3 ;; GET ADDRESS
2# : CMP #TTYIN+8.,R3 ;; BUFFER FULL?

4048	012576	101405				BLOS	48		::BR IF YES
4049	012600	104410				RDCMR			::GO READ ONE CHARACTER FROM THE TTY
4050	012502	112613				MOVB	(SP), (R3)		::GET CHARACTER
4051	012604	122713	000177		108:	CHPB	@177, (R3)		::IS IT A RUBOUT
4052	012610	001003				BNE	38		::SKIP IF NOT
4053	012612	104401	001170		48:	TYPE	, @QUES		::TYPE A '?'
4054	012616	000763				BR	18		::CLEAR THE BUFFER AND LOOP
4055	012620	111367	000044		38:	MOVB	(R3), 98		::ECHO THE CHARACTER
4056	012624	104401	012670			TYPE	, 98		
4057	012630	122723	000015			CHPB	@15, (R3)		::CHECK FOR RETURN
4058	012634	001356				BNE	28		::LOOP IF NOT RETURN
4059	012636	105063	177777			CLRB	-1(R3)		::CLEAR RETURN (THE 15)
4060	012642	104401	001172			TYPE	, @LF		::TYPE A LINE FEED
4061	012646	012603				MOV	(SP), R3		::RESTORE R3
4062	012650	011646				MOV	(SP), -(SP)		::ADJUST THE STACK AND PUT ADDRESS OF THE
4063	012652	016666	000004	000002		MOV	4(SP), 2(SP)		:: FIRST ASCII CHARACTER ON IT
4064	012660	012766	012672	000004		MOV	@TTYIN, 4(SP)		
4065	012666	000002				RTI			::RETURN
4066	012670	000			98:	.BYTE	0		::STORAGE FOR ASCII CHAR. TO TYPE
4067	012671	000				.BYTE	0		::TERMINATOR
4068	012672	000010			@TTYIN:	.BLKB	8.		::RESERVE 8 BYTES FOR TTY INPUT
4069	012702	052536	005015	000	@CNTLU:	.ASCIZ	/'U/'<15><12>		::CONTROL "U"
4070	012707	136	006507	000012	@CNTLG:	.ASCIZ	/'G/'<15><12>		::CONTROL "G"
4071	012714	005015	053523	020122	@MSMR:	.ASCIZ	<15><12>/SMR . /		
4072	012722	020075	000						
4073	012725	040	047040	053505	@MNEW:	.ASCIZ	/ NEW = /		
4074	012732	036440	000040						

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CVDVCC.P11 12-SEP-84 08:55 APT COMMUNICATIONS ROUTINE

.SBTTL APT COMMUNICATIONS ROUTINE

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4075
4076
4077
4078 012736 112767 000001 000236 $ATY1: MOVB 01,0FFLG ;:TO REPORT FATAL ERROR
4079 012744 112767 000001 000226 $ATY3: MOVB 01,0HFLG ;:TO TYPE A MESSAGE
4080 012752 000403 BR $ATYC
4081 012754 112767 000001 000220 $ATY4: MOVB 01,0FFLG ;:TO ONLY REPORT FATAL ERROR
4082 012762 $ATYC:
4083 012762 010046 MOV R0,-(SP) ;:PUSH R0 ON STACK
4084 012764 010146 MOV R1,-(SP) ;:PUSH R1 ON STACK
4085 012766 105767 000206 TSTB 0HFLG ;:SHOULD TYPE A MESSAGE?
4086 012772 001450 BEQ 50 ;:IF NOT: BR
4087 012774 122767 000001 166212 CPFB 0APTENV,0ENV ;:OPERATING UNDER APT?
4088 013002 001031 BNE 30 ;:IF NOT: BR
4089 013004 132767 000100 166203 BITB 0APTSPOOL,0ENVH ;:SHOULD SPOOL MESSAGES?
4090 013012 001425 BEQ 30 ;:IF NOT: BR
4091 013014 017600 000004 MOV 04(SP),R0 ;:GET MESSAGE ADDR.
4092 013020 062766 000002 000004 ADD 02,4(SP) ;:BUMP RETURN ADDR.
4093 013026 005767 166142 10: TST 0MSGTYPE ;:SEE IF DONE W/ LAST XMISSION?
4094 013032 001375 BNE 10 ;:IF NOT: WAIT
4095 013034 010067 166150 MOV R0,0MSGAD ;:PUT ADDR IN MAILBOX
4096 013040 105720 20: TSTB (R0). ;:FIND END OF MESSAGE
4097 013042 001376 BNE 20
4098 013044 166700 166140 SUB 0MSGAD,R0 ;:SUB START OF MESSAGE
4099 013050 006200 ASR R0 ;:GET MESSAGE LENGTH IN WORDS
4100 013052 010067 166154 MOV R0,0MSGLG ;:PUT LENGTH IN MAILBOX
4101 013056 012767 000004 166110 MOV 04,0MSGTYPE ;:TELL APT TO TAKE MSG.
4102 013064 000413 BR 50
4103 013066 017667 000004 000016 30: MOV 04(SP),40 ;:PUT MSG ADDR IN JSR LINKAGE
4104 013074 062766 000002 000004 ADD 02,4(SP) ;:BUMP RETURN ADDRESS
4105 013102 016746 166670 MOV 177776,-(SP) ;:PUSH 177776 ON STACK
4106 013106 004767 176514 JSR PC,0TYPE ;:CALL TYPE MACRO
4107 013112 000000 40: .WORD 0
4108 013114 50:
4109 013114 105767 000062 100: TSTB 0FFLG ;:SHOULD REPORT FATAL ERROR?
4110 013120 001416 BEQ 120 ;:IF NOT: BR
4111 013122 005767 166066 TST 0ENV ;:RUNNING UNDER APT?
4112 013126 001413 BEQ 120 ;:IF NOT: BR
4113 013130 005767 166040 110: TST 0MSGTYPE ;:FINISHED LAST MESSAGE?
4114 013134 001375 BNE 110 ;:IF NOT: WAIT
4115 013136 017667 000004 166032 MOV 04(SP),0FATAL ;:GET ERROR 0
4116 013144 062766 000002 000004 ADD 02,4(SP) ;:BUMP RETURN ADDR.
4117 013152 005267 166016 INC 0MSGTYPE ;:TELL APT TO TAKE ERROR
4118 013156 105067 000020 120: CLRB 0FFLG ;:CLEAR FATAL FLAG
4119 013162 105067 000013 CLRB 0LFLG ;:CLEAR LOG FLAG
4120 013166 105067 000006 CLRB 0HFLG ;:CLEAR MESSAGE FLAG

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4121	013172	012601	MOV	(SP),R1	;;POP STACK INTO R1
4122	013174	012600	MOV	(SP),R0	;;POP STACK INTO R0
4123	013176	000207	RTS	PC	;;RETURN
4124	013200	000	#MFLG: .BYTE	0	;;MESSG. FLAG
4125	013201	000	#LFLG: .BYTE	0	;;LOG FLAG
4126	013202	000	#FFLG: .BYTE	0	;;FATAL FLAG
4127		013204		.EVEN	
4128		000200	APTSIZE=	200	
4129		000001	APTENV=	001	
4130		000100	APTSPool=	100	
4131		000040	APTCSUP=	040	

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4132 .SBTTL ERROR HANDLER ROUTINE
4133
4134 ;*****
4135 ;*THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT,
4136 ;*SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
4137 ;*AND GO TO MYTYPE ON ERROR
4138 ;*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
4139 ;*SW15=1 HALT ON ERROR
4140 ;*SW13=1 INHIBIT ERROR TYPEOUTS
4141 ;*SW10=1 BELL ON ERROR
4142 ;*SW09=1 LOOP ON ERROR
4143 ;*CALL
4144 ;* ERROR N ;:ERROR=EMT AND N=ERROR ITEM NUMBER
4145
4146 #ERROR:
4147 013204 104407
4148 013204 105267 165671 74: CKSMR ;:TEST FOR CHANGE IN SOFT-SWR
4149 013212 001775 BEQ #ERFLG ;:SET THE ERROR FLAG
4150 013214 016777 165662 165720 MOV #TSTM,DISP ;:DON'T LET THE FLAG GO TO ZERO
4151 013222 032777 002000 165710 BIT #BIT10,BSWR ;:DISPLAY TEST NUMBER AND ERROR FLAG
4152 013230 001402 BEQ 14 ;:BELL ON ERROR?
4153 013232 104401 001164 TYPE ,#BELL ;:NO - SKIP
4154 013236 005267 165650 14: INC #ERTTL ;:RING BELL
4155 013242 011667 165650 MOV (SP),#ERRPC ;:COUNT THE NUMBER OF ERRORS
4156 013246 162767 000002 165642 SUB #2,#ERRPC ;:GET ADDRESS OF ERROR INSTRUCTION
4157 013254 117767 165636 165632 MOV #ERRPC,#ITEMB ;:STRIP AND SAVE THE ERROR ITEM CODE
4158 013262 032777 020000 165650 BIT #BIT13,BSWR ;:SKIP TYPEOUT IF SET
4159 013270 001004 BNE 204 ;:SKIP TYPEOUTS
4160 013272 004767 175634 JSR PC,MYTYPE ;:GO TO USER ERROR ROUTINE
4161 013276 104401 001171 TYPE ,#CRLF
4162 013302 204:
4163 013302 122767 000001 165704 CMPB #APTENV,#ENV ;:RUNNING IN APT MODE
4164 013310 001007 BNE 214 ;:NO,SKIP APT ERROR REPORT
4165 013312 116767 165576 000004 MOV #ITEMB,214 ;:SET ITEM NUMBER AS ERROR NUMBER
4166 013320 004767 177430 JSR PC,#ATY4 ;:REPORT FATAL ERROR TO APT
4167 013324 000 214: .BYTE 0
4168 013325 000 .BYTE 0
4169 013326 000777 224: BR 224 ;:APT ERROR LOOP
4170 013330 005777 165604 24: TST BSWR ;:HALT ON ERROR
4171 013334 100002 BPL 34 ;:SKIP IF CONTINUE
4172 013336 000000 HALT ;:HALT ON ERROR!
4173 013340 104407 CKSMR ;:TEST FOR CHANGE IN SOFT-SWR
4174 013342 032777 001000 165570 34: BIT #BIT09,BSWR ;:LOOP ON ERROR SWITCH SET?
4175 013350 001402 BEQ 44 ;:BR IF NO
4176 013352 016716 165532 MOV #LPERR,(SP) ;:FUDGE RETURN FOR LOOPING
4177 013356 005767 165600 44: TST #ESCAPE ;:CHECK FOR AN ESCAPE ADDRESS
4178 013362 001402 BEQ 54 ;:BR IF NONE
4179 013364 016716 165572 MOV #ESCAPE,(SP) ;:FUDGE RETURN ADDRESS FOR ESCAPE
4180 013370 54:
4181 013370 022737 011410 000042 CMP #ENDAD,#M42 ;:ACT-11 AUTO-ACCEPT?
4182 013376 001001 BNE 64 ;:BRANCH IF NO
4183 013400 000000 HALT ;:YES
4184 013402 64:
4185 013402 000002 RTI ;:RETURN

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4186 .SBTTL SCOPE HANDLER ROUTINE
4187
4188 ;*****
4189 ;*THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
4190 ;*AND LOAD THE TEST NUMBER(%TSTNM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
4191 ;*AND LOAD THE ERROR FLAG (%ERFLG) INTO DISPLAY<15:08>
4192 ;*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
4193 ;*SW14=1 LOOP ON TEST
4194 ;*SW11=1 INHIBIT ITERATIONS
4195 ;*SM09=1 LOOP ON ERROR
4196 ;*SM08=1 LOOP ON TEST IN SMR<7:0>
4197 ;*CALL
4198 ;* SCOPE ;:SCOPE=IOT
4199
4200 #SCOPE:
4201 013404 104407 CKSMR ;:TEST FOR CHANGE IN SOFT-SMR
4202 013406 032777 040000 165524 14: BIT %BIT14,%SMR ;:LOOP ON PRESENT TEST?
4203 013414 001114 BNE %OVER ;:YES IF SW14=1
4204 ;*****START OF CODE FOR THE XOR TESTER*****
4205 013416 000416 %XTSTR: BR 64 ;:IF RUNNING ON THE "XOR" TESTER CHANGE
4206 ;:THIS INSTRUCTION TO A "NOP" (NOP=240)
4207 013420 013746 000004 MOV %ERRVEC,-(SP) ;:SAVE THE CONTENTS OF THE ERROR VECTOR
4208 013424 012737 013444 000004 MOV %5,%ERRVEC ;:SET FOR TIMEOUT
4209 013432 005737 177060 TST %0177060 ;:TIME OUT ON XOR?
4210 013436 012637 000004 MOV (SP)+,%ERRVEC ;:RESTORE THE ERROR VECTOR
4211 013442 000463 BR %SVLAD ;:GO TO THE NEXT TEST
4212 013444 022626 54: CMP (SP)+,(SP)+ ;:CLEAR THE STACK AFTER A TIME OUT
4213 013446 012637 000004 MOV (SP)+,%ERRVEC ;:RESTORE THE ERROR VECTOR
4214 013452 000423 BR 74 ;:LOOP ON THE PRESENT TEST
4215 013454 64: ;*****END OF CODE FOR THE XOR TESTER*****
4216 013454 032777 000400 165456 BIT %BIT08,%SMR ;:LOOP ON SPEC. TEST?
4217 013462 001404 BEQ 24 ;:BR IF NO
4218 013464 127767 165450 165410 CMPB %SMR,%TSTNM ;:ON THE RIGHT TEST? SMR<7:0>
4219 013472 001465 BEQ %OVER ;:BR IF YES
4220 013474 105767 165403 24: TSTB %ERFLG ;:HAS AN ERROR OCCURRED?
4221 013500 001421 BEQ 34 ;:BR IF NO
4222 013502 126767 163407 165373 CMPB %ERMAX,%ERFLG ;:MAX. ERRORS FOR THIS TEST OCCURRED?
4223 013510 101015 BHI 34 ;:BR IF NO
4224 013512 032777 001000 165420 BIT %BIT09,%SMR ;:LOOP ON ERROR?
4225 013520 001404 BEQ 44 ;:BR IF NO
4226 013522 016767 165362 165356 74: MOV %LPERR,%LPAOR ;:SET LOOP ADDRESS TO LAST SCOPE
4227 013530 000446 BR %OVER
4228 013532 105067 165345 44: CLRB %ERFLG ;:ZERO THE ERROR FLAG
4229 013536 005067 165416 CLR %TIMES ;:CLEAR THE NUMBER OF ITERATIONS TO MAKE
4230 013542 000415 BR 14 ;:ESCAPE TO THE NEXT TEST
4231 013544 032777 004000 165366 34: BIT %BIT11,%SMR ;:INHIBIT ITERATIONS?
4232 013552 001011 BNE 14 ;:BR IF YES
4233 013554 005767 165422 TST %PASS ;:IF FIRST PASS OF PROGRAM
4234 013560 001406 BEQ 14 ;: INHIBIT ITERATIONS
4235 013562 005267 165316 INC %ICNT ;:INCREMENT ITERATION COUNT
4236 013566 026767 165366 165310 CMP %TIMES,%ICNT ;:CHECK THE NUMBER OF ITERATIONS MADE
4237 013574 002024 BGE %OVER ;:BR IF MORE ITERATION REQUIRED
4238 013576 012767 000001 165300 14: MOV %1,%ICNT ;:REINITIALIZE THE ITERATION COUNTER
4239 013604 016767 000052 165346 MOV %MXCNT,%TIMES ;:SET NUMBER OF ITERATIONS TO DO
4240 013612 105267 165264 %SVLAD: INCB %TSTNM ;:COUNT TEST NUMBERS
4241 013616 116767 165260 165354 MOVB %TSTNM,%TESTN ;:SET TEST NUMBER IN APT MAILBOX
    
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 CVDVCC.P11 12-SEP-84 08:55 SCOPE HANDLER ROUTINE

4242	013624	011667	165256		MOV	(SP),#LPADR	::SAVE SCOPE LOOP ADDRESS
4243	013630	011667	165254		MOV	(SP),#LPERR	::SAVE ERROR LOOP ADDRESS
4244	013634	005067	165322		CLR	#ESCAPE	::CLEAR THE ESCAPE FROM ERROR ADDRESS
4245	013640	112767	000001	165247	MOVB	#1,#ERMAX	::ONLY ALLOW ONE(1) ERROR ON NEXT TEST
4246	013646	016777	165230	165266	\$OVER: MOV	#TSTN,#DISPLAY	::DISPLAY TEST NUMBER
4247	013654	016716	165226		MOV	#LPADR,(SP)	::FLDGE RETURN ADDRESS
4248	013660	000002			RTI		::FIXES PS
4249	013662	003720			\$MXCNT: 2000.		::MAX. NUMBER OF ITERATIONS

MAINDEC-11-DVDVC-C
CVDVCC.P11 12-SEP-84

MACY11 30A(1052) 08:55

12-SEP-84 15:41 PAGE 99
CONVERT BINARY TO DECIMAL AND TYPE ROUTINE

.SBTTL CONVERT BINARY TO DECIMAL AND TYPE ROUTINE

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4250
4251
4252
4253
4254
4255
4256
4257
4258
4259
4260
4261
4262 013664
4263 013664 010046
4264 013666 010146
4265 013670 010246
4266 013672 010346
4267 013674 010546
4268 013676 012746 020200
4269 013702 016605 000020
4270 013706 100004
4271 013710 005405
4272 013712 112766 000055 000001
4273 013720 005000
4274 013722 012703 014100
4275 013726 112723 000040
4276 013732 005002
4277 013734 016001 014070
4278 013740 160105
4279 013742 002402
4280 013744 005202
4281 013746 000774
4282 013750 060105
4283 013752 005702
4284 013754 001002
4285 013756 105716
4286 013760 100407
4287 013762 106316
4288 013764 103003
4289 013766 116663 000001 177777
4290 013774 052702 000060
4291 014000 052702 000040
4292 014004 110223
4293 014006 005720
4294 014010 020027 000010
4295 014014 002746
4296 014016 003002
4297 014020 010502
4298 014022 000764
4299 014024 105726
4300 014026 100003
4301 014030 116663 177777 177776
4302 014036 105013
4303 014040 012605
4304 014042 012603
4305 014044 012602

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*****
THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 5-DIGIT
SIGNED DECIMAL (ASCII) NUMBER AND TYPE IT. DEPENDING ON WHETHER THE
NUMBER IS POSITIVE OR NEGATIVE A SPACE OR A MINUS SIGN WILL BE TYPED
BEFORE THE FIRST DIGIT OF THE NUMBER. LEADING ZEROS WILL ALWAYS BE
REPLACED WITH SPACES.
CALL:
MOV NUM,-(SP)      ;PUT THE BINARY NUMBER ON THE STACK
TYPDS              ;GO TO THE ROUTINE

TYPDS:
MOV R0,-(SP)      ;PUSH R0 ON STACK
MOV R1,-(SP)      ;PUSH R1 ON STACK
MOV R2,-(SP)      ;PUSH R2 ON STACK
MOV R3,-(SP)      ;PUSH R3 ON STACK
MOV R5,-(SP)      ;PUSH R5 ON STACK
MOV #20200,-(SP)  ;SET BLANK SWITCH AND SIGN
MOV 20(SP),R5     ;GET THE INPUT NUMBER
BPL 1#            ;BR IF INPUT IS POS.
NEG R5            ;MAKE THE BINARY NUMBER POS.
MOVB #'-,1(SP)    ;MAKE THE ASCII NUMBER NEG.
1#: CLR R0         ;ZERO THE CONSTANTS INDEX
MOV #0BLK,R3      ;SETUP THE OUTPUT POINTER
MOVB #' ,(R3)+    ;SET THE FIRST CHARACTER TO A BLANK
2#: CLR R2         ;CLEAR THE BCD NUMBER
MOV #DTBL(R0),R1  ;GET THE CONSTANT
3#: SUB R1,R5      ;FORM THIS BCD DIGIT
BLT 4#           ;BR IF DONE
INC R2           ;INCREASE THE BCD DIGIT BY 1
BR 3#
4#: ADD R1,R5     ;ADD BACK THE CONSTANT
TST R2           ;CHECK IF BCD DIGIT=0
BNE 5#           ;FALL THROUGH IF 0
TSTB (SP)        ;STILL DOING LEADING 0'S?
BMI 7#           ;BR IF YES
5#: ASLB (SP)     ;MSD?
BCC 6#           ;BR IF NO
MOVB 1(SP),-1(R3) ;YES--SET THE SIGN
6#: BIS #'0,R2    ;MAKE THE BCD DIGIT ASCII
7#: BIS #' ,R2    ;MAKE IT A SPACE IF NOT ALREADY A DIGIT
MOVB R2,(R3)+    ;PUT THIS CHARACTER IN THE OUTPUT BUFFER
TST (R0)+        ;JUST INCREMENTING
CMP R0,#10       ;CHECK THE TABLE INDEX
BLT 2#           ;GO DO THE NEXT DIGIT
BGT 8#           ;GO TO EXIT
MOV R5,R2        ;GET THE LSD
BR 6#           ;GO CHANGE TO ASCII
8#: TSTB (SP)+    ;WAS THE LSD THE FIRST NON-ZERO?
BPL 9#           ;BR IF NO
MOVB -1(SP),-2(R3) ;YES--SET THE SIGN FOR TYPING
9#: CLRB (R3)     ;SET THE TERMINATOR
MOV (SP)+,R5     ;POP STACK INTO R5
MOV (SP)+,R3     ;POP STACK INTO R3
MOV (SP)+,R2     ;POP STACK INTO R2

```

MAINDEC-11 DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 100
CVDVCC.P11 12-SEP-84 08:55 CONVERT BINARY TO DECIMAL AND TYPE ROUTINE

4306	014046	012601			MOV	(SP)+,R1	;;POP STACK INTO R1
4307	014050	012600			MOV	(SP)+,R0	;;POP STACK INTO R0
4308	014052	104401	014100		TYPE	.#DBLK	;;NOW TYPE THE NUMBER
4309	014056	016666	000002	000004	MOV	2(SP),4(SP)	;;ADJUST THE STACK
4310	014064	012616			MOV	(SP)+,(SP)	
4311	014066	000002			RTI		;;RETURN TO USER
4312	014070	023420			\$DTBL:	10000.	
4313	014072	001750				1000.	
4314	014074	000144				100.	
4315	014076	000012				10.	
4316	014100	000004			\$DBLK:	.BLKW 4	

MAINDEC-11-DVDC-C
CVDVCC.P11 12-SEP-84 08:55

MACY11 30A(1052) 12-SEP-84 15:41 PAGE 101
BINARY TO OCTAL (ASCII) AND TYPE

.SBTTL BINARY TO OCTAL (ASCII) AND TYPE

```

;*****
;THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT
;OCTAL (ASCII) NUMBER AND TYPE IT.
;#TYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE
;CALL:
;*      MOV      NUM,-(SP)      ;;NUMBER TO BE TYPED
;*      TYPOS    ;;CALL FOR TYPEOUT
;*      .BYTE   N              ;;N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE
;*      .BYTE   M              ;;M=1 OR 0
;*                                  ;;1=TYPE LEADING ZEROS
;*                                  ;;0=SUPPRESS LEADING ZEROS

```

```

;#TYPON---ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS TH. LAST
;#TYPOS OR #TYPOC

```

```

;CALL:
;*      MOV      NUM,-(SP)      ;;NUMBER TO BE TYPED
;*      TYPON    ;;CALL FOR TYPEOUT

```

```

;#TYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER

```

```

;CALL:
;*      MOV      NUM,-(SP)      ;;NUMBER TO BE TYPED
;*      TYPOC    ;;CALL FOR TYPEOUT

```

```

;#TYPOS:
MOV      8(SP),-(SP)      ;;PICKUP THE MODE
MOVB    1(SP),#OFILL     ;;LOAD ZERO FILL SWITCH
MOVB    (SP)+,#OMODE+1  ;;NUMBER OF DIGITS TO TYPE
ADD     #2,(SP)          ;;ADJUST RETURN ADDRESS
BR      #TYPON
;#TYPOC:
MOVB    #1,#OFILL       ;;SET THE ZERO FILL SWITCH
MOVB    #6,#OMODE+1     ;;SET FOR SIX(6) DIGITS
;#TYPON:
MOVB    #5,#OCNT        ;;SET THE ITERATION COUNT
MOV     R3,-(SP)        ;;SAVE R3
MOV     R4,-(SP)        ;;SAVE R4
MOV     R5,-(SP)        ;;SAVE R5
MOVB    #OMODE+1,R4     ;;GET THE NUMBER OF DIGITS TO TYPE
NEG     R4
ADD     #6,R4           ;;SUBTRACT IT FOR MAX. ALLOWED
MOVB    R4,#OMODE       ;;SAVE IT FOR USE
MOVB    #OFILL,R4       ;;GET THE ZERO FILL SWITCH
MOV     12(SP),R5       ;;PICKUP THE INPUT NUMBER
CLR     R3              ;;CLEAR THE OUTPUT WORD
18:    ROL     R5         ;;ROTATE MSB INTO "C"
BR      38              ;;GO DO MSB
24:    RCL     R5         ;;FORM THIS DIGIT
ROL     R5
ROL     R5
MOV     R5,R3
34:    ROL     R3         ;;GET LSB OF THIS DIGIT
DECB   #OMODE           ;;TYPE THIS DIGIT?
BPL     74              ;;BR IF NO
46:    BIC     #177770,R3 ;;GET RID OF JUNK
BNE     44              ;;TEST FOR 0
TST     R4              ;;SUPPRESS THIS 0?
BEQ     54              ;;BR IF YES

```

```

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4341
4342 014110 017646 000000
4343 014114 116667 000001 000211
4344 014122 112667 000207
4345 014126 062716 000002
4346 014132 000406
4347 014134 112767 000001 000171
4348 014142 112767 000006 000165
4349 014150 112767 000005 000154
4350 014156 010346
4351 014160 010446
4352 014162 010546
4353 014164 116704 000145
4354 014170 005404
4355 014172 062704 000006
4356 014176 110467 000132
4357 014202 116704 000125
4358 014206 016605 000012
4359 014212 005003
4360 014214 006105 18:
4361 014216 000404 BR 38
4362 014220 006105 24:
4363 014222 006105 ROL R5
4364 014224 006105 ROL R5
4365 014226 010503 MOV R5,R3
4366 014230 006103 34:
4367 014232 105367 000076 ROL R3
4368 014236 100016 DECB #OMODE
4369 014240 042703 177770 BPL 74
4370 014244 001002 BIC #177770,R3
4371 014246 005704 BNE 44
4372 014250 001403 TST R4
BEQ 54

```

MAINDEC-11 DVDVC C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 102
 DVDVCC.P11 12-SEP-84 08:55 BINARY TO OCTAL (ASCII) AND TYPE

4373	014252	005204		44:	INC	R4	;;DON'T SUPPRESS ANYMORE 0'S
4374	014254	052703	000060		BIS	@'0,R3	;;MAKE THIS DIGIT ASCII
4375	014260	052703	000040	54:	BIS	@',R3	;;MAKE ASCII IF NOT ALREADY
4376	014264	110367	000040		MOVB	R3,R4	;;SAVE FOR TYPING
4377	014270	104401	014330		TYPE	.R4	;;GO TYPE THIS DIGIT
4378	014274	105367	000032	74:	DECB	%CNT	;;COUNT BY 1
4379	014300	003347			BCT	24	;;BR IF MORE TO DO
4380	014302	002402			BLT	64	;;BR IF DONE
4381	014304	005204			INC	R4	;;INSURE LAST DIGIT ISN'T A BLANK
4382	014306	000744			BR	24	;;GO DO THE LAST DIGIT
4383	014310	012605		64:	MOV	(SP)+,R5	;;RESTORE R5
4384	014312	012604			MOV	(SP)+,R4	;;RESTORE R4
4385	014314	012603			MOV	(SP)+,R3	;;RESTORE R3
4386	014316	016666	000002 000004		MOV	2(SP),4(SP)	;;SET THE STACK FOR RETURNING
4387	014324	012616			MOV	(SP)+,(SP)	
4388	014326	000002			RTI		;;RETURN
4389	014330	000		84:	.BYTE	0	;;STORAGE FOR ASCII DIGIT
4390	014331	000			.BYTE	0	;;TERMINATOR FOR TYPE ROUTINE
4391	014332	000		%CNT:	.BYTE	0	;;OCTAL DIGIT COUNTER
4392	014333	000		%FILL:	.BYTE	0	;;ZERO FILL SWITCH
4393	014334	000000		%MODE:	.WORD	0	;;NUMBER OF DIGITS TO TYPE

MAINDEC-11-DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 103
 CVDVCC.P11 12-SEP 84 08:55 TRAP DECODER

.SBTTL TRAP DECODER

```

;*****
; THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
; AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
; OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL
; GO TO THAT ROUTINE.

```

```

$TRAP:  MOV    RO,-(SP)           ;;SAVE RO
        MOV    2(SP),RO         ;;GET TRAP ADDRESS
        TST    -(RO)            ;;BACKUP BY 2
        MOVB   (RO),RO          ;;GET RIGHT BYTE OF TRAP
        ASL    RO               ;;POSITION FOR INDEXING
        MOV    $TRAPAD(RO),RO   ;;INDEX TO TABLE
        RTS    RO               ;;GO TO ROUTINE

```

```

;;THIS IS USE TO HANDLE THE "GETPRI" MACRO

```

```

$TRAP2: MOV    (SP),-(SP)       ;;MOVE THE PC DOWN
        MOV    4(SP),2(SP)     ;;MOVE THE PSW DOWN
        RTI                    ;;RESTORE THE PSW

```

.SBTTL TRAP TABLE

```

; THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
; BY THE "TRAP" INSTRUCTION.

```

```

; ROUTINE
; -----
$TRAPAD: .WORD  $TRAP2          TRAP.1(104401)  TTY TYPEOUT ROUTINE
        $TYPE   ;;CALL=TYPE    TRAP.2(104402)  TYPE OCTAL NUMBER (WITH LEADING ZEROS)
        $TYPOC  ;;CALL=TYPOC   TRAP.3(104403)  TYPE OCTAL NUMBER (NO LEADING ZEROS)
        $TYPOS  ;;CALL=TYPOS   TRAP.4(104404)  TYPE OCTAL NUMBER (AS PER LAST CALL)
        $TYPON  ;;CALL=TYPON   TRAP.5(104405)  TYPE DECIMAL NUMBER (WITH SIGN)
        $TYPDS  ;;CALL=TYPDS
        $GTSMR  ;;CALL=GTSMR   TRAP.6(104406)  GET SOFT-SMR SETTING
        $CKSMR  ;;CALL=CKSMR   TRAP.7(104407)  TEST FOR CHANGE IN SOFT-SMR
        $RDCHR  ;;CALL=RDCHR   TRAP.10(104410) TTY TYPEIN CHARACTER ROUTINE
        $RDLIN  ;;CALL=RDLIN   TRAP.11(104411) TTY TYPEIN STRING ROUTINE

```

.END

```

4394
4395
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4397
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4399
4400
4401
4402 014336 010046
4403 014340 016600 000002
4404 014344 005740
4405 014346 111000
4406 014350 006300
4407 014352 016000 014372
4408 014356 000200
4409
4410
4411
4412
4413 014360 011646
4414 014362 016666 000004 000002
4415 014370 000002
4416
4417
4418
4419
4420
4421
4422
4423
4424 014372 014360
4425 014374 011626
4426 014376 014134
4427 014400 014110
4428 014402 014150
4429 014404 013664
4430
4431 014406 012232
4432
4433 014410 012162
4434 014412 012444
4435 014414 012564
4436 000001

```


PSM	177776	6520												
PMVEEC	000024	7430	1042	1043	3799	3800	3809	3815	3827	3828				
RATES	005624	2347	2428	2444										
RBUF	001262	10200	1123	1124	1950	2096	2178	2227	2259	2277	2344	2696	2785	2835
		2933	2990	3195	3307	3319								
RCSR	001260	10190	1121	1477	1490	1493	1507	1510	1524	1529	1560	1625	1854	1882
		1928	1953	2007	2047	2051	2054	2077	2099	2364	2663	2680	2687	2727
		2822	2920	2972	3019	3111	3139	3229	3293					
RCVRAC	004000	7720	1625	2007	2047	2054								
RCVRDD	000200	7760	1560	1855	1882	1929	1953	2051	2077	2099	2364	2631	2823	2973
		3294												
RCVRIE	000100	7770	1477	1490	1493	1507	1510	1524	1529	2663	2687	2727	3111	3130
		3229												
RDATA0	000001	8030												
RDATA1	000002	8020												
RDATA2	000004	8010												
RDATA3	000010	8000												
RDATA4	000020	7990												
RDATA5	000040	7980												
RDATA6	000100	7970												
RDATA7	000200	7960												
ROCR	104410	4049	44340											
ROLIN	104411	44330												
ROPRUN	000001	7830												
REC	007632	3077	31870											
REG	000004	33840	3408											
REGSAV	010464	34080	3421	34770										
RESVEC	000010	7380												
RILD	007722	31990	32000	3204	3212	32380								
R0050	005624	24530												
R0070	005625	24540												
R0110	005626	24550												
R0135	005627	24560												
R0150	005630	24570												
R0200	005633	24600												
R0300	005631	24580												
R0600	005632	24590												
R10000	005643	24680												
R1800	005634	24610												
R2000	005635	24620												
R2400	005636	24630												
R3600	005637	24640												
R4800	005640	24650												
R5STAC	001334	10260	1135											
R7200	005641	24660												
R9600	005642	24670												
SB	007566	31450	32120											
SET	177777	7610	1726	1757	1768	1784	1793	1800	1804	1853	1927	2010	2019	2523
		2679	2802	2821	2950	2971	3292	3429						
SETCLR	000006	33840	3435											
SPECIA	010732	36330	3790											
STACK	001100	6420	1034											
START	001336	875	10270	3832										
STKLMT	177774	6530												
SWR	001140	9360	1032	10540	1056	10620	10690	1083	3807	38200	3946	39830	4151	4158
		4170	4174	4202	4216	4218	4224	4231						

#F1DEC= 000220
#F1DO = 000340
#F1FAL= 000405
#F1G00= 000400

10													
10	1096	2049	2363										
10	1093	1094	1104	1105	1106	1107	1109	1113	1114	1116	1117	1118	
1119	1121	1122	1123	1124	1125	1126	1127	1128	1129	1130	1131	1132	
1133	1134	1135	1136	1148	1149	1151	1152	1153	1154	1155	1156	1157	
1158	1161	1162	1164	1165	1169	1178	1179	1180	1181	1182	1188	1189	
1190	1191	1192	1193	1215	1221	1222	1228	1229	1231	1242	1243	1244	
1245	1247	1258	1259	1261	1262	1264	1275	1276	1278	1279	1283	1303	
1309	1310	1316	1317	1319	1330	1331	1332	1333	1335	1346	1347	1349	
1350	1352	1363	1364	1366	1367	1371	1398	1399	1401	1412	1413	1414	
1415	1417	1428	1429	1431	1432	1434	1445	1446	1448	1449	1453	1474	
1475	1477	1488	1489	1490	1491	1493	1504	1505	1507	1508	1510	1521	
1522	1524	1525	1529	1538	1559	1560	1591	1598	1605	1612	1613	1622	
1623	1625	1630	1631	1660	1661	1663	1693	1699	1700	1705	1706	1710	
1711	1712	1713	1720	1721	1725	1735	1748	1749	1753	1757	1758	1767	
1775	1784	1787	1793	1794	1800	1801	1826	1832	1833	1839	1840	1842	
1843	1846	1847	1852	1862	1866	1867	1875	1876	1882	1900	1906	1907	
1913	1914	1915	1916	1921	1922	1926	1934	1935	1937	1950	1951	1953	
1971	1977	1978	1982	1985	1986	1993	1994	1995	1996	1997	1998	2003	
2004	2007	2010	2011	2015	2016	2025	2031	2032	2035	2036	2046	2047	
2051	2054	2060	2061	2065	2066	2077	2082	2083	2086	2087	2096	2097	
2099	2104	2105	2126	2132	2133	2137	2140	2141	2146	2147	2151	2152	
2158	2159	2161	2168	2169	2171	2178	2179	2182	2187	2188	2193	2194	
2201	2202	2203	2209	2210	2216	2217	2223	2224	2227	2233	2234	2238	
2239	2245	2246	2250	2251	2253	2259	2264	2265	2271	2272	2277	2282	
2283	2306	2312	2313	2318	2321	2322	2327	2328	2329	2330	2331	2332	
2333	2334	2336	2337	2339	2340	2343	2344	2345	2347	2348	2350	2351	
2353	2354	2356	2357	2358	2359	2360	2361	2364	2368	2369	2374	2375	
2376	2377	2385	2393	2406	2407	2413	2414	2415	2416	2421	2422	2425	
2426	2427	2428	2429	2432	2433	2434	2492	2495	2496	2503	2504	2507	
2508	2510	2511	2513	2514	2515	2516	2517	2518	2519	2520	2522	2532	
2533	2542	2543	2546	2553	2557	2576	2577	2579	2580	2582	2583	2590	
2595	2607	2608	2611	2612	2613	2614	2615	2616	2636	2648	2649	2650	
2651	2652	2653	2655	2656	2657	2658	2660	2661	2663	2664	2673	2674	
2678	2687	2688	2690	2696	2697	2699	2704	2705	2706	2727	2728	2732	
2733	2735	2736	2739	2740	2741	2742	2743	2744	2756	2762	2763	2768	
2769	2771	2772	2781	2785	2786	2789	2790	2792	2793	2796	2801	2809	
2811	2812	2817	2818	2820	2828	2830	2831	2835	2836	2843	2844	2845	
2846	2847	2850	2855	2856	2859	2860	2869	2870	2871	2878	2896	2903	
2904	2909	2910	2920	2921	2929	2933	2934	2936	2937	2939	2940	2943	
2949	2957	2960	2961	2967	2968	2970	2978	2983	2984	2990	2991	2994	
2995	2996	2997	2998	3001	3007	3008	3019	3020	3038	3044	3045	3051	
3074	3075	3077	3078	3079	3080	3083	3084	3085	3086	3089	3090	3092	
3093	3095	3096	3098	3099	3104	3105	3108	3109	3111	3112	3125	3126	
3128	3137	3138	3139	3140	3157	3158	3160	3161	3162	3164	3165	3167	
3171	3172	3192	3193	3195	3196	3197	3199	3200	3201	3204	3208	3212	
3213	3214	3215	3219	3220	3225	3229	3230	3239	3265	3266	3270	3273	
3274	3279	3280	3282	3283	3285	3286	3288	3289	3291	3299	3307	3311	
3312	3316	3319	3322	3323	3334	3341	3408	3409	3410	3411	3412	3413	
3421	3424	3425	3429	3430	3435	3439	3440	3452	3457	3458	3467	3506	
3507	3508	3509	3512	3514	3515	3517	3518	3521	3522	3523	3524	3525	
3529	3530	3533	3534	3535	3536	3538	3539	3562	3563	3564	3566	3567	
3570	3596	3597	3614	3615	3616	3617	3620	3623	3626	3627	3631	3635	
3636	3639	3640	3641	3642	3643	3644	3645	3646	3650	3651	3652	3653	
3654	3655	3656	3657	3663	3666	3667	3668	3669	3670	3671	3675	3678	

MAINDEC-11-DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 114
CVDVCC.P1: 12-SEP-84 08:55 CROSS REFERENCE TABLE -- USER SYMBOLS

3682	3683	3691	3692	3693	3694								
10	1169	1175	1215	1217	1224	1231	1237	1247	1253	1264	1270	1283	
1209	1303	1305	1312	1319	1325	1335	1341	1352	1358	1371	1377	1401	
1407	1417	1423	1434	1440	1453	1459	1477	1483	1493	1499	1510	1516	
1529	1535	1560	1571	1591	1595	1598	1602	1605	1609	1615	1617	1619	
1625	1642	1663	1674	1693	1695	1702	1735	1740	1753	1761	1775	1780	
1782	1784	1787	1795	1797	1802	1826	1828	1835	1862	1870	1882	1888	
1900	1902	1909	1937	1942	1953	1959	1971	1973	1980	1982	1988	2007	
2012	2017	2025	2038	2051	2054	2068	2070	2077	2089	2099	2108	2126	
2178	2135	2137	2143	2182	2195	2203	2219	2227	2241	2259	2274	2277	
2267	2306	2308	2315	2318	2324	2364	2370	2378	2385	2389	2393	2395	
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IF:INC - 000210
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 #GDDAT 001124
 #GET42 011400
 #GTSNR 012232
 #HD - 000000
 #HIBTS 001000
 #ICNT 001104
 #IFLEV- 177777

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39580	4431											
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#ILLUP 011610
 #INTAG 001135
 #ISKO - 000001

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MAINDEC-11 DVDVC C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 117
CVDVCC.P11 12-SEP 84 08:55 CROSS REFERENCE TABLE - USER SYMBOLS

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#ISK2 - 000001	1605*	1615	3319*	3324	3678*	3684							
#ITEMB 001114	924*	3728	4157*	4165	4186								
#LF 001172	951*	3936	4060	4069	4186								
#LFLG 013201	4119*	4125*											
#LOCTA- 177777	1*	1093	1095	1101	1102	1159	1170	1175	1184	1216	1218	1219	1224
	1232	1237	1248	1253	1265	1270	1284	1289	1304	1306	1307	1312	1320
	1325	1336	1341	1353	1358	1372	1377	1402	1407	1418	1423	1435	1440
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	3753	3754											
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IPASS	001202	9610	1066	3678	3769	3770	3778	3791	4233	4250				
IPASTH	001006	9060												
IPOMER	011616	3830	3837											
IPURAD	011604	3832												
IPURON	011444	1042	3799	3827										
IPURVE	011600	3830												
IPURUP	011516	3809	3815											
IGUES	001170	9490	3936	4004	4053	4069	4186							
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INDDEC	***** U	4436												
INDLIN	012364	4043	4435											
INDOCT	***** U	4436												
INDSZ	000010	4038												
INTNAD	011422	3790												
IRZA	***** U	4436												
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ISCOPE	013404	1036	4200											
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TEMP - 000300

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		25140	25150	25160	25170	25180	25190	25200	25320	25330	25420	25430	25630	25640
		25700	25720	25760	25770	25790	25800	25820	25830	26010	26070	26080	26110	26120
		26140	26150	26160	26430	26480	26490	26500	26510	26520	26530	26550	26560	26570
		26580	26600	26610	26630	26640	26730	26740	26870	26880	26960	26970	27040	27050
		27130	27140	27190	27210	27270	27280	27320	27330	27350	27360	27390	27400	27420
		27430	27440	27620	27630	27650	27680	27690	27710	27720	27850	27860	27930	27960
		28110	28120	28130	28170	28180	28300	28310	28320	28350	28360	28430	28450	28460
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		32300	32340	32650	32660	32680	32730	32740	32760	32790	32800	32820	32830	32850
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		33390	33460	34080	34090	34100	34110	34120	34130	34240	34250	34260	34270	34290
		34300	34310	34390	34400	34410	34440	3445	3447	34570	34580	34590	34600	34720
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		35290	35300	35340	35330	35340	35390	35620	35670	35700	35710	35720	35740	35750
		35800	35960	35970	36140	36150	36160	36170	36260	36270	36280	36290	36350	36360
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		36700	36710	36820	36830	36840	36860	36880	36910	36920	36930	36940	37090	37530
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\$TIMES	001160	21230	23010	24870	26340	27540	28940	30350	32570	3722	42410			
		9460	10450	11450	12110	12210	12990	13090	13880	14700	15510	15840	16120	16530
		16890	16990	18220	18320	18960	19060	19680	19770	19850	20350	20650	20860	21220
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		27620	28390	28930	29030	29600	29830	30070	30340	30440	32560	32650	32730	33570
		37680	42290	4236	42390	4249								
\$TKB	001146	9390	3909	3916	3939	3950	3967	4021	4027					
\$TKS	001144	9380	3907	3914	3939	3948	3964	39880	4019	4025				
\$TN	- 000026	10	627	1138	11450	1146	1202	12110	1212	1222	1295	12990	1300	1310
		1384	13880	1389	1466	14700	1471	1547	15510	1552	1580	15840	1585	1613
		1649	16530	1654	1683	16890	1690	1700	1810	1816	18220	1823	1833	1892
		18960	1897	1907	1962	19680	1969	1978	1986	2036	2066	2087	2111	2118
		21220	2123	2133	2141	2194	2217	2239	2272	2293	23000	2301	2313	2322
		2442	2477	24860	2487	2496	2626	26330	2634	2748	27530	2754	2763	2860
		2889	28930	2894	2904	2961	2984	3008	3029	30340	3035	3045	3141	3248
		32560	3257	3266	3274	3348	3353	33570						
\$TPB	001152	9410	39250	3936										
\$TPFLG	001157	9450	3857	3936										
\$TPS	001150	9400	3923	3936										
\$TRAP	014336	1040	44020											
\$TRAP2	014360	44130	4424											
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\$TRPAD	014372	4407	44240											
\$TSKO	- 050007	10940	1101	11600	1183	12200	1224	12330	1237	12490	1253	12660	1270	12850
		1289	13080	1312	13210	1325	13370	1341	13540	1358	13730	1377	14030	1407
		14190	1423	14360	1440	14550	1459	14790	1483	14950	1499	15120	1516	15310
		1535	15620	1571	15930	1596	15970	1619	16270	1642	15650	1674	16980	1702
		17080	1807	18310	1835	18630	1870	18840	1888	19050	1909	19380	1942	19550

	1959	1976	1980	1984	1988	2006	2019	2027	2038	2047	2072	2079	2089
	2101	2108	2131	2135	2139	2143	2184	2196	2205	2219	2229	2241	2261
	2274	2279	2287	2311	2315	2320	2324	2338	2340	2343	2419	2436	2440
	2494	2498	2555	2572	2597	2601	2641	2643	2701	2721	2761	2765	2791
	2793	2796	2865	2873	2876	2880	2883	2901	2906	2938	2940	2943	3014
	3043	3047	3117	3118	3130	3134	3169	3173	3206	3221	3227	3234	3264
	3268	3272	3276	3300	3304	3309	3314	3315	3328	3336	3339	3343	3346
	3419	3459	3469	3472	3516	3518	3521	3527	3557	3559	3562	3575	3619
	3660	3665	3688										
#TSK1 - 050010	1096	1101	1171	1175	1600	1603	1604	1617	1708	1804	1807	2009	2013
	2014	2017	2049	2072	2343	2418	2559	2564	2565	2570	2708	2714	2715
	2719	2796	2864	2943	3013	3210	3216	3318	3326	3419	3444	3459	3521
	3526	3562	3574	3622	3648	3649	3658	3677	3686				
#TSK2 - 050011	1607	1610	1611	1615	1736	1740	1755	1762	1763	1782	1786	1798	1799
	1802	2053	2070	2361	2381	2387	2390	2391	2410	2810	2813	2829	2832
	2852	2862	2958	2963	2979	2986	3003	3010	3321	3324	3423	3427	3428
	3431	3437	3441	3565	3567	3570	3572	3625	3629	3630	3637	3680	3684
#TSK3 - 050006	1776	1780	1789	1795	2056	2068	2363	2381	2397	2400	2401	2408	3570
	3571												
#TSK4 - 050123	2366	2371	2372	2378									
#TSTM 001004	905												
#TSTM 001102	918	376	4150	4186	4191	4218	4240	4241	4246	4250			
#TTYIN 012672	4046	4047	4064	4068									
#TYPBN- ***** U	4430												
#TYPDS 013664	4262	4429											
#TYPE 011626	3857	4106	4417	4425									
#TYPEC 012040	3887	3894	3901	3906	3990								
#TYPEX 012160	3929	3931	3934										
#TYPOC 014134	4347	4426											
#TYPON 014150	4346	4349	4428										
#TYPOS 014110	4342	4427											
#UNIT 001206	963												
#UNITM 001010	907												
#USMR 001220	970	1215	1303	1598	1605	1826	1900	1973	2126	2137	2306	2308	2421
	2636	2756	2895	2898	3038	3259	3261	3316	3508				
#VECT1 001244	995	3645											
#VECT2 001246	996												
#XOFF - 000023	3911	3936											
#XON - 000021	3918	3936											
#XTSTR 013416	4205												
#ARGC- 000000	1	3384	3492	3542	3602	3717							
#BYTE- 000403	1	1094	1169	1215	1217	1231	1247	1264	1283	1303	1305	1319	1335
	1352	1371	1401	1417	1434	1453	1477	1493	1510	1529	1560	1591	1598
	1605	1625	1663	1693	1695	1753	1784	1787	1826	1828	1882	1900	1902
	1953	1971	1973	1982	2007	2025	2047	2051	2054	2077	2099	2126	2128
	2137	2182	2203	2227	2259	2277	2306	2308	2318	2361	2364	2385	2393
	2395	2434	2492	2553	2557	2595	2636	2638	2699	2706	2756	2758	2850
	2871	2878	2896	2898	3001	3038	3040	3128	3167	3204	3208	3225	3259
	3261	3270	3307	3316	3319	3334	3341	3421	3435	3467	3620	3623	3663
	3675	3678											
#CASE- 000000	1												
#6DST - 000067	1	2422	3509										
#ELOC- 000403	1	116	1175	1215	1224	1231	1237	1247	1253	1264	1270	1283	1289
	1303	1312	1319	1325	1335	1341	1352	1358	1371	1377	1401	1407	1417
	1423	1434	1440	1453	1459	1477	1483	1493	1499	1510	1516	1529	1535
	1560	1571	1591	1595	1598	1602	1605	1609	1615	1617	1619	1625	1642

16630	16740	16930	17020	17350	17400	17530	1761	17750	17800	17820	17840	17870
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19530	19590	19710	19800	19820	19880	20070	2012	20170	20250	20380	20510	20540
20680	20700	20770	20890	20990	21080	21260	21350	21370	21430	21820	21960	22030
22190	22270	22410	22590	22740	22770	22870	23060	23150	23180	23240	23640	2370
23780	23850	2389	23930	2399	24080	24100	24340	24400	24920	24980	25530	25570
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29570	29630	29780	29860	30010	30100	30380	30470	31280	31340	31670	31730	32040
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33160	33190	33240	33260	33280	33340	33390	33410	33460	34210	3426	34310	34350
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12370	12470	12490	12530	12640	12660	12700	12830	12850	12890	13030	13050	13080
13120	13190	13210	13250	13350	13370	13410	13520	13540	13580	13710	13730	13770
14010	14030	14070	14170	14190	14230	14340	14360	14400	14530	14550	14590	14770
14790	14830	14930	14950	14990	15100	15120	15160	15290	15310	15350	15600	15620
15710	15910	15930	15980	16000	16050	16070	16150	16170	16190	16250	16270	16420
16630	16650	16740	16930	16950	16980	17020	17350	17400	17530	17550	17750	17800
17820	17840	17860	17870	17890	17950	18020	18260	18280	18310	18350	18620	18700
18820	18840	18880	19000	19020	19050	19090	19370	19420	19530	19550	19590	19710
19730	19760	19800	19820	19840	19880	20070	20090	20170	20250	20270	20380	20460
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21080	21260	21280	21310	21350	21370	21390	21430	21820	21840	21960	22030	22050
22190	22270	22290	22410	22590	22610	22740	22770	22790	22870	23060	23080	23110
23150	23180	23200	23240	23600	23610	23630	23640	23660	23780	23850	23870	23930
23950	23970	24080	24100	24340	24360	24400	24920	24940	24980	25530	25550	25570
25590	25700	25720	25950	25970	26010	26360	26380	26410	26430	26990	27010	27060
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31690	31730	32040	32060	32080	32100	32160	32210	32250	32270	32340	32590	32610
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33240	33260	33280	33340	33360	33390	33410	33430	33460	34210	34230	34310	34350
34370	34410	34670	34690	34720	36200	36220	36230	36250	36370	36580	36630	36650
36750	36770	36780	36800	36840	36860	36880						

##FROM= 000000

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37830												
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12650	1266	12840	1285	13060	1307	13200	1321	13360	1337	13530	1354	13720
1373	14020	1403	14180	1419	14350	1436	14540	1455	14780	1479	14940	1495
15110	1512	15300	1531	15610	1562	15920	1593	15990	1600	16060	1607	16260
1627	16640	1665	16960	1697	17350	1736	17540	1755	17750	1776	17850	1786
17880	1789	18290	1830	18620	1863	18830	1884	19030	1904	19370	1938	19540
1955	19740	1975	19830	1984	20080	2009	20220	2023	20260	2027	20480	2049
20520	2053	20550	2056	20780	2079	21000	2101	21290	2130	21380	2139	21830
2184	22040	2205	22280	2229	22600	2261	22780	2279	23090	2310	23190	2320
23620	2363	23650	2366	23860	2387	23940	2395	23960	2397	24350	2436	24930
2494	25540	2555	25580	2559	25960	2597	26390	2640	27000	2701	27070	2708
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31680	3169	32050	3206	32090	3210	32260	3227	32620	3263	32710	3272	32990
3300	33080	3309	33170	3318	33200	3321	33350	3336	33420	3343	34220	3423

BGNHRD	10														
BGNHM	10														
BGNINI	10														
BGNMOD	10	3380													
BGNMSG	10														
BGNSFT	10														
BGNSRV	10	3151	3186	3586											
BGNSUB	10	1160	1227	1241	1257	1274	1315	1329	1345	1362	1397	1411	1427	1444	1473
	1487	1503	1520	1557	1621	1659	1841	1874	1914	2150	2200	2222	2244	2518	2575
	2654														
BGNMW	10														
BRESET	10	1136	1280	1368	1450	1526	1568	1639	1671	1878	2602	3100	3330		
CALL	1108	1724	1766	1851	1925	2161	2171	2253	2521	2546	2590	2677	2690	2780	2800
	2819	2928	2948	2969	3050	3290	3452	3630							
CKLOOP	10	1536	1573	1644	1676										
CLRVEC	10	1185	2608	2736											
COMEN	10	7490													
DEALLO	34830	35340	35800	37090	37530										
DEVREG	10														
DEVTYP	10														
DISPAT	10														
ELSE	1594	1601	1608	1760	1796	2011	2369	2388	2398	2562	2712	3312	3425	3627	3646
ENDCLM	10														
ENDCOM	10	7490													
ENDOO	1100	2071	2380												
ENDHRD	10														
ENDHM	10														
ENDIF	1174	1223	1236	1252	1269	1288	1311	1324	1340	1357	1376	1406	1422	1439	1458
	1482	1498	1515	1534	1570	1614	1616	1618	1641	1673	1701	1739	1779	1781	1794
	1801	1834	1869	1887	1908	1941	1958	1979	1987	2016	2037	2067	2069	2088	2107
	2134	2142	2195	2218	2240	2273	2286	2314	2323	2377	2407	2409	2439	2497	2569
	2571	2600	2642	2718	2720	2764	2812	2831	2861	2875	2882	2905	2962	2985	3009
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	3440	3471	3636	3657	3683	3685	3687								
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ENDINI	10														
ENDLOO	1806	3458													
ENDMOD	10														
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ENDRTN	3482	3533	3579	3708	3752										
ENDSFT	10														
ENDSRV	10	3179	3240	3597											
ENDSUB	10	1176	1238	1254	1271	1290	1326	1342	1359	1378	1408	1424	1441	1450	1484
	1500	1517	1537	1574	1645	1677	1872	1889	1943	2197	2220	2242	2288	2573	2604
	2722														
ENDSM	10														
ENDTST	10	1196	1291	1379	1461	1538	1575	1646	1678	1814	1890	1960	2114	2289	2472
	2618	2746	2884	3023	3243	3350									
EQUALS	10														
ERRDF	10	1172													
ERRHRD	10	1234	1250	1267	1286	1322	1338	1355	1374	1404	1420	1437	1456	1480	1496
	1513	1532	1565	1631	1668	1737	1777	1790	1867	1885	1939	1956	2032	2061	2083
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	14170	14340	14530	14770	14930	15100	15290	15600	15910	15980	16050	16250	16630	16930	16950		
	17530	17840	17870	18260	18280	18820	19000	19020	19530	19710	19730	19820	20070	20250	20470		
	20510	20540	20770	20990	21260	21280	21370	21820	22030	22270	22590	22770	23060	23080	23180		
	23610	23640	23850	23930	23950	24340	24920	25330	25370	25950	26360	26380	26990	27060	27560		
	27580	28500	28710	28780	28960	28980	30010	30380	30400	31280	31670	32040	32080	32250	32590		
	32610	32700	33070	33160	33190	33340	33410	34210	34330	34670	34200	34230	34630	36750	36780		
	#BRANC	10950	11010	11700	11840	12150	1216	12180	12320	12480	12650	12840	13030	1304	13060	13200	
		13360	13530	13720	14020	14180	14350	14540	14780	14940	15110	15300	15610	15920	15950	15990	
		16020	16060	16090	16260	16640	16930	1694	16960	17350	17540	17610	17750	17850	17880	17970	
		18040	1805	18070	18260	1827	18290	18620	18830	19000	1901	19030	19370	19540	19710	1972	
		19740	19830	20080	20120	20190	2020	20220	20260	20480	20520	20550	20720	20780	21000	21260	
		2127	21290	21380	21830	22040	22280	22600	22780	23060	2307	23090	23190	23360	2337	23410	
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28090		28280	28510	28640	28720	28790	28960	2897	28990	29360	2937	29410	2942	29570	29780		
30020		30130	30380	3039	30410	31180	3119	31210	31290	31680	32050	32090	32260	32590	3260		
32620		32710	32990	33080	33130	33170	33200	33350	33420	34220	34260	34360	34440	3445	34460		
3447		34590	34680	34700	34740	3475	35140	3515	35190	3520	35260	35310	35550	3556	35600		
3561		35630	3564	35680	3569	35710	35740	36210	36240	36280	36470	36610	36640	36760	36790		
36950																	
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	24920	25330	25570	25950	26360	26990	27060	27560	28500	28710	28780	28960	30010	30380	31280		
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		17200	17480	17570	17930	18000	18320	18420	18460	18750	19060	19150	19210	19500	19770	19850	
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	35220	35240	35580	35660	35960	36520	36540	36560	36930								
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1695	17530		17840	17870	18260	1828	18820	19000	1902	19530	19710	1973	19820	20070	20250		

MAINDEC-11 DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 132
CVDVCC.P11 12-SEP-84 08:55 CROSS REFERENCE TABLE -- MACRO NAMES

	20460	2047	20510	20540	20770	20990	21260	2128	21370	21820	22030	22270	22590	22770	23060
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	36630	36750	36780												
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	36780														
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	27580	28500	28710	28780	28960	28980	30010	30380	30400	31280	31670	32040	32080	32250	32590
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ELSE	15950	16020	16090	17610	17970	20120	23700	23890	23990	25630	27130	33130	34260	36280	36470
IFERRMS	10930	10940	10950	11010	11020	11040	11060	11090	11130	11160	11180	11210	11230	11240	11260
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EXIF3	18040	34440													
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GENTA	10930	10940	11010	1102	11590	11690	11750	12150	12170	1219	12240	12310	12370	12470	12530
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	30400	3042	30470	31160	31180	3122	31280	31340	31670	31730	32040	32080	32160	32210	32250
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	34350	34440	34670	36200	36230	36600	36630	36750	36780					
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	3335	33410	3342	34210	3422	34350	3436	34670	3468	36200	3621	36230	3624	36600
	36630	3664	36750	3676	36780	3679								
LET	11040	11060	11130	11160	11180	11210	11230	11260	11290	11320	11350	11480	11510	11520
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PMCLON	10													
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SETH	10660														
SETS	10930	10940	10960	11010	11590	11600	11690	11710	12150	12200	12310	12330	12470	12490	12640

MAINDEC 11 DVDVC-C MACY11 30A(1052) 12-SEP-84 15:41 PAGE 141
 CVDVCC.P11 12 SEP 84 08:55 CROSS REFERENCE TABLE -- MACRO NAMES

1TYPO 10 4317
 140CA 10
 1170 10

ABS. 014416 000

ERRORS DETECTED: 0

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 RUN TIME RATIO: 246/162=1.5
 CORE USED: 43K (85 PAGES)