

The image displays a grid of 15 columns and 15 rows of small tables. Each table contains technical data, likely related to CPU instructions or memory addresses. The data is organized into columns and rows, with some tables having headers and footers. The overall layout is a dense grid of technical information.



B01

EOF1CZDE00S0411

00010000

780519

IDENTIFICATION

0#HDR1CEQKCCSEQ

00010000

780519  
SEQ 0001

PRODUCT CODE: AC-7994C-MC  
PRODUCT NAME: CEQKCCD PDP11/70 CPU INSTRUCTION EXERCISER  
DATE CREATED: 15-MAR-78  
MAINTAINER: DIAGNOSTIC ENGINEERING  
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CONTENTS

1.0	ABSTRACT
2.0	REQUIREMENTS
2.1	Equipment
2.2	Storage
2.3	Preliminary Programs
3.0	LOADING PROCEDURE
3.1	Method
4.0	STARTING PROCEDURE
4.1	Control Switch Settings
4.2	Starting Addresses
4.3	Program and Operator Action
5.0	OPERATING PROCEDURE
5.1	Operational Switch Settings
5.2	Display Register
5.3	Operator Action
6.0	ERRORS
6.1	Error Halts and Description
6.2	Error Recovery
7.0	WARNINGS AND EXCEPTIONS
7.1	Warnings
7.2	Exceptions
8.0	MISCELLANEOUS
8.1	Execution Time
8.2	Stack Pointer
8.3	Pass Count
8.4	Iterations
8.5	T Bit Trapping
8.6	ACT11 Compatability
8.7	PSW and Margin Tables
8.8	I/O Device Address Modifications
8.9	Power Failure
9.0	PROGRAM DESCRIPTION
9.1	Micro Break Test
9.2	Unibus Exerciser Function
9.3	Mass Bus Tester Function
9.4	Line Clock Initialization

1.0 ABSTRACT

This program is designed to be a comprehensive check of the PDP-11/70 cpu cluster. The program executes each instruction in all address modes and includes tests for traps, interrupts, the mapping box, memory management, memory, the Unibus, and the Mass Bus. If not deselected, the program relocates the test code throughout memory (0-2m). Also, if not deselected, the program will relocate using available disks (RPO3, RK05, RPO4, RS03/4). See section 9.5 for a description of relocation.

The main differences between revision A and revision B are routines to use the UBE and MBT (manufacturing only), worst case testing occurs with all switches down, standard SYSMAC macros, and floating point processor tests.

Also, the disk driver was rewritten to make each device have a modular driver and to cause I/O to occur concurrently on the available disks. (see section 9.5.4 for a description of disk drivers)

Since worst case testing now occurs with all switches down, precautions must be taken to ensure the protection of user disks. Refer to section 7.0 for a description of warnings and exceptions.

2.0 REQUIREMENTS

2.1 Equipment

PDP-11/70 Central Processor with 16K of memory, a line clock, and an LA30 (or equivalent) console.

2.1.1 Optional Equipment Used

1. Unibus Exerciser
2. Mass Bus Tester
3. RP11/RPO3, RK11/RK05, RH70/RPO4, RH70/RS03/RS04
4. FP11-B, FP11-C

2.2 Storage

The program loads into the first 12K of memory and runs in all memory (exclusive of the XXDP monitor if running in chain mode).

2.3 Preliminary Programs

advisable that the CPU cluster (and floating point) diagnostics run first. These consist of:

DEKBA	DEKBF
DEKBB	DEKBG
DEKBC	DEMJA
DEKBD	DEFPA
DEKBE	DEFPB

### 3.0 LOADING PROCEDURE

#### 3.1 Method

The program is supplied on the diagnostic media. Refer to the XXDP operating manual for further information.

### 4.0 STARTING PROCEDURE

#### 4.1 Console Switch Settings

See Section 5.1

#### 4.2 Starting Addresses

The starting address for the exerciser is 200.

By starting at address 210, the switch register and display lights can be checked. This routine just moves the switches to the display register allowing the operator to toggle the switches and see the corresponding lights in the display register.

By starting at address 214, the micro-break register can be checked. This test requires a maintenance card. See Section 9.0 for further details.

#### 4.3 Program and Operator Action

1. Load program into memory (See Section 3)
2. Check for any system disk packs or configuration exceptions as described in section 7.0.
3. Load address 200
4. Set switches (See Section 5.1)
5. Press Start
6. The program will loop and messages will be typed at the end of each sub-pass and each pass. (see section 8.3 for

5.0 OPERATING PROCEDURE

5.1 Operational Switch Settings

SW15	HALT ON ERROR	This switch when set will halt the processor when an error is detected. Pressing continue will cause an error message to be typed and the processor will again halt. Pressing continue again will resume testing.
SW14	LOOP ON TEST	This switch when set will cause the program to loop on the current subtest.
SW13	INHIBIT ERROR TYPEOUT	This switch when set inhibits the error typeout.
SW12	INHIBIT UBE	This switch when set inhibits the initialization of the Unibus Exerciser. See section 9.2 for a description of the UBE function.
SW11	INHIBIT SUB- TEST ITERATION	This switch when set inhibits subtest iteration after the first pass. Each subtest is executed 10 times before the next subtest is run. Setting SW11 causes each test to be executed once before starting the next subtest.
SW10	RING BELL ON ERROR	This switch when set will ring the bell when an error is detected.
SW9	LOOP ON ERROR	This switch when set will cause the program to loop on the first failure even if the failure is intermittent. See section 6.1 for a description of looping on relocation errors.
SW8	RELOCATE WITH CPU ONLY	This switch when set will cause relocation to be done by the CPU instead of a disk. See section 9.5 for a

- SW7      INHIBIT SYSTEM SIZE TYPEOUT      This switch when set will inhibit the typeout of the switch definitions and the disks that will be used for relocation. (Typeout only occurs when the program is dumped)
- SW6      INHIBIT RELOCATION      This switch when set will inhibit all relocation. Do not change this switch while the program is running.
- SW5      INHIBIT ROUND ROBIN      This switch when set will only relocate using the device selected by switches <2:0> rather than all available devices.
- SW4      INHIBIT RANDOM DISK ADDRESS      This switch when set will cause relocation to always start at address 0 on the disk(s).
- SW3      INHIBIT MBT      This switch when set inhibits the initialization of the Mass Bus Tester. See section 9.3 for a description of the MBT function.
- SW2-SW0      DEVICE CODES      These switches (along with SW5) cause the program to relocate the test code using the device specified below:

VALUE	DEVICE
0	RP11/rp03
1	RK05
2	Not used
3	Not used
4	RH70/RP04
5	RH70/RS03/RS04
6	Not used
7	Not used

NOTE

When relocating via a specific device, set in the value(SW<2:0>) to select the device then set switch 5.

Unit 0 of the load device is marked not



mode, and therefore will not be used to relocate.

5.2 Display Register

While the program is running, the low byte of the display register contains the subtest number and the high byte contains bits <14:7> of KERNEL PAR0. These bits, of kernel par0, correspond to bits <20:13> of the physical address of the relocated code. When an error is detected and loop on error is selected, the high byte contains the error count.

5.3 Operator Action

When the program is loaded\* and started with switch 7 on a zero the program will typeout the disks and unit numbers that will be used for relocation and then wait for the operator to type a character. This is to allow the operator to write protect any drive that is not to be used. If there are no devices available for relocation, operator action is not required.

If the program is loaded via ACT11 in QV or AA or with XXDP in chain mode no operator action is required and all disks not write protected (except for the XXDP media) will be used for relocation.

\*Except chain mode, QV(manufacturing only), or Auto Accept (manufacturing only)

6.0 ERRORS

6.1 Error Halts and Description

If an error is detected, the program will trap to the error handling routine (SERROR). If halt on error is enabled, the processor will halt. Pressing continue will cause an error message to be typed and the processor will halt again.

There are many different types of errors. No matter which type occurs a minimum set of information is typed as follows:

```
HHH:MM:SS
ERRORPC PHYSC PC   PSW   MAINT  TEST NO SUB-PASS CNT
UUUUUU  VVVVVVVV  WWWWWW XXXXXX YYYYYY SSSSSS PPPPPP
```

where:

```
UUUUUU   = Virtual PC of the error call.
VVVVVVVV = Physical PC of the error call.
```

XXXXXX = Contents of the maintenance register(1777750).  
 YYYYYY = Test number.  
 SSSSSS = Sub-pass count (0 thru 5)  
 PPPPPP = Pass count

HHH:MM:SS Represents the elapsed run time of the program, since the most previous start, where: HHH = hours, MM = minutes, and SS = seconds.

The Virtual PC is the 16 bit word that was pushed on the stack when the error call was made. The physical PC is calculated in one of two ways:

1. If memory management is off the contents of location "FACTOR" is subtracted from the Virtual PC. This generates the corresponding PC for the non-relocated code.
2. If memory management is on the contents of the appropriate PAR is shifted and added to the Virtual PC to generate a physical 22 bit address. In this case the virtual PC corresponds to the non-relocated code.

The contents of the maintenance register will indicate what memory margin was being performed when the error occurred.

Depending on the type of error additional information is typed as described below.

5.1.1 Unexpected Trap to 4

PCOFTP	PHYSPC	PSW	CPUERR
VVVVVV	PPPPPPP	YYYYYY	ZZZZZZ

VVVVVV = Virtual PC that was pushed on the stack when the trap occurred.  
 PPPPPPP = Physical PC calculated as described above.  
 YYYYYY = PSW that was pushed on the stack.  
 ZZZZZZ = Contents of the CPU error register(1777766).

5.1.2 Unexpected Trap to 114

PCOFTP	PHYSPC	PSW	ERRREG	ERR ADR REG
VVVVVV	PPPPPPP	YYYYYY	ZZZZZZ	EEEEEEEE

V, P, and Y = are the same as described in 5.1.1.  
 ZZZZZZ = Contents of the memory error register (777744).  
 EEEEEEEE = Contents of the error address registers combined into a 22 bit address (777740 & 777742).

5.1.3 Parity Error During Data Check

This error can only occur during the data check that is made on the relocated test code before it is executed. This check is made by comparing the unrelocated code with the relocated

destination data to the relocated code.

SRCADR DSTADR EADDRREG MEM ERR REG  
SSSSSS DDDDDDDD EEEEEEEE ZZZZZZ

SSSSSS = Virtual address of the source data.  
DDDDDDDD = Physical address of the destination data.  
EEEEEEEE = Contents of the error address registers.  
ZZZZZZ = Contents of memory error register (777744).

5.1.4 Error During Data Check-Reloc was by CP

This error is similar to 6.1.3 except instead of a parity error, it is a data comparison error. Refer to section 9.5.3 for a description of CP relocation.

Loop on error (SW<9>) has the following effect:

1. Memory Management Off- If switch<9> is set, looping will be performed on the section relocation (see section 9.5.1). If SW<9> is not set, execution will continue at the beginning of the next section.
2. Memory Management On- If SW<9> is set, looping will be performed on the program relocation (see section 9.5.2) to the same memory space that failed. If SW<9> is not set, program relocation will be retried in the same memory space.

5.1.5 Error During Data Check-Reloc was by I/O

This error is the same as 6.1.4 except relocation was performed via a disk rather than the CP. The error printout will identify which device and drive number transferred the particular word that failed. Refer to section 9.5.4 for a description of I/O relocation.

Loop on error (SW<9>) has the following effect:

1. If SW<9> is set, the device that relocated the word (that caused the data check error) is initiated to do the same transfer with the same disk address and memory addresses. This transfer will continually be initiated and checked until SW<9> is not set.

5.1.6 Device Error

This error occurs if a device error occurs while the device is doing a transfer. The device and drive number are identified and the contents of the device registers are typed.

When SW<9> (loop on error) is set, the device that failed is continually restarted with the same disk address, memory address, and function that caused the error.

6.1.7 Unibus Exerciser Failed

CC	BUSADR	CR2	CR1	PHYS BUS ADR
XXXXXX	VVVVVV	MMMMM	YYYYYY	ZZZZZZZZ

XXXXXX = Cycle count.  
 VVVVVV = Virtual bus address that the UBE failed at  
 MMMMM = Control register number 2  
 YYYYYY = Control register number 1  
 ZZZZZZZZ = Physical memory address that the UBE failed at

The physical memory address is calculated by adding the appropriate map register to the virtual bus address, forming a real 22 bit memory address.

6.1.8 UBE Non-Existant Memory Error

This error only occurs when the "NO SLAVE SYNC" error occurs in the unibus exerciser. Only the physical address that timed out is typed. This error might indicate that there is a hole in memory or that the size register (777760) is set wrong.

6.1.9 Mass Bus Tester Failed

CS1	WRDCNT	BUSADR	BADREX	MR2	CS2	ST
AAAAA	BBBBBB	CCCCC	DDDDDD	EEEEEE	FFFFFF	GGGGGG

ER	CS3
HHHHH	JJJJJJ

AAAAA = Control and status register #1 (760100).  
 BBBBBB = Word count register (760102).  
 CCCCC = Bus address register (760104).  
 DDDDDDD = Bus address extended register (760174).  
 EEEEEEE = Maintenance register #2 (760106).  
 FFFFFFF = Control and status register #2 (760110).  
 GGGGGG = Status register (760112).  
 HHHHHH = Error register (760114).  
 JJJJJJ = Control and status register #3 (760176).

6.1.10 MBT Non-Existant Memory Error

This is the same as 6.1.7 except that it is detected by the NEXM bit in CS2 of the MBT.

6.1.11 Floating Point Error

This error will only occur if the left and right hand sides of the floating point identities do not agree within the expected tolerance. The value of the calculations are typed out.

This error should only be a function of the Floating Point Processor and the FPP diagnostics (DEFPA DEFPA) should be used

### 6.1.12 Device Hung

This error will occur if a device does not finish its relocation function within 2 seconds after its initiation. If a line clock is not installed, a hung device will hang the program. Refer to section 9.5.4.4 to determine which device and drive is hung.

## 6.2 Error Recovery

Different types of errors recover in different ways as described below.

### 6.2.1 Errors Within Subtests

Execution starts with the instruction following the error call.

### 6.2.2 Relocation with Memory Mgmt. Off

Execution starts at the beginning of the next section.

### 6.2.3 Device Error or CP Relocation with Memory Mgmt. On

Relocation is restarted.

### 6.2.4 Unexpected Traps Except Parity (4,10,250)

Execution starts at the address pointed to by location "SLPERR". This location contains the address+2 of the most recently executed "SCOPE" instruction.

### 6.2.5 Unexpected Parity Error

If the parity error is fatal (Bit 2 or 3 set in error reg) the program types a restart message at restarts. Otherwise, execution starts as in 6.2.4.

## 7.0 WARNINGS AND EXCEPTIONS

### 7.1 Warnings

Any drive that is not "write protected" will be written on (except unit 0 of the XXDP load device in chain mode).

When the program is dumped (see section 5.3) and SW<7> is set, the devices and drives that are not write protected will be identified on the terminal. Before typing a character to continue, a drive can be write protected without causing an error because, the system is sized again.

-----  
If any of the devices is located at a non-standard address (see below), the device register address tables (in "common tags") should be changed to the correct addresses. Following is the default address of the control and status register of each device:

RPO3----176714  
RK05----177404  
RPO4----176700  
RS03/4--172040

If the system has both an RPO3 and an RPO4, the branch instruction at 1005, in the "size routine" must be replaced by a nop (240) for both devices to be used. This branch is approximately at address 4552.

## 8.0 MISCELLANEOUS

### 8.1 Execution Time

The execution time is dependent on the amount of memory on the system. Following are two typical run times:

1. Manufacturing Basic Line-32K memory, UBE, MBT, and no disks---3 minutes.
2. System-128K memory, 2 RK05's, RPO4, and 2 RS04's ---9 minutes.

### 8.2 Stack Pointer

The stack pointer is set to 700.

#### NOTE

When the program is running in either user or supervisor mode, the user/supervisor stack pointer is set to 700 and the Kernel stack pointer is set to 1200. The Kernel stack pointer is used only for the Error and Interrupt Service routines. routines.

### 8.3 Pass Count

There are two words used for effective pass count. Location "SUBPASS" and "SPASS". Subpass contains the ASCII representation of the subpass count. This is used to index

Six subpasses are executed for each pass. This allows all margins and PSW combinations to be tested before reporting end of pass.

At the end of each subpass the subpass number (that is being started) is typed followed by "THE QUICK BROWN FOX JUMPED OVER THE LAZY DOGS BACK 0123456789". If running on ACT11 QV or AA, only the sub-pass number is typed. At the end of each pass the elapsed run time and the message "END PASS X TOTAL ERRORS SINCE LAST REPORT Y" is typed.

#### B.4 Iterations

Sub-test iterations are not performed until the pass count (SPASS) is non-zero. This makes a QV pass as short as possible.

After the first pass, full 10 octal iterations are performed on each subtest.

#### B.5 T-Bit Trapping

T bit trapping is controlled by the PSW table. The default condition is to run with the T-Bit on during subpasses 2, 4, and 6.

#### B.5 ACT-11 Compatability

The program is fully ACT-11 compatible.

#### B.7 PSW and Margin Tables

At the end of the program, just before the messages, are the PSW and margin tables. These tables control what mode and register set and which memory margin will be executed on a subpass. Refer to section 9.5.2 for a description of how these tables are used by the program. These tables may be modified if desired.

#### B.8 I/O Device Address Modification

To modify the program address of the I/O devices patch the appropriate device table (in the common tags area) to the desired addresses.

If you are patching the RPO3 or RPO4 see section 7.2.

#### B.9 Power Fail

If a power fail occurs (followed by a power up), the word "POWER" is typed on the terminal and the program restarts.

9.0 PROGRAM DESCRIPTION

The program is divided into 9 sections of position independent relocatable test code. Each section is approximately 1K words long.

When the program is initially loaded and started it will identify itself and type the function of the switch register and the devices and drives that will be used for relocation, if SW7=0. It will also type the CP options available indicator word (OPT.CP). The contents of OPT.CP contain the following indicators:

Bit15	=	Not used
Bit14	=	Not used
Bit13=1/0	=	FPP available/not available
Bit12	=	Not used
Bit11	=	Not used
Bit10=1/0	=	MBT available/not available
Bit09=1/0	=	KW11-L available/not available
Bit08=1/0	=	Console tty available/not available
Bit07=1/0	=	UBE available/not available
Bits06-00	=	Not used

Following is a brief description of each section:

Section 0 This section causes a 256 word 3 Xor 9 test pattern to be relocated throughout memory 0 - 28K.

NOTE: This should not be construed to be a complete memory test.

Section 1 This section tests the unary instruction set executing each unary instruction in each address mode (excluding unary instructions using address mode 7).

Section 2 This section tests the unary instructions using address mode 7 and binaries in all address modes (excluding binary byte ops using address mode 7).

Section 3 This section tests binary byte ops using address mode 7, JMP, JSR and program trap (IOT, TRAP, and EMT) instruction.

Section 4 This section checks that each bit in the processor status word (PSW) can be set cleared, reserved instructions, and odd address traps.

Section 5 This section checks the SXT, XOR, SOB, MARK, RTT



Section 6 This section checks the ASH, ASHC, MUL, DIV, SPL instructions and the program interrupt request (PIRQ) logic.

Section 7 This section checks the stack limit register memory management abort logic, the memory management registers, and the mapping box registers.

Section 8 This section checks the floating point option, (FP11-B or FP11-C) if available.

Following section 8 are two routines to check the teletype printer logic and a routine to start the KW11-L clock. If the KW11-L is available the priority arbitration logic is tested.

#### 9.1 Micro-Break Test

The micro-break test is used to test the micro break comparators and the stop on micro match function of the maintenance card. To run this test the operator must have a maintenance card installed and start the program at address 214.

The program asks the operator to turn on the stop on micro match switch. It then checks certain bit patterns in the micro break register to ensure the processor does not stop when it is not supposed to.

The processor will then stop with zero in the micro address lights. The operator then hits continue, and the processor will stop with one (1) in the lights. This sequence continues with 2, 4, 10, 20, 40, and 200 appearing in the lights. The program types done when it is finished.

#### 9.2 Unibus Exerciser(UBE)

Any one of 4 UBE's will be used. The program looks for a UBE at addresses 17770004, 17770024, 17770034, and 17770044.

Test 75 will initiate the unibus exerciser if it is present. This is only done on pass 1 - subpass 1, since from that point on, the service routine takes care of restarting it.

The UBE is initially set up with a bus address of 0. The function that is loaded is "DATA IN PAUSE-DATA OUT BYTE". The word count is set for 4K bytes. It is also set to interrupt on level 5.

When an interrupt occurs a check is made to see if it was caused by an error. If there was no error, 776 is loaded as the bus address and the UBE is started again. On the next

sequence continues until a memory timeout error occurs.

When an error occurs a check is made to see if it was caused by a memory timeout. If it was, the address in the UBE bus address register is compared with the address in the system size registers. If they are the same (no holes in memory) the UBE is restarted at address 0 and the above sequence is repeated. If the addresses are not the same a memory-hole error is reported.

If the error was not due to a timeout a UBE error is reported.

### 9.3 Mass Bus Tester(MBT) -----

Any one of 4 MBT's will be used. The program looks for an MBT at addresses 17770100, 17770200, 17770300, and 17770400. If an MBT is found, the drive type register (17770X26) is checked to make sure that it really is an MBT.

Test 75 also initiates the mass bus tester. Again, this is only done on Pass 1 - subpass 1 since the service routine keeps it running.

The bus address register is initially set to 0, the word count to 2K words, and a read function is initiated.

When an interrupt occurs an error check is made. This error check is the same as that described for the UBE. If there was no error, the word count is reloaded and the function is issued. The bus address register is not changed so it will continue from where it left off.

### 9.4 Line Clock Initialization -----

Test 75 turns on the line clock. Two locations in "common tags" keep track of the elapsed run time of the program. When the clock interrupts, the low byte of location "lticks" is incremented. When this byte gets to 60(decimal) it is cleared and the high byte is incremented(seconds). When the second count gets to 60(decimal) location "mticks" is incremented and lticks is cleared. This gives the timer a 64K decimal minute range.

#### NOTE

For the UBE, MBT, and Line Clock, when an interrupt occurs, program execution returns to Kernel mode and the Kernel PAR's are mapped down to the 0-12K bank of memory. Upon returning from the interrupt the PAR's are mapped back to where they were and the previous

## 9.5 Relocation Algorithm

### 9.5.1 Section Relocation

As each section is entered the virtual start address is saved in location "FRSTAD" and the relocation factor (byte offset from non-relocated code) is calculated and saved in location "FACTOR". The test code is then executed.

At the end of each section, control is transferred to the "relocation routine". If SW<8> is set, this routine will relocate the section via the CP (see 9.5.3). If SW<8> is not set, the length of the section is calculated, saved as a word count, and control is transferred to the "I/O monitor" (see section 9.5.4) which relocates the section by using a disk.

Each section is initially relocated to the end address of the program. Subsequent relocations start at the end of the previous relocation. For example: if section 0 is 1000 bytes long and the end address of the program is 60000, the first relocation starts at address 60000, the second at 61000, the third at 62000, etc. This continues until 28K has been reached at which time execution goes to the start of the next section and the process repeats with the new section.

Each section is written in position independent code so that it can be relocated and executed without the use of memory management.

### 9.5.2 Program Relocation

When all nine sections have been relocated and executed thru 28K (see section 9.5.1), memory management is setup according to the value in location "NEXPAR". This value is initialized to 600 (or 1600 if running under the XXDP monitor), making relocation start at address 60000 (or 160000). The "I/O monitor" is then entered (see section 9.5.4) to relocate the program. When the I/O monitor completes the relocation, execution is transferred to the start of the program at the relocated position.

Each section is executed only once with memory management on. At the end of section 8, 77 is added to "NEXPAR" and relocation is performed again. This causes the next relocation to move up by 7700 bytes. For example: If nexpar=1600 the first relocation starts at address 160000, the second at address 167700, the third at 177600, etc.

This continues until the end of memory is reached and constitutes a sub-pass. The PSW and maintenance register (for memory margins) are then setup for the next sub-pass and the program restarts.

the tables (see section 8.7). The particular entry that is used is obtained by indexing the table by the sub-pass number (see section 8.3). For example, sub-pass 3 uses word 3 (the first word is counted as zero) of each table. Therefore, to change the value in the PSW or maintenance register only requires changing the value in the appropriate table.

The completion of 6 sub-passes constitutes a pass and an end of pass message is typed. The program then restarts in pass 2, sub-pass 0.

### 9.5.3 Relocation VIA CP

If SW<8> is set, both section and program relocation (see sections 9.5.1 and 9.5.2), are performed by an instruction move loop rather than a disk. For example:

```
IS: MOV (R0)+,(R2)+
      CMP R0,R3
      BNE IS
```

where R0 is the address of the code being moved, R2 is the address that it is being moved to, and R3 is the last address that is to be moved.

When this is finished, the relocated data is checked by an instruction compare loop to ensure that the relocation was performed correctly.

### 9.5.4 Relocation VIA I/O

If SW<8> is not set, both section and program relocation (see section 9.5.1 and 9.5.2), are performed by writing the data to a disk and reading it back to the relocated position. This relocation is controlled by the "I/O Monitor".

#### 9.5.4.1 Section Relocation

When the I/O monitor is entered from the "relocation routine" (see section 9.5.1) a device is selected (see 9.5.4.3), the memory addresses (from and to) and word count are passed to the device handler (see section 9.5.4.4), and the handler is called. When the handler finishes, the I/O monitor checks the relocated data with an instruction compare loop to ensure the relocated data is correct, and returns to the "relocation routine" (see 9.5.1).

#### 9.5.4.2 Program Relocation

When the I/O monitor is entered for program relocation (see section 9.5.2) the base address for the relocation is calculated from the contents of kernel par3 which was set up with memory management (see 9.5.2). If SW<8> is set,

If SW<8> is not set, a device is selected (see 9.5.4.3), the word count is set to 2K, and the memory addresses (from and to) and word count are passed to the device handler (see 9.5.4.4) and the handler is called. The I/O monitor then adds 2K to the memory addresses, selects another device, passes the addresses to the device handler, and calls the handler. This continues until all 12K has been relocated. The relocated data is then checked with an instruction compare loop. The relocated program is then executed as described in 9.5.2.

#### 9.5.4.3 Device Selection

If SW<5> is not set, an index is picked up from location "DEVINDX". This index is used to index the system size table. The system size table consists of 8 words (one for each device type). Bits <7:0> of each word are used to indicate the drive numbers that are available on the device, and are initialized in the size routine. Bits <15:8> of each word are used to indicate whether the drive has been used for a data transfer (unit used bit).

The system size table is then searched, using the index described above, for a drive that has not been used. When a drive is found, the "unit used bit" is set, the current index is put back in location DEVINDX, and execution continues as described in 9.5.4.1 or 9.5.4.2.

If an unused unit is not found, all the "unit used" bits are cleared and the search is restarted. If the search finds the system size table empty (no devices on the system), the message "NO I/O DEVICES" is typed and relocation is performed via the CP as described in 9.5.3.

If SW<5> is set, SW's<2:0> are used to index the system size table. In this case only one word of the table is used corresponding to the device being selected by SW's<2:0> (see section 5.1). In this mode, a round robin selection is performed on the drives of the selected device.

#### 9.5.4.4 Device Handlers

Each device that is used for relocation has a handler. These handlers are functionally the same.

The handler is called by the I/O Monitor (see section 9.5.4). It first clears the done bit (bit 7) in the handler status word. This prevents the monitor from calling this handler again before it is finished.

If a "device hung" error (see section 6.1.12) is detected, the handler status words can be examined to determine which device did not finish (set bit 7). The drive can then be determined by looking in the "device handler unit number" table. The

are located in the "common tags" area of the listing.

Then the handler calculates a disk address. This address is either generated from a random number (SW4=0) or is set to zero (SW4=1). The device ID, unit number, and cylinder address are combined and placed in the "RUN TABLE" (RONTBL). The position in the run table corresponds to which 2K block of the program is being transferred (i.e. the first 2K block is identified by word 1, the second 2K by word 2, etc.). The bit configuration of each word in the run table is as follows:

<15:13> = Device ID  
<12:10> = Unit Number  
<9> = not used  
<8:0> = Cylinder Address

The track-sector address of the transfer is saved in the "RUN TRACK TABLE" (RUNTRAK). The position in this table is as described above. The bit configuration of each word is the same as that for the disk address register for the particular device. Bit 15 is used to indicate a device error. It is set by the device service routine. (see section 9.5.4.5)

The handler then initializes the device registers with all the appropriate information and starts a write function. Execution then returns to the I/O Monitor at the point where the handler was called.

#### 9.5.4.5 Device Service Routines

Each device that is used for relocation has a service routine. These routines are all functionally the same.

The routine is entered by a device interrupt. The device is checked for any errors. If no error occurred the device registers are loaded and the next function to perform is initiated. Three functions are executed: Write, Write Check, and Read. All the necessary bus address information is calculated by the I/O Monitor, so the service routine just takes care of the device.

When the read function has been completed successfully, the done bit (bit 7) in the handler status word is set.

Upon initiation of a function, or completion of all three functions, the service routine returns execution to where it was when it was interrupted.

If an error is detected, the function that failed is retried two more times. If the error is still present the done bit and the error bit (bit 15) is set in the handler status word along with bit 15, in the appropriate entry, in the RUN TRACK







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.SBTTL BASIC DEFINITIONS

```
.*INITIAL ADDRESS OF THE STACK POINTER *** 1200 ***  
STACK= 1200 ;:FIRST ADDRESS OF THE STACK  
KERSTK= STACK ;:KERNEL STACK  
SUPSTK= STACK-300 ;:SUPERVISOR STACK  
USESTK= STACK-400 ;:USER STACK  
.EQUIV EMT,ERROR ;:BASIC DEFINITION OF ERROR CALL  
.EQUIV IOT,SCOPE ;:BASIC DEFINITION OF SCOPE CALL  
PS= 177776 ;:PROCESSOR STATUS WORD  
.EQUIV PS,PSW  
STKLMT= 177774 ;:STACK LIMIT REGISTER  
PIRQ= 177772 ;:PROGRAM INTERRUPT REQUEST REGISTER  
SWR= 177570 ;:SWITCH REGISTER  
DISPLAY=SWR
```

.\*MISCELLANEOUS DEFINITIONS

```
HT= 11 ;:CODE FOR HORIZONTAL TAB  
LF= 12 ;:CODE LINE FEED  
CR= 15 ;:CODE CARRIAGE RETURN  
CRLF= 200 ;:CODE FOR CARRIAGE RETURN-LINE FEED
```

.\*GENERAL PURPOSE REGISTER DEFINITIONS

```
R0= %0 ;:GENERAL REGISTER  
R1= %1 ;:GENERAL REGISTER  
R2= %2 ;:GENERAL REGISTER  
R3= %3 ;:GENERAL REGISTER  
R4= %4 ;:GENERAL REGISTER  
R5= %5 ;:GENERAL REGISTER  
R6= %6 ;:GENERAL REGISTER  
R7= %7 ;:GENERAL REGISTER  
.EQUIV R0,R10 ;:GENERAL REGISTER  
.EQUIV R1,R11 ;:GENERAL REGISTER  
.EQUIV R2,R12 ;:GENERAL REGISTER  
.EQUIV R3,R13 ;:GENERAL REGISTER  
.EQUIV R4,R14 ;:GENERAL REGISTER  
.EQUIV R5,R15 ;:GENERAL REGISTER  
SP=%6 ;:STACK POINTER  
.EQUIV SP,KSP ;:KERNEL STACK POINTER  
.EQUIV SP,SSP ;:SUPERVISOR STACK POINTER  
.EQUIV SP,USP ;:USER STACK POINTER  
PC=%7 ;:PROGRAM COUNTER
```

.\*PRIORITY LEVEL DEFINITIONS

```
PR0= 0 ;:PRIORITY LEVEL 0  
PR1= 40 ;:PRIORITY LEVEL 1  
PR2= 100 ;:PRIORITY LEVEL 2  
PR3= 140 ;:PRIORITY LEVEL 3  
PR4= 200 ;:PRIORITY LEVEL 4  
PR5= 240 ;:PRIORITY LEVEL 5  
PR6= 300 ;:PRIORITY LEVEL 6  
PR7= 340 ;:PRIORITY LEVEL 7
```

.\*"SWITCH REGISTER" SWITCH DEFINITIONS

127 040000  
128 020000  
129 010000  
130 004000  
131 002000  
132 001000  
133 000400  
134 000200  
135 000100  
136 000040  
137 000020  
138 000010  
139 000004  
140 000002  
141 000001

SW14= 40000  
SW13= 20000  
SW12= 10000  
SW11= 4000  
SW10= 2000  
SW09= 1000  
SW08= 400  
SW07= 200  
SW06= 100  
SW05= 40  
SW04= 20  
SW03= 10  
SW02= 4  
SW01= 2  
SW00= 1  
.EQUIV SW09, SW9  
.EQUIV SW08, SW8  
.EQUIV SW07, SW7  
.EQUIV SW06, SW6  
.EQUIV SW05, SW5  
.EQUIV SW04, SW4  
.EQUIV SW03, SW3  
.EQUIV SW02, SW2  
.EQUIV SW01, SW1  
.EQUIV SW00, SW0

.\*DATA BIT DEFINITIONS (BIT00 TO BIT15)

153 100000  
154 040000  
155 020000  
156 010000  
157 004000  
158 002000  
159 001000  
160 000400  
161 000200  
162 000100  
163 000040  
164 000020  
165 000010  
166 000004  
167 000002  
168 000001

BIT15= 100000  
BIT14= 40000  
BIT13= 20000  
BIT12= 10000  
BIT11= 4000  
BIT10= 2000  
BIT09= 1000  
BIT08= 400  
BIT07= 200  
BIT06= 100  
BIT05= 40  
BIT04= 20  
BIT03= 10  
BIT02= 4  
BIT01= 2  
BIT00= 1  
.EQUIV BIT09, BIT9  
.EQUIV BIT08, BIT8  
.EQUIV BIT07, BIT7  
.EQUIV BIT06, BIT6  
.EQUIV BIT05, BIT5  
.EQUIV BIT04, BIT4  
.EQUIV BIT03, BIT3  
.EQUIV BIT02, BIT2  
.EQUIV BIT01, BIT1  
.EQUIV BIT00, BIT0

.\*BASIC "CPU" TRAP VECTOR ADDRESSES

170  
171  
172  
173  
174  
175  
176  
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179  
180  
181

## BASIC DEFINITIONS

```

183      000010      RESVEC= 10      ::RESERVED AND ILLEGAL INSTRUCTIONS
184      000014      TBITVEC=14      ::"T" BIT
185      000014      TRTVEC= 14      ::TRACE TRAP
186      000014      BPTVEC= 14      ::BREAKPOINT TRAP (BPT)
187      000020      IOTVEC= 20      ::INPUT/OUTPUT TRAP (IOT) **SCOPE**
188      000024      PWRVEC= 24      ::POWER FAIL
189      000030      EMTVEC= 30      ::EMULATOR TRAP (EMT) **ERROR**
190      000034      TRAPVEC=34      ::"TRAP" TRAP
191      000060      TKVEC= 60      ::TTY KEYBOARD VECTOR
192      000064      TPVEC= 64      ::TTY PRINTER VECTOR
193      000114      CACHVEC=114     ::CACHE ERROR INTERRUPT VECTOR
194      000240      PIRQVEC=240    ::PROGRAM INTERRUPT REQUEST VECTOR
195      000250      MMVEC= 250     ::MEMORY MANAGEMENT VECTOR

```

## .SBTTL CACHE REGISTER DEFINITIONS

```

196
197
198
199
200      177740      LOADRS = 177740   ::LOWER 16 BITS OF ADDRESS THAT CAUSED ERROR
201      177742      HIADRS = 177742   ::UPPER SIX BITS OF ADDRESS THAT CAUSED ERROR
202      177744      MEMERR = 177744   ::CACHE ERROR REGISTER
203      177746      CONTRL = 177746   ::MEMORY CONTROL REGISTER
204      177750      MAINT = 177750   ::MEMORY MAINTENANCE REGISTER
205      177752      HITMIS = 177752   ::HIT MISS REGISTER "1" IMPLIES HIT IN CACHE
206
207

```

## .SBTTL CPU REGISTER DEFINITIONS

```

208
209
210
211      177760      SIZELO = 177760   ::MEMORY SIZE REGISTER NUMBER TO PUT INTO A PAR
212      177762      SIZEHI = 177762   ::TO GET TO THE LAST 32 WORDS OF MEMORY
213      177762      SIZEHI = 177762   ::HIGH SIZE REGISTER, RESERVED FOR FUTURE USE
214      177764      SYSTID = 177764   ::CURRENTLY ALL ZERO
215      177766      CPUERR = 177766   ::SYSTEM ID REGISTER
216      177766      CPUERR = 177766   ::CPU ERROR REGISTER HOLDS CONDITION THAT CAUSED
217      177766      CPUERR = 177766   ::THE TRAP TO ERRVEC (000004)
218
219

```

## .SBTTL MEMORY MANAGEMENT DEFINITIONS

```

220
221
222
223
224
225      ;#MEMORY MANAGEMENT STATUS REGISTER ADDRESSES
226
227      177572      MMRO= 177572
228      177574      MMR1= 177574
229      177576      MMR2= 177576
230      172516      MMR3= 172516
231      .EQUIV MMR0,SR0
232      .EQUIV MMR1,SR1
233      .EQUIV MMR2,SR2
234      .EQUIV MMR3,SR3
235

```

## ;#USER "I" PAGE DESCRIPTOR REGISTERS

236

237

239	177602	UIPDR1= 177602
240	177604	UIPDR2= 177604
241	177606	UIPDR3= 177606
242	177610	UIPDR4= 177610
243	177612	UIPDR5= 177612
244	177614	UIPDR6= 177614
245	177616	UIPDR7= 177616

;\*USER "D" PAGE DESCRIPTOR REGISTORS

246	177620	UDPDR0= 177620
247	177622	UDPDR1= 177622
248	177624	UDPDR2= 177624
249	177626	UDPDR3= 177626
250	177630	UDPDR4= 177630
251	177632	UDPDR5= 177632
252	177634	UDPDR6= 177634
253	177636	UDPDR7= 177636

;\*USER "I" PAGE ADDRESS REGISTERS

254	177640	UIPAR0= 177640
255	177642	UIPAR1= 177642
256	177644	UIPAR2= 177644
257	177646	UIPAR3= 177646
258	177650	UIPAR4= 177650
259	177652	UIPAR5= 177652
260	177654	UIPAR6= 177654
261	177656	UIPAR7= 177656

;\*USER "D" PAGE ADDRESS REGISTERS

262	177660	UDPAR0= 177660
263	177662	UDPAR1= 177662
264	177664	UDPAR2= 177664
265	177666	UDPAR3= 177666
266	177670	UDPAR4= 177670
267	177672	UDPAR5= 177672
268	177674	UDPAR6= 177674
269	177676	UDPAR7= 177676

;\*SUPERVISOR "I" PAGE DESCRIPTOR REGISTERS

270	172200	SIPDR0= 172200
271	172202	SIPDR1= 172202
272	172204	SIPDR2= 172204
273	172206	SIPDR3= 172206
274	172210	SIPDR4= 172210
275	172212	SIPDR5= 172212
276	172214	SIPDR6= 172214
277	172216	SIPDR7= 172216

;\*SUPERVISOR "D" PAGE DESCRIPTOR REGISTERS

278	172220	SDPDR0= 172220
-----	--------	----------------

305	172224	SDPOR2= 172224
306	172226	SDPOR3= 172226
307	172230	SDPOR4= 172230
308	172232	SDPOR5= 172232
309	172234	SDPOR6= 172234
310	172236	SDPOR7= 172236
311		
312		
313		
314		
315	172240	
316	172242	SIPAR0= 172240
317	172244	SIPAR1= 172242
318	172246	SIPAR2= 172244
319	172250	SIPAR3= 172246
320	172252	SIPAR4= 172250
321	172254	SIPAR5= 172252
322	172256	SIPAR6= 172254
323		SIPAR7= 172256
324		
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333		
334		
335		
336		
337	172260	
338	172262	SDPAR0= 172260
339	172264	SDPAR1= 172262
340	172266	SDPAR2= 172264
341	172270	SDPAR3= 172266
342	172272	SDPAR4= 172270
343	172274	SDPAR5= 172272
344	172276	SDPAR6= 172274
345		SDPAR7= 172276
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;**#SUPERVISOR "I" PAGE ADDRESS REGISTERS**

;**#SUPERVISOR "D" PAGE ADDRESS REGISTERS**

;**#KERNEL "I" PAGE DESCRIPTOR REGISTERS**

;**#KERNEL "D" PAGE DESCRIPTOR REGISTERS**

;**#KERNEL "I" PAGE ADDRESS REGISTERS**

351 172346  
352 172350  
353 172352  
354 172354  
355 172356

KIPAR3= 172346  
KIPAR4= 172350  
KIPAR5= 172352  
KIPAR6= 172354  
KIPAR7= 172356

;\*KERNEL "D" PAGE ADDRESS REGISTERS

358 172360  
359 172362  
360 172364  
361 172366  
362 172370  
363 172372  
364 172374  
365 172376

KDPAR0= 172360  
KDPAR1= 172362  
KDPAR2= 172364  
KDPAR3= 172366  
KDPAR4= 172370  
KDPAR5= 172372  
KDPAR6= 172374  
KDPAR7= 172376

.SBTTL UNIBUS MAP REGISTER DEFINITIONS

;\*THE LOWER 16 BITS OF THE MAP REGISTERS ARE LABELED 'MAPLXX'  
;\*THE UPPER 6 BITS OF THE MAP REGISTERS ARE LABELED 'MAPHXX'

378 170200  
379 170202  
380 170204  
381 170206  
382 170210  
383 170212  
384 170214  
385 170216  
386 170220  
387 170222  
388 170224  
389 170226  
390 170230  
391 170232  
392 170234  
393 170236  
394 170240  
395 170242  
396 170244  
397 170246  
398 170250  
399 170252  
400 170254  
401 170256  
402 170260  
403 170262  
404 170264  
405 170266

MAPL00 = 170200  
MAPH00 = 170202  
MAPL01 = 170204  
MAPH01 = 170206  
MAPL02 = 170210  
MAPH02 = 170212  
MAPL03 = 170214  
MAPH03 = 170216  
MAPL04 = 170220  
MAPH04 = 170222  
MAPL05 = 170224  
MAPH05 = 170226  
MAPL06 = 170230  
MAPH06 = 170232  
MAPL07 = 170234  
MAPH07 = 170236  
MAPL10 = 170240  
MAPH10 = 170242  
MAPL11 = 170244  
MAPH11 = 170246  
MAPL12 = 170250  
MAPH12 = 170252  
MAPL13 = 170254  
MAPH13 = 170256  
MAPL14 = 170260  
MAPH14 = 170262  
MAPL15 = 170264  
MAPH15 = 170266

407	170272	MAPH16	=	170272
408	170274	MAPL17	=	170274
409	170276	MAPH17	=	170276
410	170300	MAPL20	=	170300
411	170302	MAPH20	=	170302
412	170304	MAPL21	=	170304
413	170306	MAPH21	=	170306
414	170310	MAPL22	=	170310
415	170312	MAPH22	=	170312
416	170314	MAPL23	=	170314
417	170316	MAPH23	=	170316
418	170320	MAPL24	=	170320
419	170320	MAPH24	=	170320
420	170324	MAPL25	=	170324
421	170326	MAPH25	=	170326
422	170330	MAPL26	=	170330
423	170332	MAPH26	=	170332
424	170334	MAPL27	=	170334
425	170336	MAPH27	=	170336
426	170340	MAPL30	=	170340
427	170342	MAPH30	=	170342
428	170344	MAPL31	=	170344
429	170346	MAPH31	=	170346
430	170350	MAPL32	=	170350
431	170352	MAPH32	=	170352
432	170354	MAPL33	=	170354
433	170356	MAPH33	=	170356
434	170360	MAPL34	=	170360
435	170362	MAPH34	=	170362
436	170364	MAPL35	=	170364
437	170366	MAPH35	=	170366
438	170370	MAPL36	=	170370
439	170372	MAPH36	=	170372
440	170374	MAPL37	=	170374
441	170376	MAPH37	=	170376
442		.EQUIV	MAPL00,	MAPL0
443		.EQUIV	MAPH00,	MAPH0
444		.EQUIV	MAPL01,	MAPL1
445		.EQUIV	MAPH01,	MAPH1
446		.EQUIV	MAPL02,	MAPL2
447		.EQUIV	MAPH02,	MAPH2
448		.EQUIV	MAPL03,	MAPL3
449		.EQUIV	MAPH03,	MAPH3
450		.EQUIV	MAPL04,	MAPL4
451		.EQUIV	MAPH04,	MAPH4
452		.EQUIV	MAPL05,	MAPL5
453		.EQUIV	MAPH05,	MAPH5
454		.EQUIV	MAPL06,	MAPL6
455		.EQUIV	MAPH06,	MAPH6
456		.EQUIV	MAPL07,	MAPL7
457		.EQUIV	MAPH07,	MAPH7
458				
459				
460				
461				

463 000001  
464 000002  
465 000003  
466 000004  
467 000005  
468  
469 172540  
470 172542

AC1= %1  
AC2= %2  
AC3= %3  
AC4= %4  
AC5= %5  
;LINE CLOCK AND PROGRAMMABLE LINE CLOCK REGISTERS  
FLKCSR=172540  
FLKCSB=172542



472 177546  
 473 000100  
 474  
 475  
 476 170000  
 477 170002  
 478 170004  
 479 170006  
 480 170010  
 481 170014  
 482 170016  
 483 000510  
 484  
 485  
 486 160100  
 487 160102  
 488 160104  
 489 160106  
 490 160110  
 491 160112  
 492 160114  
 493 160116  
 494 160120  
 495 160124  
 496 160126  
 497 160174  
 498 160176  
 499 000774  
 500 000776  
 501  
 502  
 503 100000  
 504 040000  
 505 020000  
 506 002000  
 507 001000  
 508 000400  
 509 000200  
 510  
 511  
 512  
 513  
 514 000010  
 515 000000  
 516 140000  
 517 000000  
 518 030000  
 519 177770  
 520  
 521  
 522  
 523 000000  
 524  
 525  
 526

LKS=177546  
 LKVEC=100  
 ;UNIBUS EXERCISOR REGISTER  
 UBEDB= 170000 ; DATA BUFFER  
 UBEC= 170002 ; CYCLE COUNT  
 UBEB= 170004 ; BUS ADDRESS  
 UBECR1= 170006 ; CONTROL REGISTER 1  
 UBECR= 170010 ; ERROR CLEAR  
 UBEG= 170014 ; MULTI-EXERCISOR GO  
 UBECR2= 170016 ; CONTROL REGISTER 2  
 UBVE= 510 ; INTERRUPT VECTOR  
 ;MASS BUS TESTER REGISTERS  
 MBTCS1= 160100  
 MBTWC= 160102  
 MBTBA= 160104  
 MBTMR2= 160106  
 MBTCS2= 160110  
 MBTST= 160112  
 MBTER= 160114  
 MBTAS= 160116  
 MBTDB= 160120  
 MBTMR1= 160124  
 MBTDT= 160126  
 MBTBAE= 160174  
 MBTCS3= 160176  
 MBTVEC= 774  
 MBTPSW= 776  
 ;MISCELLANEOUS BIT ASSIGNMENTS (USED IN OPT.CP)  
 KTOPT= 100000 ; BELOW BIT ASSIGNMENTS ARE USED  
 EISOPT= 040000 ; IN THE CPCHK ROUTINE  
 FPOPT= 020000 ; A BIT FOR EACH OPTION PRESENT  
 MBTOPT= 002000  
 LKOPT= 001000  
 TTOPT= 000400  
 UBEOPT= 000200  
 .EQUIV ERROR,HLT  
 .EQUIV BIT14,SM  
 .EQUIV BIT12,PSM  
 .EQUIV BIT11,REG  
 CALLHANDLER=10  
 KM=0  
 UM=140000  
 PKM=0  
 PUM=30000  
 UBREAK=177770  
 .SBTTL TRAP CATCHER  
 .=0  
 ;\*ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A ".+2,HALT"  
 ;\*SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS  
 ;\*LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS



```

562 ;*****
563
564 .SBTTL COMMON TAGS
565
566 ;*THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
567 ;*USED IN THE PROGRAM.
568
569 001200 . =1200
570
571 001200 SCMTAG: ; START OF COMMON TAGS
572 001200 000000 SPASS: .WORD 0 ; CONTAINS PASS COUNT
573 001202 000000 STSTNM: .WORD 00 ; CONTAINS THE TEST NUMBER
574 001204 000 SERFLG: .BYTE 0 ; CONTAINS ERROR FLAG
575 001206 001206 .EVEN
576 001206 000000 SICNT: .WORD 0 ; CONTAINS SUBTEST ITERATION COUNT
577 001210 000000 SLPADR: .WORD 00 ; CONTAINS SCOPE LOOP 1200
578 001212 000000 SLPERR: .WORD 00 ; CONTAINS SCOPE RETURN FOR ERRORS
579 001214 000000 SERTTL: .WORD 00 ; CONTAINS TOTAL ERRORS DETECTED
580 001216 000 SITEMB: .BYTE 0 ; CONTAINS ITEM CONTROL BYTE
581 001217 001 SERMAX: .BYTE 1 ; CONTAINS MAX. ERRORS PER TEST
582 001220 000000 SERRPC: .WORD 0 ; CONTAINS PC OF LAST ERROR INSTRUCTION
583 001222 000000 SGADR: .WORD 00 ; CONTAINS 1200 OF 'GOOD' DATA
584 001224 000000 SBDADR: .WORD 00 ; CONTAINS 1200 OF 'BAD' DATA
585 001226 000000 SGDDAT: .WORD 00 ; CONTAINS 'GOOD' DATA
586 001230 000000 SBDDAT: .WORD 00 ; CONTAINS 'BAD' DATA
587 001232 000000 000000 000000 .WORD 0,0,0 ; RESERVED--NOT TO BE USED
588 001240 177560 STKS: 177560 ; TTY KBD STATUS
589 001242 177562 STKB: 177562 ; TTY KBD BUFFER
590 001244 177564 STPS: 177564 ; TTY PRINTER STATUS REG. 1200
591 001246 177566 STPB: 177566 ; TTY PRINTER BUFFER REG. 1200
592 001250 000 SNLL: .BYTE 0 ; CONTAINS NULL CHARACTER FOR FILLS
593 001251 002 SFILLS: .BYTE 2 ; CONTAINS # OF FILLER CHARACTERS REQUIRED
594 001252 012 SFILLC: .BYTE 12 ; INSERT FILL CHARS. AFTER A "LINE FEED"
595 001253 000 STPFLG: .BYTE 0 ; "TERMINAL AVAILABLE" FLAG (BIT<07>=0=YES)
596 001254 000000 SREGAD: .WORD 0 ; CONTAINS THE 1200 FROM WHICH (SREG) WAS OBTAINED
597
598 001256 000000 SREG0: .WORD 0 ; CONTAINS ((SREGAD)+0)
599 001260 000000 SREG1: .WORD 00 ; CONTAINS ((SREGAD)+2)
600 001262 000000 SREG2: .WORD 00 ; CONTAINS ((SREGAD)+4)
601 001264 000000 SREG3: .WORD 00 ; CONTAINS ((SREGAD)+6)
602 001266 000000 SREG4: .WORD 00 ; CONTAINS ((SREGAD)+10)
603 001270 000000 SREG5: .WORD 00 ; CONTAINS ((SREGAD)+12)
604 001272 000000 SREG6: .WORD 00 ; CONTAINS ((SREGAD)+14)
605 001274 000000 SREG7: .WORD 00 ; CONTAINS ((SREGAD)+16)
606 001276 000000 STMP0: .WORD 00 ; USER DEFINED
607 001300 000000 STMP1: .WORD 00 ; USER DEFINED
608 001302 000000 STMP2: .WORD 00 ; USER DEFINED
609 001304 000000 STMP3: .WORD 00 ; USER DEFINED
610 001306 000000 STMP4: .WORD 00 ; USER DEFINED
611 001310 000000 STMP5: .WORD 00 ; USER DEFINED
612 001312 000000 STMP6: .WORD 00 ; USER DEFINED
613 001314 000000 STMP7: .WORD 0 ; USER DEFINED
614 001316 000000 STIMES: 0 ; MAX. NUMBER OF ITERATIONS
615 001320 000000 SESCAPE: 0 ; ESCAPE ON ERROR 1200
616 001322 177607 000377 SBELL: .ASCIZ <207><377><377> ; CODE FOR BELL

```

618	001327	015	SCRLF:	.ASCII	<15>	::CARRIAGE RETURN
619	001330	000012	SLF:	.ASCIZ	<12>	::LINE FEED
620	001332	000000	ERRRTN:	.WORD		
621	001334	000044	SFLBUFF:	.BLKB	44	::BUFFER FOR FLOATING POINT CONVERSION
622	001400	000000	SBUFF:	.WORD		
623	001402	000000	SACO:	.WORD		::EXTENDED EXPONENT VALUES
624	001404	000000	SAC1:	.WORD		::FOR THE SIX FLOATING POINT
625	001406	000000	SAC2:	.WORD		::ACCUMULATORS
626	001410	000000	SAC3:	.WORD		
627	001412	000000	SAC4:	.WORD		
628	001414	000000	SAC5:	.WORD		
629	001416	000000	SSTMP4:	.WORD		
630	001420	000000	SSTMP6:	.WORD		
631	001422	000004	FLTMP0:	.BLKW	4	::FLOATING POINT DBL PREC BUFFER
632	001432	000004	FLTMP1:	.BLKW	4	
633	001442	001444	TKBFRP:	.WORD	TKBFR	::POINTER FOR KEYBOARD BUFFER
634	001444	000011	TKBFR:	.BLKW	11	::KEYBOARD BUFFER
635	001466	000000	NOTYPE:	.WORD		::NO TYPEOUT FLAG (INHIBIT WHEN SET)
636	001470	000000	OPT.CP:	.WORD		::CPU OPTION FLAGS
637	001472	000006	SRTAN:	.RTT		::RETURN FOR T-BIT TRAP
638	001474	000000	VADR:	.WORD		::BUFFER FOR VIRTUAL ADDRESS
639	001476	000000	PA1500:	.WORD		::BUFFER FOR PHYSICAL ADDRESS BITS<15:00>
640	001500	000000	PA2116:	.WORD		::PHYSICAL ADDRESS BITS<21:16>
641	001502	000	NEXEC:	.BYTE		::NO EXECUTE FLAG(NO TEST EXECUTION WHEN SET)
642	001503	000	MMON:	.BYTE		::MEMORY MGMT FLAG(MGMT IS ON WHEN NON-ZERO)
643	001504	000	QV:	.BYTE		::QV FLAG(QV PASS WHEN SET)
644	001505	000	AA:	.BYTE		::AUTO ACCEPT FLAG (AA PASS WHEN SET)
645	001506	000000	FACTOR:	.WORD		::RELOCATION FACTOR(NUMBER OF
646	001510	000000	SFACTOR:	.WORD		::BYTES ABOVE BASE CODE)
647	001512	000000	FRSTAD:	.WORD		::FIRST ADDRESS OF SECTION BEING EXECUTED
648	001514	000000	FRSTMEN:	.WORD		::ADDRESS OF FIRST FREE MEMORY
649	001516	000000	LSTMEN:	.WORD		::ADDRESS OF LAST FREE MEMORY(IN 28K)
650	001520	000000	NEXPAR:	.WORD		::NEXT VALUE TO PUT IN PAR0
651	001522	123456	SLONUM:	.WORD	123456	::LOW 16 BITS OF RANDOM NUMBER
652	001524	065432	SHINUM:	.WORD	65432	::HIGH 16 BITS OF RANDOM NUMBER
653	001526	001	NULLS:	.BYTE	1,1,1,0	::BUFFER FOR PRINTER TEST
654	001531	000				
655	001532	000060	SUBPASS:	.WORD	60	::SUB-PASS COUNT IN ASCII
656	001534	000000	SERPSW:	.WORD		::ERROR PSW FOR TYPEOUT
657	001536	000000	EXITFL:	.WORD		
658	001540	000000	OLDBASE:	.WORD		::SOURCE BASE ADDRESS FOR DEVICE RELOCATION
659	001542	000000	NWBASL:	.WORD		::DEST ADDRESS FOR DEVICE RELOC BITS<15:00>
660	001544	000000	NWBASH:	.WORD		::DEST ADDRESS FOR DEVICE RELOC BITS<21:16>
661	001546	000000	IOWC:	.WORD		::TWO'S COMPLIMENT WORD COUNT FOR DEVICE RELOC
662	001550	000000	DEVICE:	.WORD		
663	001552	000000	DEVINDX:	.WORD		::DEVICE INDEX (0 TO 7)
664	001554	000000	UNITNO:	.WORD		::DEVICE UNIT NUMBER
665	001556	000000	RNTBINX:	.WORD		::INDEX TO RUN TABLE
666	001560	000000	MXMMHI:	.WORD		::BITS<21:16> OF LAST MEM ADDRESS ON SYSTEM
667	001562	000000	MXMML0:	.WORD		::BITS<15:00> OF LAST MEM ADDRESS ON SYSTEM
668	001564	000000	RP310:	.WORD		::DATA TO LOAD INTO RPO3 CS REGISTER
669	001566	000000	RP311:	.WORD		::RPO3 FLAG FOR FIRST 2K OF PROGRAM
670	001570	000000	RK10:	.WORD		::DATA TO LOAD INTO RK05 CS REGISTER
671	001572	000000	RK11:	.WORD		::RK05 FLAG FOR FIRST 2K OF PROGRAM
672	001574	000000	RP411:	.WORD		::RPO4 FLAG FOR FIRST 2K OF PROGRAM

```

674 001600 000000 MTICKS: .WORD ;ELAPSED RUN TIME IN MINUTES
675 001602 000000 LTICKS: .WORD ;LOW BYTE=NUMBER OF CLOCK INTERRUPTS (0 TO 59)
676 ;HIGH BYTE=ELAPSED RUN TIME IN SECONDS(0 TO 59)
677 001604 000000 $MAINT: .WORD ;CURRENT VALUE IN MAINTENANCE REGISTER
678 001606 000010 SYSSIZE: .BLKW 10 ;SYSTEM SIZE TABLE(ONE ENTRY FOR EACH DEVICE)
679 001626 000006 RUNTBL: .BLKW 6 ;RUN TIME TABLE(ONE ENTRY FOR EACH 2K BLOCK)
680 001642 000006 RUNTRAK: .BLKW 6 ;RUN TRACK TABLE(ONE ENTRY FOR EACH 2K BLOCK)
681 001656 177777 MAPTBL: .WORD -1 ;MAP TABLE(ONE BYTE FOR EACH UNIBUS DEVICE)
682 001660 177777 ;UNUSED=377, USED=LOW 5 BITS OF MAP ADDRESS
683 001662 000002 UBESAV: .BLKW 2 ;BASE ADDRESS OF UBE TRANSFER IN PROGRESS
684 001666 000002 UBEADR: .BLKW 2 ;ADDRESS THAT GETS LOADED INTO UBE BA REG
685 001672 000002 ERBA: .BLKW 2 ;18 BIT UNIBUS ADDRESS WHEN DEVICE DETECTED AN ERROR
686 .SBTTL DEVICE HANDLER STATUS WORDS
687 ;* EACH WORD HAS THE FOLLOWING BIT ASSIGNMENTS:
688 ;* 7 HANDLER READY
689 ;* 8 REPEAT LAST FUNCTION
690 ;* 15 ERROR
691 001676 000200 RP3HSTAT: .WORD 200 ;RP03
692 001700 000200 RKHSTAT: .WORD 200 ;RK05
693 001702 000200 SPARE0: .WORD 200 ;
694 001704 000200 SPARE1: .WORD 200 ;
695 001706 000200 RP4HSTAT: .WORD 200 ;RP04
696 001710 000200 RSHSTAT: .WORD 200 ;RS04
697 001712 000200 .WORD 200 ;SPARE
698 001714 000200 .WORD 200 ;SPARE
699
700 .SBTTL DEVICE HANDLER WORD COUNTS
701 ;* THIS TABLE GETS LOADED BY THE I/O
702 ;* RELOCATION ROUTINE WITH THE TWO'S COMPLIMENT WORD
703 ;* COUNT FOR THE TRANSFER FOR THE PARTICULAR DEVICE.
704 001716 000000 RP3HWC: .WORD ;RP03
705 001720 000000 RKHWC: .WORD ;RK05
706 001722 000000 .WORD ;SPARE
707 001724 000000 .WORD ;SPARE
708 001726 000000 RP4HWC: .WORD ;RP04
709 001730 000000 RSHWC: .WORD ;RS04
710
711 .SBTTL DEVICE HANDLER OLD BASE ADDRESS
712 ;* THIS TABLE GETS LOADED BY THE I/O RELOCATION ROUTINE
713 ;* WITH THE BASE ADDRESS OF THE SOURCE DATA FOR THE
714 ;* DEVICE THAT IS GOING TO TRANSFER THE DATA.
715 001732 000000 RP3OLD: .WORD ;RP03
716 001734 000000 .WORD ;
717 001736 000000 RKOLD: .WORD ;RK05
718 001740 000000 .WORD ;
719 001742 000000 .WORD ;SPARE
720 001744 000000 .WORD ;SPARE
721 001746 000000 .WORD ;
722 001750 000000 .WORD ;SPARE
723 001752 000000 RP4OLD: .WORD ;RP04
724 001754 000000 .WORD ;
725 001756 000000 RSOLD: .WORD ;RS04
726 001760 000000 .WORD ;
727
728 .SBTTL DEVICE HANDLER NEW BASE ADDRESSES
    
```

# K03

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730      ;*      WITH THE BASE ADDRESS OF THE DESTINATION FOR THE
731      ;*      PARTICULAR DEVICE THAT IS GOING TO DO THE TRANSFER.
732      001762 000000      RP3NWL: .WORD      ;RPO3
733      001764 000000      RP3NWH: .WORD
734      001766 000000      RKNEWL: .WORD      ;RK05
735      001770 000000      RKNEWH: .WORD
736      001772 000000      .WORD      ;SPARE
737      001774 000000      .WORD
738      001776 000000      .WORD      ;SPARE
739      002000 000000      .WORD
740      002002 000000      RP4NWL: .WORD      ;RPO4
741      002004 000000      RP4NWH: .WORD
742      002006 000000      RSNEWL: .WORD      ;RSO4
743      002010 000000      RSNEWH: .WORD
744
745      .SBTTL  DEVICE HANDLER UNIT NUMBER
746      ;*      THIS TABLE GETS LOADED BY THE I/O RELOCATION ROUTINE.
747      ;*      IT TELLS THE DEVICE HANDLER WHICH UNIT NUMBER IS
748      ;*      TO DO THE TRANSFER.
749      002012 000000      RP3UNIT: .WORD      ;RPO3
750      002014 000000      RKUNIT:  .WORD      ;RK05
751      002016 000000      .WORD      ;SPARE
752      002020 000000      .WORD      ;SPARE
753      002022 000000      RP4UNIT: .WORD      ;RPO4
754      002024 000000      RSUNIT:  .WORD      ;RSO4
755
756      .SBTTL  ADDRESS OF THE DEVICE HANDLERS
757      ;*      THIS TABLE CONTAINS THE ADDRESS OF THE DEVICE HANDLER
758      ;*      ROUTINES. IT IS USED BY THE I/O RELOCATION ROUTINE
759      ;*      TO TRANSFER CONTROL TO THE DEVICE HANDLER.
760      002026 036772      RP3HANA: .WORD      RP3DRV  ;RPO3
761      002030 037410      RKHANA:  .WORD      RKDRV   ;RK05
762      002032 000000      .WORD      ;SPARE
763      002034 000000      .WORD      ;SPARE
764      002036 040004      RP4HANA: .WORD      RP4DRV  ;RPO4
765      002040 040354      RSHANA:  .WORD      RSDRV   ;RSO4
766
767      .SBTTL  DEVICE HANDLER DISK ADDRESS TABLE
768      ;*      THIS TABLE GETS LOADED BY THE DEVICE HANDLER WITH THE
769      ;*      DISK ADDRESS(SECTOR AND CYLINDER) OF THE CURRENT
770      ;*      TRANSFER.
771      002042 000000      RP3HDA: .WORD      ;RPO3 DISK ADDRESS
772      002044 000000      RP3HDC: .WORD      ;RPO3 DESIRED CYLINDER
773      002046 000000      RKHDA:  .WORD      ;RK05 DISK ADDRESS
774      002050 000000      .WORD      ;SPARE
775      002052 000000      RP4HDA: .WORD
776      002054 000000      RP4HDC: .WORD      ;RPO4 DESIRED CYLINDER
777      002056 000000      RSHDA:  .WORD      ;RSO4 DISK ADDRESS
778
779      .SBTTL  DEVICE HANDLER FUNCTION TABLE
780      ;*      THIS TABLE GETS LOADED BY THE DEVICE HANDLERS
781      ;*      AND THE DEVICE SERVICE ROUTINES. IT TELLS THE ROUTINES
782      ;*      WHICH FUNCTION TO DO NEXT.
783      002060 000000      RP3FUN: .WORD      ;RPO3
784      002062 000000      RKFUN:  .WORD      ;RK05
    
```

786 002066 000000  
 787 002070 000000  
 788  
 789  
 790  
 791  
 792  
 793  
 794  
 795 002072 000  
 796 002073 000  
 797 002074 000  
 798 002075 000  
 799 002076 000  
 800 002100

RP4FUN: .WORD ;RPO4  
 RSFUN: .WORD ;RSO4  
 .SBTTL DEVICE HANDLER RETRY COUNT  
 : \* THIS TABLE GETS LOADED BY THE DEVICE HANDLERS AND IS USED  
 : \* BY THE DEVICE SERVICE ROUTINES. IF AN ERROR OCCURS  
 : \* THE DEVICE SERVICE ROUTINE WILL RETRY THE FUNCTION UNTIL  
 : \* THE BYTE IN THIS TABLE GOES TO ZERO. IT IS INITIALIZED  
 : \* TO A -3.  
 RP3TRY: .BYTE ;RPO3  
 RKTRY: .BYTE ;RK05  
 : .BYTE ;SPARE  
 RP4TRY: .BYTE ;RPO4  
 RSTRY: .BYTE ;RSO4  
 .EVEN

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 810 002100 176710  
 811 002102 176712  
 812 002104 176714  
 813 002106 176716  
 814 002110 176720  
 815 002112 176724  
 816 002114 176722  
 817 002116 000254  
 818 002120 000256  
 819  
 820

.SBTTL DEVICE REGISTER TABLES  
 : \* THE FOLLOWING TABLES CONTAIN THE STANDARD ADDRESS FOR  
 : \* THE DEVICES USED BY THIS PROGRAM. IF A DEVICE IS PLACED  
 : \* AT A NON-STANDARD ADDRESS THE APPROPRIATE TABLE CAN BE  
 : \* CHANGED AND THE PROGRAM WILL OPERATE THAT DEVICE.  
 : \* EXCEPTION--SEE DOCUMENTATION FOR RPO3 AND RPO4 PROBLEMS.  
 .SBTTL RP11/RPO3 REGISTERS  
 RP3DS: .WORD 176710 ;DRIVE STATUS  
 RP3ER: .WORD 176712 ;ERROR REGISTER  
 RP3CS: .WORD 176714 ;CONTROL AND STATUS  
 RP3WC: .WORD 176716 ;WORD COUNT  
 RP3BA: .WORD 176720 ;BUS ADDRESS  
 RP3DA: .WORD 176724 ;DISK ADDRESS  
 RP3DC: .WORD 176722 ;DESIRED CYLINDER  
 RP3VEC: .WORD 254 ;INTERRUPT VECTOR  
 RP3PSW: .WORD 256 ;INTERRUPT VECTOR+2

821 002122 177400  
 822 002124 177402  
 823 002126 177404  
 824 002130 177406  
 825 002132 177410  
 826 002134 177412  
 827 002136 000220  
 828 002140 000222  
 829  
 830

.SBTTL RK11/RK05 REGISTERS  
 RKDS: .WORD 177400 ;DRIVE STATUS  
 RKER: .WORD 177402 ;ERROR REGISTER  
 RKCS: .WORD 177404 ;CONTROL AND STATUS  
 RKWC: .WORD 177406 ;WORD COUNT  
 RKBA: .WORD 177410 ;BUS ADDRESS  
 RKDA: .WORD 177412 ;DISK ADDRESS  
 RKVEC: .WORD 220 ;INTERRUPT VECTOR  
 RKPSW: .WORD 222 ;INTERRUPT VECTOR+2

831 002142 176700  
 832 002144 176702  
 833 002146 176704  
 834 002150 176750  
 835 002152 176706  
 836 002154 176710  
 837 002156 176752  
 838 002160 176712  
 839 002162 176714  
 840 002164 176734

.SBTTL RH70/RPO4 REGISTERS  
 RP4CS1: .WORD 176700 ;CONTROL AND STATUS #1  
 RP4WC: .WORD 176702 ;WORD COUNT  
 RP4BA: .WORD 176704 ;BUS ADDRESS  
 RP4BAE: .WORD 176750 ;BUS ADDRESS EXTENDED  
 RP4DA: .WORD 176706 ;DISK ADDRESS  
 RP4CS2: .WORD 176710 ;CONTROL AND STATUS #2  
 RP4CS3: .WORD 176752 ;CONTROL AND STATUS #3  
 RP4DS: .WORD 176712 ;DRIVE STATUS  
 RP4ER1: .WORD 176714 ;ERROR REG #1  
 RP4DC: .WORD 176734 ;DESIRED CYLINDER

## RH70/RP04 REGISTERS

842	002170	176742	RP4ER3:	.WORD	176742	:ERROR REG #3
843	002172	176736	RPCC:	.WORD	176736	:CURRENT CYLINDER
844	002174	176732	RP4OF:	.WORD	176732	:OFFSET REGISTER
845	002176	000254	RP4VEC:	.WORD	254	:INTERRUPT VECTOR
846	002200	000256	RP4PSW:	.WORD	256	:INTERRUPT VECTOR+2

## .SBTTL RH70/R504 REGISTERS

849	002202	172040	RSCS1:	.WORD	172040	:CONTROL AND STATUS #1
850	002204	172042	RSWC:	.WORD	172042	:WORD COUNT
851	002206	172044	RSBA:	.WORD	172044	:BUS ADDRESS
852	002210	172070	RSBAE:	.WORD	172070	:BUS ADDRESS EXTENDED
853	002212	172046	RSDA:	.WORD	172046	:DISK ADDRESS
854	002214	172050	RSCS2:	.WORD	172050	:CONTROL AND STATUS #2
855	002216	172072	RSCS3:	.WORD	172072	:CONTROL AND STATUS #3
856	002220	172052	RSDS:	.WORD	172052	:DRIVE STATUS
857	002222	172054	RSER:	.WORD	172054	:ERROR REG
858	002224	000204	RSVEC:	.WORD	204	:INTERRUPT VECTOR
859	002226	000206	RSPSW:	.WORD	206	:INTERRUPT VECTOR+2

## .SBTTL UNIBUS EXERCISOR REGISTER ADDRESS TABLE

```

:* THIS TABLE IS ASSEMBLED FOR UBE #0. IF THE UBE
:* ADDRESSES ARE CUT FOR OTHER THAN UNIT #0, THE PROGRAM
:* WILL CHANGE THIS TABLE. THE PROGRAM LOOKS FOR A
:* UBE AT ADDRESSES 770002, 770022, 770032, AND 770042.

```

866	002230	170002	UBETBL:	.WORD	UBECC	:CYCLE COUNT
867	002232	170004		.WORD	UBEBA	:BUS ADDRESS REG
868	002234	170016		.WORD	UBECR2	:CONTROL REGISTER #2
869	002236	170006		.WORD	UBECR1	:CONTROL REGISTER #1
870	002240	170010		.WORD	UBECLR	:UBE CLEAR ADDRESS
871	002242	000510		.WORD	UBEVEC	:INTERRUPT VECTOR
872	002244	000512		.WORD	UBEVEC+2	:INTERRUPT VECTOR +2

## .SBTTL MASS BUS TESTER REGISTER ADDRESSES

```

:* THE PROGRAM IS ASSEMBLED WITH ADDRESSES FOR A MBT
:* AT 770100. IF THE MBT IS AT ANOTHER ADDRESS THE PROGRAM
:* WILL CHANGE THIS TABLE. THE PROGRAM LOOKS FOR A UBE
:* AT ADDRESSES 770100, 770200, 770300, AND 770400.

```

879	002246	160100	MBTTBL:	.WORD	MBTCS1	:CONTROL AND STATUS #1
880	002250	160102		.WORD	MBTWC	:WORD COUNT
881	002252	160104		.WORD	MBTBA	:BUS ADDRESS
882	002254	160174		.WORD	MBTBAE	:BUS ADDRESS EXTENDED
883	002256	160106		.WORD	MBTHR2	:MAINTENANCE REGISTER #2
884	002260	160110		.WORD	MBTCS2	:CONTROL REGISTER #2
885	002262	160112		.WORD	MBTST	:STATUS REGISTER
886	002264	160114		.WORD	MBTER	:ERROR REGISTER
887	002266	160176		.WORD	MBTCS3	:CONTROL REGISTER #3
888	002270	000774		.WORD	MBTVEC	:INTERRUPT VECTOR
889	002272	000776		.WORD	MBTPSW	:INTERRUPT VECTOR+2
890	002274	160126		.WORD	MBTDI	:DRIVE TYPE REGISTER
891	002276	160200	MBTN2:	.WORD	160200	:MASS BUS TESTER #2
892	002300	160300	MBTN3:	.WORD	160300	:MASS BUS TESTER #3
893	002302	160400	MBTN4:	.WORD	160400	:MASS BUS TESTER #4



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002306 055021  
002310 055066  
002312 055060  
002314 055100  
002316 055126  
002320 055154  
002322 055060  
002324 055164  
002326 055221  
002330 055270  
002332 055060  
002334 055304  
002336 055333  
002340 055270  
002342 055411  
002344 055416  
002346 055455  
002350 055526  
002352 055522  
002354 055540  
002356 055610  
002360 055630  
002362 055411  
002364 000000  
002366 000000  
002370 000000  
002372 000000  
002374 055636  
002376 055707

```

;*****
.SBTTL ERROR POINTER TABLE
;THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
;THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
;LOCATION SITEMB, THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
;NOTE1: IF SITEMB IS 0 THE ONLY PERTINENT DATA IS (SERRPC).
;NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:
    
```

```

;*      EM      ;;POINTS TO THE ERROR MESSAGE
;*      DH      ;;POINTS TO THE DATA HEADER
;*      DT      ;;POINTS TO THE DATA
;*      DF      ;;POINTS TO THE DATA FORMAT
    
```

```

SERRTB:
: ITEM 1
EM1      :UNEXPECTED TRAP TO 4
DH1      :PCOFTP  PHYSPC  PSW  CPUERR
DT1      :VADR,VADR,STMP0,STMP2
DF1      :0,1,0,0,0
: ITEM 2
EM2      :UNEXPECTED TRAP TO 10
DH2      :PCOFTP  PHYSPC  PSW
DT2      :VADR,VADR,STMP0
DF1      :
: ITEM 3
EM3      :UNEXPECTED TRAP TO 250(MGMT)
DH3      :PCOFTP  PHYSPC  PSW  MMR0  MMR2
DT3      :VADR,VADR,STMP0,,STMP2,,STMP3
DF1      :
: ITEM 4
EM4      :UNEXPECTED TRAP TO 114
DH4      :PCOFTP  PHYSPC  PSW  ERADREG MEMERRREG
DT3      :VADR,VADR,STMP0,STMP3,STMP2
DF4      :0,1,0,2,0
: ITEM 5
EM5      :PARITY ERROR DURING DATA CHECK
DH5      :SRCADR DSTADR ERRADREG MEM ERR REG
DT5      :STMP0,PA1500,STMP3,STMP2
DF5      :
: ITEM 6
EM6      :ERROR DURING CHECK OF RELOCATED DATA
DH6      :SRCADR DSTADR
DT6      :STMP0,PA1500
DF4      :
: ITEM 7
0
0
0
0
: ITEM 10
EM10     :ERROR DURING DATA CHECK-RELOC WAS BY I/O
DH:0     :SRCADR DSTADR DEVICE THAT DID XFER
    
```

951	002402	055756	DF10	:0,1,3,0
952			:ITEM 11	
953	002404	055774	EM11	:BIT(S) STUCK IN MICRO-BREAK REG
954	002406	056041	DH11	:GOOD DAT BAD DAT
955	002410	056064	DT11	:STMP0,STMP1
956	002412	056062	DF11	:0,0
957			:ITEM 12	
958	002414	056072	EM12	:UNIBUS EXERCISOR NON-EXISTANT MEMOREY
959	002416	056130	DH12	:PHYSICAL ADDRESS
960	002420	056146	DT12	:PA1500
961	002422	056144	DF12	:2
962			:ITEM 13	
963	002424	056152	EM13	:MASS BUS TESTER NON-EXISTANT MEMORY
964	002426	056210	DH13	:PHYSICAL ADDRESS
965	002430	056146	DT12	
966	002432	056144	DF12	
967			:ITEM 14	
968	002434	056225	EM14	:FLOATING POINT ERROR
969	002436	056252	DH14	:DATA1 DATA2
970	002440	056272	DT14	:STMP4,\$REG2,STMP6,\$REG3
971	002442	056304	DF14	:4,0,4,0
972			:ITEM 15	
973	002444	056310	EM15	:DEVICE HUNG
974	002446	000000	0	
975	002450	000000	0	
976	002452	000000	0	
977			:ITEM 16	
978	002454	056225	EM14	:FLOATING POINT ERROR
979	002456	056324	DH16	
980	002460	056356	DT16	:FLTMP0,\$REG2,FLTMP1,\$REG3

```

982 002464 013737 177570 177570 START1: MOV
983 002472 000774 BR
984 .SBTTL PROGRAM INITIALIZATION
985
986
987
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1000
1001 002474 012706 001100
1002 002500 012737 051374 000034
1003 002506 012737 044774 000030
1004 002514 012700 000377
1005 002520 010037 177770
1006 002524 020037 177770
1007 002530 001024
1008 002532 005000
1009 002534 010037 177770
1010 002540 020037 177770
1011 002544 001016
1012 002546 012700 000125
1013 002552 010037 177770
1014 002556 020037 177770
1015 002562 001007
1016 002564 012700 000252
1017 002570 010037 177770
1018 002574 020037 177770
1019 002600 001411
1020 002602 010067 176470
1021 002606 013737 177770 001300
1022 002614 012737 002474 001212
1023 002622 104011
1024
1025 002624 012737 000100 177770
1026 002632 104400 002640
1027 002636 000421
1028
1029 002702
1030 002702 104400 002710
1031 002706 000407
1032
1033 002726
1034 002726 000000
1035 002730 012737 000012 000010
1036 002736 012737 000002 000012

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*****
:SBTTL MICRO-BREAK REGISTER TEST
:THIS TEST IS EXECUTED BY STARTING THE PROGRAM AT ADDRESS 214.
:THIS TEST REQUIRES A MAINTENANCE CARD AND OPERATOR INTERVENTION.
:THE PROCESSOR SHOULD STOP 8 TIMES. FOLLOWING IS THE DATA
:THAT SHOULD BE IN THE MICRO-ADRESS DATA LIGHTS EACH TIME:
:
: 1 000
: 2 001
: 3 002
: 4 004
: 5 010
: 6 020
: 7 040
: 8 200
*****
START2: MOV #100,SP ;SETUP THE SP
MOV #TRAP,@TRAPVEC ;SETUP TRAP VECTOR
MOV #ERROR,@EMTVEC ;SETUP EMT VECTOR
MOV #377,R0 ;PUT MICRO-BREAK DATA IN R0
MOV R0,@UBREAK ;LOAD U BREAK REG
CMP R0,@UBREAK ;LOAD OK?
BNE UBRERR ;BRANCH IF NO
CLR R0
MOV R0,@UBREAK
CMP R0,@UBREAK
BNE UBRERR
MOV #125,R0
MOV R0,@UBREAK
CMP R0,@UBREAK
BNE UBRERR
MOV #252,R0
MOV R0,@UBREAK
CMP R0,@UBREAK
BEQ UBRK2
UBRERR: MOV R0,$TMP0
MOV @UBREAK,@$TMP1
MOV $START2,@$SLPERR
ERROR 11
;TEST TO ENSURE U BREAK COMPARATORS DO NOT COME ON.
UBRK2: MOV #100,@UBREAK ;PUT SAFE VALUE IN REG
TYPE 65$ ;;TYPE ASCIZ STRING
BR 64$ ;;GET OVER THE ASCIZ
65$: .ASCIZ /SET MAINT TO STOP ON MICRO-BREAK/<<CRLF>
64$:
67$: .ASCIZ /HIT CONTINUE/<<CRLF>
66$:
65$:
HALT
MOV #12,@RESVEC
MOV #2,@RESVEC+2

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1038 002750 012701 000010      MOV      #10,R1      ;SET SOB COUNT
1039 002754 012702 003161      MOV      @UBRTBL+1,R2 ;GET ADRS OF UBREAK DATA TABLE
1040 002760 112237 177770      1S:     MOVVB   (R2)+,@UBREAK ;LOAD MICRO-BREAK FROM TABLE
1041 002764 000010      10      ;EXEC RES INSTR (ROM ADRS 000)
1042 002766 005037 177770      CLR      @UBREAK
1043 002772 077105      SOB     R1,1S      ;CONTINUE
1044 002774 012737 000125 177770      MOV      @125,@UBREAK ;SET MICRO-BREAK DATA PATTERN
1045 003002 005400      MARK   0          ;EXEC MARK (ROM ADRS 252)
1046 003004 005037 177770      2S:     CLR      @UBREAK
1047 003010 012706 001100      MOV      @1100,SP   ;RESTORE SP
1048 003014 012737 000006 000004      MOV      @6,@ERRVEC
1049 003022 012737 000002 000006      MOV      @2,@ERRVEC+2
1050 003030 052737 040000 177776      BIS      @114,@PSW  ;GO TO SUPER MODE
1051 003036 012706 000700      MOV      @700,SP   ;SET SUPER SP
1052 003042 012746 003064      MOV      @3S,-(SP) ;SETUP STACK FOR JSR INSTR
1053 003046 005000      CLR      R0        ;SETUP R0
1054 003050 012701 000007      MOV      @7,R1     ;SET SOB COUNT
1055 003054 012702 003174      MOV      @INSTBL+2,R2 ;GET ADRS OF TABLE OF INSTRUCTIONS
1056 003060 012217      4S:     MOV      (R2)+,(PC) ;GET INSTRUCTION
1057 003062 000000      .WORD   ;EXECUTE INSTRUCTION
1058 003064 077103      3S:     SOB     R1,4S     ;CONTINUE
1059 003066 012737 000100 177770      MOV      @100,@UBREAK ;PUT SAFE VALUE IN UBREAK REG
1060 003074 005000      CLR      R0
1061 003076 012702 003160      MOV      @UBRTBL,R2
1062 003102 012703 003172      MOV      @INSTBL,R3
1063 003106 012701 000010      MOV      @10,R1
1064 003112 012746 003126      MOV      @5S,-(SP)
1065 003116 112237 177770      6S:     MOVVB   (R2)+,@UBREAK ;LOAD UBREAK REG FROM TABLE
1066 003122 012317      MOV      (R3)+,(PC) ;GET INSTR FROM TABLE
1067 003124 000000      .WORD   ;EXECUTE INSTR. PROCESSOR SHOULD STOP
1068      ;WITH THE CORRECT ROM ADR IN THE LIGHTS
1069 003126 077105      5S:     SOB     R1,6S     ;CONTINUE
1070 003130 111237 177770      MOVVB   (R2),@UBREAK ;PUT SAFE VALUE IN UBREAK REG
1071 003134 005037 177776      CLR      @PSW      ;GO BACK TO KERNEL MODE
1072 003140 104400 003146      TYPE   69S        ;TYPE ASCIZ STRING
1073 003144 000403      BR      68S        ;GET OVER THE ASCIZ
1074      ;69S: .ASCIZ /DONE/<CRLF>
1075      68S:
1076 003154 000000      HALT
1077 003156 000415      BR
1078 003160      000      001      002  UBRTBL: .BYTE 0,1,2,4,10,20,40,200,100
1079 003163      004      010      020
1080 003166      040      200      100
1081 003172      .EVEN
1082 003172 000010 005010 005020  INSTBL: .WORD 10,5010,5020,5040,0,5200,207,5010
1083 003200 005040 000000 005200

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1085 003212 012706 001200 START: MOV #KERSTK,SP ;SET KERNEL STACK PTR
1086 003216 012737 076543 001524 MOV #76543,2#SHINUM ;INITIALIZE RANDOM NUM GEN
1087 003224 012737 123456 001522 MOV #123456,2#SLONUM
1088
1089 ;DETERMINE HOW PROGRAM WAS LOADED AND WHAT MODE (IF ACT11)
1090 ;AND SET MEMORY PROTECTION.
1091 003232 005037 001504 CLR 2#QV ;SET NOT QV NOR AA MODE
1092 003236 005027 CLR (PC)+ ;SET NOT XXDP
1093 003240 000 .BYTE 0 ;XXDP INDICATOR
1094 003241 000 .BYTE 0 ;XXDP CHAIN MODE INDICATOR
1095 003242 005027 CLR (PC)+ ;CLEAR MEMORY PROTECTION LIMIT
1096 003244 000000 PROT: .WORD 0 ;WILL CONTAIN MEM PROT LIMIT
1097 003246 005737 036756 TST 2#SENDAD+4 ;BRANCH IF NOT QV
1098 003252 100003 BPL 1$
1099 003254 110637 001504 MOVB SP,2#QV ;SET ACT11 QV MODE
1100 003260 000411 BR 3$
1101
1102 003262 001003 1$: BNE 2$
1103 003264 110637 001505 MOVB SP,2#AA ;SET ACT11 AA MODE
1104 003270 000405 BR 3$
1105
1106 003272 005737 000042 2$: TST 2#42 ;BRANCH IF NOT IN CHAIN MODE
1107 003276 001402 BEQ 3$
1108 003300 110637 003241 MOVB SP,2#XXDPC ;SET CHAIN MODE INDICATOR
1109
1110 ;SET MEMORY PROTECTION LIMITS
1111 003304 005737 001504 3$: TST 2#QV ;BRANCH IF QV OR AA
1112 003310 001006 BNE MEMSIZ
1113 003312 005737 003240 TST 2#XXDP ;BRANCH IF NOT VIA XXDP
1114 003316 001403 BEQ MEMSIZ
1115 003320 012737 005700 003244 MOV #5700,2#PROT ;PROTECT XXDP MONITOR
1116 003326 012737 157776 001516 MEMSIZ: MOV #157776,2#LSTMEM ;SET VALUE INTO LSTMEM
1117 003334 163737 003244 001516 SUB 2#PROT,2#LSTMEM ;SET PROTECTION
1118 003342 012737 056372 001514 MOV #ENDTAG+2,2#FRSTMEM ;SET FIRST RELOCATION ADDRESS
1119
1120 ;GET ADDRESS OF THE LAST MEMORY LOCATION ON THE SYSTEM
1121 003350 005002 CLR R2
1122 003352 013703 177760 MOV 2#SIZELO,R3 ;GET ADDRESS OF SIZE REG LO
1123 003356 073227 000006 ASHC #6,R2 ;SHIFT TO FORM ADDRESS
1124 003362 052703 000077 BIS #7,R3 ;ENSURE LOWER SIX BITS SET
1125 003366 062703 000001 ADD #1,R3
1126 003372 005502 ADC R2
1127 003374 010237 001560 MOV R2,2#MXMMHI ;SAVE UPPER SIX BITS
1128 003400 010337 001562 MOV R3,2#MXMML0 ;SAVE LOWER 16 BITS
1129
1130 003404 012706 001200 MOV #KERSTK,SP ;SET STACK PTR
1131 003410 005037 001200 CLR 2#SPASS ;CLEAR PASS COUNT
1132 003414 105037 001503 CLRB 2#MMON ;SET MEM MGMT ON IND=NOT ON
1133 003420 012737 000600 001520 MOV #600,2#NEXPAR ;SET FIRST 'PAR' VALUE
1134 003426 005737 003244 TST 2#PROT
1135 003432 001403 BEQ 1$
1136 003434 012737 001600 001520 MOV #1600,2#NEXPAR
1137 003442 1$: MOV #27,R0 ;SET SOB COUNT
1138 003442 012700 000027 CLR R1 ;SETUP INDEX
1139 003446 005001

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1141 003454 062701 000002      ADD      #2,R1
1142 003460 077005              SOB      R0,2$          ;CONTINUE
1143 003462 012737 177777 001656  MOV      #-1,2$MAPTBL  ;INITIALIZE MAP TABLE
1144 003470 012737 177777 001660  MOV      #-1,2$MAPTBL+2
1145 003476 012700 000010      MOV      #10,R0        ;SET SOB COUNT
1146 003502 012701 001676      MOV      #RPNHSTAT,R1  ;GET ADDRESS OF HANDLER STAT
1147 003506 012721 000200      MOV      #200,(R1)+    ;INITIALIZE STATUS TABLE
1148 003512 077003              SOB      R0,3$          ;CONTINUE
1149 003514 012737 000060 001532  MOV      #60,2$SUBPASS ;INIT SUBPASS TO ASCII 0
1150 003522 012700 047106      MOV      #TIMEBUF,R0   ;GET ADR OF TIME BUFFER
1151 003526 012701 000012      MOV      #12,R1        ;SET SOB COUNT
1152 003532 112720 000060      MOV      #60,(R0)+    ;INIT TIME BUFFER
1153 003536 077103              SOB      R1,4$
1154 003540 105040              CLRB    -(R0)          ;INSERT TERMINATOR
1155 003542 112737 000072 047111  MOV      #72,2$TIMEBUF+3 ;INSERT COLON
1156 003550 112737 000072 047114  MOV      #72,2$TIMEBUF+6
1157 003556 012737 000340 177776  MOV      #340,2$PS     ;LOCK OUT ALL INTERRUPTS
1158 003564 012706 001200      MOV      #SCMTAG,R6    ;FIRST LOCATION TO BE CLEARED
1159 003570 005026      CLR     (R6)+          ;CLEAR MEMORY LOCATION
1160 003572 022706 001240      CMP     #STKS,R6      ;DONE?
1161 003576 001374      BNE    -6             ;LOOP BACK IF NO
1162 003600 012706 001200      MOV     #STACK,SP     ;SETUP THE STACK POINTER
1163 003604 012737 044534 000020  MOV     #SSCOPE,2$IOTVEC ;IOT VECTOR FOR SCOPE ROUTINE
1164 003612 012737 000340 000022  MOV     #340,2$IOTVEC+2 ;LEVEL 7
1165 003620 012737 044774 000030  MOV     #SEAROR,2$EMTVEC ;EMT VECTOR FOR ERROR ROUTINE
1166 003626 012737 000340 000032  MOV     #340,2$EMTVEC+2 ;LEVEL 7
1167 003634 012737 051374 000034  MOV     #STRAP,2$TRAPVEC ;TRAP VECTOR FOR TRAP CALLS
1168 003642 012737 000340 000036  MOV     #340,2$TRAPVEC+2 ;LEVEL 7
1169 003650 012737 051230 000024  MOV     #SPWRDN,2$PWVEC  ;POWER FAILURE VECTOR
1170 003656 012737 000340 000026  MOV     #340,2$PWVEC+2  ;LEVEL 7
1171 003664 016767 032726 032716  MOV     #SENDC,SEOPCT  ;SETUP END-OF-PROGRAM COUNTER
1172 003672 005067 175420      CLR     #TIMES        ;INITIALIZE NUMBER OF ITERATIONS
1173 003676 005067 175416      CLR     #ESCAPE       ;CLEAR THE ESCAPE ON ERROR ADDRESS
1174 003702 112767 000001 175307  MOV     #1,SERMAX     ;ALLOW ONE ERROR PER TEST
1175 003710 012767 003710 175272  MOV     #.,SLPADR     ;INITIALIZE THE LOOP ADDRESS FOR SCOPE
1176 003716 012767 003716 175266  MOV     #.,SLPERR     ;SETUP THE ERROR LOOP ADDRESS
1177
1178 ;CLEAR PROGRAM INDICATORS
1179 003724 052777 000100 175306  BIS     #100,2$TKS    ;SET IE BIT IN KEYBOARD STATUS REG
1180 003732 012737 052400 000060  MOV     #TKISR,2$TKVEC ;SETUP KEYBOARD VECTOR
1181 003740 012737 000200 000062  MOV     #PR4,2$TKVEC+2
1182 003746 012737 052612 000064  MOV     #TPISR,2$TPVEC
1183 003754 012737 000200 000066  MOV     #PR4,2$TPVEC+2
1184 003762 005037 001466      CLR     #NOTYPE      ;CLEAR 'NO TYPING' INDICATOR
1185
1186 ;THE BELOW ROUTINE ASCERTAINS WHICH CP & CP OPTIONS THE PROGRAM IS RUN-
1187 ;NING ON AND SETS AN INDICATOR IN OPT.CP ACCORDINGLY.
1188 003766 012737 000006 000004  CPCHK: MOV     #ERRVEC+2,2$ERRVEC ;SET UP ERROR TRAP TO RETURN
1189 003774 012737 000002 000006  MOV     #2,2$ERRVEC+2
1190 004002 012737 000012 000010  MOV     #RESVEC+2,2$RESVEC ;AND ALSO RESERVED INST TRAP
1191 004010 012737 000002 000012  MOV     #2,2$RESVEC+2
1192 004016 012702 144006      MOV     #144006,R2    ;SET 11/70 NON-OPTION BITS
1193 004022 000261      SEC
1194 004024 170500      TSTF   R0            ;WILL CLEAR CARRY IF 11/45 FLOATING POINT
1195 004026 170000      CFCC                    ;IS AVAIL. COPY FLOATING CC'S INTO PSW

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1197	004032	052702	020000		BIS	#FPOPT,R2		;SET FP OPTION AVAIL INDICATOR
1198	004036	000261		6\$:	SEC			
1199	004040	005737	177546		TST	#LKS		;BRANCH IF NO KW11-L
1200	004044	103402			BCS	7\$		
1201	004046	052702	001000		BIS	#LKOPT,R2		;SET OPTION INDICATOR
1202	004052	000261		7\$:	SEC			
1203	004054	005777	175164		TST	#STPS		;BRANCH IF NO CONSOLE TTY
1204	004060	103402			BCS	9\$		
1205	004062	052702	000400		BIS	#TTOPT,R2		
1206	004066	005003		9\$:	CLR	R3		
1207	004070	000261			SEC			
1208	004072	005737	170000		TST	#UBEDB		; IS UBE1 THERE?
1209	004076	103410			BCS	12\$		;BRANCH IF NO
1210	004100	105037	170006		CLRB	#UBECR1		; IS THIS A TESTER OR EXERCISOR?
1211	004104	105737	170006		TSTB	#UBECR1		
1212	004110	100045			BPL	15\$		;BRANCH IF TESTER
1213	004112	052702	000200	16\$:	BIS	#UBEOPT,R2		;SET INDICATOR
1214	004116	000425			BR	17\$		
1215	004120	000261		12\$:	SEC			
1216	004122	005737	170020		TST	#UBEDB+20		; IS UBE2 THERE?
1217	004126	103403			BCS	13\$		;BRANCH IF NO
1218	004130	012703	000020		MOV	#20,R3		;SET OFFSET IN R3
1219	004134	000766			BR	16\$		
1220	004136	000261		13\$:	SEC			
1221	004140	005737	170040		TST	#UBEDB+40		; IS UBE3 THERE?
1222	004144	103403			BCS	14\$		;BRANCH IF NO
1223	004146	012703	000040		MOV	#40,R3		;PUT OFFSET IN R3
1224	004152	000757			BR	16\$		
1225	004154	000261		14\$:	SEC			
1226	004156	005737	170060		TST	#UBEDB+60		; IS UBE4 THERE?
1227	004162	103420			BCS	15\$		;BRANCH IF NO
1228	004164	012703	000060		MOV	#60,R3		;PUT OFFSET IN R3
1229	004170	000750			BR	16\$		
1230	004172	005227	177777	17\$:	INC	#-1		
1231	004176	001012			BNE	15\$		
1232	004200	012704	002230		MOV	#UBETBL,R4		;GET ADDRESS OF UBE TABLE
1233	004204	012705	000005		MOV	#5,R5		;SET SOB COUNT
1234	004210	060324		18\$:	ADD	R3,(R4)+		;ADJUST UBE TABLE ENTRIES
1235	004212	077502			SOB	R5,18\$		;CONTINUE
1236	004214	006003			ROR	R3		
1237	004216	006003			ROR	R3		;ADJUST OFFSET FOR UBE VECTOR
1238	004220	060324			ADD	R3,(R4)+		;ADJUST UBEVEC ENTRY
1239	004222	060314			ADD	R3,(R4)		;ADJUST UBEVEC PSW ENTRY
1240	004224	005003		15\$:	CLR	R3		;INIT R3
1241	004226	000261			SEC			
1242	004230	005777	176012		TST	#MBTTBL		; IS MASS BUS TESTER THERE?
1243	004234	103403			BCS	20\$		;BRANCH IF NO
1244	004236	052702	002000	21\$:	BIS	#MTOPT,R2		;SET OPTION AVAILABLE
1245	004242	000422			BR	24\$		
1246	004244	005777	176026	20\$:	TST	#MBTN2		; IS MBT2 THERE?
1247	004250	103403			BCS	22\$		;BRANCH IF NO
1248	004252	012703	000100		MOV	#100,R3		;SETUP R3
1249	004256	000767			BR	21\$		
1250	004260	005777	176014	22\$:	TST	#MBTN3		; IS MBT3 THERE?
1251	004264	103403			BCS	23\$		;BRANCH IF NO

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1253 004272 000761          BR      21$
1254 004274 005777 176002 23$:  TST    3MBTN4      ; IS MBT4 THERE?
1255 004300 103427          BCS    30$          ; BRANCH IF NO
1256 004302 012703 000300          MOV    300,R3
1257 004306 000753          BR      21$
1258 004310 005227 177777 24$:  INC    8-1
1259 004314 001021          BNE    30$
1260 004316 012704 002246          MOV    3MBTTBL,R4      ; GET ADDRESS OF MBT TABLE
1261 004322 012705 000011          MOV    811,R5          ; SET SOB COUNT
1262 004326 060324          ADD    R3,(R4)+        ; ADJUST MBT TABLE
1263 004330 077502          SOB    R5,25$         ; CONTINUE
1264 004332 060337 002274          ADD    R3,3MBTTBL+26   ; ADJUST DRIVE TYPE ADDRESS
1265 004336 112777 000007 175714          MOV    87,3MBTTBL+12   ; SET UNIT NUMBER
1266 004344 122777 000040 175722          CMPB   840,3MBTTBL+26  ; IS THIS REALLY A MBT?
1267 004352 001402          BEQ    30$            ; BRANCH IF YES
1268 004354 042702 002000          BIC    3MBOPT,R2       ; CLEAR OPTION AVAILABLE BIT
1269 004360 012737 053442 000004 30$:  MOV    8ERPRT,3ERRVEC  ; RESTORE ERROR TRAP
1270 004366 012737 053370 000010          MOV    8RESERR,3RESVEC ; AND ALSO RESERVED INST TRAP
1271 004374 010237 001470          MOV    R2,3OPT.CP     ; LOAD INDICATOR
1272 004400 005227 177777          INC    8-1            ; FIRST TIME?
1273 004404 001031          BNE    64$            ; BRANCH IF NO
1274 004406 022737 036752 000042          CMP    8SENDAD,3#42   ; ACT-11?
1275 004414 001425          BEQ    64$            ; BRANCH IF YES
1276 004416 104400 004424          TYPE   65$            ; TYPE ASCIZ STRING
1277 004422 000422          BR      64$            ; GET OVER THE ASCIZ
1278                                     ; 65$: .ASCIZ <CRLF>"CEQKCC...PDP 11/70 CPU EXERCISOR"<CRLF>
1279 004470          64$:
1280                                     ; *****
1281                                     ; SBTTL SYSTEM SIZER
1282                                     ; THIS ROUTINE DETERMINES WHAT DRIVES ARE AVAILABLE ON
1283                                     ; THE FOLLOWING DEVICES: RK05, RPO3, RPO4, AND RSO4. THE
1284                                     ; INFORMATION IS STORED IN THE TABLE "SYSSIZE" IN THE FOLLOWING FORMAT:
1285                                     ; A. EACH DEVICE IS ASSIGNED A WORD
1286                                     ; B. THE LOW BYTE OF THIS WORD INDICATES WHICH DRIVES ARE AVAILABLE
1287                                     ; C. THE HIGH BYTE INDICATES WHICH DRIVES HAVE BEEN USED
1288                                     ; BY THE RELOCATION ROUTINE.
1289                                     ; *****
1290 004470 012737 004602 000004 SIZE: MOV    821$,3ERRVEC    ; SETUP TIMEOUT VECTOR
1291 004476 005037 001276          CLR    3$STMP0        ; ENSURE $TMP0 CLEAR
1292 004502 005000          CLR    R0              ; USED TO SET THE UNIT AVAIL BITS
1293 004504 012701 000010          MOV    810,R1          ; SOB COUNT
1294 004510 013777 001276 175416 9$:  MOV    3$STMP0,3RKDA   ; SET UNIT NUMBER
1295 004516 012777 000015 175402          MOV    815,3RKCS      ; SEND DRIVE RESET
1296 004524 032777 000200 175372          BIT    8BIT7,3RKER    ; NON EXISTANT DISK?
1297 004532 001011          BNE    7$              ; BRANCH IF YES
1298 004534 017702 175362          MOV    3RKDS,R2       ; GET DRIVE STATUS
1299 004540 042702 177537          BIC    8177537,R2     ; GET BITS 5 & 7 ONLY
1300 004544 022702 000200          CMP    8200,R2        ; IS DRIVE READY?
1301 004550 001002          BNE    7$              ; BRANCH IF NO
1302 004552 052700 000400          BIS    8BIT8,R0       ; SET UNIT AVAILABLE
1303 004556 006000          ROR    R0
1304 004560 012777 000001 175340          MOV    81,3RKCS      ; CLEAR THE ERRORS
1305 004566 062737 020000 001276          ADD    820000,3$STMP0 ; SELECT NEXT UNIT
1306 004574 077133          SOB    R1,9$          ; CONTINUE
1307 004576 110037 001610          MOV    R0,3$SYSSIZE+2 ; STORE IN TABLE

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1309          ;*****
1310          ;THIS CODE DETERMINES IF THERE IS AN RPO3 OR AN RPO4 OR BOTH.
1311          ;IF BOTH ARE ON THE SYSTEM, THE OPERATOR MUST CHANGE THE RPO4
1312          ;ADDRESSES IN THE TABLE IN "COMMON TAGS" AND "NOP" THE BRANCH
1313          ;AT "100$".
1314
1315 004602 012737 005042 000004 21$:  MOV    #11$, @ERRVEC      ;SET THE ERROR VECTOR
1316 004610 005737 176710          TST    @#176710      ;IS THERE AN RP ON THE SYSTEM?
1317          ;STAY HERE IF YES
1318 004614 012737 004630 000004          MOV    #1$, @ERRVEC
1319 004622 005777 175314          TST    @RP4CS1      ;IS THERE AN RPO4 ON SYSTEM?
1320 004626 000441          100$: BR    10$      ;BRANCH IF YES
1321          ;*****
1322
1323          ;*****
1324 004630 012737 004732 000004 1$:  MOV    #10$, @ERRVEC  ;SETUP TIMEOUT VEC FOR RPO3 TEST
1325 004636 012737 000001 001276          MOV    #1, @STMP0    ;SETUP TEMPO
1326 004644 005000          CLR    R0            ;USED TO SET UNIT AVAILABLE BITS
1327 004646 012701 000010          MOV    #10, R1       ;SOB COUNT
1328 004652 013777 001276 175224 3$:  MOV    @STMP0, @RP3CS ;SET FUNCTION IDLE WITH UNIT NO
1329 004660 005777 175220          TST    @RP3CS        ;WAS THERE AN ERROR?
1330 004664 100006          BPL    6$            ;BRANCH IF NO
1331 004666 006000          ROR    R0            ;UNIT NOT AVAILABLE
1332 004670 062737 000400 001276          ADD    #400, @STMP0  ;SELECT NEXT UNIT
1333 004676 077113          SOB    R1, 3$        ;CONTINUE
1334 004700 000412          BR    5$
1335 004702 017702 175172          MOV    @RP3DS, R2    ;GET STATUS REGISTER
1336 004706 042702 136377          BIC    #136377, R2   ;GET BITS 14, 9 & 8 ONLY
1337 004712 022702 041400          CMP    #41400, R2    ;IS DRIVE READY?
1338 004716 001363          BNE    4$            ;BRANCH IF NO
1339 004720 052700 000400          BIS    #BIT8, R0     ;SET DRIVE AVAILABLE BIT
1340 004724 000760          BR    4$            ;CONTINUE
1341 004726 110037 001606          5$:  MOVB   R0, @SYSSIZE  ;STORE IN TABLE
1342
1343          ;*****
1344 004732 012737 005042 000004 10$: MOV    #11$, @ERRVEC  ;SETUP ERROR VEC FOR RPO4 TEST
1345 004740 005037 001276          CLR    @STMP0
1346 004744 005000          CLR    R0            ;UNIT AVAILABLE WORD
1347 004746 012701 000010          MOV    #10, R1       ;SOB COUNT
1348 004752 113777 001276 175174 14$: MOVB   @STMP0, @RP4CS2 ;SET UNIT NUMBER
1349 004760 012777 000021 175154          MOV    #21, @RP4CS1 ;TRY READ-IN-PRESET
1350 004766 032777 010000 175160          BIT    #BIT12, @RP4CS2 ;NON EXISTANT DRIVE?
1351 004774 001011          BNE    12$           ;BRANCH IF YES
1352 004776 017702 175156          MOV    @RP4DS, R2    ;GET DRIVE STATUS
1353 005002 042702 163277          BIC    #163277, R2   ;GET BITS 12, 11, 8, & 6 ONLY
1354 005006 022702 010500          CMP    #10500, R2    ;IS DRIVE READY?
1355 005012 001002          BNE    12$           ;BRANCH IF NO
1356 005014 052700 000400          BIS    #BIT8, R0     ;SET UNIT AVAILABLE
1357 005020 006000          ROR    R0
1358 005022 052777 000040 175124          BIS    #BITS, @RP4CS2 ;CLEAR ERROR BITS
1359 005030 005237 001276          INC    @STMP0        ;SELECT NEXT DRIVE
1360 005034 077132          SOB    R1, 14$       ;CONTINUE
1361 005036 110037 001616          MOVB   R0, @SYSSIZE+10 ;STORE IN TABLE
1362
1363          ;*****

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1365 005050 005037 001276          CLR      @STMP0
1366 005054 005000          CLR      R0
1367 005056 012701 000010          MOV      @10,R1          ; SOB COUNT
1368 005062 113777 001276 175124 18$: MOVB     @STMP0,@RSCS2   ; SET UNIT NUMBER
1369 005070 012777 000001 175104 18$: MOV      @1,@RSCS1     ; TRY NOP OPERATION
1370 005076 032777 010000 175110 18$: BIT      @BIT12,@RSCS2 ; NON EXISTANT DRIVE?
1371 005104 001011          BNE      16$           ; BRANCH IF YES
1372 005106 017702 175106          MOV      @RSDS,R2       ; GET DRIVE STATUS
1373 005112 042702 163577          BIC      @163577,R2     ; GET BITS 12, 11, & 7 ONLY
1374 005116 022702 010200          CMP      @10200,R2     ; IS DRIVE READY?
1375 005122 001002          BNE      16$           ; BRANCH IF NO
1376 005124 052700 000400          BIS      @BIT8,R0       ; SET DRIVE AVAILABLE BIT
1377 005130 006000          ROR      R0
1378 005132 052777 000040 175054 16$: BIS      @BITS,@RSCS2   ; CLEAR ANY ERROR BITS
1379 005140 005237 001276          INC      @STMP0         ; SELECT NEXT UNIT
1380 005144 077132          SOB      R1,18$        ; CONTINUE
1381 005146 110037 001620          MOVB     R0,@SYSSIZE+12 ; STORE IN TABLE
1382
1383          ; NEXT, DELETE XXDP UNIT 0 FROM TABLE
1384 005152 122737 000002 000041 15$: CMPB     @2,@#41      ; RK?
1385 005160 001004          BNE      19$           ; BRANCH IF NO
1386 005162 042737 000001 001610 15$: BIC      @BIT0,@SYSSIZE+2 ; MAKE UNIT ZERO NOT AVAILABLE
1387 005170 000420          BR       20$
1388 005172 113700 000041 19$: MOVB     @#41,R0       ; GET LOCATION 41
1389 005176 042700 177770          BIC      @177770,R0    ; GET LEAST SIG 3 BITS
1390 005202 000241          CLC
1391 005204 006100          ROL      R0            ; ENSURE C CLEAR
1392 005206 122700 000002          CMPB     @2,R0         ; ADJUST
1393 005212 002404          BLT      40$           ; BRANCH IF NO
1394 005214 042737 000001 001606 40$: BIC      @BIT0,@SYSSIZE
1395 005222 000403          BR       20$
1396 005224 042760 000001 001612 40$: BIC      @BIT0,SYSSIZE+4(R0)
1397 005232 005227 177777 20$: INC      #-1
1398 005236 001055          BNE      LOOP          ; BRANCH IF NOT FIRST TIME
1399 005240 104400 054764          TYPE     MSG25
1400 005244 013746 001470          MOV      @#OPT.CP,-(SP)
1401 005250 104402          TYPCC
1402 005252 104400 001327          TYPE     SCRLF
1403 005256 005737 001504          TST      @#QV          ; ACT11?
1404 005262 001043          BNE      LOOP          ; BRANCH IF YES
1405 005264 105737 003241 50$: TSTB     @#XXDPC       ; XXDP CHAIN MODE?
1406 005270 001040          BNE      LOOP          ; BRANCH IF YES
1407 005272 105737 001200          TSTB     @#SPASS       ; FIRST PASS?
1408 005276 001035          BNE      LOOP          ; BRANCH IF NO
1409 005300 032737 000200 177570 50$: BIT      @SW7,@SWR     ; INHIBIT SIZE TYPEOUT?
1410 005306 001031          BNE      LOOP          ; BRANCH IF YES
1411 005310 004767 041604          JSR      PC,TYPSIZ     ; GO TYPE SYSTEM SIZE
1412 005314 104400 005322          TYPE     65$          ; TYPE ASCIZ STRING
1413 005320 000417          BR       64$          ; GET OVER THE ASCIZ
1414          ; 65$: .ASCIZ /TYPE A CHARACTER TO CONTINUE/<CRLF>
1415 005360 64$: CLR      @PSW
1416 005360 005037 177776          WAIT
1417 005364 000001          JMP      @#SIZE        ; GO CHECK SYSTEM AGAIN
1418 005366 000137 004470          ; PROGRAM RESTARTS HERE AFTER RELOCATION ABOVE 28K IS COMPLETE.
1419

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1421 005372 012706 000700      LOOP:  MOV      #SUPSTK,SP          ;SET THE STACK...WILL BE DIFFERENT
1422                                     ;THAN KERN STACK WHEN IN OUTER MODE
1423 005376 012700 000004      MOV      #ERRVEC,RO          ;GET CURRENT PSW
1424 005402 013701 177776      MOV      @#PSW,R1           ;SET ERROR VEC
1425 005406 012720 053442      MOV      #ERRPT,(RO)+       ;SET ERROR VEC
1426 005412 052701 000340      BIS      #PR7,R1           ;SET PRIORITY 7 IN CURRENT PSW
1427 005416 042701 000020      BIC      #BIT4,R1          ;CLEAR T BIT
1428 005422 010120             MOV      R1,(RO)+           ;SET RESERVED INST TRAP VECTOR
1429 005424 012720 053370      MOV      #RESERR,(RO)+      ;SET RESERVED INST TRAP VECTOR
1430 005430 010120             MOV      R1,(RO)+           ;SET T BIT VEC
1431 005432 012720 001472      MOV      #STRN,(RO)+       ;SET T BIT VEC
1432 005436 042701 000340      BIC      #PR7,R1
1433 005442 005020             CLR      (RO)+              ;SET TBIT VEC+2
1434 005444 005720             TST      (RO)+              ;BUMP RO TO SCOPE VEC+2
1435 005446 005020             CLR      (RO)+              ;SET SCOPE VEC+2
1436 005450 062700 000006      ADD      #6,RO              ;SET RO TO ERROR TRAP VEC
1437 005454 012720 000340      MOV      #PR7,(RO)+        ;SET ERROR VEC
1438 005460 005720             TST      (RO)+              ;SET TRAP VEC+2
1439 005462 012720 000340      MOV      #PR7,(RO)+        ;SET TRAP VEC+2
1440 005466 012737 052642 000114  MOV      #PARSRV,@#CACHVEC  ;SET PARITY ERROR VECTOR
1441 005474 052701 000340      BIS      #PR7,R1
1442 005500 010137 000116      MOV      R1,@#CACHVEC+2
1443 005504 012737 053274 000250  MOV      #KTABRT,@#MMVEC    ;SET KT11 ABORT VECTOR
1444 005512 010137 000252      MOV      R1,@#MMVEC+2
1445 005516 042737 000340 177776  BIC      #PR7,@#PSW
1446                                     ;*****
1447                                     ;*TEST 1 MEMORY VERIFICATION TEST
1448                                     ;*****
1449 005524 112737 000001 001202  ST1:  MOV      #1,@#STSTNM    ;LOAD TEST NUMBER
1450 005532 012767 000001 173556      MOV      #1,STIMES          ;;DO 1 ITERATION
1451 005540 000004
1452
1453                                     .SBTTL START OF SECTION D
1454 :000000000000 FIRST ADDRESS TO BE RELOCATED 00000000
1455 RELO: MOV      PC,RO          ;GET PC
1456 TST      -(RO)             ;RO CONTAINS THE ADDRESS OF RELO
1457 MOV      RO,@#FRSTAD       ;SAVE
1458 MOV      PC,RO             ;GET CURRENT PC
1459 SUB      #,RO              ;SUBTRACT RELOCATION FACTOR
1460 MOV      RO,@#FACTOR       ;SAVE RELOCATION FACTOR
1461 MOV      PC,@#SLPERR       ;SET LOOP ADDRESS
1462 ADD      #30,@#SLPERR      ;ADJUST
1463 MOV      @#SLPERR,@#SLPADR
1464 TSTB     @#NEXEC            ;BR IF TEST CODE TO BE EXECUTED
1465 BEQ     .+6
1466 JMP     RELO
1467 ;MEMORY AND DISK (IF SELECTED) VERIFICATION TEST.
1468 JMP     1$
1469 .WORD   -1,-1,-1,-1,0,0,0,0
1470 .WORD   000000 000000
1471 .WORD   000000 000000
1472 .WORD   -1,-1,-1,-1,0,0,0,0
1473 .WORD   000000 000000
1474 .WORD   000000 000000
1475 .WORD   -1,-1,-1,-1,0,0,0,0

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1477	005676	000000	000000		
1478	005702	177777	177777	177777	.WORD -1,-1,-1,-1,0,0,0,0
1479	005710	177777	000000	000000	
1480	005716	000000	000000		
1481	005722	177777	177777	177777	.WORD -1,-1,-1,-1,0,0,0,0
1482	005730	177777	000000	000000	
1483	005736	000000	000000		
1484	005742	177777	177777	177777	.WORD -1,-1,-1,-1,0,0,0,0
1485	005750	177777	000000	000000	
1486	005756	000000	000000		
1487	005762	177777	177777	177777	.WORD -1,-1,-1,-1,0,0,0,0
1488	005770	177777	000000	000000	
1489	005776	000000	000000		
1490	006002	177777	177777	177777	.WORD -1,-1,-1,-1,0,0,0,0
1491	006010	177777	000000	000000	
1492	006016	000000	000000		
1493	006022	177777	177777	177777	.WORD -1,-1,-1,-1,0,0,0,0
1494	006030	177777	000000	000000	
1495	006036	000000	000000		
1496	006042	177777	177777	177777	.WORD -1,-1,-1,-1,0,0,0,0
1497	006050	177777	000000	000000	
1498	006056	000000	000000		
1499	006062	177777	177777	177777	.WORD -1,-1,-1,-1,0,0,0,0
1500	006070	177777	000000	000000	
1501	006076	000000	000000		
1502	006102	177777	177777	177777	.WORD -1,-1,-1,-1,0,0,0,0
1503	006110	177777	000000	000000	
1504	006116	000000	000000		
1505	006122	177777	177777	177777	.WORD -1,-1,-1,-1,0,0,0,0
1506	006130	177777	000000	000000	
1507	006136	000000	000000		
1508	006142	177777	177777	177777	.WORD -1,-1,-1,-1,0,0,0,0
1509	006150	177777	000000	000000	
1510	006156	000000	000000		
1511	006162	177777	177777	177777	.WORD -1,-1,-1,-1,0,0,0,0
1512	006170	177777	000000	000000	
1513	006176	000000	000000		
1514	006202	177777	177777	177777	.WORD -1,-1,-1,-1,0,0,0,0
1515	006210	177777	000000	000000	
1516	006216	000000	000000		
1517	006222	177777	177777	177777	.WORD -1,-1,-1,-1,0,0,0,0
1518	006230	177777	000000	000000	
1519	006236	000000	000000		
1520	006242	177777	177777	177777	.WORD -1,-1,-1,-1,0,0,0,0
1521	006250	177777	000000	000000	
1522	006256	000000	000000		
1523	006262	177777	177777	177777	.WORD -1,-1,-1,-1,0,0,0,0
1524	006270	177777	000000	000000	
1525	006276	000000	000000		
1526	006302	177777	177777	177777	.WORD -1,-1,-1,-1,0,0,0,0
1527	006310	177777	000000	000000	
1528	006316	000000	000000		
1529	006322	177777	177777	177777	.WORD -1,-1,-1,-1,0,0,0,0
1530	006330	177777	000000	000000	
1531	006336	000000	000000		

1533	006350	177777	000000	000000	
1534	006358	000000	000000		
1535	006366	177777	177777	177777	.WORD -1,-1,-1,-1,0,0,0,0
1536	006370	177777	000000	000000	
1537	006376	000000	000000		
1538	006402	177777	177777	177777	.WORD -1,-1,-1,-1,0,0,0,0
1539	006410	177777	000000	000000	
1540	006416	000000	000000		
1541	006422	177777	177777	177777	.WORD -1,-1,-1,-1,0,0,0,0
1542	006430	177777	000000	000000	
1543	006436	000000	000000		
1544	006442	177777	177777	177777	.WORD -1,-1,-1,-1,0,0,0,0
1545	006450	177777	000000	000000	
1546	006456	000000	000000		
1547	006462	177777	177777	177777	.WORD -1,-1,-1,-1,0,0,0,0
1548	006470	177777	000000	000000	
1549	006476	000000	000000		
1550	006502	177777	177777	177777	.WORD -1,-1,-1,-1,0,0,0,0
1551	006510	177777	000000	000000	
1552	006516	000000	000000		
1553	006522	177777	177777	177777	.WORD -1,-1,-1,-1,0,0,0,0
1554	006530	177777	000000	000000	
1555	006536				IS:
1556	006536	000004			RELO: SCOPE
1557	006540	010702			MOV PC,R2
1558	006542	062702	000012		ADD #12,R2
1559	006546	012707	034242		MOV #RELOC,PC ;GO RELOCATE PROGRAM CODE
1560	006552	000000			RELO: .WORD 0
1561					;000000000000 LAST ADDRESS OF CODE TO BE RELOCATED 0000000000
1562					*****
1563					*****
1564					TEST 2 CHECK BRANCH INSTRUCTIONS
1565					*****
1566	006554	112737	000002	001202	TST2: MOVB #2,#STSTNM ;LOAD TEST NUMBER
1567	006562	012767	000001	172526	MOV #1,#STIMES ;;DO 1 ITERATION
1568	006570	000004			SCOPE
1569					
1570					.SBTTL START OF SECTION 1
1571					:1111111111111111 FIRST ADDRESS TO BE RELOCATED 1111111111
1572	006572	010700			REL1: MOV PC,RO ;GET PC
1573	006574	005740			TST -(RO) ;RO CONTAINS THE ADDRESS OF REL1
1574	006576	010037	001512		MOV RO,#FRSTAD ;SAVE
1575	006602	010700			MOV PC,RO ;GET CURRENT PC
1576	006604	162700	006604		SUB #,RO ;SUBTRACT RELOCATION FACTOR
1577	006610	010037	001506		MOV RO,#FACTOR ;SAVE RELOCATION FACTOR
1578	006614	010737	001212		MOV PC,#SLPERR ;SET LOOP ADDRESS
1579	006620	062737	000030	001212	ADD #30,#SLPERR ;ADJUST
1580	006626	013737	001212	001210	MOV #SLPERR,#SLPADR
1581	006634	105737	001502		TSTB #NEXEC ;BR IF TEST CODE TO BE EXECUTED
1582	006640	001402			.+6
1583	006642	000167	004052		JMP RELE1
1584					
1585	006646	000257			;CC'S=0000
1586	006650	103407			BCS CCO ;SAME AS BLO
1587	006652	102406			BVS CCO

1589	006656	100404	BMI	CC0
1590	006660	002403	BLT	CC0
1591	006662	003402	BLE	CC0
1592	006664	101401	BLOS	CC0
1593	006666	101001	BHI	.+4
1594	006670	104000	HLT	
1595				
1596				
1597	006672	000270	SEN	
1598	006674	100003	BPL	CC1
1599	006676	002002	BGE	CC1
1600	006700	003001	BGT	CC1
1601	006702	002401	BLT	.+4
1602	006704	104000	HLT	
1603				
1604				
1605	006706	000262	SEV	
1606	006710	102003	BVC	CC2
1607	006712	002402	BLT	CC2
1608	006714	003401	BLE	CC2
1609	006716	002001	BGE	.+4

CC0: ;ONE OF THE ABOVE BRANCHES FAILED

;CONTINUE ;CC'S=1000

CC1: ;ONE OF THE ABOVE BRANCHES FAILED

;CONTINUE ;CC'S=1010

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1611
1612 ;CONTINUE
1613 006722 000261 SEC ;CC'S=1011
1614 006724 103002 BCC CC3
1615 006726 101001 BHI CC3
1616 006730 003001 BGT .+4
1617 006732 104000 CC3: HLT ;ERROR! ONE OF THE ABOVE BRANCHES FAILED
1618
1619 ;CONTINUE
1620 006734 000264 SEZ ;CC'S=1111
1621 006736 001003 BNE CC4
1622 006740 003002 BGT CC4
1623 006742 101001 BHI CC4
1624 006744 003401 BLE .+4
1625 006746 104000 CC4: HLT ;ERROR! ONE OF THE ABOVE BRANCHES FAILED
1626
1627 ;*****
1628 ;*TEST 3 TEST UNIARY CONDITION CODES
1629 ;*****
1629 006750 112737 000003 001202 TST3: MOVB #3,2#STSTNM ;LOAD TEST NUMBER
1630 006756 000004 SCOPE
1631 ;CLR
1632 006760 000277 RO
1633 006762 000244 SCC
1634 006764 005000 CLZ ;RO=0,CC'S=0100
1635 006766 103404 CLR RO
1636 006770 102403 BCS CLRO
1637 006772 001002 BVS CLRO
1638 006774 100401 BNE CLRO
1639 006776 003401 BMI CLRO
1640 007000 104000 CLRO: BLE .+4 ;ERROR! INCORRECT CC'S AFTER CLR
1641 HLT
1642 007002 000277 SCC
1643 007004 000244 CLZ ;RO=0,CC'S=0100
1644 007006 005700 TST RO
1645 007010 103404 BCS TSTO
1646 007012 102403 BVS TSTO
1647 007014 001002 BNE TSTO
1648 007016 100401 BMI TSTO
1649 007020 101401 BLOS .+4
1650 007022 104000 TSTO: HLT ;ERROR! INCORRECT CC'S AFTER TST
1651
1652 007024 000257 CCC
1653 007026 000266 +SEZ!SEV
1654 007030 005100 COM RO ;RO=-1,CC'S=1001
1655 007032 103004 BCC COMO
1656 007034 102403 BVS COMO
1657 007036 001402 BEQ COMO
1658 007040 100001 BPL COMO
1659 007042 002401 BLT .+4
1660 007044 104000 COMO: HLT ;ERROR! INCORRECT CC'S AFTER COM
1661
1662 007046 000261 SEC
1663 007050 005500 ADC RO ;RO=000000,CC'S=0101
1664 007052 103003 BCC ADCO
1665 007054 102402 BVS ADCO

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1667	007060	002001		BGE	.+4	
1668	007062	104000	ADC:	HLT		;ERROR! INCORRECT CC'S AFTER ADC
1669						
1670	007064	000261		SEC		
1671	007066	006000		ROR	RO	;RO=100000,CC'S=1010
1672	007070	103404		BCS	RORO	
1673	007072	102003		BVC	RORO	
1674	007074	001402		BEQ	RORO	
1675	007076	100001		BPL	RORO	
1676	007100	003001		BGT	.+4	
1677	007102	104000	ROR:	HLT		;ERROR! INCORRECT CC'S AFTER ROR
1678	007104	000277		SCC		
1679	007106	000242		CLV		
1680	007110	005300		DEC	RO	;RO=077777,CC'S=0011
1681	007112	103004		BCC	DECO	
1682	007114	102003		BVC	DECO	
1683	007116	001402		BEQ	DECO	
1684	007120	100401		BMI	DECO	
1685	007122	003401		BLE	.+4	
1686	007124	104000	DECO:	HLT		;ERROR! INCORRECT CC'S AFTER DEC
1687						
1688	007126	000257		CCC		
1689	007130	005200		INC	RO	;RO=100000,CC'S=1010
1690	007132	103404		BCS	INCO	
1691	007134	102003		BVC	INCO	
1692	007136	001402		BEQ	INCO	
1693	007140	100001		BPL	INCO	
1694	007142	003001		BGT	.+4	
1695	007144	104000	INCO:	HLT		;ERROR! INCORRECT CC'S AFTER INC
1696						
1697	007146	000277		SCC		
1698	007150	000242		CLV		
1699	007152	005400		NEG	RO	;RO=100000,CC'S=1011
1700	007154	103003		BCC	NEGO	
1701	007156	102002		BVC	NEGO	
1702	007160	001401		BEQ	NEGO	
1703	007162	002001		BGE	.+4	
1704	007164	104000	NEGO:	HLT		;ERROR! INCORRECT CC'S AFTER NEG
1705						
1706	007166	000261		SEC		
1707	007170	006300		ASL	RO	;RO=000000,CC'S=0111
1708	007172	103004		BCC	ASLO	
1709	007174	102003		BVC	ASLO	
1710	007176	001002		BNE	ASLO	
1711	007200	100401		BMI	ASLO	
1712	007202	101401		BLOS	.+4	
1713	007204	104000	ASLO:	HLT		;ERROR! INCORRECT CC'S AFTER ASL
1714						
1715	007206	006100		ROL	RO	;RO=000001,CC'S=0000
1716	007210	103402		BCS	ROLO	
1717	007212	003401		BLE	ROLO	
1718	007214	002001		BGE	.+4	
1719	007216	104000	ROLO:	HLT		;ERROR! INCORRECT CC'S AFTER ROL
1720						
1721	007220	006200		ASR	RO	;RO=000000,CC'S=0111



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1723 007224 102002          BVC  ASRO
1724 007226 001001          BNE  ASRO
1725 007230 002401          BLT  .+4
1726 007232 104000          ASRO: HLT          ;ERROR! INCORRECT CC'S AFTER ASR
1727
1728 007234 000277          SCC
1729 007236 005600          SBC  RO          ;RO=-1,CC'S=1001
1730 007240 103002          BCC  SBCO
1731 007242 102401          BVS  SBCO
1732 007244 003401          BLE  .+4
1733 007246 104000          SBCO: HLT          ;ERROR! INCORRECT CC'S AFTER SBC
1734
1735 007250 005400          NEG  RO          ;RO=000001,CC'S=00001
1736 007252 000300          SWAB RO          ;RO=000400 ,CC'S=0100
1737 007254 103403          BCS  SWABO
1738 007256 102402          BVS  SWABO
1739 007260 001001          BNE  SWABO
1740 007262 002001          BGE  .+4
1741 007264 104000          SWABO: HLT          ;ERROR! INCORRECT CC'S AFTER SWAB
1742
1743
1744
1745 007266 112737 000004 001202  ;*****
1746 007274 000004          ;*TEST 4 CHECK REGISTER SELECTION
1747 007276 012737 000005 001316  ;*****
1748 007304 005000          †ST4: MOV  #4,#STSTNM          ;LOAD TEST NUMBER
1749 007306 000277          SCOPE
1750 007310 006100          MOV  #5,#STTIMES          ;SET ITERATION COUNT TO 5
1751 007312 010002          CLR  RO
1752 007314 006302          ROL  RO          ;RO=1
1753 007316 010203          MOV  RO,R2
1754 007320 006303          ASL  R2,R3          ;R2=2
1755 007322 010304          MOV  R2,R3
1756 007324 006304          ASL  R3,R4          ;R3=4
1757 007326 010405          MOV  R3,R4
1758 007330 006305          ASL  R4,R5          ;R4=10
1759 007332 010546          MOV  R5,R5          ;R5=20
1760 007334 050416          BIS  R5, -(SP)          ;SET BITS SET IN REGISTERS
1761 007336 050316          BIS  R4, (SP)          ;INTO STACK ADDRESS
1762 007340 050216          BIS  R3, (SP)
1763 007342 050016          BIS  R2, (SP)
1764 007344 022726 000037          BIS  RO, (SP)
1765 007350 001401          CMP  #37, (SP)+
1766 007352 104000          BEQ  .+4          ;WERE SET
1767
1768
1769          ;CHECK THAT ALL BITS CAN BE SET & CLEARED IN ALL REGISTERS
1770 007354 000257          CCC
1771 007356 112700 000377          IS: MOV  #377,RO          ;SET ALL BITS (MOVB EXTENDS SIGN)
1772 007362 006100          ROL  RO          ;ROTATE A 0 THROUGH ALL BIT
1773 007364 103776          BCS  1$          ;POSITIONS
1774 007366 005200          INC  RO          ;FINAL RESULT IS -1
1775 007370 001401          BEQ  .+4
1776 007372 104000          HLT          ;ERROR!
1777

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E05

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1779 007400 005002
1780 007402 000261      2S:  CLR      R2
1781 007404 006002      ROR      R2      ; ROTATE 1 THROUGH ALL BIT POSITS
1782 007406 005300      DEC      R0      ; DECREMENT SHIFT COUNT
1783 007410 001374      BNE     2S
1784 007412 005102      COM     R2      ; R2 SHOULD CONTAIN -1
1785 007414 001401      BEQ     .+4
1786 007416 104000      HLT
1787
1788 007420 012703 100000      MOV     #100000,R3
1789 007424 006203      3S:  ASR     R3      ; EXTEND 1 BIT THROUGH ALL POSITIONS
1790 007426 103376      BCC     3S
1791 007430 005203      INC     R3
1792 007432 001401      BEQ     .+4
1793 007434 104000      HLT      ; ERROR!
1794
1795 007436 112704 177401      MOVB   #177401,R4
1796 007442 060404      4S:  ADD     R4,R4      ; R4=1
1797 007444 103376      BCC     4S      ; HAS THE AFFECT OF SHIFTING A BIT
1798 007446 005704      TST     R4      ; THROUGH ALL POSITIONS
1799 007450 001401      BEQ     .+4      ; RESULT SHOULD BE 0
1800 007452 104000      HLT
1801
1802 007454 012705 000001      MOV     #1,R5
1803 007460 006305      5S:  ASL     R5
1804 007462 102376      BVC     5S
1805 007464 006305      ASL     R5
1806 007466 103002      BCC     6S
1807 007470 005705      TST     R5
1808 007472 001401      BEQ     .+4
1809 007474 104000      6S:  HLT
1810
1811      ; CHECK REGISTER VOLITILITY
1812 007476 005002      CLR     R2
1813 007500 005102      COM     R2      ; R2=-1
1814 007502 010203      MOV     R2,R3
1815 007504 000257      CCC
1816 007506 006002      ROR     R2      ; R2=LOOP COUNT
1817 007510 006202      ASR     R2
1818 007512 010304      7S:  MOV     R3,R4
1819 007514 005302      DEC     R2      ; DECREMENT LOOP COUNT
1820 007516 001375      BNE     7S
1821 007520 005203      INC     R3      ; CHECK R3
1822 007522 001002      BNE     8S
1823 007524 005204      INC     R4      ; CHECK R4
1824 007526 001401      BEQ     .+4
1825 007530 104000      8S:  HLT
1826
1827      ; CHECK TRANSFER OF REGISTER DATA BETWEEN THE GS AND GD REGISTERS
1828 007532 032737 000020 177776  GSTST:  BIT     #20,#PSW      ; CHECK IF 'T' BIT IS SET
1829 007540 001050      BNE     7S      ; SKIP TEST IF 'T' BIT SET
1830 007542 010627      MOV     SP,(PC)+      ; SAVE STACK PTR
1831 007544 000000      1S:  .WORD 0      ; CONTAINS SAVED STACK PTR
1832 007546 010727      MOV     PC,(PC)+      ; LOAD DATA. THE CURRENT PC IS USED AS
1833 007550 000000      2S:  .WORD 0      ; DATA. IF THIS TEST FAILS 2S CON-

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# F05

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1835 007552 005267 177772          INC      2$          ;MAKE ODD TO CHECK BIT 0
1836 007556 016700 177766          3$:     MOV      2$,R0    ;LOAD GD REGISTER 0
1837 007562 010001          MOV      R0,R1      ;TRANSFER GS REG 0 TO GD REG 1
1838 007564 010102          MOV      R1,R2      ;AND GS REG 1 TO GD REG 2
1839 007566 010203          MOV      R2,R3      ;ETC...
1840 007570 010304          MOV      R3,R4
1841 007572 010405          MOV      R4,R5
1842 007574 152737 000340 177776      BISB    #340,2$PSW   ;SET PRIORITY LEVEL 7
1843 007602 010506          MOV      R5,SP      ;TRANSFER GS REG 5 TO GD STK PTR
1844 007604 010627          MOV      SP,(PC)+   ;TRANSFER GS STK PTR TO MEMORY
1845 007606 000000          4$:     .WORD    0      ;CONTAINS GS STACK PTR
1846 007610 016706 177730          MOV      1$,SP      ;RESTORE STK PTR NEEDED FOR HLT/SCOPE
1847 007614 142737 000340 177776      BICB    #340,2$PSW   ;SET PRIORITY LEVEL 0
1848 007622 026700 177760          CMP      4$,R0      ;COMPARE GS/GD STACK WITH GS REG 0
1849 007626 001004          BNE      5$          ;BRANCH IF THEY WERE NOT =
1850 007630 006367 177714          ASL      2$          ;SHIFT TEST DATA UNTIL = 000000
1851 007634 001350          BNE      3$
1852 007636 000411          BR       6$
1853 007640 010046          5$:     MOV      R0,-(SP)   ;GET GS REG 0
1854 007642 010146          MOV      R1,-(SP)   ;ETC...
1855 007644 010246          MOV      R2,-(SP)
1856 007646 010346          MOV      R3,-(SP)
1857 007650 010446          MOV      R4,-(SP)
1858 007652 010546          MOV      R5,-(SP)
1859 007654 104000          HLT
1860          ;ERROR! DATA IN GS STK PTR NOT = GS REG 0
1861 007656 016706 177662          MOV      1$,SP      ;GS REG 0-GS REG 5 ARE ON THE STACK
1862 007662          ;RESTORE STACK PTR
1863 007662          6$:
1864          7$:
1865          ;*****
1866          ;#TEST 5 TEST UNIARY WORD INSTRUCTIONS USING ADDRESS MODE 1
1867          ;*****
1867 007662 112737 000005 001202      1$T5:  MOVVB   #5,2$STSTNM ;LOAD TEST NUMBER
1868 007670 000004          SCOPE
1869 007672 012737 000005 001316          MOV      #5,2$STIMES
1870 007700 000401          BR       .+4
1871 007702 000000          .WORD   0          ;RESERVE ADDRESS FOR TESTS
1872 007704 010702          MOV      PC,R2
1873 007706 162702 000004          SUB      #4,R2      ;R2 POINTS TO RESERVED WORD
1874 007712 005012          CLR      (R2)      ;PRESET (R2)
1875
1876 007714 000261          SEC
1877 007716 006012          ROR     (R2)        ;(R2)=100000,CC=1010
1878 007720 101402          BLOS   ROR1
1879 007722 100001          BPL    ROR1
1880 007724 002001          BGE    .+4
1881 007726 104000          ROR1:  HLT          ;ERROR! INCORRECT CC'S AS SHOWN ABOVE
1882
1883 007730 000257          CCC
1884 007732 000261          SEC
1885 007734 005312          DEC     (R2)        ;(R2)=077777,CC=0011
1886 007736 103001          BCC    DEC1
1887 007740 003401          BLE    .+4
1888 007742 104000          DEC1:  HLT          ;ERROR! INCORRECT CC'S AS SHOWN ABOVE
1889
    
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1891	007746	000261	SEC		
1892	007750	005512	ADC	(R2)	;(R2)=100000,CC=1010
1893	007752	103403	BCS	ADC1	
1894	007754	102002	BVC	ADC1	
1895	007756	100001	BPL	ADC1	
1896	007760	001001	BNE	+.4	
1897	007762	104000	ADC1:	HLT	;ERROR! INCORRECT CC'S AS SHOWN ABOVE
1898					
1899	007764	006112	ROL	(R2)	;(R2)=000000,CC=0111
1900	007766	103003	BCC	ROL1	
1901	007770	102002	BVC	ROL1	
1902	007772	001001	BNE	ROL1	
1903	007774	100001	BPL	+.4	
1904	007776	104000	ROL1:	HLT	;ERROR! INCORRECT CC'S AS SHOWN ABOVE
1905					
1906	010000	006112	ROL	(R2)	;(R2)=000001,CC=0000
1907	010002	101402	BLOS	ROL1A	;BRANCH IF C OR Z IS SET
1908	010004	102401	BVS	ROL1A	
1909	010006	100001	BPL	+.4	
1910	010010	104000	ROL1A:	HLT	
1911					
1912	010012	006212	ASR	(R2)	;(R2)=000000,CC=0111
1913	010014	103003	BCC	ASR1	
1914	010016	102002	BVC	ASR1	
1915	010020	001001	BNE	ASR1	
1916	010022	100001	BPL	+.4	
1917	010024	104000	ASR1:	HLT	;ERROR! INCORRECT CC'S AS SHOWN ABOVE
1918					
1919	010026	006012	ROR	(R2)	;(R2)=100000,CC=1010
1920	010030	103403	BCS	ROR1A	
1921	010032	102002	BVC	ROR1A	
1922	010034	001401	BEQ	ROR1A	
1923	010036	100401	BMI	+.4	
1924	010040	104000	ROR1A:	HLT	
1925					
1926	010042	000261	SEC		
1927	010044	005212	INC	(R2)	;(R2)=100001,CC=1001
1928	010046	103003	BCC	INC1	
1929	010050	102402	BVS	INC1	
1930	010052	001401	BEQ	INC1	
1931	010054	100401	BMI	+.4	
1932	010056	104000	INC1:	HLT	;ERROR! INCORRECT CC'S AS SHOWN ABOVE
1933					
1934	010060	005612	SBC	(R2)	;(R2)=100000,CC=1000
1935	010062	103403	BCS	SBC1	
1936	010064	102402	BVS	SBC1	
1937	010066	001401	BEQ	SBC1	
1938	010070	100401	BMI	+.4	
1939	010072	104000	SBC1:	HLT	;ERROR! INCORRECT CC'S AS SHOWN ABOVE
1940					
1941	010074	000261	SEC		
1942	010076	005612	SBC	(R2)	;(R2)=077777,CC=0010
1943	010100	103403	BCS	SBC1A	
1944	010102	102002	BVC	SBC1A	
1945	010104	001401	BEQ	SBC1A	

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1947 010110 104000 SBC1A: HLT ;ERROR! INCORRECT CC'S AS SHOEN ABOVE
1948
1949 010112 000261 SEC ;(R2)=100000,CC=1010
1950 010114 005512 ADC (R2)
1951 010116 100401 BMI .+4
1952 010120 104000 HLT
1953
1954 010122 000261 SEC ;(R2)=000000,CC=0111
1955 010124 006312 ASL (R2)
1956 010126 103003 BCC ASL1
1957 010130 102002 BVC ASL1
1958 010132 001001 BNE ASL1
1959 010134 100001 BPL .+4
1960 010136 104000 ASL1: HLT ;ERROR! INCORRECT CC'S AS SHOWN ABOVE
1961
1962 010140 005112 COM (R2) ;(R2)=177777,CC=1001
1963 010142 103002 BCC COM1
1964 010144 102401 BVS COM1
1965 010146 100401 BMI .+4
1966 010150 104000 COM1: HLT ;ERROR! INCORRECT CC'S AS SHOWN ABOVE
1967
1968 010152 000250 CLN ;(R2)=177777,CC=1000
1969 010154 005712 TST (R2)
1970 010156 103403 BCS TEST1
1971 010160 102402 BVS TEST1
1972 010162 100001 BPL TEST1
1973 010164 001001 BNE .+4
1974 010166 104000 TEST1: HLT ;ERROR! INCORRECT CC'S AS SHOWN ABOVE
1975
1976 010170 000262 SEV ;(R2)=000001,CC=0000
1977 010172 005412 NEG (R2)
1978 010174 103002 BCC NEG1
1979 010176 102401 BVS NEG1
1980 010200 001001 BNE .+4
1981 010202 104000 NEG1: HLT ;ERROR! INCORRECT CC'S AS SHOWN ABOVE
1982
1983 010204 005312 DEC (R2) ;(R2)=000000,CC=0101
1984 010206 103001 BCC DEC1A
1985 010210 001401 BEQ .+4
1986 010212 104000 DEC1A: HLT ;ERROR! INCORRECT CC'S AS SHOWN ABOVE
1987
1988 ;*****
1989 ;*TEST 6 CHECK UNIARY BYTE INSTRUCTIONS USING ADDRESS MODE 1
1990 ;*****
1990 010214 112737 000006 001202 TST6: MOVB #6,3#STSTNM ;LOAD TEST NUMBER
1991 010222 000004 SCOPE
1992 010224 000401 BR .+4 ;RESERVE A WORD
1993 010226 000000 .WORD 0 ;ADDRESS RESERVED FOR TESTS
1994 010230 010703 MOV PC,R3
1995 010232 162703 000004 SUB #4,R3 ;R3 POINTS TO EVEN BYTE OF WRD
1996 010236 010304 MOV R3,R4 ;R4 POINTS TO ODD BYTE OF WORD
1997 010240 005204 INC R4
1998 010242 005013 CLR (R3) ;PRESET DATA
1999
2000 010244 000261 1$: SEC
2001 010246 105513 ADCB (R3) ;ADD CARRY TO EVEN BYTE

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# I05

2003	010252	105214		INCB	(R4)	;INCREMENT ODD BYTE
2004	010254	000773		BR	1\$	
2005	010256	102401	2\$:	BVS	+.4	; (R3)=077600={0774}[200],CC=1010
2006	010260	104000		HLT		
2007	010262	000242		CLV		
2008	010264	105214		INCB	(R4)	; (R3)=100200={1000}[200],CC=1010
2009	010266	103402		BCS	INCB1	
2010	010270	102001		BVC	INCB1	
2011	010272	100401		BMI	+.4	
2012	010274	104000	INCB1:	HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2013						
2014	010276	106114		ROLB	(R4)	; (R3)=000200={0000}[200],CC=0111
2015	010300	103002		BCC	ROLB1	
2016	010302	102001		BVC	ROLB1	
2017	010304	001401		BEQ	+.4	
2018	010306	104000	ROLB1:	HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2019						
2020	010310	105614		SBCB	(R4)	; (R3)=177600={1774}[200], CC=1001
2021	010312	103002		BCC	SBCB1	
2022	010314	102401		BVS	SBCB1	
2023	010316	100401		BMI	+.4	
2024	010320	104000	SBCB1:	HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2025						
2026	010322	106313		ASLB	(R3)	; (R3)=177400,CC=0111
2027	010324	103002		BCC	ASLB1	
2028	010326	102001		BVC	ASLB1	
2029	010330	001401		BEQ	+.4	
2030	010332	104000	ASLB1:	HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2031						
2032	010334	105413		NEGB	(R3)	; (R3)=177400,CC=0100
2033	010336	103402		BCS	NEGB1	
2034	010340	102401		BVS	NEGB1	
2035	010342	001401		BEQ	+.4	
2036	010344	104000	NEGB1:	HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2037						
2038	010346	000277		SCC		
2039	010350	105313		DECB	(R3)	; (R3)=177777,CC=1001
2040	010352	103002		BCC	DECB1	
2041	010354	102401		BVS	DECB1	
2042	010356	001001		BNE	+.4	
2043	010360	104000	DECB1:	HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2044						
2045	010362	000241		CLC		
2046	010364	106013		RORB	(R3)	; (R3)=177577,CC=0011
2047	010366	103002		BCC	RORB1	
2048	010370	102001		BVC	RORB1	
2049	010372	100001		BPL	+.4	
2050	010374	104000	RORB1:	HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2051						
2052	010376	000241		CLC		
2053	010400	105114		COMB	(R4)	; (R3)=000177,CC=0101
2054	010402	103002		BCC	COMB1	
2055	010404	102401		BVS	COMB1	
2056	010406	001401		BEQ	+.4	
2057	010410	104000	COMB1:	HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE

2059	010412	106213	1S:	ASRB	(R3)	;SHIFT EVEN BYTE UNTIL V CLEARS
2060	010414	102002		BVC	2S	
2061	010416	105514		ADCB	(R4)	;AND ADD CARRY TO ODD BYTE
2062	010420	000774		BR	1S	
2063	010422	103401	2S:	BCS	ASRB1	
2064	010424	001401		BEQ	.+4	
2065	010426	104000	ASRB1:	HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2066						
2067	010430	106214		ASRB	(R4)	
2068	010432	106214		ASRB	(R4)	; (R3)=000400,CC=0011
2069	010434	103002		BCC	ASRB1A	
2070	010436	102001		BVC	ASRB1A	
2071	010440	001001		BNE	.+4	
2072	010442	104000	ASRB1A:	HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2073						
2074	010444	105314		DECB	(R4)	; (R3)=000000,CC=0100
2075	010446	001401		BEQ	.+4	
2076	010450	104000		HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2077						
2078	010452	000261		SEC		
2079	010454	106014		RORB	(R4)	; (R3)=100000,CC=1010
2080	010456	103402		BCS	RORB1A	
2081	010460	102001		BVC	RORB1A	
2082	010462	100401		BMI	.+4	
2083	010464	104000	RORB1A:	HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2084						
2085	010466	000242		CLV		
2086	010470	105314		DECB	(R4)	; (R3)=077400,CC=0100
2087	010472	102401		BVS	.+4	
2088	010474	104000		HLT		
2089						
2090	010476	000261		SEC		
2091	010500	105313		DECB	(R3)	; (R3)=077777,CC=1001
2092	010502	103002		BCC	DECB1A	
2093	010504	102401		BVS	DECB1A	
2094	010506	100401		BMI	.+4	
2095	010510	104000	DECB1A:	HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2096						
2097	010512	000277		SCC		
2098	010514	000313		SWAB	(R3)	; (R3)=177577=[1774][177],CC=0000
2099	010516	103402		BCS	SWAB1	
2100	010520	102401		BVS	SWAB1	
2101	010522	100001		BPL	.+4	
2102	010524	104000	SWAB1:	HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2103						
2104	010526	105714		TSTB	(R4)	; (R3)=177577=[1774][177],CC=1000
2105	010530	103402		BCS	TSTB1	
2106	010532	102401		BVS	TSTB1	
2107	010534	100401		BMI	.+4	
2108	010536	104000	TSTB1:	HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2109						
2110	010540	105014		CLRB	(R4)	; (R3)=000177=[0000][177],CC=0100
2111	010542	001401		BEQ	.+4	
2112	010544	104000		HLT		
2113	010546	106313		ASLB	(R3)	; (R3)=000376 ,CC=1010

# K05

CEQKCC PDP 11/70 CPU EXERCISOR  
CEQKCC.P11 03-MAR-78 13:13

MACY11 30A(1052)  
T6

03-MAR-78 13:15 PAGE 43  
CHECK UNIARY BYTE INSTRUCTIONS USING ADDRESS MODE 1

SEQ 0062

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2115 010552 102001          BVC    ASLB1A
2116 010554 100401          BMI    .+4
2117 010556 104000          ASLB1A: HLT                ;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2118
2119 010560 105113          COMB   (R3)                ;(R3)=000001,CC=0001
2120 010562 103002          BCC   COMB1A
2121 010564 102401          BVS   COMB1A
2122 010566 100001          BPL   .+4
2123 010570 104000          COMB1A: HLT               ;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2124
2125 010572 000313          SWAB  (R3)                ;(R3)=000400, CC=0100
2126 010574 001401          BEQ   .+4
2127 010576 104000          HLT
2128
2129 010600 105213          INCB  (R3)
2130 010602 000261          SEC
2131 010604 105613          SBCB  (R3)                ;(R3)=000400,CC=0100
2132 010606 001401          BEQ   .+4
2133 010610 104000          HLT
2134 010612 022713 000400          CMP   #400,(R3)          ;CHECK REMAINING RESULT
2135 010616 001401          BEQ   .+4
2136 010620 104000          HLT
2137
2138      ;*****
2139      ;*TEST 7 CHECK UNIARY WORD OPS USING ADDRESS MODES 2 & 4
2140      ;*****
2141 010622 112737 000007 001202 TST7:  MOVB  #7,#STSTNM          ;LOAD TEST NUMBER
2142 010630 000004          SCOPE
2143 010632 000401          BR    .+4
2144 010634 000000          .WORD 0                    ;ADDRESS RESERVED FOR TESTS
2145 010636 010704          MOV   PC,R4
2146 010640 162704 000004          SUB   #4,R4                ;R4 AND R5 POINT TO
2147 010644 010405          MOV   R4,R5                ;RESERVED WORD
2148 010646 005015          CLR   (R5)                 ;PRESET DATA=0
2149 010650 000277          SCC
2150 010652 000244          CLZ
2151 010654 005725          TST   (R5)+                ;(R5)=000000,CC=0100
2152 010656 103402          BCS   TEST2
2153 010660 102401          BVS   TEST2
2154 010662 001401          BEQ   .+4
2155 010664 104000          TEST2: HLT                ;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2156
2157 010666 005145          COM   -(R5)                ;(R5)=177777,CC=1001
2158 010670 103001          BCC   COM4
2159 010672 100401          BMI   .+4
2160 010674 104000          COM4: HLT                ;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2161
2162 010676 000241          CLC
2163 010700 006024          ROR   (R4)+                ;(R4)=077777,CC=0011
2164 010702 103002          BCC   ROR2
2165 010704 102001          BVC   ROR2
2166 010706 100001          BPL   .+4
2167 010710 104000          ROR2: HLT                ;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2168
2169 010712 000257          CCC

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L05

CEQKCC PDP 11/70 CPU EXERCISOR  
CEQKCC.P11 03-MAR-78 13:13

MACY11 30A(1052)  
T7

03-MAR-78 13:15 PAGE 44  
CHECK UNIARY WORD OPS USING ADDRESS MODES 2 & 4

SEQ 0063

2171	010716	102002	BVC	INC4	
2172	010720	001401	BEQ	INC4	
2173	010722	100401	BMI	+.4	
2174	010724	104000	HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2175					
2176	010726	000261	SEC		
2177	010730	000324	SWAB	(R4)+	; (R4)=000200,CC=1000
2178	010732	103401	BCS	SWAB2	
2179	010734	100401	BMI	+.4	
2180	010736	104000	HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2181					
2182	010740	005425	NEG	(R5)+	; (R5)=177600,CC=1001
2183	010742	103001	BCC	NEG2	
2184	010744	100401	BMI	+.4	
2185	010746	104000	HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2186					
2187	010750	005044	CLR	-(R4)	; (R4)=000000,CC=0100
2188	010752	001401	BEQ	+.4	
2189	010754	104000	HLT		
2190					
2191	010756	000261	SEC		
2192	010760	006045	ROR	-(R5)	; (R5)=100000,CC=1010
2193	010762	000261	SEC		
2194	010764	005525	ADC	(R5)+	; (R5)=100001,CC=1000
2195	010766	102401	BVS	ADC2	
2196	010770	100401	BMI	+.4	
2197	010772	104000	HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2198					
2199	010774	000262	SEV		
2200	010776	006224	ASR	(R4)+	; (R4)=140000,CC=1001
2201	011000	103002	BCC	ASR2	
2202	011002	102401	BVS	ASR2	
2203	011004	100401	BMI	+.4	
2204	011006	104000	HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2205					
2206	011010	000262	SEV		
2207	011012	006144	ROL	-(R4)	; (R4)=100001, CC=1001
2208	011014	103002	BCC	ROL4	
2209	011016	102401	BVS	ROL4	
2210	011020	100401	BMI	+.4	
2211	011022	104000	HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2212					
2213	011024	005645	SBC	-(R5)	; (R5)=100000,CC=1000
2214	011026	103001	BCC	+.4	
2215	011030	104000	HLT		;ERROR! 'C' BIT FAILED TO CLEAR
2216					
2217	011032	005325	DEC	(R5)+	; (R5)=077777,CC=0010
2218	011034	103402	BCS	DEC2	
2219	011036	102001	BVC	DEC2	
2220	011040	100001	BPL	+.4	
2221	011042	104000	HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2222					
2223	011044	006324	ASL	(R4)+	; (R4)=177776,CC=1010
2224	011046	102401	BVS	+.4	
2225	011050	104000	HLT		

M05

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2227 011054 103003          BCC      ASL4
2228 011056 102402          BVS      ASL4
2229 011060 001401          BEQ      ASL4
2230 011062 100401          BMI      .+4
2231 011064 104000          ASL4:   HLT      ;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2232
2233 011066 022724 177774      CMP      #177774,(R4)+
2234 011072 001401          BEQ      .+4
2235 011074 104000          HLT
2236 011076 020405          CMP      R4,R5
2237 011100 001401          BEQ      .+4
2238 011102 104000          HLT
2239
2240 ;*****
2241 ;*TEST 10 CHECK UNIARY BYTE OPS USING ADDRESS MODES 2 & 4
2242 ;*****
2243 011104 112737 000010 001202  TST10:  MOVB     #10,#STSTNM ;LOAD TEST NUMBER
2244 011112 000004          SCOPE
2245 011114 000401          BR       .+4 ;RESERVE A WORD
2246 011116 000000          .WORD   0 ;RESERVED WORD
2247 011120 010705          MOV      PC,R5
2248 011122 162705 000004      SUB      #4,R5 ;R5 POINTS TO EVEN BYTE OF RESERVED WORD
2249 011126 010500          MOV      R5,R0
2250 011130 010002          MOV      R0,R2
2251 011132 005202          INC      R2 ;R2 POINTS TO ODD BYTE OF RESERVED WORD
2252 011134 005010          CLR      (R0) ;PRESET
2253
2254 011136 000277          SCC
2255 011140 000241          CLC
2256 011142 105125          COMB     (R5)+ ;(R0)=000377,CC=1001
2257 011144 103002          BCC     COMB2
2258 011150 100401          BVS     COMB2
2259 011152 104000          BMI     .+4
2260 COMB2:  HLT      ;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2261 011154 105542          ADCB     -(R2) ;(R0)=000000,CC=0101
2262 011156 001401          BEQ     .+4
2263 011160 104000          HLT ;ERROR! INCORRECT RESULT AS SHOWN ABOVE
2264 011162 105525          ADCB     (R5)+ ;(R0)=000400,CC=0000
2265 011164 103401          BCS     ADCB2
2266 011166 001001          BNE     .+4
2267 011170 104000          ADCB2:  HLT      ;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2268
2269 011172 000263          +SEC!SEV
2270 011174 106045          RORB     -(R5) ;(R0)=100000,CC=1001
2271 011176 103003          BCC     RORB4
2272 011200 102402          BVS     RORB4
2273 011202 001401          BEQ     RORB4
2274 011204 100401          BMI     .+4
2275 011206 104000          RORB4:  HLT      ;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2276
2277 011210 000277          SCC
2278 011212 106122          ROLB     (R2)+ ;(R0)=100001,CC=0000
2279 011214 103403          BCS     ROLB2
2280 011216 102402          BVS     ROLB2
2281 011220 001401          BEQ     ROLB2

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# N05

CEQKCC PDP 11/70 CPU EXERCISOR MACY11 30A(1052) 03-MAR-78 13:15 PAGE 46  
 CEQKCC.P11 03-MAR-78 13:13 T10 CHECK UNIARY BYTE OPS USING ADDRESS MODES 2 & 4

SEQ 0065

2283	011224	104000	ROLB2:	HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2284						
2285	011226	000257		CCC		
2286	011230	106225		ASRB	(R5)+	;(R0)=140001, CC=1010
2287	011232	103402		BCS	ASRB2	
2288	011234	102001		BVC	ASRB2	
2289	011236	100401		BMI	.+4	
2290	011240	104000	ASRB2:	HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2291						
2292	011242	105242		INCB	-(R2)	;(R0)=140002, CC=0000
2293	011244	000277		SCC		
2294	011246	106222		ASRB	(R2)+	;(R0)=140001, CC=0000
2295	011250	103402		BCS	ASRB2A	
2296	011252	102401		BVS	ASRB2A	
2297	011254	100001		BPL	.+4	
2298	011256	104000	ASRB2A:	HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2299						
2300	011260	000266		+SEZ:SEV		;SET Z V
2301	011262	106345		ASLB	-(R5)	;(R0)=100001, CC=1001
2302	011264	103003		BCC	ASLB4	
2303	011266	102402		BVS	ASLB4	
2304	011270	001401		BEQ	ASLB4	
2305	011272	100401		BMI	.+4	
2306	011274	104000	ASLB4:	HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2307						
2308	011276	105322		DECB	(R2)+	;(R0)=077401=[0774][001], CC=0010
2309	011300	103002		BCC	DECB2	
2310	011302	102001		BVC	DECB2	
2311	011304	100001		BPL	.+4	
2312	011306	104000	DECB2:	HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2313						
2314	011310	105645		SBCB	-(R5)	;(R0)=077400, CC=0100
2315	011312	103402		BCS	SBCB4	
2316	011314	102401		BVS	SBCB4	
2317	011316	001401		BEQ	.+4	
2318	011320	104000	SBCB4:	HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2319						
2320	011322	105442		NEGB	-(R2)	;(R0)=10400, CC=1001
2321	011324	103002		BCC	NEGB4	
2322	011326	102401		BVS	NEGB4	
2323	011330	100401		BMI	.+4	
2324	011332	104000	NEGB4:	HLT		;ERROR! INCORRECT CC'S AS SHOWN ABOVE
2325						
2326	011334	105725		TSTB	(R5)+	;(R0)=100400, CC=0100
2327	011336	103401		BCS	TSTB2	
2328	011340	001401		BEQ	.+4	
2329	011342	104000	TSTB2:	HLT		
2330						
2331	011344	105722		TSTB	(R2)+	;(R0)=100400, CC=1000
2332	011346	001401		BEQ	TSTB2A	
2333	011350	100401		BMI	.+4	
2334	011352	104000	TSTB2A:	HLT		
2335						
2336	011354	000261		SEC		
2337	011356	000342		SWAB	-(R2)	;(R0)=000201, CC=1000

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2339 011362 100401          BMI      .+4
2340 011364 104000          SWAB4:  HLT
2341 011366 000277          SCC
2342 011370 105225          INCB      (R5)+          ;(R0)=000601=(0004)(201),CC=0000
2343 011372 103003          BCC      INCB2
2344 011374 102402          BVS      INCB2
2345 011376 001401          BEQ      INCB2
2346 011400 100001          BPL      .+4
2347 011402 104000          INCB2:  HLT
2348 011404 022227 000601    CMP      (R2)+, #000601 ;CHECK END RESULT
2349 011410 001401          BEQ      .+4
2350 011412 104000          HLT
2351 011414 020205          CMP      R2, R5          ;CHECK REGISTERS
2352 011416 001401          BEQ      .+4
2353 011420 104000          HLT
2354 *****
2355 ;*TEST 11 CHECK UNIARY WORD OPS USING ADDRESS MODES 3 & 5
2356 ;*****
2357 ;*ST11: MOV# #11, #STSTNM ;LOAD TEST NUMBER
2358
2359 011422 112737 000011 001202  SCOPE
2360 011430 000004          BR      .+6          ;RESERVE 2 WORDS
2361 011432 000402          .WORD 0          ;1 FOR THE ADDRESS
2362 011434 000000          .WORD 0          ;AND 1 FOR DATA
2363 011436 000000
2364 011440 010703          MOV      PC, R3
2365 011442 162703 000004    SUB      #4, R3
2366 011446 005013          CLR      (R3)          ;PRESET DATA
2367 011450 010300          MOV      R3, R0          ;R0 POINTS TO DATA WORD
2368 011452 005743          TST      -(R3)
2369 011454 010013          MOV      R0, (R3)
2370 011456 010304          MOV      R3, R4
2371
2372 011460 000257          CCC
2373 011462 005733          TST      @ (R3)+          ;(R0)=000000,CC=0100
2374 011464 001401          BEQ      .+4
2375 011466 104000          HLT
2376
2377 011470 000261          SEC
2378 011472 006053          ROR      @-(R3)          ;(R0)=100000,CC=1010
2379 011474 103402          BCS      RORS
2380 011476 102001          BVC      RORS
2381 011500 100401          BMI      .+4
2382 011502 104000          RORS:  HLT
2383
2384 011504 000257          CCC
2385 011506 006234          ASR      @ (R4)+          ;(R0)=140000,CC=1010
2386 011510 102001          BVC      ASR3
2387 011512 100401          BMI      .+4
2388 011514 104000          ASR3:  HLT
2389
2390 011516 000250          CLN
2391 011520 006333          ASL      @ (R3)+          ;(R0)=100000,CC=1001
2392 011522 103002          BCC      ASL3
2393 011524 102401          BVS      ASL3
    
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011530 104000 ASL3: HLT
011532 000277 SCC
011534 005354 DEC @-(R4) ;(R0)=077777, CC=0010
011536 103003 BCC DECS
011540 102002 BVC DECS
011542 001401 BEQ DECS
011544 100001 BPL .+4
011546 104000 DECS: HLT
011550 005453 NEG @-(R3) ;(R0)=100001, CC=1001
011552 103002 BCC NEG5
011554 102401 BVS NEG5
011556 100401 BMI .+4
011560 104000 NEG5: HLT
011562 000262 SEV
011564 005134 COM @-(R4)+ ;(R0)=077776, CC=0001
011566 103001 BCC COM3
011570 102001 BVC .+4
011572 104000 COM3: HLT
011574 005233 INC @-(R3)+ ;(R0)=077777, CC=0001
011576 103001 BCC INC3
011600 100001 BPL .+4
011602 104000 INC3: HLT
011604 005554 ADC @-(R4) ;(R0)=100000, CC=1010
011606 103402 BCS ADC5
011610 102001 BVC ADC5
011612 100401 BMI .+4
011614 104000 ADC5: HLT
011616 000257 CCC
011620 006134 ROL @-(R4)+ ;(R0)=000000, CC=0111
011622 103002 BCC ROL3
011624 102001 BVC ROL3
011626 001401 BEQ .+4
011630 104000 ROL3: HLT
011632 005253 INC @-(R3) ;(R0)=000001, CC=0001
011634 005654 SBC @-(R4) ;(R0)=000000, CC=0100
011636 103401 BCS SBC5
011640 001401 BEQ .+4
011642 104000 SBC5: HLT
*****
:TEST 12 CHECK UNIARY BYTE OPS USING ADDRESS MODES 3 & 5
*****
011644 112737 000012 001202 TST12: MOVB #12,@STSTNM ;LOAD TEST NUMBER
011652 000004 SCOPE
011654 000403 BR .+10 ;RESERVE 3 WORDS
011656 000000 .WORD 0 ;1 FOR EVEN BYTE ADDRESS
011660 000000 .WORD 0 ;1 FOR ODD BYTE ADDRESS
011662 000000 .WORD 0 ;AND 1 FOR DATA
011664 010702 MOV PC,R2

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2451	011670	005742	TST	-(R2)	: DATA WORD
2452	011672	010200	MOV	R2, R0	: R0 POINTS TO THE DATA WORD
2453	011674	005010	CLR	(R0)	: PRESET DATA
2454	011676	005742	TST	-(R2)	: BACK R2 UP TO
2455	011700	005742	TST	-(R2)	: EVEN BYTE ADDRESS WORD
2456	011702	010022	MOV	R0, (R2)+	: LOAD ADDRESS
2457	011704	005200	INC	R0	: ODD BYTE ADDRESS
2458	011706	010022	MOV	R0, (R2)+	: LOAD ODD BYTE ADDRESS
2459	011710	010200	MOV	R2, R0	: RESET R0
2460	011712	010200	MOV	R2, R5	
2461	011714	105152	COMB	2-(R2)	;(R0)=177400, CC=1001
2462	011716	103001	BCC	COMB5	
2463	011720	100401	BMI	.+4	
2464	011722	104000	COMB5:	HLT	
2465	011724	105752	TSTB	2-(R2)	;(R0)=177400, CC=0100
2466	011726	001401	BEG	.+4	
2467	011730	104000	HLT		
2468	011732	000262	SEV		
2469	011734	106255	ASRB	2-(R5)	;(R0)=177400, CC=1001
2470	011736	103002	BCC	ASRB5	
2471	011740	102401	BVS	ASRB5	
2472	011742	100401	BMI	.+4	
2473	011744	104000	ASRB5:	HLT	
2474					
2475	011746	105232	INCB	2(R2)+	;(R0)=177401, CC=000
2476	011750	103001	BCC	INCB3	
2477	011752	100001	BPL	.+4	
2478	011754	104000	INCB3:	HLT	
2479					
2480	011756	000241	CLC		
2481	011760	106055	RORB	2-(R5)	;(R0)=177400, CC=0111
2482	011762	103003	BCC	RORB5	
2483	011764	102002	BVC	RORB5	
2484	011766	001001	BNE	RORB5	
2485	011770	100001	BPL	.+4	
2486	011772	104000	RORB5:	HLT	
2487					
2488	011774	106332	ASLB	2(R2)+	;(R0)=177000, CC=1001
2489	011776	103002	BCC	ASLB3	
2490	012000	102401	BVS	ASLB3	
2491	012002	100401	BMI	.+4	
2492	012004	104000	ASLB3:	HLT	
2493					
2494	012006	105552	ADCB	2-(R2)	;(R0)=177400, CC=1000
2495	012010	103401	BCS	ADCB5	
2496	012012	100401	BMI	.+4	
2497	012014	104000	ADCB5:	HLT	
2498					
2499	012016	000277	SCC		
2500	012020	106135	ROLB	2(R5)+	;(R0)=177401, CC=0000
2501	012022	101402	BLOS	ROLB3	: BRANCH IF C OR Z IS SET
2502	012024	102401	BVS	ROLB3	
2503	012026	100001	BPL	.+4	
2504	012030	104000	ROLB3:	HLT	
2505					

E06

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2507 012034 100401      BMI      .+4
2508 012036 104000      HLT
2509
2510 012040 000261      SEC
2511 012042 105635      SBCB      @ (R5)+      ; (R0)=000377, CC=0100
2512 012044 103401      BCS      SBCB3
2513 012046 001401      BEQ      .+4
2514 012050 104000      SBCB3:  HLT
2515
2516 012052 105432      NEGB     @ (R2)+      ; (R0)=000001
2517 012054 105352      DECB     @ -(R2)      ; (R0)=000000, CC=0101
2518 012056 103001      BCC
2519 012060 001401      BEQ      .+4
2520 012062 104000      DECBS:  HLT
2521
2522 *****
2523 :;TEST 13 CHECK UNIARY WORD OPS USING ADDRESS MODE 6 (PC)
2524 :;*****
2525
2526 012064 112737 000013 001202  ST13:  MOV      #13,@STSTNM      ;LOAD TEST NUMBER
2527 012072 000004      SCOPE
2528 012074 005027      CLR      (PC)+      ;PRESET DATA = 0
2529 012076 000000      UWM6:  .WORD      0      ;RESERVED FOR DATA
2530 012100 010700      MOV      PC,R0
2531 012102 024040      CMP      -(R0),-(R0)  ;R0 POINTS TO DATA WORD
2532 012104 000277      SCC
2533 012106 006167 177764      ROL      UWM6      ; (R0)=000001,CC=0000
2534 012112 103403      BCS      ROL6
2535 012114 102402      BVS      ROL6
2536 012116 001401      BEQ      ROL6
2537 012120 100001      BPL      .+4
2538 012122 104000      ROL6:  HLT
2539
2540 012124 005167 177746      COM      UWM6      ; (R0)=177776, CC=1001
2541 012130 103002      BCC      COM6
2542 012132 102401      BVS      COM6
2543 012134 100401      BMI      .+4
2544 012136 104000      COM6:  HLT
2545 012140 006267 177732      ASR      UWM6      ; (R0)=177777, CC=1010
2546 012144 103402      BCS      ASR6
2547 012146 102001      BVC      ASR6
2548 012150 100401      BMI      .+4
2549 012152 104000      ASR6:  HLT
2550
2551 012154 000277      SCC
2552 012156 005467 177714      NEG      UWM6      ; (R0)=000001, CC=0001
2553 012162 103003      BCC      NEG6
2554 012164 102402      BVS      NEG6
2555 012166 001401      BEQ      NEG6
2556 012170 100001      BPL      .+4
2557 012172 104000      NEG6:  HLT
2558
2559 012174 000277      SCC
2560 012176 006067 177674      ROR      UWM6      ; (R0)=100000, CC=1001
2561 012202 103003      BCC      ROR6
2562 012204 102402      BVS      ROR6
2563 012206 001401      BEQ      ROR6

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F06

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2563 012212 104000 ROR6: HLT
2564
2565 012214 005667 177656 SBC UWM6 ;(RO)=077777, CC=0010
2566 012220 103402 BCS SBC6
2567 012222 102001 BVC SBC6
2568 012224 100001 BPL .+4
2569 012226 104000 SBC6: HLT
2570
2571 012230 000242 CLV
2572 012232 005267 177640 INC UWM6 ;(RO)=100000, CC=1011
2573 012236 103403 BCS INC6
2574 012240 102002 BVC INC6
2575 012242 001401 BEQ INC6
2576 012244 100401 BMI .+4
2577 012246 104000 INC6: HLT
2578
2579 012250 006267 177622 ASR UWM6 ;(RO)=140000, CC=1010
2580 012254 000261 SEC
2581 012256 006367 177614 ASL UWM6 ;(RO)=100000, CC=1001
2582 012262 103002 BCC ASL6
2583 012264 102401 BVS ASL6
2584 012266 100401 BMI .+4
2585 012270 104000 ASL6: HLT
2586
2587 012272 005367 177600 DEC UWM6 ;(RO)=077777, CC=0011
2588 012276 103002 BCC DEC6
2589 012300 102001 BVC DEC6
2590 012302 100001 BPL .+4
2591 012304 104000 DEC6: HLT
2592
2593 012306 005567 177564 ADC UWM6 ;(RO)=100000, CC=1010
2594 012312 103402 BCS ADC6
2595 012314 102001 BVC ADC6
2596 012316 100401 BMI .+4
2597 012320 104000 ADC6: HLT
2598 012322 000242 CLV
2599 012324 000367 177546 SWAB UWM6
2600 012330 100401 BMI .+4
2601 012332 104000 HLT
2602 012334 022710 000200 CMP #200, (RO)
2603 012340 001401 BEQ .+4
2604 012342 104000 HLT
2605
2606 ::*****
2607 ::*TEST 14 CHECK UNIARY BYTE OPS (EVEN/ODD) USING ADDRESS MODE 6 (PC)
2608 ::*****
2609 TST14: MOVB #14, @#STSTNM ;LOAD TEST NUMBER
2610 SCOPE
2611 MOV #UBM6, RO ;RO POINTS TO ADDRESS OF DATA
2612 ADD @#FACTOR, RO ;CLEAR DATA
2613 CLR UBM6
2614 SCC
2615 CLZ
2616 TSTB UBM6
2617 BCS TSTB6
    BVS TSTB6
    
```



G06

CEQKCC PDP 11/70 CPU EXERCISOR  
CEQKCC.P11 03-MAR-78 13:13

MACY11 30A(1052) 03-MAR-78 13:15 PAGE 52  
T14 CHECK UNIARY BYTE OPS (EVEN/ODD) USING ADDRESS MODE 6 (PC)

SEQ 0071

2619	012406	100001		BPL	.+4	
2620	012410	104000		TSTB6:	HLT	
2621						
2622	012412	000257		CCC		
2623	012414	105767	000277	TSTB	UBM6+1	; TEST ODD BYTE
2624	012420	001401		BEQ	.+4	
2625	012422	104000		HLT		
2626						
2627	012424	105667	000266	SBCB	UBM6	; (RO)=000000, CC=0100
2628	012430	103402		BCS	SBCB6	
2629	012432	102401		BVS	SBCB6	
2630	012434	001401		BEQ	.+4	
2631	012436	104000		SBCB6:	HLT	
2632						
2633	012440	000261		1\$:	SEC	
2634	012442	105267	000250	INCB	UBM6	; LOOP UNTIL (RO)=077600, CC=1011
2635	012446	100403		BMI	2\$	
2636	012450	105567	000243	ADCB	UBM6+1	; INCB INST INCREMENTS EVEN BYTE
2637	012454	000771		BR	1\$	; ADCB INCREMENTS ODD BYTE
2638	012456	103001		2\$:	BCC	INCB6
2639	012460	102401		BVS	.+4	
2640	012462	104000		INCB6:	HLT	
2641						
2642	012464	106367	000226	ASLB	UBM6	; (RO)=077400, CC=0111
2643	012470	103003		BCC	ASLB6	
2644	012472	102002		BVC	ASLB6	
2645	012474	001001		BNE	ASLB6	
2646	012476	100001		BPL	.+4	
2647	012500	104000		ASLB6:	HLT	
2648						

2650	012504	105567	000207		ADCB	UBM6+1	;(RO)=100000, CC=1010
2651	012510	103402			BCS	ADCB6	
2652	012512	102001			BVC	ADCB6	
2653	012514	100401			BMI	.+4	
2654	012516	104000		ADCB6:	HLT		
2655							
2656	012520	000261			SEC		
2657	012522	106067	000171		RORB	UBM6+1	;(RO)=140000, CC=1010
2658	012526	103402			BCS	RORB6	
2659	012530	102001			BVC	RORB6	
2660	012532	100401			BMI	.+4	
2661	012534	104000		RORB6:	HLT		
2662							
2663	012536	105167	000154		COMB	UBM6	;(RO)=140377 CC=1001
2664	012542	103002			BCC	COMB6	
2665	012544	102401			BVS	COMB6	
2666	012546	100401			BMI	.+4	
2667	012550	104000		COMB6:	HLT		
2668							
2669	012552	000262			SEV		
2670	012554	105467	000137		NEGB	UBM6+1	;(RO)=040377, CC=0001
2671	012560	103002			BCC	NEGB6	
2672	012562	102401			BVS	NEGB6	
2673	012564	100001			BPL	.+4	
2674	012566	104000		NEGB6:	HLT		
2675							
2676	012570	106167	000123		ROLB	UBM6+1	;(RO)=100777, CC=1010
2677	012574	103402			BCS	ROLB6	
2678	012576	102001			BVC	ROLB6	
2679	012600	100401			BMI	.+4	
2680	012602	104000		ROLB6:	HLT		
2681							
2682	012604	106267	000106		ASRB	UBM6	;(RO)=100777, CC=1001
2683	012610	103002			BCC	ASRB6	
2684	012612	102401			BVS	ASRB6	
2685	012614	100401			BMI	.+4	
2686	012616	104000		ASRB6:	HLT		
2687							
2688	012620	105267	000072		INCB	UBM6	;(RO)=100400, CC=0101
2689	012624	103002			BCC	INCB6A	
2690	012626	102401			BVS	INCB6A	
2691	012630	001401			BEQ	.+4	
2692	012632	104000		INCB6A:	HLT		
2693							
2694	012634	105367	000057		DECB	UBM6+1	;(RO)=100000, CC=1001
2695	012640	103003			BCC	DECB6A	
2696	012642	102402			BVS	DECB6A	
2697	012644	001401			BEQ	DECB6A	
2698	012646	100401			BMI	.+4	
2699	012650	104000		DECB6A:	HLT		
2700							
2701	012652	000367	000040		SWAB	UBM6	;(RO)=000200, CC=1000
2702	012656	103401			BCS	SWAB6	
2703	012660	100401			BMI	.+4	
2704	012662	104000		SWAB6:	HLT		

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2706 012664 106167 000026          ROLB  UBM6          ;(RO)=000000, CC=0111
2707 012670 103002          BCC   ROLB6A
2708 012672 102001          BVC   ROLB6A
2709 012674 001401          BEQ   .+4
2710 012676 104000          ROLB6A: HLT
2711
2712 012700 005767 000012          TST   UBM6          ;(RO)=000000, CC=0100
2713 012704 103402          BCS   TEST6
2714 012706 102401          BVS   TEST6
2715 012710 001401          BEQ   .+4
2716 012712 104000          TEST6: HLT
2717
2718 012714 000401          BR    .+4          ;RESERVE A WORD
2719 012716 000000          UBM6: .WORD 0      ;WORD RESERVED FOR DATA
2720 012720 000004          REL1: SCOPE
2721 012722 010702          MOV   PC,R2
2722 012724 062702 000012          ADD   #12,R2
2723 012730 012707 034242          MOV   #RELOC,PC    ;GO RELOCATE PROGRAM CODE
2724 012734 000000          REL11: .WORD 0
2725          ;11111111111111 LAST ADDRESS OF CODE TO BE RELOCATED 1111111111
2726
2727          ;*****
2728          ;*TEST 15 CHECK UNIARY WORD OPS USING ADDRESS MODE 7
2729          ;*****
2730 012736 112737 000015 001202  TST15: MOVB  #15,#STSTNM ;LOAD TEST NUMBER
2731 012744 012767 000001 166344  MOV   #1,$TIMES    ;;DO 1 ITERATION
2732 012752 000004          SCOPE
2733
2734          .SBTTL START OF SECTION 2
2735          :2222222222222222 FIRST ADDRESS TO BE RELOCATED 222222222
2736 012754 010700          REL2: MOV   PC,RO    ;GET PC
2737 012756 005740          TST   -(RO)       ;RO CONTAINS THE ADDRESS OF REL2
2738 012760 010037 001512          MOV   RO,#FRSTAD  ;SAVE
2739 012764 010700          MOV   PC,RO       ;GET CURRENT PC
2740 012766 162700 012766          SUB   #,RO        ;SUBTRACT RELOCATION FACTOR
2741 012772 010037 001506          MOV   RO,#FACTOR  ;SAVE RELOCATION FACTOR
2742 012776 010737 001212          MOV   PC,#SLPERR  ;SET LOOP ADDRESS
2743 013002 062737 000030 001212  ADD   #30,#SLPERR ;ADJUST
2744 013010 013737 001212 001210  MOV   #SLPERR,#SLPADR
2745 013016 105737 001502          TSTB  #NEXEC      ;BR IF TEST CODE TO BE EXECUTED
2746 013022 001402          BEQ   .+6
2747 013024 000167 004060          JMP   RELE2
2748 013030 000403          BR    UW7
2749 013032 000000          UWM7: .WORD 0      ;RESERVE 3 WORDS FOR ADDRESSES & DATA
2750 013034 000000          .WORD 0          ;CONTAINS ADDRESS OF UWM7
2751 013036 000000          .WORD 0          ;CONTAINS DATA
2752          ;CONTAINS ADDRESS OF UWM7
2753 013040 010700          UW7: MOV   PC,RO
2754 013042 005740          TST   -(RO)
2755 013044 005740          TST   -(RO)
2756 013046 005040          CLR   -(RO)      ;CLEAR TEST DATA
2757 013050 010002          MOV   RO,R2
2758 013052 010240          MOV   R2,-(RO)   ;SET UP ADDRESS
2759 013054 005720          TST   (RO)+      ;MOVE RO TO NEXT ADDRESS
2760 013056 005720          TST   (RO)+

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2762	013062	010200		MOV	R2,RO	;SET RO POINTING TO DATA
2763	013064	000277		SCC		
2764	013066	000244		CLZ		
2765	013070	005772	000002	TST	2(2)	;(RO)=000000, CC=0100
2766	013074	001401		BEQ	.+4	
2767	013076	104000		HLT		
2768						
2769	013100	000277		SCC		
2770	013102	005672	177776	SBC	2-2(2)	;(RO)=177777, CC=1001
2771	013106	103002		BCC	SBC7	
2772	013110	102401		BVS	SBC7	
2773	013112	100401		BMI	.+4	
2774	013114	104000		HLT		
2775				SBC7:		
2776	013116	000277		SCC		
2777	013120	000241		CLC		
2778	013122	006372	000002	ASL	2(2)	;(RO)=177776, CC=1001
2779	013126	103002		BCC	ASL7	
2780	013130	102401		BVS	ASL7	
2781	013132	100401		BMI	.+4	
2782	013134	104000		HLT		
2783				ASL7:		
2784	013136	000257		CCC		
2785	013140	005372	000002	DEC	2(2)	;(RO)=177775, CC=1000
2786	013144	103402		BCS	DEC7	
2787	013146	102401		BVS	DEC7	
2788	013150	100401		BMI	.+4	
2789	013152	104000		HLT		
2790				DEC7:		
2791	013154	000262		SEV		
2792	013156	006272	177776	ASR	2-2(2)	;(RO)=177776, CC=1001
2793	013162	103002		BCC	ASR7	
2794	013164	102401		BVS	ASR7	
2795	013166	100401		BMI	.+4	
2796	013170	104000		HLT		
2797				ASR7:		
2798	013172	000241		CLC		
2799	013174	000262		SEV		
2800	013176	006072	177776	ROR	2-2(2)	;(RO)=077777, CC=0000
2801	013202	101402		BLOS	ROR7	;BRANCH IF C OR Z IS SET
2802	013204	102401		BVS	ROR7	
2803	013206	100001		BPL	.+4	
2804	013210	104000		HLT		
2805				ROR7:		
2806	013212	000262		SEV		
2807	013214	005472	000002	NEG	2(2)	;(RO)=100001, CC=1001
2808	013220	103002		BCC	NEG7	
2809	013222	102401		BVS	NEG7	
2810	013224	100401		BMI	.+4	
2811	013226	104000		HLT		
2812				NEG7:		
2813	013230	000250		CLN		
2814	013232	000372	177776	SWAB	2-2(2)	;(RO)=000600, CC=1000
2815	013236	103401		BCS	SWAB7	
2816	013240	100401		BMI	.+4	

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2818
2819 013244 000262 SEV
2820 013246 005172 000002 COM 2(2) ;(R0)=177177, CC=1001
2821 013252 103002 BCC COM7
2822 013254 102401 BVS COM7
2823 013256 100401 BMI .+4
2824 013260 104000 COM7: HLT
2825
2826 013262 000372 000002 SWAB 2(2) ;(R0)=077776, CC=1000
2827 013266 100401 BMI .+4
2828 013270 104000 HLT
2829
2830 013272 000277 SCC
2831 013274 005572 177776 ADC 2-2(2) ;(R0)=077777, CC=0000
2832 013300 103402 BCS ADC7
2833 013302 102401 BVS ADC7
2834 013304 100001 BPL .+4
2835 013306 104000 ADC7: HLT
2836
2837 013310 005272 000002 INC 2(2) ;(R0)=100000, CC=1010
2838 013314 102001 BVC INC7
2839 013316 100401 BMI .+4
2840 013320 104000 INC7: HLT
2841
2842 013322 000257 CCC
2843 013324 006172 177776 ROL 2-2(2) ;(R0)=000000, CC=0111
2844 013330 103002 BCC ROL7
2845 013332 102001 BVC ROL7
2846 013334 001401 BEQ .+4
2847 013336 104000 ROL7: HLT
2848
2849 ;*****
2850 ;*TEST 16 CHECK UNIARY BYTE OPS USING ADDRESS MODE 7
2851 ;*****
2851 013340 112737 000016 001202 TST16: MOVB #16,2#STSTNM ;LOAD TEST NUMBER
2852 013346 000004 SCOPE
2853 013350 012700 013034 MOV #UWM7,R0
2854 013354 063700 001506 ADD 2#FACTOR,R0
2855 013360 010002 MOV R0,R2
2856 013362 010067 177450 MOV R0,UWM7+2
2857 013366 005720 TST (R0)+
2858 013370 005210 INC (R0) ;WORD FOLLOWING UWM7 CONTAINS ADDRESS
2859 013372 005740 TST -(R0) ;OF ODD BYTE, R0 POINTS TO DATA WORD
2860 013374 005010 CLR (R0) ;PRESET DATA
2861 013376 010067 177430 MOV R0,UWM7-2
2862 ;NOTE: 2(2) REFERENCES THE ODD BYTE, AND 2-2(2) REFERENCES THE EVEN BYTE.
2863
2864 013402 000263 +SEC!SEV ;SET C AND V
2865 013404 105672 000002 SBCB 2(2) ;(R0)=177400, CC=1001
2866 013410 103003 BCC SBCB7
2867 013412 102402 BVS SBCB7
2868 013414 001401 BEQ SBCB7
2869 013416 100401 BMI .+4
2870 013420 104000 SBCB7: HLT
2871
2872 013422 000277 SCC ;SET CONDITION CODES

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2874	013430	103403		BCS	ADCB7	
2875	013432	102402		BVS	ADCB7	
2876	013434	001401		BEQ	ADCB7	
2877	013436	100001		BPL	.+4	
2878	013440	104000		ADCB7:	HLT	
2879						
2880	013442	105172	177776	COMB	2-2(2)	;(RO)=177776, CC=1001
2881	013446	103002		BCC	COMB7	
2882	013450	102401		BVS	COMB7	
2883	013452	100401		BMI	.+4	
2884	013454	104000		COMB7:	HLT	
2885						
2886	013456	000241		CLC		;CLEAR CARRY
2887	013460	106072	000002	RORB	2(2)	;(RO)=077776, CC=0011
2888	013464	103002		BCC	RORB7	
2889	013466	102001		BVC	RORB7	
2890	013470	100001		BPL	.+4	
2891	013472	104000		RORB7:	HLT	
2892						
2893	013474	105272	000002	INCB	2(2)	;(RO)=100376, CC=1011
2894	013500	103002		BCC	INCB7	
2895	013502	102001		BVC	INCB7	
2896	013504	100401		BMI	.+4	
2897	013506	104000		INCB7:	HLT	
2898						
2899	013510	105372	177776	DECB	2-2(2)	;(RO)=100375, CC=1001
2900	013514	103002		BCC	DECB7	
2901	013516	102401		BVS	DECB7	
2902	013520	100401		BMI	.+4	
2903	013522	104000		DECB7:	HLT	
2904						
2905	013524	106372	000002	ASLB	2(2)	;(RO)=000375, CC=0111
2906	013530	103002		BCC	ASLB7	
2907	013532	102001		BVC	ASLB7	
2908	013534	001401		BEQ	.+4	
2909	013536	104000		ASLB7:	HLT	
2910						
2911	013540	000241		CLC		;CLEAR CARRY
2912	013542	106272	177776	ASRB	2-2(2)	;(RO)=000376, CC=1001
2913	013546	103002		BCC	ASRB7	
2914	013550	102401		BVS	ASRB7	
2915	013552	100401		BMI	.+4	
2916	013554	104000		ASRB7:	HLT	
2917						
2918	013556	105472	000002	NEGB	2(2)	;(RO)=000376, CC=0100
2919	013562	103402		BCS	NEGB7	
2920	013564	102401		BVS	NEGB7	
2921	013566	001401		BEQ	.+4	
2922	013570	104000		NEGB7:	HLT	
2923						
2924	013572	000262		SEV		
2925	013574	106172	177776	ROLB	2-2(2)	;(RO)=00374, CC=1001
2926	013600	103002		BCC	ROLB7	
2927	013602	102401		BVS	ROLB7	
2928	013604	100401		BMI	.+4	

M06

CEKCC PDP 11/70 CPU EXERCISOR  
CEKCC.P11 03-MAR-78 13:13

MACY11 30A(1052) 03-MAR-78 13:15 PAGE 58  
T16 CHECK UNIARY BYTE OPS USING ADDRESS MODE 7

SEQ 0077

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2930
2931 013610 105272 177776          INCB      2-2(2)          ;(R0)=000375, CC=1001
2932 013614 105272 177776          INCB      2-2(2)          ;(R0)=000376, CC=1001
2933 013620 105572 177776          ADCB      2-2(2)          ;(R0)=000377, CC=1000
2934 013624 105172 177776          COMB      2-2(2)          ;(R0)=000000, CC=0100
2935 013630 001401                    BEQ      .+4
2936 013632 104000                    HLT
2937
2938 ;*****
2939 ;#TEST 17 CHECK BINARY OPS USING ADDRESS MODE 0
2940 ;*****
2941 013634 112737 000017 001202  TST17:  MOVB    #17,2#STSTNM          ;LOAD TEST NUMBER
2942 013642 000004                    SCOPE
2943 013644 000277                    SCC
2944 013646 010700                    MOV      PC,R0          ;SET CONDITION CODES
2945 013650 103002                    BCC     MOV0          ;RO=PC, CC=X001
2946 013652 102401                    BVS     MOV0
2947 013654 001001                    BNE     .+4
2948 013656 104000                    HLT
2949
2950 013660 010002                    MOV      R0,R2          ;R2=R0
2951 013662 000262                    SEV
2952 013664 160002                    SUB      R0,R2          ;SET V
2953 013666 103402                    BCS     SUB0          ;R2=000000, CC=0100
2954 013670 102401                    BVS     SUB0
2955 013672 001401                    BEQ     .+4
2956 013674 104000                    HLT
2957
2958 013676 000244                    CLZ
2959 013700 010203                    MOV      R2,R3          ;R2=R3=000000, CC=0100
2960 013702 103401                    BCS     MOV0A
2961 013704 001401                    BEQ     .+4
2962 013706 104000                    HLT
2963
2964 013710 000257                    CCC
2965 013712 000272                    +SEV!SEN          ;SET V & N
2966 013714 020203                    CMP      R2,R3          ;R2=R3=000000, CC=0100
2967 013716 103403                    BCS     CMPO
2968 013720 102402                    BVS     CMPO
2969 013722 001001                    BNE     CMPO
2970 013724 100001                    BPL     .+4
2971 013726 104000                    HLT
2972
2973 013730 010002                    MOV      R0,R2          ;R0=R2
2974 013732 010203                    MOV      R2,R3          ;R0=R2=R3
2975 013734 060203                    ADD      R2,R3          ;R3=2*R0
2976 013736 006302                    ASL     R2              ;R2=2*R0
2977 013740 020203                    CMP      R2,R3          ;R2=R3=2*R0
2978 013742 001401                    BEQ     .+4
2979 013744 104000                    HLT          ;ERROR! CHECK ADD INSTRUCTION
2980
2981 ;THE FOLLOWING SUBTEST SHIFTS A BIT THROUGH R2 AND R5 AND DOES A
2982 ;BIT TEST (BIT) USING R2 AND R5.
2983 013746 005002                    CLR     R2
2984 013750 005202                    INC     R2
2985 013752 000402                    BR      2$

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2986	013756	100407		BMI	45	
2987	013760	010205	25:	MOV	R2,R5	
2988	013762	000277		SCC		
2989	013764	030205		BIT	R2,R5	;R2=R5
2990	013766	103002		BCC	35	
2991	013770	102401		BVS	35	
2992	013772	001370		BNE	15	
2993	013774	104000	35:	HLT		
2994	013776	010205	45:	MOV	R2,R5	
2995	014000	000257		CCC		
2996	014002	030205		BIT	R2,R5	
2997	014004	100401		BMI	.+4	
2998	014006	104000		HLT		
2999						
3000	014010	005002		CLR	R2	
3001	014012	000277		SCC		
3002	014014	050002		BIS	R0,R2	
3003	014016	103002		BCC	BISO	
3004	014020	102401		BVS	BISO	
3005	014022	001001		BNE	.+4	
3006	014024	104000	BISO:	HLT		
3007						
3008	014026	010003		MOV	R0,R3	
3009	014030	000277		SCC		
3010	014032	000244		CLZ		
3011	014034	040003		BIC	R0,R3	
3012	014036	103003		BCC	BICO	
3013	014040	102402		BVS	BICO	
3014	014042	001001		BNE	BICO	
3015	014044	100001		BPL	.+4	
3016	014046	104000	BICO:	HLT		
3017						
3018	014050	010004		MOV	R0,R4	
3019	014052	005104		COM	R4	
3020	014054	040004		BIC	R0,R4	
3021	014056	005104		COM	R4	
3022	014060	020004		CMR	R0,R4	
3023	014062	001401		BEQ	.+4	
3024	014064	104000		HLT		
3025						
3026	014066	010004		MOV	R0,R4	
3027	014070	005104		COM	R4	
3028	014072	010403		MOV	R4,R3	
3029	014074	050003		BIS	R0,R3	
3030	014076	103001		BCC	BISOR	
3031	014100	100401		BMI	.+4	
3032	014102	104000	BISOR:	HLT		
3033	014104	005203		INC	R3	
3034	014106	001401		BEQ	.+4	
3035	014110	104000		HLT		
3036	014112	010304		MOV	R3,R4	
3037	014114	005103		COM	R3	:R3=R4=0
3038	014116	000261		SEC		:R3=177777
3039	014120	006004		ROR	R4	:SET C
3040	014122	060304		ADD	R3,R4	:R4=100000
						:R3=177777,R4=077777,CC=0011



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3042 014126 102002      BVC      ADD0
3043 014130 001401      BEQ      ADD0
3044 014132 100001      BPL      .+4
3045 014134 104000      ADD0:   HLT
3046 014136 010700      MOV      PC,R0
3047 014140 022020      CMP      (R0)+,(R0)+
3048 014142 020007      CMP      R0,PC
3049 014144 001401      BEQ      .+4
3050 014146 104000      HLT
3051
3052 014150 010700      MOV      PC,R0
3053 014152 062700 000010      ADD      #10,R0
3054 014156 010002      MOV      R0,R2
3055 014160 020700      CMP      PC,R0
3056 014162 001002      BNE      CMP0A
3057 014164 020200      CMP      R2,R0
3058 014166 001401      BEQ      .+4
3059 014170 104000      CMP0A: HLT
3060
3061
3062
3063 014172 112737 000020 001202  *TEST 20 CHECK BINARY OPS USING ADDRESS MODE 1
3064 014200 000004      ST20:  MOVB   #20,#STSTNM ;LOAD TEST NUMBER
3065 014202 000402      SCOPE
3066 014204 000000      BR      .+6 ;RESERVE TWO WORDS
3067 014206 000000      .WORD  0 ;RESERVED FOR SOURCE DATA
3068 014210 010704      .WORD  0 ;RESERVED FOR DESTINATION DATA
3069 014212 005744      MOV      PC,R4
3070 014214 005044      TST     -(R4) ;R4 POINTS TO DESTINATION DATA
3071 014216 010403      CLR     -(R4)
3072 014220 005043      MOV      R4,R3 ;R3 POINTS TO SOURCE DATA
3073 014222 005113      CLR     -(R3)
3074 014224 005214      COM     (R3) ;(R3)=177777
3075 014226 000262      INC     (R4) ;(R4)=000001
3076 014230 061314      SEV     ;SET V
3077 014232 103002      ADD     (R3),(R4) ;(R3)=177777,(R4)=000000, CC=0101
3078 014234 102401      BCC    ADD1
3079 014236 001401      ADD1:  ADD1   ADD1
3080 014240 104000      BEQ     .+4
3081
3082
3083 014242 000277      SCC
3084 014244 000250      CLN
3085 014246 021314      CMP     (R3),(R4) ;(R3)=177777,(R4)=000000, CC=1000
3086 014250 103403      BCS    CMP1
3087 014252 102402      BVS    CMP1
3088 014254 001401      BEQ    CMP1
3089 014256 100401      BMI    .+4
3090 014260 104000      CMP1:  HLT
3091
3092 014262 000277      SCC
3093 014264 000244      CLZ
3094 014266 031314      BIT     (R3),(R4) ;(R3)=177777,(R4)=000000, CC=0101
3095 014270 103002      BITT1
3096 014272 102401      BVS    BITT1

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3098	014276	104000	BITT1:	HLT		
3099						
3100	014300	000277		SCC		
3101	014302	000245		+CLC!CLZ		
3102	014304	005114		COM	(R4)	;(R4)=177777
3103	014306	161314		SUB	(R3),(R4)	;(R3)=177777,(R4)=000000, CC=0100
3104	014310	103402		BCS	SUB1	
3105	014312	102401		BVS	SUB1	
3106	014314	001401		BEQ	.+4	
3107	014316	104000	SUB1:	HLT		
3108						
3109	014320	105013		CLRB	(R3)	;(R3)=177400
3110	014322	000313		SWAB	(R3)	;(R3)=000377
3111	014324	000270		SEN		
3112	014326	011314		MOV	(R3),(R4)	;(R3)=(R4)=000377
3113	014330	100001		BPL	.+4	
3114	014332	104000		HLT		
3115	014334	000314		SWAB	(R4)	;(R3)=000377,(R4)=177400
3116	014336	000263		+SEC!SEV		;SET C & V
3117	014340	051314		BIS	(R3),(R4)	;(R3)=000377,(R4)=177777, CC=1001
3118	014342	103002		BCC	BIS1	
3119	014344	102401		BVS	BIS1	
3120	014346	100401		BMI	.+4	
3121	014350	104000	BIS1:	HLT		
3122						
3123	014352	041314		BIC	(R3),(R4)	;(R3)=000377,(R4)=177400, CC=1001
3124	014354	103002		BCC	BIC1	
3125	014356	102401		BVS	BIC1	
3126	014360	100401		BMI	.+4	
3127	014362	104000	BIC1:	HLT		
3128						
3129	014364	000262		SEV		;SET V
3130	014366	021314		CMP	(R3),(R4)	;(R3)=000377,(R4)=177400, CC=0001
3131	014370	103003		BCC	CMP1A	
3132	014372	102402		BVS	CMP1A	
3133	014374	001401		BEQ	CMP1A	
3134	014376	100001		BPL	.+4	
3135	014400	104000	CMP1A:	HLT		
3136						
3137	014402	005013		CLR	(R3)	;(R3)=000000
3138	014404	000261		SEC		
3139	014406	006013		ROR	(R3)	;(R3)=100000
3140	014410	011314		MOV	(R3),(R4)	;(R3)=(R4)=100000
3141	014412	005114		COM	(R4)	;(R4)=077777
3142	014414	161314		SUB	(R3),(R4)	;(R3)=100000,(R4)=177777, CC=1011
3143	014416	103002		BCC	SUB1A	
3144	014420	102001		BVC	SUB1A	
3145	014422	100401		BMI	.+4	
3146	014424	104000	SUB1A:	HLT		
3147						
3148	014426	000277		SCC		
3149	014430	161314		SUB	(R3),(R4)	;(R3)=100000,(R4)=077777, CC=0000
3150	014432	101402		BLOS	SUB1B	;BRANCH IF C OR Z IS SET
3151	014434	102401		BVS	SUB1B	
3152	014436	100001		BPL	.+4	

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3154
3155 014442 011314      MOV      (R3), (R4)      ; (R3)=100000, (R4)=100000, CC=1000
3156 014444 001401      BEQ      MOV1
3157 014446 100401      BMI      .+4
3158 014450 104000      MOV1:   HLT
3159
3160 014452 061314      ADD      (R3), (R4)      ; (R3)=100000, (R4)=000000, CC=0111
3161 014454 103003      BCC      ADD1A
3162 014456 102002      BVC      ADD1A
3163 014460 001001      BNE      ADD1A
3164 014462 100001      BPL      .+4
3165 014464 104000      ADD1A:  HLT
3166
3167 014466 005113      COM      (R3)            ; (R3)=077777
3168 014470 011314      MOV      (R3), (R4)      ; (R4)=077777
3169 014472 061314      ADD      (R3), (R4)      ; (R3)=077777, (R4)=177776, CC=1010
3170 014474 103402      BCS      ADD1B
3171 014476 102001      BVC      ADD1B
3172 014500 100401      BMI      .+4
3173 014502 104000      ADD1B:  HLT
3174
3175 014504 062714 000002      ADD      #2, (R4)
3176 014510 005714      TST      (R4)            ; CHECK FINAL RESULT
3177 014512 001401      BEQ      .+4
3178 014514 104000      HLT
3179
3180
3181
3182 014516 112737 000021 001202  *TEST 21 CHECK BINARY BYTE OPS USING ADDRESS MODE 1
3183 014524 000004      TST21:  MOVB   #21, #STSTNM ; LOAD TEST NUMBER
3184 014526 000402      SCOPE
3185 014530 000000      BR      .+6
3186 014532 000000      .WORD  0
3187 014534 010705      .WORD  0
3188 014536 005745      MOV     PC, R5
3189 014540 005045      TST    -(R5)            ; (R5)=000000
3190 014542 010502      CLR    -(R5)
3191 014544 005042      MOV    R5, R2           ; (R2)=000000
3192 014546 005202      CLR    -(R2)           ; R2 POINTS TO ODD BYTE
3193 014550 105112      INC    R2              ; (R2)=177400
3194
3195 014552 000277      SCC
3196 014554 111215      MOVB   (R2), (R5)      ; (R2)=177400, (R5)=000377, CC=1001
3197 014556 103005      BCC    MOVB1
3198 014560 102404      BVS    MOVB1
3199 014562 001403      BEQ    MOVB1
3200 014564 100002      BPL    MOVB1
3201 014566 105215      INCB  (R5)            ; CHECK RESULT
3202 014570 001401      BEQ    .+4
3203 014572 104000      MOVB1:  HLT
3204
3205 014574 106312      ASLB  (R2)            ; SHIFT (R2) UNTIL
3206 014576 102376      BVC   -2              ; (R2)=000000
3207 014600 106012      RORB  (R2)           ; (R2)=100000
3208 014602 105315      DECB  (R5)           ; (R5)=00377

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3210	014606	000257	CCC			
3211	014610	121512	CMPB	(R5) (R2)		;(R5)=000177, (R2)=100000, CC=1010
3212	014612	102001	BVC	CMPB1		
3213	014614	100401	BMI	.+4		
3214	014616	104000	CMPB1:	HLT		
3215						
3216	014620	005003	CLR	R3		
3217	014622	000261	SEC			
3218	014624	006003	ROR	R3		;R3=100000
3219	014626	050315	BIS	R3 (R5)		;(R5)=100177
3220	014630	000273	+SEC!SEV!SEN			;SET C V & N
3221	014632	131215	BITB	(R2) (R5)		;(R2)=100000, (R5)=100177, CC=0101
3222	014634	103002	BCC	BITB1		
3223	014636	102401	BVS	BITB1		
3224	014640	001401	BEQ	.+4		
3225	014642	104000	BITB1:	HLT		
3226						
3227	014644	151215	BISB	(R2) (R5)		;(R2)=100000, (R5)=100377, CC=1001
3228	014646	103001	BCC	BISB1		
3229	014650	100401	BMI	.+4		
3230	014652	104000	BISB1:	HLT		
3231						
3232	014654	141215	BICB	(R2) (R5)		;(R2)=100000, (R5)=100177, CC=0001
3233	014656	103002	BCC	BICB1		
3234	014660	001401	BEQ	BICB1		
3235	014662	100001	BPL	.+4		
3236	014664	104000	BICB1:	HLT		
3237						
3238	014666	105112	COMB	(R2)		;(R2)=077400, (R5)=100177
3239	014670	121215	CMPB	(R2) (R5)		
3240	014672	001401	BEQ	.+4		
3241	014674	104000	HLT			
3242						
3243	014676	141512	BICB	(R5) (R2)		;(R5)=100177, (R2)=000000, CC=0100
3244	014700	001002	BNE	BICB1A		
3245	014702	105712	TSTB	(R2)		
3246	014704	001401	BEQ	.+4		
3247	014706	104000	BICB1A:	HLT		
3248						
3249	014710	000402	BR	.+6		;RESERVE TWO WORDS FOR DATA
3250	014712	000000	.WORD	0		;SOURCE DATA
3251	014714	000000	.WORD	0		;DEST DATA
3252	014716	010705	MOV	PC R5		
3253	014720	005745	TST	-(R5)		
3254	014722	105045	CLRB	-(R5)		;R5 POINTS TO DEST ODD BYTE
3255	014724	010504	MOV	R5 R4		
3256	014726	105044	CLRB	-(R4)		;R4 POINTS TO DEST EVEN BYTE
3257	014730	010403	MOV	R4 R3		
3258	014732	105043	CLRB	-(R3)		;R3 POINTS TO SOURCE ODD BYTE
3259	014734	010302	MOV	R3 R2		
3260	014736	105042	CLRB	-(R2)		;R2 POINTS TO SOURCE EVEN BYTE
3261						
3262						
3263						
3264	014740	000261	SEC			;COMMENTS ARE LEAST SIGNIFICANT 4 BITS OF BYTES POINTED TO BY R2,R3 ;R4, AND R5 RESPECTIVELY AND THE REMAINING BITS ARE 0'S. ;SET CARRY

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3266 014742 106112 ROLB (R2) ;0001,0000,0000,0000
3267 014744 111214 MOVB (R2),(R4) ;0001,0000,0001,0000
3268 014746 106112 ROLB (R2) ;0010,0000,0001,0000
3269 014750 111213 MOVB (R2),(R3) ;0010,0010,0001,0000
3270 014752 106112 ROLB (R2) ;0100,0010,0001,0000
3271 014754 111315 MOVB (R3),(R5) ;0100,0010,0001,0010
3272 014756 106112 ROLB (R2) ;1000,0010,0001,0010
3273 014760 106113 ROLB (R3) ;1000,0100,0001,0010
3274 014762 151215 BISB (R2),(R5) ;1000,0100,0001,1010
3275 014764 131512 BITB (R5),(R2) ;1000,0100,0001,1010
3276 014766 001426 BEQ BIN1
3277 014770 151314 BISB (R3),(R4) ;1000,0100,0101,1010
3278 014772 131413 BITB (R4),(R3) ;1000,0100,0101,1010
3279 014774 001423 BEQ BIN1
3280 014776 105213 INCB (R3) ;1000,0101,0101,1010
3281 015000 121314 CMPB (R3),(R4) ;1000,0101,0101,1010
3282 015002 001020 BNE BIN1
3283 015004 106113 ROLB (R3) ;1000,1010,0101,1010
3284 015006 121315 CMPB (R3),(R5) ;1000,1010,0101,1010
3285 015010 001015 BNE BIN1
3286 015012 106212 ASRB (R2) ;0100,1010,0101,1010
3287 015014 131214 BITB (R2),(R4) ;0100,1010,0101,1010
3288 015016 001412 BEQ BIN1
3289 015020 106015 RORB (R5) ;0100,1010,0101,0101
3290 015022 121415 CMPB (R4),(R5) ;0100,1010,0101,0101
3291 015024 001007 BNE BIN1
3292 015026 105314 DECB (R4) ;0100,1010,0100,0101
3293 015030 141214 BICB (R2),(R4) ;0100,1010,0000,0101
3294 015032 001004 BNE BIN1
3295 015034 111314 MOVB (R3),(R4) ;0100,1010,1010,0101
3296 015036 106213 ASRB (R3) ;0100,0101,1010,0101
3297 015040 141315 BICB (R3),(R5) ;0100,0101,1010,0101
3298 015042 001401 BEQ .+4
3299 015044 104000 HLT
3300 BIN1:
3301 ;*****
3302 ;*TEST 22 CHECK BINARY WORD OPS USING ADDRESS MODE 2 & 4
3303 ;*****
3303 015046 112737 000022 001202 †ST22: MOVB #22,2#STSTNM ;LOAD TEST NUMBER
3304 015054 000004 SCOPE
3305 015056 012704 014714 MOV #BICB1A+6,R4
3306 015062 012702 014712 MOV #BICB1A+4,R2
3307 015066 063702 001506 ADD 2#FACTOR,R2
3308 015072 063704 001506 ADD 2#FACTOR,R4
3309 015076 010405 MOV R4,R5 ;SET DESTINATION REGISTER
3310 015100 012715 000001 MOV #1,(R5)
3311 015104 012712 177777 MOV #-1,(R2)
3312 015110 000257 CCC
3313 015112 000262 SEV
3314 015114 062225 ADD (R2)+,(R5)+ ;(R2)=177777,(R5)=000000,CC=0101
3315 015116 103002 BCC ADD2
3316 015120 102401 BVS ADD2
3317 015122 001401 BEQ .+4
3318 015124 104000 ADD2: HLT
3319
3320 015126 000262 SEV ;SET V

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G07

3322	015134	103002		BCC	CMP2		
3323	015136	102401		BVS	CMP2		
3324	015140	100401		BMI	.+4		
3325	015142	104000		HLT			
			CMP2:				
3327	015144	054225		BIS	-(R2), (R5)+	;(R2)=177777, (R5)=177777, CC=1001	
3328	015146	103001		BCC	BIS2		
3329	015150	100401		BMI	.+4		
3330	015152	104000		HLT			
			BIS2:				
3331	015154	000277		SCC			
3332	015156	000244		CLZ			
3333	015160	162245		SUB	(R2)+, -(R5)	;(R2)=177777, (R5)=000000, CC=0100	
3334	015162	103402		BCS	SUB2		
3335	015164	102401		BVS	SUB2		
3336	015166	001401		BEQ	.+4		
3337	015170	104000		HLT			
			SUB2:				
3339	015172	005442		NEG	-(R2)	;(R2)=000001	
3340	015174	005115		COM	(R5)	;(R5)=177777	
3341	015176	000277		SCC			
3342	015200	000250		CLN			
3343	015202	042225		BIC	(R2)+, (R5)+	;(R2)=000001, (R5)=177776, CC=1001	
3344	015204	103003		BCC	BIC2		
3345	015206	102402		BVS	BIC2		
3346	015210	001401		BEQ	BIC2		
3347	015212	100401		BMI	.+4		
3348	015214	104000		HLT			
			BIC2:				
3350	015216	012742	125252	MOV	#125252, -(R2)		
3351	015222	012245		MOV	(R2)+, -(R5)		
3352	015224	005125		COM	(R5)+	;(R5)=052525	
3353	015226	000262		SEV			
3354	015230	034245		BIT	-(R2), -(R5)	;(R2)=125252, (R5)=052525, CC=0101	
3355	015232	103002		BCC	BITT2		
3356	015234	102401		BVS	BITT2		
3357	015236	001401		BEQ	.+4		
3358	015240	104000		HLT			
			BITT2:				
3360	015242	000262		SEV			
3361	015244	052225		BIS	(R2)+, (R5)+	;(R2)=125252, (R5)=177777, CC=1001	
3362	015246	103002		BCC	BIS2A		
3363	015250	102401		BVS	BIS2A		
3364	015252	100401		BMI	.+4		
3365	015254	104000		HLT			
			BIS2A:				
3367	015256	042745	125252	BIC	#125252, -(R5)	;(R5)=052525	
3368	015262	005125		COM	(R5)+	;(R5)=125252	
3369	015264	024245		CMP	-(R2), -(R5)		
3370	015266	001401		BEQ	.+4		
3371	015270	104000		HLT			
3373	015272	005012		CLR	(R2)		
3374	015274	005122		COM	(R2)+	;(R2)=177777	
3375	015276	162742	000001	SUB	#1, -(R2)	;(R2)=177776, CC=1000	
3376	015302	103402		BCS	SUB2A		

H07

CEQKCC PDP 11/70 CPU EXERCISOR  
CEQKCC.P11 03-MAR-78 13:13

MACY11 30A(1052) 03-MAR-78 13:15 PAGE 66  
T22

CHECK BINARY WORD OPS USING ADDRESS MODE 2 & 4

SEQ 0085

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3378 015306 100401
3379 015310 104000
3380 015312 010702
3381 015314 010205
3382 015316 124245
3383 015320 001401
3384 015322 104000
3385 015324 020237 001512
3386 015330 001372
3387
3388
3389
3390 015332 112737 000023 001202
3391 015340 000004
3392 015342 000402
3393 015344 000000
3394 015346 000000
3395 015350 010703
3396 015352 005743
3397
3398
3399 015354 010300
3400 015356 010002
3401 015360 005302
3402 015362 010604
3403 015364 010605
3404 015366 005745
3405
3406 015370 114046
3407 015372 020506
3408 015374 001021
3409 015376 020200
3410 015400 001017
3411 015402 122026
3412 015404 020406
3413 015406 001014
3414 015410 020003
3415 015412 001012
3416 015414 154640
3417 015416 020506
3418 015420 001007
3419 015422 020200
3420 015424 001005
3421 015426 142620
3422 015430 020406
3423 015432 001002
3424 015434 020003
3425 015436 001401
3426 015440 104000
3427 015442 010003
3428 015444 112743 000200
3429 015450 112743 000377
3430 015454 010304
3431 015456 112744 000177
3432 015462 112744 000000

SUB2A: BMI .+4
        HLT
        MOV PC,R2 ;GET CURRENT PC
        MOV R2,R5 ;MOVE TO R5
1S:     CMPB -(R2),-(R5) ;COMPARE ALL PREVIOUS MEMORY ADDRESSES
        BEQ .+4
        HLT ;ERROR!
        CMP R2,#FRSTAD ;CHECK FOR LOW LIMIT
        BNE 1S
;*****
; *TEST 23 CHECK BINARY BYTE OPS USING ADDRESS MODE 2 & 4
;*****
ST23:  MOVB #23,#STSTNM ;LOAD TEST NUMBER
        SCOPE
        BR .+6 ;RESERVE TWO WORDS
        .WORD 0 ;SOURCE DATA
        .WORD 0 ;DESTINATION DATA
        MOV PC,R3
        TST -(R3)
;FIRST CHECK AUTO INCREMENT/DECREMENT
        MOV R3,R0
        MOV R0,R2
        DEC R2
        MOV SP,R4
        MOV SP,R5
        TST -(R5)
        MOVB -(R0),-(SP)
        CMP R5,SP
        BNE BINB
        CMP R2,R0
        BNE BINB
        CMPB (R0)+,(SP)+
        CMP R4,SP
        BNE BINB
        CMP R0,R3
        BNE BINB
        BISB -(SP),-(R0)
        CMP R5,SP
        BNE BINB
        CMP R2,R0
        BNE BINB
        BICB (SP)+,(R0)+
        CMP R4,SP
        BNE BINB
        CMP R0,R3
        BEQ .+4
BINB:  HLT
        MOV R0,R3
        MOVB #200,-(R3)
        MOVB #377,-(R3) ;(R3)=100377
        MOV R3,R4
        MOVB #177,-(R4)
        MOVB #0,-(R4) ;(R4)=077400

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3434 015470 104000 HLT
3435
3436 015472 152324 BISB (R3)+,(R4)+ ;(R3)=100377,(R4)=077777
3437 015474 100401 BMI .+4
3438 015476 104000 HLT
3439
3440 015500 122324 CMPB (R3)+,(R4)+
3441 015502 103402 BCS CMPB2
3442 015504 102001 BVC CMPB2
3443 015506 100001 BPL .+4
3444 015510 104000 CMPB2: HLT
3445
3446 015512 000261 SEC
3447 015514 134344 BITB -(R3),-(R4)
3448 015516 103002 BCC BITB2
3449 015520 102401 BVS BITB2
3450 015522 001401 BEQ .+4
3451 015524 104000 BITB2: HLT
3452
3453 015526 000244 CLZ
3454 015530 144344 BICB -(R3),-(R4) ;(R3)=100377,(R4)=077400
3455 015532 001401 BEQ .+4
3456 015534 104000 HLT
3457
3458 *****
3459 *TEST 24 CHECK BINARY WORD OPS USING ADDRESS MODES 3 & 5
3460 *****
3461 015536 112737 000024 001202 †ST24: MOVB #24,2#STSTNM ;LOAD TEST NUMBER
3462 015544 000004 SCOPE
3463 015546 000404 BR 2$ ;RESERVE SPACE FOR DATA AND ADDRESSES
3464 015550 000000 .WORD 0 ;CONTAINS ADDRESS OF SOURCE DATA
3465 015552 000000 .WORD 0 ;CONTAINS ADDRESS OF DEST DATA
3466 015554 000000 .WORD 0 ;CONTAINS SOURCE DATA
3467 015556 000000 .WORD 0 ;CONTAINS DEST DATA
3468 015560 010701 2$: MOV PC,R1
3469 015562 010100 MOV R1,R0 ;SET SCOPE PTR
3470 015564 024040 CMP -(R0),-(R0) ;ADJUST R0
3471 015566 010005 MOV R0,R5 ;R5 POINTS TO DEST DATA
3472 015570 024545 CMP -(R5),-(R5) ;SUB 4 FROM R5
3473 015572 010015 MOV R0,(R5) ;R5 POINTS TO ADDRESS OF DEST DATA
3474 015574 010502 MOV R5,R2
3475 015576 010004 MOV R0,R4 ;R4 POINTS TO DEST DATA
3476 015600 005740 TST -(R0)
3477 015602 010003 MOV R0,R3 ;R3 POINTS TO SOURCE DATA
3478 015604 010042 MOV R0,-(R2) ;R2 POINTS TO ADDRESS OF SOURCE DATA
3479 015606 005013 CLR (R3) ;PRESET SOURCE DATA
3480 015610 005014 CLR (R4) ;PRESET DEST DATA
3481 015612 000277 SCC
3482 015614 000244 CLZ
3483 015616 163235 SUB 2(R2)+,2(R5)+ ;(R3)=000000,(R4)=000000, CC=0100
3484 015620 103402 SUB3
3485 015622 102401 BCS SUB3
3486 015624 001401 BVS SUB3
3487 015626 104000 BEQ .+4
3488 SUB3: HLT

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3490 015634 062755 000001      ADD    #1,2-(R5)      ;(R4)=000001
3491 015640 163235      SUB    2(R2)+,2(R5)+ ;(R3)=100000,(R4)=100001,CC=1011
3492 015642 103002      BCC   SUB3A
3493 015644 102001      BVC   SUB3A
3494 015646 100401      BMI   .+4
3495 015650 104000      SUB3A: HLT
3496
3497 015652 005414      NEG   (R4)           ;(R4)=077777
3498 015654 035255      BIT   2-(R2),2-(R5) ;(R3)=100000,(R4)=077777
3499 015656 001401      BEQ   .+4
3500 015660 104000      HLT
3501 015662 023235      CMP   2(R2)+,2(R5)+
3502 015664 102401      BVS   .+4
3503 015666 104000      HLT
3504 015670 005152      COM   2-(R2)
3505 015672 000257      CCC
3506 015674 063255      ADD   2(R2)+,2-(R5)
3507 015676 102001      BVC   ADD3
3508 015700 100401      BMI   .+4
3509 015702 104000      ADD3: HLT
3510 015704 000261      SEC
3511 015706 045235      BIC   2-(R2),2(R5)+ ;(R3)=077777,(R4)=100000
3512 015710 103001      BCC   BIC3
3513 015712 100401      BMI   .+4
3514 015714 104000      BIC3: HLT
3515
3516 015716 005155      COM   2-(R5)         ;(R4)=077777
3517 015720 023235      CMP   2(R2)+,2(R5)+ ;(R3)=077777,(R4)=077777
3518 015722 001401      BEQ   .+4
3519 015724 104000      HLT
3520
3521      ;*****
3522      ;*TEST 25 CHECK BINARY BYTE OPS USING ADDRESS MODES 3 & 5
3523      ;*****
3524      ;TEST25: MOVB #25,2#STSTNM ;LOAD TEST NUMBER
3525
3526      ;RESERVE SPACE FOR ADDRESS AND DATA
3527      ;CONTAINS ADDRESS OF SOURCE DATA (EVEN BYTE)
3528      ;CONTAINS ADDRESS OF SOURCE DATA (ODD BYTE)
3529      ;CONTAINS ADDRESS OF DEST DATA (EVEN BYTE)
3530      ;CONTAINS ADDRESS OF DEST DATA (ODD BYTE)
3531      ;CONTAINS SOURCE DATA
3532      ;CONTAINS DEST DATA
3533
3534      15: MOV PC,R0
3535      CMP -(R0),-(R0) ;R0=ADDRESS OF DEST DATA
3536      MOV R0,R3
3537      MOV R3,R5
3538      TST -(R3)
3539      MOV R0,-(R3) ;R3 POINTS TO ADDRESS OF DEST DATA
3540      INC (R3) ;ODD BYTE
3541      MOV R0,-(R3) ;EVEN BYTE
3542      MOV R3,R4
3543      TST -(R0)
3544      MOV R0,-(R4) ;R0=ADDRESS OF SOURCE DATA
3545      INC (R4) ;R4 POINTS TO ADDRESS OF SOURCE DATA
3546      ;ODD BYTE

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3546
3547 016006 000261          SEC          ;SET CARRY
3548 016010 012734 177001  MOV          #177001,2(R4)+
3549 016014 112734 000200  MOVB        #200,2(R4)+ ;SOURCE DATA=100001
3550 016020 115433          MOVB        2-(R4),2(R3)+
3551 016022 115433          MOVB        2-(R4),2(R3)+ ;DEST DATA=000600
3552 016024 103401          BCS         .+4
3553 016026 104000          HLT
3554 016030 022715 000600  CMP         #600,(R5) ;ERROR! MOV DOES AFFECT C BIT IN PSW
3555 016034 001401          BEQ         .+4 ;CHECK DEST DATA
3556 016036 104000          HLT
3557 016040 024343          CMP         -(R3),-(R3) ;ERROR! INCORRECT RESULT
3558 016042 153433          BISB        2(R4)+,2(R3)+ ;POINT R4 BACK TO EVEN BYTE
3559 016044 153433          BISB        2(R4)+,2(R3)+
3560 016046 022715 100601  CMP         #100601,(R5) ;DEST DATA=100601
3561 016052 001401          BEQ         .+4 ;CHECK RESULT
3562 016054 104000          HLT
3563 016056 145453          BICB        2-(R4),2-(R3) ;ERROR! INCORRECT DEST DATA AFTER BISB
3564 016060 145453          BICB        2-(R4),2-(R3)
3565 016062 133433          BITB        2(R4)+,2(R3)+
3566 016064 001002          BNE         BITB3
3567 016066 135433          BITB        2-(R4),2(R3)+
3568 016070 001001          BNE         .+4
3569 016072 104000          HLT
3570
3571 016074 123453          CMPB        2(R4)+,2-(R3)
3572 016076 001002          BNE         CMPB3
3573 016100 123453          CMPB        2(R4)+,2-(R3)
3574 016102 001401          BEQ         .+4
3575 016104 104000          HLT
3576
3577
3578
3579 016106 112737 000026 001202  *ST26: MOV      #26,2*STSTNM ;LOAD TEST NUMBER
3580 016114 000004          SCOPE
3581 016116 000402          BR         .+6 ;RESERVE TWO LOCATIONS
3582 016120 000000          SDATA: .WORD 0 ;RESERVED FOR SOURCE DATA
3583 016122 000000          DDATA: .WORD 0 ;RESERVED FOR DESTINATION DATA
3584
3585 016124 013702 001506          MOV      2*FACTOR,R2 ;GET RELOCATION FACTOR AND USE AS AN
3586 016130 010205          MOV      R2,R5 ;INDEX VALUE TO POINT TO DATA
3587 016132 005065 016122          CLR      DDATA(5) ;PRESET DESTINATION DATA
3588 016136 012762 000001 016120          MOV      #1,SDATA(2) ;THIS ROUTINE PUT A 1 BIT INTO EVERY
3589 016144 056265 016120 016122 1$: BIS      SDATA(2),DDATA(5) ;OTHER BIT POSITION IN THE DEST-
3590 016152 006362 016120          ASL      SDATA(2) ;INATION ADDRESS (52525)
3591 016156 006362 016120          ASL      SDATA(2)
3592 016162 103370          BCC      1$
3593 016164 022765 052525 016122          CMP      #52525,DDATA(5) ;CHECK RESULT
3594 016172 001401          BEQ      .+4
3595 016174 104000          HLT ;ERROR! INCORRECT RESULT
3596 016176 012762 177777 016120          MOV      #-1,SDATA(2)
3597 016204 046562 016122 016120          BIC      DDATA(5),SDATA(2) ;SOURCE DATA=125252
3598 016212 036265 016120 016122          BIT      SDATA(2),DDATA(5)
3599 016220 001401          BEQ      .+4
3600 016222 104000          HLT ;ERROR! BIT INST FAILED

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3602 016230 026265 016120 016122      CMP      SDATA(2),DDATA(5)
3603 016236 001401                      BEQ      .+4
3604                                     HLT
3605                                     ;ERROR! CMP INST FAILED
3606 016240 104000                      CCC
3607 016242 000257                      ADD      SDATA(2),DDATA(5)
3608 016244 066265 016120 016122      BCC      ADD6
3609 016252 103002                      BVC      ADD6
3610 016254 102001                      BPL      .+4
3611 016256 100001                      ADD6:   HLT
3612 016260 104000
3613 016262 006362 016120                ASL      SDATA(2) ;SDATA=52524
3614 016266 166265 016120 016122      SUB      SDATA(2),DDATA(5)
3615 016274 103401                      BCS      SUB6
3616 016276 001401                      BEQ      .+4
3617 016300 104000                      SUB6:   HLT
3618
3619 016302 112700 000377                MOVB     #377,R0 ;R0=177777 (MOVB %R EXTENDS SIGN)
3620 016306 010062 016120                MOV      R0,SDATA(2)
3621 016312 012765 177777 016122      MOV      #-1,DDATA(5)
3622 016320 166500 016122                SUB      DDATA(5),R0
3623 016324 001401                      BEQ      .+4
3624 016326 104000                      HLT
3625 016330 066265 016120 016122 1$:   ADD      SDATA(2),DDATA(5)
3626 016336 006362 016120                ASL      SDATA(2)
3627 016342 005162 016120                COM      SDATA(2)
3628 016346 036265 016120 016122      BIT      SDATA(2),DDATA(5)
3629 016354 001401                      BEQ      .+4
3630 016356 104000                      HLT
3631 016360 005162 016120                COM      SDATA(2)
3632 016364 026265 016120 016122      CMP      SDATA(2),DDATA(5)
3633 016372 001401                      BEQ      .+4
3634 016374 104000                      HLT
3635 016376 026200 016120                CMP      SDATA(2),R0
3636 016402 001352                      BNE     1$
3637                                     ;*****
3638                                     ;*TEST 27 CHECK BINARY BYTE OPS USING ADDRESS MODE 6
3639                                     ;*****
3640 016404 112737 000027 001202 1$T27: MOVB     #27,%STSTNM ;LOAD TEST NUMBER
3641 016412 000004                      SCOPE
3642                                     ;NOTE: SDATAB(2), AND DDATAB(4) REFERENCE EVEN BYTE OF SOURCE & DEST DATA
3643                                     ;AND SDATAB(3), AND DDATAB(5) FEFERENCE ODD BYTE OF SOURCE & DEST DATA
3644
3645 016414 013702 001506                MOV      %FACTOR,R2 ;GET INDEX VALUE
3646 016420 010204                      MOV      R2,R4 ;R2 FOR SOURCE EVEN BYTE INDEX, R4 FOR
3647 016422 010403                      MOV      R4,R3 ;DEST ODD BYTE, R3 FOR SOURCE EVEN
3648 016424 005203                      INC      R3 ;AND R5 FOR DEST ODD BYTE
3649 016426 010305                      MOV      R3,R5
3650 016430 000261                      SEC ;SET CARRY
3651 016432 012762 125252 016554          MOV      #125252,SDATAB(2)
3652 016440 112763 177125 016554          MOVB     #177125,SDATAB(3) ;SOURCE DATA = 052652
3653 016446 016264 016554 016556          MOV      SDATAB(2),DDATAB(4)
3654 016454 052764 125125 016556          BIS      #125125,DDATAB(4) ;DEST DATA = 177777
3655 016462 136263 016554 016554          BITB     SDATAB(2),SDATAB(3)
3656 016470 001401                      BEQ      .+4

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3658 016474 146264 016554 016556 BICB SDATAB(2),DDATAB(4)
3659 016502 103401 BCS .+4
3660 016504 104000 HLT
3661 016506 126364 016554 016556 CMPB SDATAB(3),DDATAB(4) ;ERROR MOV,BIS,BIT;BIC DO NOT AFFECT 'C'
3662 016514 001401 BEQ .+4
3663 016516 104000 HLT
3664
3665 016520 146365 016554 016556 BICB SDATAB(3),DDATAB(5)
3666 016526 126265 016554 016556 CMPB SDATAB(2),DDATAB(5)
3667 016534 001401 BEQ .+4
3668 016536 104000 HLT
3669
3670 016540 136564 016556 016556 BITB DDATAB(5),DDATAB(4)
3671 016546 001401 BEQ .+4
3672 016550 104000 HLT
3673 016552 000412 BR UB7 ;RESERVE TWO WORDS
3674 016554 000000 SDATAB: .WORD 0 ;RESERVED FOR SOURCE DATA
3675 016556 000000 DDATAB: .WORD 0 ;RESERVED FOR DEST DATA
3676
3677
3678 ;*****
3679 ;*TEST 30 CHECK BINARY WORD OPS USING ADDRESS MODE 7
3680 ;* R2=ADDRESS OF SOURCE DATA, AND R3= ADDRESS OF DEST DATA
3681 ;*****
3682 016560 112737 000030 001202 ST30: MOVB #30,2#STSTNM ;LOAD TEST NUMBER
3683 016566 000004 SCOPE
3684 016570 000000 SBIN7: .WORD 0 ;CONTAINS ADDRESS OF SOURCE DATA
3685 016572 000000 DBIN7: .WORD 0 ;CONTAINS ADDRESS OF DEST DATA
3686 016574 000000 .WORD 0 ;CONTAINS SOURCE DATA
3687 016576 000000 .WORD 0 ;CONTAINS DEST DATA
3688
3689 UB7: MOV PC,R0
3690 016602 024040 CMP -(R0),-(R0)
3691 016604 010002 MOV R0,R2
3692 016606 024242 CMP -(R2),-(R2)
3693 016610 010012 MOV R0,(R2)
3694 016612 010203 MOV R2,R3
3695 016614 024043 CMP -(R0),-(R3)
3696 016616 010013 MOV R0,(R3)
3697
3698 016620 000261 SEC
3699 016622 012777 100000 177740 MOV #100000,2#SBIN7 ;SOURCE DATA = 100000
3700 016630 017777 177734 177734 MOV 2#SBIN7,2#DBIN7 ;DEST DATA = 100000
3701 016636 103001 MOV7
3702 016640 100401 BMI .+4
3703 016642 104000 HLT
3704 016644 006377 177722 MOV7: ASL 2#DBIN7 ;DEST DATA = 000000
3705 016650 102001 BVC .+4
3706 016652 001401 BEQ .+4
3707 016654 104000 HLT
3708
3709 016656 027777 177706 177706 CMP 2#SBIN7,2#DBIN7 ;(R2)=100000,(R3)=000000
3710 016664 103402 BCS CMP7
3711 016666 102401 BVS CMP7
3712 016670 100401 BMI .+4

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N07

CEQKCC PDP 11/70 CPU EXERCISOR MACY11 30A(1052) 03-MAR-78 13:15 PAGE 72  
CEQKCC.P11 03-MAR-78 13:13 T30 CHECK BINARY WORD OPS USING ADDRESS MODE 7

SEQ 0091

3714

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3716 016702 103003      BCC      SUB7
3717 016704 102002      BVC      SUB7
3718 016706 001401      BEQ      SUB7
3719 016710 100401      BMI      .+4
3720 016712 104000      SUB7:    HLT
3721
3722 016714 006277 177650      ASR      @SBIN7      ;(R2)=140000
3723 016720 067777 177644 177644      ADD      @SBIN7,@DBIN7 ;(R2)=140000,(R3)=040000
3724 016726 103003      BCC      ADD7
3725 016730 102002      BVC      ADD7
3726 016732 001401      BEQ      ADD7
3727 016734 100001      @PL     .+4
3728 016736 104000      ADD7:    HLT
3729
3730 016740 047777 177624 177624      BIC      @SBIN7,@DBIN7 ;(R2)=140000,(R3)=000000
3731 016746 001401      BEQ      .+4
3732 016750 104000      HLT
3733
3734 016752 057777 177612 177612      BIS      @SBIN7,@DBIN7 ;(R2)=140000,(R3)=140000
3735 016760 100401      BMI      .+4
3736 016762 104000      HLT
3737
3738 016764 027777 177600 177600      CMP      @SBIN7,@DBIN7
3739 016772 001401      BEQ      .+4
3740 016774 104000      HLT
3741
3742      ;*****
3743      ;*TEST 31      SOME MISCELLANEOUS OPERATIONS INVOLVING THE PC
3744      ;*      NOTE: NONE OF THESE OPERATIONS SHOULD AFFECT THE PC
3745      ;*****
3745 016776 112737 000031 001202  TST31:  MOVB      #31,@STSTNM      ;LOAD TEST NUMBER
3746 017004 000004      SCOPE
3747 017006 005000      CLR      RO
3748 017010 005067 000072      CLR      1$
3749 017014 010707      MOV      PC,PC
3750 017016 120707      CMPB     PC,PC
3751 017020 030707      BIT      PC,PC
3752 017022 060007      ADD      RO,PC
3753 017024 105707      TSTB     PC
3754 017026 005507      ADC      PC
3755 017030 021007      CMP      (RO),PC
3756 017032 131007      BITB     (RO),PC
3757 017034 062707 000000      ADD      #0,PC
3758 017040 023707 001506      CMP      @FACTOR,PC
3759 017044 133707 001506      BITB     @FACTOR,PC
3760 017050 000240      NOP
3761
3762      ;THE NEXT TWO INSTRUCTION CAUSE THE PROGRAM TO JUMP TO THE UNRELOCATED
3763      ;CODE AND TO RETURN ON THE FOLLOWING INST (IF THE CODE IS RELOCATED)
3763 017052 163707 001506      SUB      @FACTOR,PC      ;JUMPS TO UNRELOCATED CODE
3764 017056 063707 001506      ADD      @FACTOR,PC      ;RETURNS
3765 017062 000240      NOP
3766 017064 024607      CMP      -(SP),PC
3767 017066 132607      BITB     (SP)+,PC
3768 017070 026707 000012      CMP      1$,PC
3769 017074 166707 000006      SUB      1$,PC
3770 017100 046707 000002      BIC      1$,PC

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3772 017106 000000
3773 017110 000004
3774 017112 010702
3775 017114 062702 000012
3776 017120 012707 034242
3777 017124 000000
3778
3779
3780
3781
3782
3783 017126 112737 000032 001202
3784 017134 012767 000001 162154
3785 017142 000004
3786
3787
3788
3789 017144 010700
3790 017146 005740
3791 017150 010037 001512
3792 017154 010700
3793 017156 162700 017156
3794 017162 010037 001506
3795 017166 010737 001212
3796 017172 062737 000030 001212
3797 017200 013737 001212 001210
3798 017206 105737 001502
3799 017212 001402
3800 017214 000167 002250
3801 017220 012703 125252
3802 017224 010304
3803 017226 140304
3804 017230 022704 125000
3805 017234 001401
3806 017236 104000
3807
3808 017240 005004
3809 017242 150304
3810 017244 022704 000252
3811 017250 001401
3812 017252 104000
3813
3814 017254 110404
3815 017256 022704 177652
3816 017262 001401
3817 017264 104000
3818
3819 017266 132704 177525
3820 017272 001401
3821 017274 104000
3822
3823 017276 105104
3824 017300 110404
3825 017302 022704 000125
3826 017306 001401

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15: 0
RELE2: SCOPE
      MOV PC,R2
      ADD #1,R2
      MOV #RELOC,PC ;GO RELOCATE PROGRAM CODE
REL22: .WORD 0
;222222222222 LAST ADDRESS OF CODE TO BE RELOCATED 2222222222
;*****
; *TEST 32 CHECK BINARY BYTE OPS USING ADDRESS MODE 0
;*****
TST32: MOVB #32,#STSTNM ;LOAD TEST NUMBER
      MOV #1,#TIMES ;;DO 1 ITERATION
      SCOPE
      .SBTTL START OF SECTION 3
;333333333333 FIRST ADDRESS TO BE RELOCATED 3333333333
REL3: MOV PC,R0 ;GET PC
      TST -(R0) ;R0 CONTAINS THE ADDRESS OF REL3
      MOV R0,#FRSTAD ;SAVE
      MOV PC,R0 ;GET CURRENT PC
      SUB #R0 ;SUBTRACT RELOCATION FACTOR
      MOV R0,#FACTOR ;SAVE RELOCATION FACTOR
      MOV PC,#SLPERR ;SET LOOP ADDRESS
      ADD #30,#SLPERR ;ADJUST
      MOV #SLPERR,#SLPADR
      TSTB #NEXEC ;BR IF TEST CODE TO BE EXECUTED
      BEQ .+6
      JMP RELE3
      MOV #125252,R3
      MOV R3,R4 ;R3=R4=125252
      BICB R3,R4 ;R3=125252,R4=125000
      CMP #125000,R4 ;CHECK RESULT
      BEQ .+4
      CLR R4 ;R3=125252,R4=0
      BICB R3,R4 ;R3=125252,R4=000252
      CMP #252,R4 ;CHECK RESULT
      BEQ .+4
      MOVB R4,R4 ;R4=177652
      CMP #177652,R4 ;CHECK RESULT
      BEQ .+4
      BITB #177525,R4
      BEQ .+4
      COMB R4 ;R4=177525
      MOVB R4,R4 ;R4=000125
      CMP #125,R4 ;CHECK RESULT
      BEQ .+4

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3828
3829 017312 150304          BISB   R3,R4          ;R3=125252,R4=000377
3830 017314 105204          INCB   R4
3831 017316 001401          BEQ    .+4
3832 017320 104000          HLT
3833
3834 ;*****
3835 ;*TEST 33 CHECK BINARY BYTE OPS USING ADDRESS MODE 7
3836 ;*****
3836 017322 112737 000033 001202 TST33: MOVB  #33,@$TSTNM ;LOAD TEST NUMBER
3837 017330 000004          SCOPE
3838 017332 000406          BR     BINB7          ;RESERVE SPACE FOR ADDRESSES & DATA
3839 017334 000000          SBINB7: .WORD 0      ;CONTAINS ADDRESS OF SOURCE EVEN BYTE
3840 017336 000000          .WORD 0              ;CONTAINS ADDRESS OF SOURCE ODD BYTE
3841 017340 000000          .WORD 0              ;CONTAINS ADDRESS OF DEST EVEN BYTE
3842 017342 000000          .WORD 0              ;CONTAINS ADDRESS OF DEST ODD BYTE
3843 017344 000000          DBINB7: .WORD 0      ;CONTAINS SOURCE DATA
3844 017346 000000          .WORD 0              ;CONTAINS DEST DATA
3845
3846 017350 010700          BINB7: MOV    PC,R0
3847 017352 024040          CMP    -(R0),-(R0)   ;R0 = ADDRESS OF DEST DATA
3848 017354 010060 177772          MOV    R0,-6(R0)    ;LOAD ADDRESS OF DEST EVEN BYTE DATA
3849 017360 010060 177774          MOV    R0,-4(R0)
3850 017364 005260 177774          INC    -4(R0)        ;LOAD ADDRESS OF DEST ODD BYTE DATA
3851 017370 005740          TST   -(R0)          ;R0=ADDRESS OF SOURCE DATA
3852 017372 010060 177770          MOV    R0,-10(R0)   ;LOAD ADDRESS OF SOURCE EVEN BYTE DATA
3853 017376 010060 177772          MOV    R0,-6(R0)
3854 017402 005260 177772          INC    -6(R0)        ;LOAD ADDRESS OF SOURCE ODD BYTE DATA
3855
3856 017406 005002          CLR    R2            ;SET INDEX REGISTERS
3857 017410 012703 000002          MOV    #2,R3        ;@SBINB7(2);@SBINB7(3) REFERENCE EVEN &
3858 017414 012704 177774          MOV    #-4,R4       ;ODD BYTE SOURCE DATA; @DBINB7(4);@DBINB7(5)
3859 017420 012705 177776          MOV    #-2,R5       ;REFERENCE DEST EVEN& ODD BYTE DATA
3860
3861
3862 017424 005020          CLR    (R0)+         ;PRESET SOURCE DATA
3863 017426 005010          CLR    (R0)          ;PRESET DEST DATA
3864 017430 013746 001506          MOV    @#FACTOR,-(SP);GET RELOCATION FACTOR
3865 017434 061602          ADD    (SP),R2       ;AND ADD TO INDEX VALUES
3866 017436 061603          ADD    (SP),R3
3867 017440 061604          ADD    (SP),R4
3868 017442 062605          ADD    (SP),R5
3869
3870 017444 112773 177777 017334          MOVB  #-1,@SBINB7(3);SRC DATA = 177400
3871 017452 132772 000377 017334          BITB  #377,@SBINB7(2);CHECK THAT EVEN BYTE WAS NOT AFFECTED
3872 017460 001401          BEQ    .+4           ;BY MOVB INSTRUCTION
3873 017462 104000          HLT
3874
3875 017464 157374 017334 017344          BISB  @SBINB7(3),@DBINB7(4)
3876 017472 105274 017344          INCB  @DBINB7(4)    ;CHECK THAT BIS SET ALL BITS
3877 017476 001401          BEQ    .+4
3878 017500 104000          HLT
3879
3880 017502 105375 017344          DECB  @DBINB7(5)    ;DEST DATA = 177400
3881 017506 005274 017344          INC   @DBINB7(4)    ;DEST DATA = 177401
3882 017512 127375 017334 017344          CMPB  @SBINB7(3),@DBINB7(5)

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3884 017522 104000 HLT
3885
3886 017524 147375 017334 017344 BICB @SBINB7(3),@DBINB7(5)
3887 017532 001401 BEQ .+4
3888 017534 104000 HLT
3889
3890 017536 105073 017334 CLRB @SBINB7(3) ;SRC DATA = 000000
;THIS ROUTINE SETS ALL BITS IN THE SOURCE ODD BYTE BY BISING A BIT FROM
;THE DEST EVEN BYTE INTO THE SOURCE ODD BYTE
3891 BIS7: BISB @DBINB7(4),@SBINB7(3)
3892 ROLB @DBINB7(4)
3893 017542 157473 017344 017334 BCC BIS7
3894 017550 106174 017344 BCC BIS7
3895 017554 103372 BCC BIS7
3896 017556 022772 177400 017334 CMP #177400,@SBINB7(2) ;CHECK RESULT
3897 017564 001401 BEQ .+4
3898 017566 104000 HLT
3899
3900 017570 000372 017334 SWAB @SBINB7(2) ;SRC DATA = 000377
3901 017574 112775 000200 017344 MOVB #200,@DBINB7(5) ;DEST DATA = 100000
3902
3903 017602 147572 017344 017334 BIC7: BICB @DBINB7(5),@SBINB7(2)
3904 017610 106075 017344 RORB @DBINB7(5)
3905 017614 103372 BCC BIC7
3906 017616 005772 017334 TST @SBINB7(2)
3907 017622 001401 BEQ .+4
3908 017624 104000 HLT
3909
3910 017626 012702 000001 OAERR: MOV #1,R2 ;LOAD R2 WITH ODD #
3911 017632 010703 MOV PC,R3
3912 017634 000401 BR .+4 ;RESERVE SPACE FOR A WORD
3913 017636 000000 .WORD 0 ;WILL CONTAIN AN ODD ADDRESS
3914 017640 005723 TST (R3)+ ;STEP R3 TO POINT TO WORD ABOVE
3915 017642 010313 MOV R3,(R3)
3916 017644 005213 INC (R3) ;AND MAKE ODD
3917 017646 012737 017774 000004 MOV #1,@ERRVEC ;SET ODD ADDRESS & RESERVED INSTRUCTION
3918 017654 063737 001506 000004 ADD @FACTOR,@ERRVEC
3919 017662 013737 000004 000010 MOV @ERRVEC,@RESVEC ;TO TRAP TO 1$ BELOW
3920
3921 017670 000277 SCC ;SET ALL CC'S
3922 017672 160212 SUB R2,(R2)
3923 017674 104000 HLT
3924 017676 060222 ADD R2,(R2)+
3925 017700 104000 HLT
3926 017702 006342 ASL -(R2)
3927 017704 104000 HLT
3928 017706 106512 MFPD (R2) ;NOTE: MAY BE RESERVED
3929 017710 104000 HLT
3930 017712 170412 CLR (R2)
3931 017714 104000 HLT
3932 017716 042202 BIC (R2)+,R2
3933 017720 104000 HLT
3934 017722 164202 SUB -(R2),R2
3935 017724 104000 HLT
3936 017726 155202 BISB @-(R2),R2
3937 017730 104000 HLT
3938 017732 105532 ADCB @R2)+

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F08

CEQKCC PDP 11/70 CPU EXERCISOR  
CEQKCC.P11 03-MAR-78 13:13

MACY11 30A(1052) 03-MAR-78 13:15 PAGE 77  
T33 CHECK BINARY BYTE OPS USING ADDRESS MODE 7

SEQ 0096

3940	017736	163302				SUB	@(R3)+,R2	
3941	017740	104000				HLT		
3942	017742	005733				TST	@(R3)+	
3943	017744	104000				HLT		
3944	017746	106533				MFPD	@(R3)+	
3945	017750	104000				HLT		
3946	017752	170453				CLRD	@-(R3)	
3947	017754	104000				HLT		
3948	017756	137702	177775			BITB	@.+1,R2	
3949	017762	104000				HLT		
3950	017764	105477	177773			NEGB	@.-1	
3951	017770	104000				HLT		
3952	017772	000406				BR	25	
3953								
3954	017774	062716	000002		15:	ADD	@2,(SP)	;ADJUST RETURN PC
3955	020000	052766	000017	000002		BIS	@17,2(SP)	;SET CONDITION CODES ON RETURN
3956	020006	000002				RTI		
3957								
3958	020010	012706	000700		25:	MOV	@SUPSTK,SP	;RESET STACK PTR
3959	020014	012737	053442	000004		MOV	@ERPRT,@ERRVEC	;RESET TIME OUT VECTOR
3960	020022	012737	053370	000010		MOV	@RESERR,@RESVEC	
3961								
3962								
3963								
3964	020030	112737	000034	001202		TST34:	MOVB @34,@\$STSTN	;LOAD TEST NUMBER
3965	020036	000004				SCOPE		
3966	020040	010700				MOV	PC,R0	
3967	020042	062700	000012			ADD	@12,R0	;SET ADDRESS FOR JMP INST
3968	020046	000277				SCC		;SET CC'S
3969	020050	000110				JMP	(R0)	
3970	020052	000402				BR	+.6	
3971	020054	000250				CLN		;JMP INST JUMPS HERE
3972	020056	000775				BR	-.4	
3973								
3974	020060	103003				BCC	JMP1	
3975	020062	102002				BVC	JMP1	
3976	020064	001001				BNE	JMP1	
3977	020066	100001				BPL	+.4	
3978	020070	104000			JMP1:	HLT		;ERROR! INCORRECT CC'S AFTER JMP
3979								
3980	020072	005002				CLR	R2	;SET INDICATOR
3981	020074	010703				MOV	PC,R3	
3982	020076	000401				BR	+.4	;RESERVE WORD FOR JMP ADDRESS
3983	020100	000000				.WORD	0	;CONTAINS ADDRESS FOR JMP INST
3984	020102	005723				TST	(R3)+	
3985	020104	010313				MOV	R3,(R3)	
3986	020106	010300				MOV	R3,R0	
3987	020110	062713	000022			ADD	@2,(R3)	; (R3) IS JMP ADDRESS
3988	020114	010300				MOV	R3,R0	
3989	020116	000133				JMP	@(R3)+	;JUMP TO ADDRESS CONTAINED IN R3
3990	020120	000402				BR	+.6	
3991	020122	005102				COM	R2	;COMPLEMENT INDICATOR
3992	020124	000775				BR	-.4	
3993	020126	005202				INC	R2	;CHECK INDICATOR
3994	020130	001003				BNE	JMP3	

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3996 020134 020003      CMP      R0,R3      ;CHECK AUTO-INC R3
3997 020136 001401      BEQ      .+4
3998 020140 104000      JMP3:    HLT
3999
4000 020142 005002      CLR      R2      ;SET INDICATOR
4001 020144 010704      MOV      PC,R4      ;SET UP JMP REGISTER
4002 020146 010400      MOV      R4,R0      ;SET UP CHECK REGISTER
4003 020150 000402      BR      1$
4004 020152 005102      COM      R2      ;COMPLEMENT INDICATOR
4005 020154 000403      BR      2$
4006 020156 022424      1$:     CMP      (R4)+,(R4)+
4007 020160 005724      TST      (R4)+      ;R4=JMP ADDRESS
4008 020162 000144      JMP      -(R4)      ;USE R4 AS ADDRESS
4009 020164 005202      2$:     INC      R2      ;CHECK INDICATOR
4010 020166 001003      BNE     JMP4
4011 020170 022020      CMP      (R0)+,(R0)+
4012 020172 020004      CMP      R0,R4      ;CHECK AUTO-DEC R4
4013 020174 001401      BEQ      .+4
4014 020176 104000      JMP4:    HLT
4015
4016 020200 010703      MOV      PC,R3
4017 020202 000401      BR      .+4      ;RESERVE WORD FOR JMP ADDRESS
4018 020204 000000      1$:     .WORD   0      ;CONTAINS JUMP ADDRESS
4019 020206 005723      TST      (R3)+
4020 020210 010313      MOV      R3,(R3)
4021 020212 062723      ADD      #16,(R3)+
4022 020216 010300      MOV      R3,R0      ;LOAD CHECK REGISTER
4023 020220 000402      BR      3$
4024 020222 005102      2$:     COM      R2
4025 020224 000401      BR      4$
4026 020226 000153      3$:     JMP      @-(R3)      ;JUMP TO 2$ VIA 1$ ABOVE
4027 020230 005202      4$:     INC      R2      ;CHECK INDICATOR
4028 020232 001003      BNE     JMP5
4029 020234 005740      TST      -(R0)
4030 020236 020003      CMP      R0,R3      ;CHECK AUTO-DEC R3
4031 020240 001401      BEQ      .+4
4032 020242 104000      JMP5:    HLT
4033
4034 020244 000402      BR      2$
4035 020246 005102      1$:     COM      R2      ;COMPLEMENT INDICATOR
4036 020250 000402      BR      3$
4037 020252 000167      2$:     JMP      1$
4038 020256 005202      3$:     INC      R2
4039 020260 001401      BEQ      .+4
4040 020262 104000      JMP6:    HLT
4041
4042 020264 012767      MOV      #1$,7$      ;SET UP JMP ADDRESS
4043 020272 063767      ADD      @#FACTOR,7$ ;ADD RELOCATION FACTOR
4044 020300 000402      BR      2$      ;GO TO JMP @7$ INST
4045 020302 005102      1$:     COM      R2      ;COMPLEMENT INDICATOR
4046 020304 000403      BR      3$      ;GO TO CHECK ROUTINE
4047 020306 000177      2$:     JMP      @7$      ;JMP TO 1$ ABOVE VIA 7$
4048 020312 000000      7$:     .WORD   0      ;CONTAINS JMP ADDRESS
4049 020314 005202      3$:     INC      R2      ;CHECK INDICATOR
4050 020316 001401      BEQ      .+4

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4052      ;*****
4053      ;*TEST 35      CHECK JSR INSTRUCTIONS
4054      ;*****
4055      020322 112737 000035 001202 †ST35:  MOV#B  #35,‡#STSTNM      ;LOAD TEST NUMBER
4056      020330 000004          SCOPE
4057      020332 013705 001506 JSR1:  MOV  ‡#FACTOR,R5      ;GET RELOCATION FACTOR
4058      020336 012702 020370      MOV  #3$,R2      ;FORM DEST ADRS
4059      020342 060502          ADD  R5,R2      ;ADD RELOCATION FACTOR
4060      020344 000277          SCC          ;PRESET CC'S
4061      020346 000242          CLV
4062      020350 004512          JSR  R5,(R2)      ;GO TO 3$ VIA R2
4063      020352 005702          1$:  TST  R2      ;CHECK INDICATOR
4064      020354 001017          BNE  4$      ;R2 SHOULD=0
4065      020356 023705 001506      CMP  ‡#FACTOR,R5 ;CHECK THAT RTS  R5 RESTORED R5
4066      020362 001014          BNE  4$
4067      020364 000414          BR   JSR3      ;GO TO NEXT TEST
4068      020366 000205          2$:  RTS  R5      ;RETURN FROM SUBROUTINE
4069      020370 103011          3$:  BCC  4$      ;CHECK THAT JSR DID NOT
4070      020372 102410          BVS  4$
4071      020374 001007          BNE  4$      ;AFFECT CC'S
4072      020376 100006          BPL  4$
4073      020400 005002          CLR  R2      ;CLEAR INDICATOR
4074      020402 012704 020352      MOV  #1$,R4      ;GET UNRELOCATED RETURN ADDRESS
4075      020406 061604          ADD  (SP),R4     ;ADD RELOCATION FACTOR (OLD R5)
4076      020410 020405          CMP  R4,R5      ;CHECK THAT OLD R5 WAS PLACED ON THE
4077      020412 001765          BEQ  2$      ;STACK & THAT NEW R5 CONTAINS RETURN PC
4078      020414 104000          4$:  HLT          ;ERROR! ABOVE
4079
4080      ;CHECK JSR INSTRUCTION ADDRESS MODE 3
4081      020416 013704 001506 JSR3:  MOV  ‡#FACTOR,R4      ;GET RELOCATION FACTOR
4082      020422 005000          CLR  R0      ;SET INDICATOR
4083      020424 012705 020444      MOV  #1$,R5
4084      020430 060405          ADD  R4,R5      ;SET UP JSR DEFERRED ADRS
4085      020432 010502          MOV  R5,R2
4086      020434 012715 020462      MOV  #5$,(R5)
4087      020440 060415          ADD  R4,(R5)      ;(R5)=DEST ADRS
4088      020442 000401          BR   2$      ;RESERVE WORD FOR ADDRESS
4089      020444 000000          1$:  .WORD 0      ;CONTAINS DEST ADRS FOR JSR
4090      020446 004435          2$:  JSR  R4,‡(R5)+ ;JSR TO 5$ VIA 1$ ABOVE
4091      020450 005200          3$:  INC  R0      ;CHECK INDICATOR
4092      020452 001013          BNE  6$
4093      020454 000413          BR   JSR4
4094      020456 005100          4$:  COM  R0      ;COMPLEMENT INDICATOR
4095      020460 000204          RTS  4      ;RETURN FROM SUBROUTINE
4096      020462 012703 020450          5$:  MOV  #3$,R3      ;GET UNRELOCATED RETURN ADDRESS
4097      020466 061603          ADD  (SP),R3     ;ADD RELOCATION FACTOR (OLD R4)
4098      020470 020403          CMP  R4,R3
4099      020472 001003          BNE  6$
4100      020474 005722          TST  (R2)+
4101      020476 020205          CMP  R2,R5      ;CHECK AUTO-INC R5
4102      020500 001766          BEQ  4$      ;GO TO RTS
4103      020502 104000          6$:  HLT          ;ERROR ABOVE
4104
4105      ;CHECK JSR INST ADDRESS MODE 4
4106      020504 013704 001506 JSR4:  MOV  ‡#FACTOR,R4

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4108 020512 010703      MOV      PC,R3
4109 020514 000401      BR       2$
4110 020516 000405      1$:     BR       4$
4111 020520 022323      2$:     CMP      (R3)+,(R3)+
4112 020522 000277      SCC
4113 020524 004443      JSR      R4,-(R3)      ;GO TO 2$
4114 020526 104000      3$:     HLT
4115 020530 000414      BR       JSR6          ;GO TO NEXT TEST
4116 020532 103012      4$:     BCC      5$
4117 020534 102011      BVC      5$
4118 020536 001010      BNE      5$
4119 020540 100007      BPL      5$
4120 020542 012702 020526  MOV      #3$,R2      ;GET UNRELOCATED RETURN ADDRESS
4121 020546 061602      ADD      (SP),R2     ;ADD RELOCATION FACTOR (OLD R4)
4122 020550 020204      CMP      R2,R4       ;CHECK THAT CALCULATED RETURN
4123 020552 001002      BNE      5$          ;PC = NEW R4
4124 020554 005724      TST      (R4)+
4125 020556 000204      RTS      R4
4126 020560 104000      5$:     HLT

:TEST JSR INST ADDRESS MODE 6
4128 020562 000401      JSR6:   BR       2$
4129 020564 000405      1$:     BR       3$
4130 020566 010700      2$:     MOV      PC,R0
4131 020570 004767 177770  JSR      PC,1$
4132 020574 100407      BMI      JSR7          ;GO TO NEXT TEST
4133 020576 104000      HLT      ;ERROR ON CC'S
4134 020600 022020      3$:     CMP      (R0)+,(R0)+
4135 020602 020016      CMP      R0,(SP)     ;CHECK THAT RETURN ADDRESS IS ON THE
4136 020604 001401      BEQ      .+4         ;STACK
4137 020606 104000      HLT
4138 020610 000270      SEN
4139 020612 000207      RTS      PC          ;SET N

:TEST JSR INST ADDRESS MODE 7
4141 020614 013746 001506  JSR7:   MOV      2*FACTOR,-(SP) ;GET RELOCATION FACTOR
4142 020620 062716 020640  ADD      #1$,(SP)   ;FORM ADDRESS OF 1$ BELOW
4143 020624 000277      SCC      ;SET ALL CC'S
4144 020626 004076 000000  JSR      R0,2(SP)   ;JSR TO 1$
4145 020632 003003      BGT      3$
4146 020634 102002      BVC      3$
4147 020636 000402      BR       4$
4148 020640 000200      1$:     RTS      R0      ;RETURN
4149 020642 104000      3$:     HLT      ;ERROR!! INCORRECT CC'S
4150 020644

:*****
4151 :*TEST 36 CHECK IOT TRAP (AND ROLB/ASLB)
4152 :* THIS TEST CHECKS THAT THE PSW IS CORRECT AFTER THE IOT AND THAT THE
4153 :* 'NEW'PSW (FROM IOTVEC+2) IS CORRECT.
4154 :*****
4155 020644 112737 000036 001202  ST36:  MOVB     #36,2*STSTNM ;LOAD TEST NUMBER
4156 020652 000004      SCOPE
4157 020654 012705 000022  IOTTST: MOV     #IOTVEC+2,R5
4158 020660 005000      CLR      R0

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# JOB

CEQKCC PDP 11/70 CPU EXERCISOR MACY11 30A(1052) 03-MAR-78 13:15 PAGE 81  
 CEQKCC.P11 03-MAR-78 13:13 T36 CHECK IOT TRAP (AND ROLB/ASLB)

SEQ 0100

4164	020666	011015				MOV	(R0),(R5)		;SET IOTVEC+2 = PSW
4165	020670	011504				MOV	(R5),R4		;SAVE IN R4
4166	020672	010746				MOV	PC -(SP)		
4167	020674	062716	000036			ADD	#1\$-(SP)		
4168	020700	012645				MOV	(SP)+,(R5)		;LOAD IOT TRAP VECTOR
4169	020702	042710	000357			BIC	#PR7+17,(R0)		
4170	020706	052710	000244			BIS	#PRS+4,(R0)		;PSW=X XXX X00 101 1X1 000
4171	020712	012003				MOV	(R0)+,R3		;R3 = PSW ABOVE
4172	020714	010340				MOV	R3,-(R0)		;RESTORE PSW (MOV CHANGED IT)
4173	020716	000004				IOT			
4174	020720	012737	044534	000020	10\$:	MOV	##SCOPE,2#IOTVEC		;RESTORE IOT VECTOR
4175	020726	104000				HLT			;ERROR! IOT FAILED TO TRAP
4176	020730	000457				BR	TST37	;;GO TO	NEXT TEST
4177									
4178	020732	012002			1\$:	MOV	(R0)+,R2		;GET PSW AFTER IOT TRAP
4179									;NOTE: R0=0
4180	020734	012725	044534			MOV	##SCOPE,(R5)+		;RESTORE IOTVEC
4181	020740	012715	000200			MOV	#PR4,(R5)		;AND IOTVEC+2
4182	020744	010746				MOV	PC -(SP)		;FORM PC OF 10\$ ABOVE
4183	020746	062716	177752			ADD	#10\$-(SP)		
4184	020752	022626				CMP	(SP)+,(SP)+		;CHECK RETURN PC ON STACK
4185	020754	001036				BNE	99\$		
4186	020756	022603				CMP	(SP)+,R3		;CHECK SAVED PSW
4187	020760	001034				BNE	99\$		
4188	020762	032703	140000			BIT	#UM,R3		;BRANCH TO 3\$ IF IN USER MODE
4189	020766	100413				BMI	3\$		
4190	020770	001003				BNE	2\$		;BRANCH TO 2\$ IF IN SUPER MODE
4191	020772	020204				CMP	R2,R4		;CHECK PSW AFTER IOT
4192	020774	001026				BNE	99\$		
4193	020776	000413				BR	4\$		
4194									
4195	021000	042704	030000		2\$:	BIC	#PUM,R4		;CLEAR PREV MODE BITS
4196	021004	052704	010000			BIS	#PSM,R4		;SET PREV SUPER MODE
4197	021010	020204				CMP	R2,R4		;CHECK PSW AFTER IOT
4198	021012	001017				BNE	99\$		
4199	021014	000404				BR	4\$		
4200									
4201	021016	052704	030000		3\$:	BIS	#PUM,R4		;SET PREV USER MODE
4202	021022	020204				CMP	R2,R4		;CHECK PSW AFTER IOT
4203	021024	001012				BNE	99\$		
4204									
4205	021026	005002			4\$:	CLR	R2		
4206	021030	000261				SEC			
4207	021032	106100				ROLB	R0		;ROTATE R0
4208	021034	102376				BVC	.-2		;UNTIL V SETS (R0=200)
4209									
4210	021036	106300				ASLB	R0		;SHIFT SHOULD SET CARRY
4211	021040	103004				BCC	99\$		
4212	021042	102003				BVC	99\$		
4213	021044	001002				BNE	99\$		
4214	021046	005700				TST	R0		
4215	021050	001401				BEQ	.-4		
4216	021052	104000			99\$:	HLT			;ERROR! ROL/ASL FAILED TO SET
4217									;CC'S PROPERLY (IF R2=0) OR IN-
4218									;CORRECT PSW AFTER IOT (IF R2 NOT 0)

K08

CEQKCC PDP 11/70 CPU EXERCISOR  
CEQKCC.P11 03-MAR-78 13:13

MACY11 30A(1052) 03-MAR-78 13:15 PAGE 82  
T36 CHECK IOT TRAP (AND ROLB/ASLB)

SEQ 0101

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4220 021060 010437 177776          MOV      R4, @PSW          ;RESTORE PSW
4221 021064 012706 000700          MOV      @SUPSTK, SP      ;RESTORE STACK PTR
4222                                     ;*****
4223                                     ;*TEST 37 CHECK EMT TRAP SEQUENCE
4224                                     ;*****
4225 021070 112737 000037 001202  TST37:  MOV     @37, @STSTNM      ;LOAD TEST NUMBER
4226 021076 000004                                     SCOPE
4227                                     .EQUIV IOT, HLT          ;REDEFINE HLT CALL
4228 021100 012737 044774 000020  MOV     @SERROR, @IOTVEC   ;SETUP VECTOR
4229 021106 012737 000340 000022  MOV     @PR7, @IOTVEC+2
4230 021114 005000          CLR      R0
4231 021116 010746          MOV     PC, -(SP)
4232 021120 062716 000030          ADD     @EMT1-, (SP)
4233 021124 012637 000030          MOV     (SP)+, @EMTVEC
4234 021130 000262                                     SEV
4235 021132 013737 177776 000032  MOV     @PSW, @EMTVEC+2   ;SET V
4236 021140 000265          +SEZ! SEC                ;RETAIN CURRENT PSW ON TRAP
4237 021142 104000          EMT
4238 021144 001433          BEQ     EMT1C             ;TRAP TO EMT1
4239 021146 000004          HLT
4240 021150 102027          BVC     EMT1B             ;GO TO EMT1C
4241 021152 105100          COMB   R0                 ;ERROR! INCORRECT CC'S WERE SET ON RETURN
4242 021154 105500          ADCB   R0                 ;'V' SHOULD'VE SET ON EMT TRAP
4243 021156 106000          RORB   R0                 ;RO=000377, CC'S=1001
4244 021160 102023          BVC     EMT1B             ;RO=000000, CC'S=0101
4245 021162 100022          BPL    EMT1B             ;RO=000200, CC'S=1010
4246 021164 000257          CCC
4247 021166 105400          NEGB   R0                 ;RO=000200, CC'S=1010
4248 021170 102017          BVC     EMT1B
4249 021172 100016          BPL    EMT1B
4250 021174 000242          CLV
4251 021176 000261          SEC
4252 021200 105300          DECB   R0                 ;CLEAR 'V'
4253 021202 102012          BVC     EMT1B             ;AND SET 'C'
4254 021204 100411          BMI    EMT1B             ;RO=000177, CC'S=0011
4255 021206 000242          CLV
4256 021210 105200          INCB   R0                 ;CLEAR 'V'
4257 021212 103006          BCC    EMT1B             ;RO=000200, CC'S=1011
4258 021214 102005          BVC     EMT1B
4259 021216 100004          BPL    EMT1B
4260 021220 000242          CLV
4261 021222 106200          ASRB   R0                 ;CLEAR 'V'
4262 021224 102776          BVS    -2                 ;SHIFT R0 UNTIL 'V' CLEARS
4263 021226 000401          BR     +4
4264 021230 000004          HLT
4265 021232 000002          RTI
4266 021234 105500          ADCB   R0                 ;ERROR!
4267 021236 103003          BCC    EMT1D             ;EXIT WITH RO=000377
4268 021240 001002          BNE    EMT1D             ;RO=000000
4269 021242 005700          TST   R0
4270 021244 001401          BEQ    +4
4271 021246 000004          HLT
4272 021250 012737 044774 000030  MOV     @SERROR, @EMTVEC   ;RESTORE EMT TO ERROR
4273 021256 012737 000340 000032  MOV     @PR7, @EMTVEC+2   ;SET PRIORITY 7 ON ERROR
4274 021264 012737 044534 000020  MOV     @SCOPE, @IOTVEC   ;RESTORE IOT VECTOR

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4276 .EQUIV ERROR,HLT ;REDEFINE HLT CALL
4277 ::*****
4278 ::*TEST 40 CHECK TRAP INSTRUCTION TRAP SEQUENCE
4279 ::*****
4280 021276 112737 000040 001202 †ST40: MOVB #40,‡STSTNM ;LOAD TEST NUMBER
4281 021304 000004 SCOPE
4282 021306 052737 000340 177776 BIS #PR7,‡‡PSW ;LOCK OUT LINE CLOCK
4283 021314 052737 000340 000016 BIS #PR7,‡‡TBITVEC+2
4284 021322 010746 MOV PC,‡(SP)
4285 021324 062716 000056 ADD #TRAP1-,(SP)
4286 021330 012637 000034 MOV (SP)+,‡‡TRAPVEC
4287 021334 000270 SEN ;SET N
4288 021336 013737 177776 000036 MOV ‡‡PSW,‡‡TRAPVEC+2 ;RETAIN CURRENT PSW ON TRAP
4289 021344 000261 SEC ;SET CARRY
4290 021346 010700 MOV PC,R0
4291 021350 000264 SEZ ;SET Z BIT
4292 021352 104400 TRAP ;TRAP TO TRAP1
4293 021354 103404 BCS .+12
4294 021356 012737 051374 000034 MOV #STRAP,‡‡TRAPVEC ;RESTORE TRAP VECTOR
4295 021364 104000 HLT
4296 021366 001404 BEQ .+12
4297 021370 012737 051374 000034 MOV #STRAP,‡‡TRAPVEC ;RESTORE TRAP VECTOR
4298 021376 104000 HLT
4299 021400 000420 BR TRAP1C
4300 021402 100404 TRAP1: BMI .+12 ;N BIT GOT SET ON TRAP
4301 021404 012737 051374 000034 MOV #STRAP,‡‡TRAPVEC ;RESTORE TRAP VECTOR
4302 021412 104000 HLT
4303 021414 062700 000004 ADD #4,‡0
4304 021420 020016 CMP RO,(SP) ;CHECK LOW BYTE OF RETURN PC ON
4305 021422 001404 BEQ .+12 ;STACK
4306 021424 012737 051374 000034 MOV #STRAP,‡‡TRAPVEC ;RESTORE TRAP VECTOR
4307 021432 104000 HLT
4308 021434 124646 CMPB -(SP),-(SP)
4309 021436 032626 BIT (SP)+,(SP)+
4310 021440 000002 RTI ;RETURN TO INST FOLLOWING TRAP (1$)
4311
4312 021442 012702 000036 TRAP1C: MOV #TRAPVEC+2,R2 ;RESTORE VECTORS
4313 021446 012712 000340 MOV #PR7,(R2)
4314 021452 012742 051374 MOV #STRAP,-(R2)
4315 021456 042737 000340 000016 BIC #PR7,‡‡TBITVEC+2
4316 021464 105037 177776 CLRB ‡‡PSW ;GO BACK TO PRIORITY 0
4317
4318 021470 000004 RELE3: SCOPE
4319 021472 010702 MOV PC,R2
4320 021474 062702 000012 ADD #12,R2
4321 021500 012707 034242 MOV #RELOC,1'C ;GO RELOCATE PROGRAM CODE
4322 021504 000000 REL33: .WORD 0
4323 ;33333333333333 LAST ADDRESS OF CODE TO BE RELOCATED 333333333333
4324
4325 ::*****
4326 ::*TEST 41 CHECK STACK OVERFLOW
4327 ::*****
4328 021506 112737 000041 001202 †ST41: MOVB #41,‡‡STSTNM ;LOAD TEST NUMBER
4329 021514 012767 000001 157574 MOV #1,$TIMES ;DO 1 ITERATION
4330 021522 000004 SCOPE

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4388 022000 012600      25:  MOV    (SP)+,R0      ;SAVE PC OF INSTRUCTION THAT TRAPPED
4389 022002 012602      MOV    (SP)+,R2      ;SAVE PSW
4390 022004 012706 000700  MOV    #SUPSTK,SP    ;SET STACK PTR
4391 022010 104000      HLT                    ;ERROR! AN INSTRUCTION THAT WAS NOT
                                ;SUPPOSED TO TRAP TRAPPED
                                ;R0 CONTAINS PC, R2 CONTAINS PSW
4392
4393
4394 022012 000443      BR     65              ;EXIT TEST
                                ;THE BELOW INSTRUCTIONS WILL CAUSE A STACK OVERFLOW
                                ;STACK PTR IS AT 376
4395
4396
4397 022014 062737 000066 000004 35:  ADD    #45-25,2#ERRVEC ;SET ERROR VECTOR TO 45
4398 022022 010306      MOV    R3,SP          ;SET STACK PTR AT 376
4399 022024 112702 000001      MOV    #1,R2
4400 022030 005000      CLRB  R0
4401 022032 005016      CLR   (SP)           ;SETS BIT 0 IN R0
4402 022034 006302      ASL   R2             ;SHIFT INDICATOR BIT
4403 022036 105226      INCB  (SP)+          ;SETS BIT 1 IN R0
4404 022040 006302      ASL   R2
4405 022042 060746      ADD   PC,-(SP)       ;SETS BIT 2 IN R0
4406 022044 006302      ASL   R2
4407 022046 000003      BPT                    ;SETS BIT 3 IN R0
4408 022050 006302      ASL   R2
4409 022052 004767 000014      JSR   PC,40$         ;SETS BIT 4 IN R0
4410 022056 006302      ASL   R2
4411 022060 050666 177776      BIS   SP,-2(SP)      ;SETS BIT 5 IN R0
4412 022064 000410      BR     55
4413
4414
4415 022066 050200      ;PROGRAM WILL TRAP HERE ON OVERFLOW TRAP
4416 022070 000002 45:  BIS   R2,R0          ;SET APPROPRIATE BIT IN R0
                                RTI                    ;RETURN FROM TRAP
4417
4418 022072 052700 001000 40$:  BIS   #1000,R0       ;SET IND THAT JSR WAS EXECUTED
4419 022076 000207      RTS   PC
4420
4421 022100 052700 000400 41$:  BIS   #400,R0        ;SET IND THAT BPT WAS EXECUTED
4422 022104 000002      RTI
4423
4424
4425 022106 012706 000700 ;CHECK THAT ABOVE INSTRUCTIONS DID TRAP
4426 022112 022700 001477 55:  MOV    #SUPSTK,SP    ;SET STACK PTR
                                CMP    #1477,R0        ;EACH INSTRUCTION SET A BIT IN R0
                                BEQ    .+4           ;R0= 1477
4427 022116 001401      HLT
4428 022120 104000
4429
4430
4431 022122 012706 001200 ;EXIT ROUTINE
4432 022126 016737 177516 000014 65:  MOV    #KERSTK,SP    ;SET KERNEL STACK PTR
                                MOV    43$,2#BPTVEC    ;RESTORE BPT VECTOR
4433 022134 005037 000016      CLR   2#BPTVEC+2
4434 022140 012746      MOV    (PC)+,-(SP)   ;PUSH OLD PSW ONTO STACK
4435 022142 000000 75:  .WORD 0              ;CONTAINS SAVED PSW
4436 022144 010746      MOV    PC,-(SP)      ;PUSH CURRENT PC ONTO STACK
4437 022146 062716 000006      ADD   #6,(SP)        ;ADD OFFSET
4438 022152 000002      RTI
4439 022154 012706 000700      MOV    #SUPSTK,SP    ;SET STACK PTR
4440 022160 012737 053442 000004      MOV    #ERPRT,2#ERRVEC ;RESET TIME OUT VECTOR
4441 022166 013737 177776 000006      MOV    2#PSW,2#ERRVEC+2
4442 022174 052737 000340 000006      BIS   #PR7,2#ERRVEC+2
    
```

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4444 022210 005037 177766          CLR      @#CPUERR
4445  ;*****
4446  ;*TEST 42      CHECK THAT ALL RESERVED INSTRUCTIONS TRAP
4447  ;*****
4448  022214 112737 000042 001202  †ST42:  MOV#B  @42,@#STSTNM      ;LOAD TEST NUMBER
4449  022222 000004          SCOPE
4450  022224 012702 022330          RESTRP: MOV      @5$ R2      ;GET ADDRESS OF RESERVED INSTRUCTION TABLE
4451  022230 063702 001506          ADD      @#FACTOR,R2
4452  022234 132737 000040 001471          BITB    @40,@#OPT.CP+1    ;CHECK IF 11/45 FLOATING POINT IS AVAIL.
4453  022242 001402          BEQ     .+6                ;BRANCH IF NOT AVAILABLE
4454  022244 005067 000110          CLR     50$              ;SET TABLE TERMINATOR AT GROUP 7
4455  022250 012737 022306 000010          MOV     @4$,@#RESVEC     ;SET RESERVED INSTRUCTION TRAP
4456  022256 063737 001506 000010          ADD     @#FACTOR,@#RESVEC
4457  022264 012203          1$:    MOV     (R2)+,R3    ;GET FIRST RESERVED INSTRUCTION
4458  022266 001437          BEQ     7$              ;0 TERMINATES THE TABLE
4459  022270 012204          MOV     (R2)+,R4    ;GET LAST RESERVED INSTRUCTION IN GROUP
4460  022272 010317          2$:    MOV     R3,(PC)    ;EXECUTE RESERVED INSTRUCTION
4461  022274 000000          3$:    .WORD   0        ;CONTAINS RESERVED INSTRUCTION
4462  022276 104000          HLT                    ;ERROR! INSTRUCTION IN R3
4463  022300 104000          HLT                    ;(2$) ABOVE FAILED TO CAUSE A
4464  022302 104000          HLT                    ;RESERVED INSTRUCTION TRAP
4465  022304 000405          BR     41$
4466  022306 012716 022320          4$:    MOV     @41$(SP)    ;ADJUST RETURN PC
4467  022312 063716 001506          ADD     @#FACTOR,(SP)  ;TO RETURN TO 41$
4468  022316 000002          RTI                    ;RETURN TO 41$
4469  022320 020304          41$:   CMP     R3,R4        ;HAS GROUP OF RESERVED INSTRUCTIONS
4470  022322 001760          BEQ     1$              ;BEEN EXECUTED
4471  022324 005203          INC     R3              ;INCREMENT THIS RESERVED INSTRUCTION
4472  022326 000761          BR     2$              ;TO NEXT ONE AND EXECUTE
4473  ;TABLE OF 11/40,11/45 RESERVED INSTRUCTIONS (0 TERMINATES THE TABLE)
4474  5$:    7          ;GROUP 1
4475  77          ;
4476  210        ;GROUP 2
4477  227        ;
4478  7000       ;GROUP 3
4479  7777       ;
4480  75040      ;GROUP 4
4481  76777      ;
4482  106400     ;GROUP 5
4483  106477     ;
4484  106700     ;GROUP 6
4485  107777     ;
4486  170000     50$:   ;GROUP 7      FLOATING POINT
4487  177777     ;          INSTRUCTIONS
4488  0           ;0 TERMINATES THE TABLE
4489
4490  022366 012737 053370 000010  7$:    MOV     @#RESERR,@#RESVEC ;RESTORE RESERVED TRAP
4491  ;*****
4492  ;*TEST 43      CHECK THAT ALL BITS IN THE PSW CAN BE SET AND CLEARED
4493  ;*****
4494  022374 112737 000043 001202  †ST43:  MOV#B  @43,@#STSTNM      ;LOAD TEST NUMBER
4495  022402 000004          SCOPE
4496  022404 105737 001503          PSWCHK: TSTB   @#MMON    ;IF MEM MGMT IS ON SKIP THIS TEST
4497  022410 001065          BNE    4$
4498  022412 013767 177776 000132          MOV     @#PSW,3$      ;SAVE STATUS

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4500 022424 004737 052340 JSR PC,#CLRTBIT ;GO CLEAR 'T' BIT IF SET
4501 022430 013746 000016 MOV #170357,R0 ;LOAD ADDRESS OF PSW INTO R4
4502 022434 012704 177776 MOV #PSW,R4 ;CHECK THAT PSW WAS CLEARED
4503 022440 000250 CLN (R4) ;ERROR! PSW FAILED TO CLEAR
4504 022442 005714 TST .+4 ;SET BITS 15-12 IF MEM MGMT
4505 022444 001401 BEQ .+4 ;R2 = TEST BIT
4506 022446 104000 HLT ;CHECK IF BIT CAN BE SET/CLEARED
4507 022450 012700 170357 MOV #170357,R0
4508 022454 052700 170000 BIS #170000,R0
4509 022460 012702 000001 MOV #1,R2
4510 022464 030200 10S: BIT R2,R0
1S: BEQ 2S ;CHECK IF TEST WILL SET 'T' BIT
4511 022466 001423 BEQ 2S
4512 022470 005037 000016 CLR #170357,R0
4513 022474 030227 000020 BIT R2,#20
4514 022480 001403 BEQ 20S ;CHECK IF TEST WILL SET 'T' BIT
4515 022482 012737 000002 000016 MOV #RTI,#170357,R0 ;SET RTI INTO RETURN
4516 022486 005014 20S: CLR (R4) ;CLEAR PSW
4517 022490 050214 BIS R2,(R4) ;SET R2 INTO PSW
4518 022494 011403 MOV (R4),R3 ;GET BIT
4519 022498 020203 CMP R2,R3 ;CHECK THAT BIT WAS SET IN PSW
4520 022502 001401 BEQ .+4
4521 022506 104000 HLT ;ERROR! BIT IN R2 FAILED TO SET IN PSW
4522 022510 000244 CLZ ;CLEAR Z BIT
4523 022514 040214 BIC R2,(R4) ;CLEAR BIT IN PSW
4524 022518 011403 MOV (R4),R3 ;GET PSW RESULT
4525 022522 001401 BEQ 2S ;BRANCH IF BIC ABOVE CLEARED BIT IN PSW
4526 022526 104000 HLT ;ERROR! BIT IN R2 FAILED TO CLEAR IN PSW
4527 022530 006302 2S: ASL R2 ;SHIFT TEST BIT
4528 022534 103351 BCC 1S ;BRANCH IF ALL BITS NOT TESTED
4529 022538 005014 CLR (R4) ;CLEAR STATUS
4530 022542 012637 000016 MOV (SP)+,#170357,R0 ;RESTORE T BIT RETURN
4531 022546 012746 3S: MOV (PC)+,-(SP) ;PUSH ORIGINAL STATUS ON STACK
4532 022550 000000 .WORD 0 ;CONTAINS ORIGINAL PSW
4533 022554 010746 MOV PC,-(SP) ;SET RETURN PC
4534 022558 062716 000006 ADD #6,(SP)
4535 022562 000002 RTI ;RETURN
4536 022566 013704 177776 4S: MOV #PSW,R4 ;SAVE PSW IN R4
4537 022570 112737 000340 177776 MOVB #340,#PSW ;SET PRIORITY LEVEL 7
4538 022574 004737 052340 JSR PC,#CLRTBIT ;GO CLEAR 'T' BIT IF SET
4539 *****
4540 ;*TEST 44 CHECK THAT ALL BITS IN THE CURRENT STACK PTR CAN BE SET CLEARED
4541 *****
4542 022602 112737 000044 001202 TST44: MOVB #44,#STSTNM ;LOAD TEST NUMBER
4543 022610 000004 SCOPE
4544 022612 010603 CHKSP: MOV SP,R3 ;SAVE STACK PTR
4545 022614 000257 CCC
4546 022616 112706 000377 MOVB #377,SP ;SET STACK PTR = -1
4547 022622 006006 1S: ROR SP ;ROTATE 0 BIT THROUGH ALL BIT
4548 022624 103776 BCS 1S ;BIT POSITIONS
4549 022626 005206 INC SP ;SHOULD INCREMENT SP TO 0
4550 022630 001403 BEQ 2S
4551 022632 010602 MOV SP,R2 ;SAVE ERROR STACK PTR
4552 022634 010306 MOV R3,SP ;SET STACK PTR FOR TRAP
4553 022636 104000 HLT ;ERROR!
4554

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4556
4557
4558 022642 010600
4559 022644 010003
4560
4561 022646 005043
4562 022650 112746 177777
4563 022654 022713 000377
4564 022660 001002
4565 022662 020306
4566 022664 001401
4567 022666 104000 1S:
4568
4569 022670 105226
4570 022672 005723
4571 022674 001002
4572 022676 020006
4573 022700 001401
4574 022702 104000 2S:
4575
4576 022704 005143
4577 022706 144613
4578 022710 022713 177400
4579 022714 001002
4580 022716 020603
4581 022720 001401
4582 022722 104000 3S:
4583
4584 022724 132627 000377
4585 022730 001002
4586 022732 020600
4587 022734 001401
4588 022736 104000 4S:
4589
4590 022740 012746 000001
4591 022744 062706 000002
4592 022750 012702 177401
4593 022754 120246
4594 022756 001004
4595 022760 122602
4596 022762 001002
4597 022764 020006
4598 022766 001401
4599 022770 104000 5S:
4600 022772 105037 177776
4601 022776 010446
4602 023000 010746
4603 023002 062716 000006
4604 023006 000002
4605
4606
4607
4608 023010 112737 000045 001202
4609 023016 000004
4610 023020 012727 177776

;CHECK BYTE OPERATIONS USING THE STACK
SPCHK: MOV SP,R0 ;SAVE STACK PTR
        MOV R0,R3
        CLR -(R3)
        MOVB #-1,-(SP) ;(SP) = 377
        CMP #377,(R3) ;CHECK THAT ONLY EVEN BYTE WAS AFFECTED
        BNE 1S
        CMP R3,SP ;CHECK AUTO-DEC
        BEQ .+4
        HLT

        INCB (SP)+
        TST (R3)+ ;CHECK RESULT
        BNE 2S
        CMP R0,SP ;CHECK AUTO-INC
        BEQ .+4
        HLT

        COM -(R3) ;(R3)=177777
        BICB -(SP),(R3)
        CMP #177400,(R3) ;CHECK RESULT
        BNE 3S
        CMP SP,R3
        BEQ .+4
        HLT

        BITB (SP)+,#377
        BNE 4S
        CMP SP,R0
        BEQ .+4
        HLT

        MOV #1,-(SP)
        ADD #2,SP
        MOV #177401,R2
        CMPB R2,-(SP)
        BNE 5S
        CMPB (SP)+,R2
        BNE 5S
        CMP R0,SP
        BEQ .+4
        HLT
        CLRB @#PSW
        MOV R4,-(SP) ;RESTORE ORIGINAL PSW TO STACK
        MOV PC,-(SP)
        ADD #6,(SP)
        RTI

;*****
;TEST 45 CHECK THAT 'C' BIT SETS/CLEARs PROPERLY
;*****
TST45: MOVB #45,@#STSTNM ;LOAD TEST NUMBER
        SCOPE
CBIT: MOV #177776,(PC)+ ;LOAD CONSTANT

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4612 023026 010700          MOV      PC,R0          ;GET CURRENT PC
4613 023030 162700 000004   SUB      #4,R0         ;POINT R0 TO 1$ ABOVE
4614 023034 005520          ADC      (R0)+        ;ADD 'C' BIT TO 1$ ABOVE
4615 023036 006340          ASL      -(R0)        ;SHIFT 1$
4616 023040 102375          BVC     2$           ;UNTIL 'V' BIT SETS
4617 023042 022767 077776 177754  CMP      #077776,1$   ;CHECK RESULT
4618 023050 001401          BEQ     .+4          ;
4619 023052 104000          HLT                      ;ERROR! INCORRECT RESULT IN 1$ ABOVE
4620                                     ;RO=ADDRESS OF DATA
4621
4622                                     ;CHECK THAT CONDITION CODES ARE SET PROPERLY WHEN A NUMBER (CURRENT PC)
4623                                     ;AND THAT NUMBER +1 ARE COMPARED, AND VICE VERSA.
4624 023054 010700  CMPN:  MOV      PC,R0          ;GET CURRENT PC
4625 023056 010002          MOV      R0,R2        ;SAVE IN R2
4626 023060 005202          INC      R2           ;MAKE R2 = R0+1
4627 023062 000277          SCC                      ;
4628 023064 000251          +CLC!CLN             ;CLEAR C & N BITS
4629 023066 020002          CMP      R0,R2        ;COMPARE # WITH #+1
4630 023070 103003          BCC     1$           ;CARRY BIT SHOULD SET
4631 023072 102402          BVS     1$           ;V BIT SHOULD CLEAR
4632 023074 001401          BEQ     1$           ;Z BIT SHOULD CLEAR
4633 023076 100401          BMI     .+4          ;N BIT SHOULD SET
4634 023100 104000          HLT                      ;ERROR! COMPARE # WITH #+1 FAILED TO
4635                                     ;SET CONDITION CODES IN PSW CORRECTLY
4636
4637 023102 000277          SCC                      ;SET CONDITION CODES IN PSW
4638 023104 120200          CMPB    R2,R0         ;COMPARE #+1 WITH #
4639 023106 103403          BCS     2$           ;C BIT SHOULD CLEAR
4640 023110 102402          BVS     2$           ;V BIT SHOULD CLEAR
4641 023112 001401          BEQ     2$           ;Z BIT SHOULD CLEAR
4642 023114 100001          BPL     .+4          ;N BIT SHOULD CLEAR
4643 023116 104000          HLT                      ;ERROR! COMPARE #+1 WITH # FAILED TO SET
4644                                     ;CONDITION CODES IN PSW CORRECTLY
4645 023120 105037 177776          CLRB    @#PSW        ;ENSURE PRIORITY 0
4646 023124 000004  RELE4:  SCOPE
4647 023126 010702          MOV      PC,R2
4648 023130 062702 000012  ADD      #12,R2
4649 023134 012707 034242  MOV      @RELOC,PC   ;GO RELOCATE PROGRAM CODE
4650 023140 000000  REL44:  .WORD 0
4651                                     ;4444444444444444 LAST ADDRESS OF CODE TO BE RELOCATED 444444444444
4652
4653                                     ;*****
4654                                     ;*TEST 46 CHECK EXTENDED INSTRUCTION SET
4655                                     ;*****
4656 023142 112737 000046 001202  †ST46:  MOVB    #46,@#STSTNM ;LOAD TEST NUMBER
4657 023150 012767 000001 156140  MOV      #1,$TIMES   ;;DO 1 ITERATION
4658 023156 000004          SCOPE
4659
4660                                     .SBTTL START OF SECTION 5
4661                                     ;5555555555555555 FIRST ADDRESS TO BE RELOCATED 5555555555
4662 023160 010700  RELS:  MOV      PC,R0          ;GET PC
4663 023162 005740          TST     -(R0)        ;RO CONTAINS THE ADDRESS OF RELS
4664 023164 010037 001512  MOV      R0,@#FRSTAD ;SAVE
4665 023170 010700          MOV      PC,R0        ;GET CURRENT PC
4666 023172 162700 023172  SUB      #.,R0        ;SUBTRACT RELOCATION FACTOR

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4668 023202 010737 001212          MOV      PC, @SLPERR      ;SET LOOP ADDRESS
4669 023206 062737 000030 001212    ADD      #30, @SLPERR    ;ADJUST
4670 023214 013737 001212 001210    MOV      @SLPERR, @SLPADR
4671 023222 105737 001502          TSTB     @NEXEC         ;BR IF TEST CODE TO BE EXECUTED
4672 023226 001402          BEQ      .+6
4673 023230 000167 001454          JMP      RELES
4674 023234 005000          EXTINST: CLR      RO
4675 023236 000277          SCC      ;PRESET CC'S
4676 023240 006700          SXT      RO             ;EXTEND SIGN (1) INTO RO
4677 023242 103005          BCC     SXTO          ;CHECK RESULT CC'S
4678 023244 102404          BVS     SXTO
4679 023246 001403          BEQ     SXTO
4680 023250 100002          BPL     SXTO
4681 023252 005200          INC     SXTO          ;CHECK RESULT
4682 023254 001401          BEQ     .+4
4683 023256 104000          SXT0:   HLT
4684
4685 023260 010700          MOV      PC, RO
4686 023262 010002          MOV      RO, R2
4687 023264 012703 177777    MOV      #-1, R3
4688 023270 005102          COM     R2
4689 023272 000243          +CLV!CLC             ;CLEAR C AND V BITS
4690 023274 074003          XOR     RO, R3        ;R3 SHOULD CONTAIN COMPLEMENT OF RO
4691 023276 103404          BCS     XORO          ;CHECK THAT C WAS NOT AFFECTED
4692 023278 102403          BVS     XORO          ;AND THAT V WAS CLEARED
4693 023280 001402          BEQ     XORO
4694 023282 020203          CMP     R2, R3
4695 023284 001401          BEQ     .+4           ;CHECK RESULT
4696 023286 104000          XORO:   HLT           ;ERROR! XOR FAILED
4697
4698 023312 010700          MOV      PC, RO
4699 023314 022020          CMP     (RO)+, (RO)+ ;SET ADDRESS REGISTER
4700 023316 000401          BR      1$           ;RESERVE WORD FOR TEST DATA
4701 023320 000000          .WORD   0           ;CONTAINS TEST DATA
4702 023322 005700          1$:     TST      RO    ;EXTEND SIGN OF ADDRESS INTO
4703 023324 006710          SXT     (RO)        ;ADDRESS (RO)=-1 IF MSB RO=1
4704 023326 005002          CLR     R2         ;OTHERWISE, (RO)=0
4705 023330 005700          TST     RO         ;CHECK SIGN OF ADDRESS
4706 023332 100001          BPL     .+4
4707 023334 005102          COM     R2         ;COMPLEMENT CHECK REG IF NEG
4708 023336 021002          CMP     (RO), R2   ;CHECK RESULT OF SXT
4709 023340 001401          BEQ     .+4
4710 023342 104000          SXT1:   HLT           ;ERROR! SXT FAILED TO EXTEND SIGN PROPERLY
4711
4712 023344 012710 100000    MOV      #100000, (RO) ;PRESET DATA
4713 023350 011002          MOV      (RO), R2
4714 023352 000277          SCC      ;PRESET CC'S
4715 023354 074210          XOR     R2, (RO)    ;XOR 100000 WITH 100000 RESULT = 0
4716 023356 103007          BCC     XORI        ;CHECK CC'S AFTER XOR
4717 023360 102406          BVS     XORI
4718 023362 001005          BNE     XORI
4719 023364 100404          BMI     XORI
4720 023366 005710          TST     (RO)       ;CHECK RESULT (0)
4721 023370 001002          BNE     XORI
4722 023372 005402          NEG     R2         ;CHECK THAT REG WAS NOT AFFECTED

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4724	023376	104000		XOR1:	HLT		
4725							
4726	023400	010702			MOV	PC,R2	
4727	023402	022222			CMP	(R2)+,(R2)+	
4728	023404	000401			BR	SXT4	;PRESERVE WORD FOR DATA
4729	023406	000000			.WORD	0	;RESERVED FOR DATA
4730	023410	012722	125252	SXT4:	MOV	#125252,(R2)+	;PRESET DATA
4731	023414	006742			SXT	-(R2)	;EXTEND SIGN
4732	023416	074722			XOR	PC,(R2)+	
4733	023420	010700			MOV	PC,R0	;GET PC
4734	023422	005740			TST	-(R0)	;SUBTRACT 2 FROM PC
4735	023424	005100			COM	R0	;R0=RESULT OF XOR PC-1 ABOVE
4736	023426	074042			XOR	R0,-(R2)	;CHECK RESULT OF SXT AND XOR ABOVE
4737	023430	001401			BEQ	+.4	
4738	023432	104000		XOR24:	HLT		;ERROR! SXT & XOR ABOVE INCORRECT
4739							
4740	023434	012704	000001		MOV	#1,R4	;SET R4
4741	023440	006767	000060		SXT	XOR6A	;PRESET DATA=0
4742	023444	074467	000054	2\$:	XOR	R4,XOR6A	
4743	023450	100423			BMI	XOR6	
4744	023452	006304			ASL	R4	;SHIFT R4
4745	023454	102373			BVC	2\$	;UNTIL V SETS (R4=100000)
4746	023456	100020			BPL	XOR6	;BRANCH IF 'N' IS CLEAR
4747	023460	074467	000040		XOR	R4,XOR6A	;XOR6A=177777
4748	023464	100015			BPL	XOR6	
4749	023466	074767	000032		XOR	PC,XOR6A	;XOR PC WITH XOR6A (177777)
4750	023472	010767	000030		MOV	PC,XOR6B	;FORM PC AS USED IN XOR ABOVE
4751	023476	162767	000004	000022	SUB	#4,XOR6B	
4752	023504	005167	000016		COM	XOR6B	
4753	023510	026767	000012	000006	CMP	XOR6B,XOR6A	;XOR6A SHOULD = COMPLEMENT OF PC
4754	023516	001401			BEQ	+.4	
4755	023520	104000		XOR6:	HLT		;ERROR! XOR TESTS ABOVE FAILED
4756							
4757	023522	000402			BR	+.6	
4758							
4759	023524	000000		XOR6A:	.WORD	0	;CONTAINS DATA USED BY TEST ABOVE
4760	023526	000000		XOR6B:	.WORD	0	
4761							
4762							
4763	023530	012700	077777		MOV	#077777,R0	;SET SOURCE OPERAND FOR ADD
4764	023534	006767	177764		SXT	XOR6A	;CLEAR XOR6A
4765	023540	001004			BNE	SXT6	;CHECK CC'S AFTER EXTENDING ZERO'S
4766	023542	100403			BMI	SXT6	
4767	023544	103402			BCS	SXT6	
4768	023546	102401			BVS	SXT6	
4769	023550	000401			BR	+.4	
4770	023552	104000		SXT6:	HLT		;ERROR! SXT FAILED
4771							
4772	023554	012702	000001		MOV	#1,R2	;SET DEST OPERAND FOR ADD
4773	023560	013703	001506		MOV	J#FACTOR,R3	;LOAD INDEX REGISTER
4774	023564	060002			ADD	R0,R2	;RESULT OF ADD=100000
4775	023566	006763	023524		SXT	XOR6A(3)	;EXTEND SIGN OF ADD ABOVE
4776	023572	001403			BEQ	SXT6A	
4777	023574	005267	177724		INC	XOR6A	;CHECK RESULT OF SXT
4778	023600	001401			BEQ	+.4	



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4780                                     ;SIGN
4781 023604 010703                       MOV    PC,R3
4782 023606 000402                       BR     0+6
4783 023610 000000                       SXRA:  .WORD 0
4784 023612 000000                       SXRB:  .WORD 0
4785 023614 005723                       TST   (R3)+
4786 023616 010304                       MOV   R3,R4
4787 023620 000250                       CLN   R3
4788 023622 006724                       SXT   (R4)+
4789 023624 001401                       BEQ   .+4
4790 023626 104000                       SXT2:  HLT
4791                                     ;PRESERVE 2 WORDS FOR DATA
4792 023630 010467 177754                MOV   R4,SXRA
4793 023634 000257                       CCC
4794 023636 006733                       SXT   2(R3)+
4795 023640 001401                       BEQ   .+4
4796 023642 104000                       SXT3:  HLT
4797                                     ;R3 = ADDRESS OF SXRA
4798 023644 000270                       SEN
4799 023646 006753                       SXT   2-(R3)
4800 023650 100401                       BMI   .+4
4801 023652 104000                       SXT5:  HLT
4802                                     ;SET N BIT
4803 023654 012704 025252                MOV   #025252,R4
4804 023660 074433                       XOR   R4,2(R3)+
4805 023662 005002                       CLR   R2
4806 023664 074253                       XOR   R2,2-(R3)
4807 023666 001405                       BEQ   XOR35
4808 023670 100004                       BPL   XOR35
4809 023672 005104                       COM   R4
4810 023674 020467 177712                CMP   R4,SXRB
4811 023700 001401                       BEQ   .+4
4812 023702 104000                       XOR35: HLT
4813                                     ;CHECK CONDITION CODES
4814 023704 005743                       TST   -(R3)
4815 023706 000250                       CLN   R3
4816 023710 006773 000002                SXT   2(R3)
4817 023714 001401                       BEQ   .+4
4818 023716 104000                       SXT7:  HLT
4819                                     ;R3 = ADDRESS OF SXRA-2
4820 023720 074473 000002                XOR   R4,2(R3)
4821 023724 020473 000002                CMP   R4,2(R3)
4822 023730 001401                       BEQ   .+4
4823 023732 104000                       XOR7:  HLT
4824                                     ;SXRB = R4
4825                                     ;CHECK XOR
4826                                     ;ERROR! XOR FAILED
4827                                     ;*****
4828                                     ;*TEST 47 SOB TEST
4829                                     ;* NOTE: DO NOT INSERT ANY CODE IN FOLLOWING SOB TESTS
4830                                     ;* SINCE IT TESTS THE MAXIMUM BRANCH WIDTH OF THE INSTRUCTION.
4831                                     ;*****
4829 023734 112737 000047 001202        ST47: MOVB #47,2#STSTNM
4830 023742 000004                                     ;LOAD TEST NUMBER
4831                                     SCOPE

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4833 023746 000407          BR      SOB0          ;BRANCH TO SOB TEST
4834
4835 023750 005004          SOB10: CLR      R4          ;R4 = 0
4836 023752 005705          TST      R5          ;CHECK ERROR INDICATOR
4837 023754 001401          BEQ      .+4         ;SOB BRANCHED CORRECTLY
4838 023756 104000          HLT
4839
4840 023760 005005          SOB9:  CLR      R5          ;CLEAR INDICATOR (R5)
4841 023762 006004          ROR      R4          ;ROTATE RIGHT R4
4842 023764 000467          BR      SOB8
4843
4844 023766 012700 000010          SOB0:  MOV      #10,R0        ;RO=10
4845 023772 000277          SCC
4846 023774 001012          SOB1:  BNE      SOB2        ;SET CONDITION CODES
4847 023776 100011          BPL      SOB2        ;CHECK CONDITION CODES AFTER SOB
4848 024000 102010          BVC      SOB2        ;SOB SHOULD NOT EFFECT THE
4849 024002 103007          BCC      SOB2        ;CONDITION CODES.
4850 024004 077005          SOB      RO,SOB1
4851 024006 001005          BNE      SOB2        ;CHECK CONDITION CODES AFTER
4852 024010 100004          BPL      SOB2        ;SOB FALLS THROUGH
4853 024012 102003          BVC      SOB2        ;SOB SHOULD NOT EFFECT
4854 024014 103002          BCC      SOB2        ;CONDITION CODES.
4855 024016 005700          TST      R0          ;CHECK IF RO=0
4856 024020 001401          BEQ      .+4
4857 024022 104000          SOB2:  HLT
4858
4859 024024 012702 000100          MOV      #100,R2       ;R2=100
4860 024030 012700 000101          MOV      #101,R0       ;SET CHECK REGISTER, RO=101
4861 024034 001414          SOB3:  BEQ      SOB4        ;CHECK CONDITION CODES AFTER
4862 024036 100413          BMI      SOB4        ;SOB BRANCH,
4863 024040 102412          BVS      SOB4        ;SOB SHOULD NOT EFFECT
4864 024042 103411          BCS      SOB4        ;CONDITION CODES.
4865 024044 005300          DEC      R0          ;DECREMENT CHECK REGISTER
4866 024046 020002          CMP      R0,R2        ;CHECK THAT SOB DECREMENTS
4867 024050 001006          BNE      SOB4
4868 024052 000257          CCC
4869 024054 077211          SOB      R2,SOB3       ;SET CONDITION CODES BEFORE SOB
4870 024056 001403          BEQ      SOB4        ;BRANCH TO SOB3 UNTIL R2=0
4871 024060 100402          BMT      SOB4        ;CHECK CONDITION CODES AFTER
4872 024062 005702          TST      R2          ;SOB FALLS THROUGH
4873 024064 001401          BEQ      .+4         ;CHECK IF R2=0
4874 024066 104000          SOB4:  HLT
4875
4876 024070 012700 000001          SOB5:  MOV      #1,R0        ;RO=1
4877 024074 000401          BR      .+4
4878 024076 104000          HLT
4879 024100 077002          SOB      RO,-2        ;ERROR!
4880
4881 024102 005700          TST      R0          ;SOB SHOULD NOT BRANCH
4882 024104 001401          BEQ      .+4         ;CHECK IF RO=0 AFTER SOB
4883 024106 104000          HLT
4884
4885 024110 012704 100000          SOB5A: MOV      #100000,R4   ;R4=100000
4886 024114 000403          BR      1$
4887 024116 005204          3$:   INC      R4          ;R4=100000

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4889 024122 104000 HLT ;ERROR! SOB DID NOT
4890 ;INCREMENT PROPERLY
4891
4892 024124 077404 15: SOB R4,35 ;SOB SHOULD BRANCH
4893 024126 104000 HLT ;ERROR! SOB DID NOT BRANCH
4894
4895 024130 012703 000100 25: MOV #100,R3 ;R3=100
4896 024134 077301 SOB R3,S0B6 ;USE SOB TO BRANCH TO ITSELF
4897 024136 005703 TST R3 ;CHECK IF R3=0
4898 024140 001703 BEQ SOB10
4899 024142 104000 SOB7: HLT ;ERROR!
4900
4901 024144 005705 SOB8: TST R5 ;CHECK INDICATOR (R5)
4902 ;IF SOB BRANCHES INCORRECTLY
4903 ;WHEN CHECKING MAX. BRANCH,
4904 ;R5 WILL NOT BE CLEARED AT
4905 ;THIS POINT INDICATING AN ERROR.
4906
4907 024146 001401 BEQ .+4 ;BRANCH IF SOB BRANCHES CORRECTLY
4908 024150 104000 HLT ;ERROR!
4909
4910 024152 005205 INC R5 ;SET INDICATOR (R5)
4911 024154 077477 SOB R4,S0B9 ;TEST MAX. BRANCH OF SOB
4912 024156 005704 TST R4 ;CHECK IF R4=0
4913 024160 001401 BEQ .+4
4914 024162 104000 HLT ;ERROR!
4915
4916 ;*****
4917 ;*TEST 50 CHECK THE MARK INSTRUCTION
4918 ;*****
4918 024164 112737 000050 001202 TST50: MOVB #50,S0B$TSTNM ;LOAD TEST NUMBER
4919 024172 000004 SCOPE
4920 024174 010602 MRKTST: MOV SP,R2
4921 024176 010705 MOV PC,R5 ;THE STACK LOOKS LIKE THIS AFTER
4922 024200 010500 MOV R5,R0 ;THE JSR INSTRUCTION
4923 024202 010546 MOV R5,-(SP) ; -2(SP)=R0 THIS IS A
4924 024204 010746 MOV PC,-(SP) ; -4(SP)=PC STRING
4925 024206 010746 MOV PC,-(SP) ; -6(SP)=PC+2 OF
4926 024210 010746 MOV PC,-(SP) ; -10(SP)=PC+4 FIVE
4927 024212 010746 MOV PC,-(SP) ; -12(SP)=PC+6 DUMMY
4928 024214 010746 MOV PC,-(SP) ; -14(SP)=PC+10 ARGUMENTS
4929 024216 012746 006405 MOV #MARK+5,-(SP) ; -16(SP)=MARK 5
4930 024222 010605 SP,R5 ; -20(SP)=PC PUSHED BY JSR
4931 024224 004767 000002 JSR PC,MARK1
4932 024230 000403 BR .+10
4933 024232 000205 MARK1: RTS R5
4934 024234 104000 HLT ;ERROR! SHOULD BE DOING MARK 5 INST.
4935 024236 000407 BR MARKEX
4936 024240 020602 CMP SP,R2
4937 024242 001402 BEQ .+6
4938 024244 104000 HLT ;ERROR! SP NOT RETURNED TO PROPER
4939 024246 000403 BR MARKEX ;VALUE BY MARK INSTRUCTION
4940 024250 020005 CMP R0,R5
4941 024252 001401 BEQ .+4
4942 024254 104000 HLT ;ERROR! DID NOT RESTORE R5 FROM STACK
4943 024256 010206 MARKEX: MOV R2,SP ;RESTORE SP

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4945 : *TEST 51 RTT/RTI TEST
4946 : * RTT/RTI TEST INSURES THAT CP DOES THE INSTRUCTION FOLLOWING
4947 : * AN RTT IF THE "T"BIT IS SET IN THE PSW,BUT DOES HONOR
4948 : * THE TRAP IMMEDIATELY IF IT EXECUTES AN RTI
4949 : * INSTRUCTION SEQUENCE-RTT
4950 : * 2$: RTT RO ;NO 'T' TRAP AFTER RTT
4951 : * INC RO ;RO=000001
4952 : * ;'T' TRAP TO 5$ AFTER INC
4953 : * 5$: COM RO ;RO=177776
4954 : * MOV SAVPSW,2(SP) ;CLEAR 'T' BIT IN RETURN PSW
4955 : * RTI ;RETURN TO INSTRUCTION FOLLOWING INC
4956 : * CMP #RTT,2$ ;CHECK
4957 : * ETC
4958 : *
4959 : * INSTRUCTION SQUENCE-RTI
4960 : * 2$: RTI ;'T' TRAP AFTER RTI
4961 : * 5$: COM RO ;RO=177777
4962 : * MOV SAVPSW,2(SP) ;CLEAR 'T' BIT IN RETURN PSW
4963 : * RTI ;RETURN TO INC INSTRUCTION
4964 : * INC RO ;RO=000000
4965 : * CMP #RTT,2$ ;CHECK
4966 : * ETC
4967 : *
4968 024260 112737 000051 001202 TST51: MOVB #51,#STSTNM ;LOAD TEST NUMBER
4969 024266 000004 SCOPE
4970 024270 013767 177776 000202 RTT1: MOV #PSW,SAVPSW ;SAVE PSW
4971 024276 032767 000020 000174 BIT #20,SAVPSW ;CHECK IF "T"BIT SET
4972 024304 001176 BNE RTT2EX ;BRANCH TO EXIT
4973 024306 010746 1$: MOV PC,-(SP) ;GET CURRENT PC
4974 024310 062716 000116 ADD #5$-.,(SP) ;FORM RELOCATED PC
4975 024314 012637 000014 MOV (SP)+,#TBITVEC ;LOAD INTO TRAP VECTOR
4976 024320 016746 000154 MOV SAVPSW,-(SP) ;GET CURRENT PSW
4977 024324 011637 000016 MOV (SP),#TBITVEC+2
4978 024330 052737 000340 177776 BIS #PR7,#PSW ;SET PRIORITY LEVEL 7
4979 024336 005000 CLR RO
4980 024340 052716 000360 BIS #PR7+20,(SP) ;SET "T"BIT IN PSW ON STACK
4981 024344 010746 MOV PC,-(SP) ;PUT THE PC ON THE STACK
4982 024346 062716 000006 ADD #6,(SP) ;ADJUST PC FOR NEXT INSTRUCTION
4983 024352 000006 2$: RTT
4984 024354 005200 INC RO ;DONE TO SEE IF INSTR. FOLLOWING
4985 : * RTT IS EXECUTED IF T-BIT SET
4986 024356 042737 000340 177776 BIC #PR7,#PSW ;SET PRIORITY LEVEL 0
4987 024364 022767 000006 177760 CMP #RTT,2$
4988 024372 001005 BNE 3$ ;CHECK IF INC WAS EXECUTED
4989 024374 022700 177776 CMP #177776,RO ;CHECK IF COM-RO EXECUTED
4990 024400 001406 BEQ 4$
4991 024402 104000 HLT ;ERROR!RO NOT COMPLIMENTED
4992 024404 000415 BR 6$ ;EXIT TEST
4993 024406 005700 3$: TST RO ;TEST IF TRAPED BEFORE INC INST.
4994 : * WAS EXECUTED
4995 024410 001413 BEQ 6$
4996 024412 104000 HLT ;ERROR!
4997 024414 000411 BR 6$ ;EXIT TEST
4998 024416 012767 000002 177726 4$: MOV #RTI,2$
4999 024424 000730 BR 1$

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5001 024430 016766 000044 000002      MOV      SAVPSW,2(SP)
5002 024436 000002                RTI
5003 024440 012767 000006 177704 6$:      MOV      #RTT,2$
5004 024446 012737 001472 000014      MOV      #SATAN,2#TBITVEC      ;RESTORE 'T' TRAP VECTOR
5005 024454 005037 000016      CLR      2#TBITVEC+2
5006 024460 042737 000360 000016      BIC      #PR7+BIT4,2#TBITVEC+2
5007 024466
5008
5009
5010
5011 024466 112737 000052 001202  RTT1EX:
;*****
;:TEST 52 SECOND RTT TEST
;*****
5012 024474 000004      TST52:  MOVB   #52,2#STSTNM      ;LOAD TEST NUMBER
5013 024476 000401      SCOPE
5014 024500 000000      BR      RTT2A
5015 024502 016700 177772      SAVPSW: .WORD 0
5016 024506 105000      RTT2A:  MOV    SAVPSW,R0      ;GET SAVED PSW
;CLR8    R0      ;CLEAR PRIORITY LEVEL,T, AND COND CODES
5017 024510 012702 144000      MOV    #UM+REG,R2
5018 024514 074002      XOR   R0,R2
5019 024516 001435      BEQ   2$      ;USER MODE REG. SET #1 ON
5020 024520 012702 044000      MOV    #SM+REG,R2
5021 024524 074002      XOR   R0,R2
5022 024526 001447      BEQ   3$      ;SUPER MODE REG. SET #1 ON
5023 024530 032700 140000      BIT   #UM,R0
5024 024534 001062      BNE   RTT2EX
5025
5026
;TEST THAT RTT CLEARS BITS 11,12,13 & PRIORITY LEVEL BITS IN KERNEL MODE
5027 024536 012702 177777      MOV    #-1,R2
5028 024542 012737 034240 177776      MOV    #PUM+REG+PRS,2#PSW      ;KERNEL MODE REG. SET 0 ON
5029 024550 005002      CLR   R12      ;SELECT REG. SET #1
;SHOULD CLEAR REG #12
5030 024552 012746 000100      MOV    #PR2,-(SP)
5031 024556 010746      MOV    PC,-(SP)
5032 024560 062716 000006      ADD   #1$-.,(SP)      ;FORM NEW PC
5033 024564 000006      RTT
5034 024566 013700 177776      1$:   MOV    2#PSW,R0      ;NOW USING REG SET 0
5035 024572 005702      TST   R2      ;SHOULD TEST R2 NOT R12
5036 024574 001001      BNE   4$
5037 024576 104000      HLT
;ERROR!DID NOT CLEAR BIT #11 OF PSW
5038 024600 022700 000100      4$:   CMP    #PR2,R0      ;TESTS THE PSW AFTER THE RTT
5039 024604 001436      BEQ   RTT2EX
5040 024606 104000      HLT      ;ERROR! INCORRECT PSW AFTER THE RTT
5041 024610 000434      BR    RTT2EX
5042
5043
;TEST TO INSURE THAT RTI DOES NOT CLEAR BITS 11-15 IN USER MODE
5044 024612 052737 030340 177776 2$:   BIS    #PUM+PR7,2#PSW ;PSW<15-5>=144X
5045 024620 005046      CLR   -(SP)
5046 024622 010746      MOV   PC,-(SP)
5047 024624 062716 000006      ADD   #5$-.,(SP)
5048 024630 000002      RTI
;ATTEMPS TO INSERT A PSW OF 0
5049 024632 022737 174340 177776 5$:   CMP    #UM+PUM+REG+PR7,2#PSW ;SHOULD CHECK AGAINST REG #0
5050 024640 001420      BEQ   RTT2EX
5051 024642 104000      HLT      ;ERROR! RTI CLEARED BITS IN PSW
5052 024644 000416      BR    RTT2EX
5053
5054
;TEST THAT BITS 11-15 AND PRIORITY BITS ARE NOT ALTERED IN SUPER MODE
5055 024646 052737 030200 177776 3$:   BIS    #PUM+PR4,2#PSW ;PSW<15-5>=044X

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T52 SECOND RTT TEST

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5057 024660 010746      MOV    PC, -(SP)
5058 024662 062716 000006  ADD    #6$, -(SP)
5059 024666 000006      RTT                    ;ATTEMPTS TO CLEAR 11-15 AND ALTER PR
5060
5061 024670 022737 074200 177776 6$:  CMP    #SM+PUM+REG+PR4, 2#PSW
5062 024676 001401      BEQ    RTT2EX
5063 024700 104000      HLT                    ;ERROR! RTT ALTERED PR IN
5064                                     ;SUPER MODE OR BITS 11-15.
5065 024702 016737 177572 177776 RTT2EX: MOV    SAVPSW, 2#PSW
5066 024710 000004 RELES:  SCOPE
5067 024712 010702      MOV    PC, R2
5068 024714 062702 000012  ADD    #12, R2
5069 024720 012707 034242  MOV    #RELOC, PC      ;GO RELOCATE PROGRAM CODE
5070 024724 000000 RELES:  .WORD    0
5071                                     ;5555555555555555 LAST ADDRESS OF CODE TO BE RELOCATED 555555555555
5072
5073                                     ;*****
5074                                     ;*TEST 53 CHECK ASH, ASHC, MUL, AND DIV INSTRUCTIONS
5075                                     ;*****
5076 024726 112737 000053 001202 †ST53: MOV    #53, 2#STSTNM      ;LOAD TEST NUMBER
5077 024734 012767 000001 154354  MOV    #1, $TIMES      ;;DO 1 ITERATION
5078 024742 000004      SCOPE
5079
5080                                     .SBTTL  START OF SECTION 6
5081                                     ;6666666666666666 FIRST ADDRESS TO BE RELOCATED 6666666666
5082 024744 010700 REL6:  MOV    PC, R0      ;GET PC
5083 024746 005740      TST    -(R0)          ;R0 CONTAINS THE ADDRESS OF REL6
5084 024750 010037 001512  MOV    R0, 2#FRSTAD   ;SAVE
5085 024754 010700      MOV    PC, R0        ;GET CURRENT PC
5086 024756 162700 024756  SUB    #, R0          ;SUBTRACT RELOCATION FACTOR
5087 024762 010037 001506  MOV    R0, 2#FACTOR   ;SAVE RELOCATION FACTOR
5088 024766 010737 001212  MOV    PC, 2#SLPERR   ;SET LOOP ADDRESS
5089 024772 062737 000030 001212  ADD    #30, 2#SLPERR  ;ADJUST
5090 025000 013737 001212  MOV    2#SLPERR, 2#SLPADR
5091 025006 105737 001502  TSTB  2#NEXEC        ;BR IF TEST CODE TO BE EXECUTED
5092 025012 001402      BEQ    +6
5093 025014 000167 002016  JMP    REL6
5094 025020 012700 000001  ASHL0: MOV    #1, R0   ;R0 WILL BE THE SHIFT COUNT
5095 025024 012703 000021  MOV    #17., R3      ;MAX SHIFT COUNT
5096 025030 005067 000014  1$:  CLR    2$          ;PRESET SAVED CC'S LOCATION=0
5097 025034 010002  MOV    R0, R2        ;GET SHIFT COUNT FOR PASS
5098 025036 010705  MOV    PC, R5        ;R5 & R4 WILL BE DATA SHIFTED BY
5099 025040 010504  MOV    R5, R4        ;ASH & ASL INSTRUCTIONS
5100 025042 072502  ASH    R2, R5        ;SHIFT R5
5101 025044 113727 177776  MOV    2#PSW, (PC)+ ;SAVE CC'S
5102 025050 000000  2$:  .WORD    0        ;CONTAINS ASH CC'S IN EVEN BYTE
5103                                     ;ASL CC'S IN ODD BYTE
5104 025052 006304  3$:  ASL    R4            ;SHIFT R4
5105 025054 113746 177776  MOV    2#PSW, -(SP)  ;SAVE PSW ON STACK
5106 025060 132716 000002  BITB  #2, (SP)      ;CHECK IF ASL SET V BIT
5107 025064 001403  BEQ    30$
5108 025066 152767 000002 177755  BISB  #2, 2$+1      ;IF ASL SET V THEN SET V IN 2$+1
5109 025074 112637 177776  30$: MOV    (SP)+, 2#PSW ;RESTORE ORIGINAL PSW
5110 025100 077214  MOV    R2, 3$
5111 025102 153767 177776 177741  BISB  2#PSW, 2$+1  ;SAVE CC'S AFTER ASL

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5113	025112	001004			BNE	4\$		
5114	025114	126767	177730	177727	CMPB	2\$, 2\$+1		; CHECK ASH & ASL CC'S
5115	025122	001401			BEQ	.+4		
5116	025124	104000			4\$: HLT			; ERROR! INCORRECT RESULT OR CC'S
5117	025126	005200			INC	RO		; INCREMENT PASS SHIFT COUNT
5118	025130	020003			CMP	RO, R3		
5119	025132	001336			BNE	1\$		
5120								
5121	025134	012700	177777		ASHRO: MOV	#-1, RO		; RO = RIGHT SHIFT COUNT FOR PASS
5122	025140	012703	177757		MOV	#-1, R3		; MAX SHIFT COUNT
5123	025144	010002			1\$: MOV	RO, R2		; GET SHIFT COUNT FOR PASS
5124	025146	010705			MOV	PC, R5		; R5 & R4 = DATA TO BE SHIFTED
5125	025150	010504			MOV	R5, R4		; BY ASH & ASR INSTRUCTIONS
5126	025152	072502			ASH	R2, R5		; SHIFT R5 R2 TIMES
5127	025154	113727	177776		MOVW	2\$PSW, (PC)+		; SAVE CC'S IN EVEN BYTE
5128	025160	000000			2\$: .WORD	0		; CONTAINS ASH CC'S IN EVEN BYTE
5129								; ASR CC'S IN ODD BYTE
5130	025162	005402			NEG	R2		
5131	025164	006204			3\$: ASR	R4		; SHIFT R4
5132	025166	077202			SOB	R2, 3\$		; SHIFT R4 R2 TIMES
5133	025170	113767	177776	177763	MOVW	2\$PSW, 2\$+1		; SAVE CC'S AFTER ASR
5134	025176	142767	000002	177755	BICB	2\$, 2\$+1		; ASH RIGHT WILL NOT SET V ASR MAY SET V
5135	025204	020504			CMP	R5, R4		; CHECK ASH & ASR RESULTS
5136	025206	001004			BNE	4\$		
5137	025210	126767	177744	177743	CMPB	2\$, 2\$+1		; CHECK ASH & ASR CC'S
5138	025216	001401			BEQ	.+4		
5139	025220	104000			4\$: HLT			; DECREMENT PASS SHIFT COUNT
5140	025222	005300			DEC	RO		
5141	025224	020003			CMP	RO, R3		
5142	025226	001346			BNE	1\$		
5143								
5144	025230	012746	000037		ASHCLO: MOV	#31, -(SP)		; PUT MAX SHIFT COUNT ON STACK
5145	025234	012746	000001		MOV	#1, -(SP)		; PUT LEFT SHIFT COUNT ON STACK
5146	025240	011600			1\$: MOV	(SP), RO		; GET PASS SHIFT COUNT
5147	025242	010705			MOV	PC, R5		; CURRENT PC IS DATA TO BE SHIFTED
5148	025244	010503			MOV	R5, R3		; ASHC SHIFTS R4, R5; ASL, ROL SHIFTS R2, R3
5149	025246	005004			CLR	R4		
5150	025250	005002			CLR	R2		
5151	025252	073400			2\$: ASHC	RO, R4		; SHIFT R4 LEFT AS SPECIFIED BY RO
5152	025254	006303			ASL	R3		; SHIFT R2, R3 LEFT
5153	025256	006102			ROL	R2		; AS SPECIFIED BY RO
5154	025260	077003			SOB	RO, 2\$		
5155	025262	020402			CMP	R4, R2		; CHECK RESULTS
5156	025264	001002			BNE	3\$		
5157	025266	020503			CMP	R5, R3		
5158	025270	001401			BEQ	.+4		
5159	025272	104000			3\$: HLT			; INCREMENT NEXT PASS SHIFT COUNT
5160	025274	005216			INC	(SP)		; REACHED MAX COUNT (31.)
5161	025276	021666	000002		CMP	(SP), 2(SP)		
5162	025302	001356			BNE	1\$		
5163	025304	022626			CMP	(SP)+, (SP)+		; RESTORE STACK PTR
5164								
5165	025306	012746	177740		ASHCRO: MOV	#-32, -(SP)		; PUT MAX RIGHT SHIFT COUNT ON STACK
5166	025312	012746	177777		MOV	#-1, -(SP)		; PUT PASS SHIFT COUNT ON STACK
5167	025316	011600			1\$: MOV	(SP), RO		; GET PASS SHIFT COUNT

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5169 025322 010204      MOV      R2,R4      ;TO BE SHIFTED BY TEST
5170 025324 005003      CLR      R3
5171 025326 005005      CLR      R5
5172 025328 000276      SEV
5173 025330 073200      ASHC     R0,R2      ;SET V BIT IN PSW
5174 025332 102410      BVS     J$         ;SHIFT R2,R3 RIGHT R0 TIMES
5175 025334 005400      NEG     R0         ;SHIFT RIGHT CLEARS V
5176 025336 006204      ASR     R4         ;NEGATE SHIFT COUNT FOR SOB
5177 025338 006005      ROR     R5         ;SHIFT R4,R5 RIGHT R0 TIMES
5178 025340 077003      SOB     R0,R2$
5179 025342 020204      CMP     R2,R4      ;CHECK RESULT
5180 025344 001002      BNE     J$
5181 025346 020305      CMP     R3,R5
5182 025348 001401      BEQ     .+4
5183 025350 104000      HLT
5184 025360 005316      DEC     (SP)       ;SET SHIFT COUNT FOR NEXT PASS
5185 025362 021666 000002      CMP     (SP),2(SP) ;CHECK IF MAX SHIFT COUNT
5186 025364 001353      BNE     1$
5187 025370 022626      CMP     (SP)+,(SP)+ ;RESTORE STACK PTR
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5194 025372 112737 000054 001202 TST54: MOV# 854,28STSTM ;LOAD TEST NUMBER
5195 025400 000004      SCOPE
5196 025402 012700 000001      MULO:  MOV     #1,R0      ;R0 CONTAINS MULTIPLIER FOR MUL
5197 025406 012706 000700      MOV     @SUPSTK,SP      ;SETUP THE STACK
5198 025412 005016      CLR     (SP)          ;(SP) CONTAINS SHIFT VALUE FOR ASHC
5199 025414 010702      MOV     PC,R2         ;R3,R2 & R5,R4 ARE DATA REGISTERS
5200 025416 010227      MOV     R2,(PC)+      ;SAVE MULTIPICAND
5201 025420 000000      .WORD  0             ;CONTAINS ORIGINAL MULTIPICAND
5202 025422 005003      CLR     R3
5203 025424 005004      CLR     R4
5204 025426 010205      MOV     R2,R5         ;FOR MUL AND ASHC
5205 025430 100001      BPL     .+4          ;IF MULTIPICAND IS NEG THEN SET R4 = -1
5206 025432 005104      COM     R4           ;FOR ASHC
5207 025434 000277      SCC     R4           ;PRESET CC'S
5208 025436 070200      MUL     R0,R2         ;MULTIPLY R2 BY R0 LEAVE PRODUCT
5209                                     ;IN R2,R3 MSH IN R2,LSH IN R3
5210 025440 102406      BVS     2$
5211 025442 001405      BEQ     2$
5212 025444 073416      ASHC   (SP),R4      ;PRODUCT WILL NEVER BE = 0
5213                                     ;'MULTIPLY' R4,R5 BY (SP) LEAVE PRODUCT
5214                                     ;IN R4,R5 MSH IN R4,LSH IN R5
5215 025446 020204      CMP     R2,R4        ;CHECK MSH RESULT
5216 025450 001002      BNE     2$
5217 025452 020305      CMP     R3,R5
5218 025454 0014C1      BEQ     .+4          ;CHECK LSH RESULT
5219 025456 104000      HLT
5220 025460 005216      INC     (SP)         ;INCREMENT ASHC SHIFT COUNT
5221 025462 006300      ASL     R0           ;SHIFT MUL MULTIPLIER
5222 025464 102353      BVC     1$
5223 025466 010702      ;CHECK MUL INST WITH MULTIPLIER (R0) = 100000
5223                                     ;R2 = MULTIPICAND
5223      MOV     PC,R2

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5225 025472 010227      MOV      R2,(PC)+      ;SAVE MULTIPICAND
5226 025474 000000      .WORD    0            ;CONTAINS ORIGINAL MULTIPICAND
5227 025476 005103      COM      R3
5228 025500 010204      MOV      R2,R4        ;R4 WILL BE MSH 'PRODUCT'
5229 025502 006204      ASR      R4           ;FORM 'PRODUCT'
5230 025504 005104      COM      R4           ;COMPLEMENT MSH 'PRODUCT'
5231 025506 070200      MUL      R0,R2        ;MULTIPLY R2 BY 10000 LEAVING
5232 025510 020204      CMP      R2,R4        ;R2 = MSH, R3 = LSH PRODUCT
5233 025512 001002      BNE      3$          ;COMPARE MSH PRODUCTS
5234 025514 020003      CMP      R0,R3
5235 025516 001401      BEQ      .+4         ;CHECK LSH PRODUCT
5236 025520 104000      BEQ      .+4
5237 025520 104000      HLT
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5245 025522 112737 000055 001202 3$: *****
5246 025530 000004          ;TEST 55 CHECK THE DIV INSTRUCTION
5247 025532 012700 000001          ; THE BELOW TEST OF THE DIV INSTRUCTION DIVIDES THE CURRENT PC BY
5248 025536 010716          ; 1,2,4,8,ETC LEAVING THE QUOTIENT/REMAINDER IN R2/R3. NEXT THE QUOTIENT
5249 025540 011603          ; IS MULTIPLIED BY 1,2,4,8,ETC AND THE REMAINDER ADDED. THE RESULT IS
5250 025542 005002          ; THEN COMPARED WITH THE ORIGINAL CURRENT PC.
5251 025544 000277          ;*****
5252 025546 071200          ;*****
5253
5254 025550 103417          TST55: MOVB  #55,#STSTNM ;LOAD TEST NUMBER
5255 025552 100416          SCOPE
5256 025554 102007          DIV0:  MOV  #1,R0        ;RO=DIVISOR
5257 025556 022700 000001          MOV  PC,(SP)         ;SAVE DATA ON STACK
5258 025562 001012          1$:  MOV  (SP),R3       ;GET DATA
5259 025564 032716 100000          CLR  R2              ;CLEAR MSH DIVIDEND
5260 025570 001407          SCC  R2              ;DIVIDE R2 BY R0 LEAVING QUOTIENT IN R2
5261 025572 000407          DIV  R0,R2           ;AND REMAINDER IN R3
5262 025574 010204          BCS  2$              ;BRANCH IF DIVIDE WORKED
5263 025576 070400          BMI  2$              ;V BIT SHOULD ONLY SET IF DIVIDING BY 1
5264 025600 060305          BVC  20$             ;AND THE LSH OF DIVIDEND
5265 025602 103402          CMP  #1,R0           ;IS NEGATIVE
5266 025604 021605          BIT  #100000,(SP)
5267 025606 001401          BEQ  2$
5268 025610 104000          BR   3$
5269
5270
5271
5272 025612 006300          20$: MOV  R2,R4          ;GET QUOTIENT
5273 025614 102351          MUL  R0,R4           ;MULTIPLY QUOTIENT BY DIVISOR
5274
5275          ADD  R3,R5         ;ADD REMAINDER TO LSH PRODUCT
5276          BCS  2$          ;SHOULD BE NO CARRY
5277          CMP  (SP),R5    ;CHECK RESULT
5278          BEQ  .+4
5279          HLT
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5281 025634 010304          MOV      R3,R4
5282 025636 072316          ASH      (SP),R3          ;SHIFT R3 LEFT (SP) TIMES
5283 025640 013727 177776      MOV      @#PSW,(PC)+    ;SAVE CC'S
5284 025644 000000          .WORD   0              ;CONTAINS ASH (SP),R3 CC'S IN EVEN BYTE
5285                                     ;AND ASH R0,R4 CC'S IN ODD BYTE
5286                                     ;SHIFT R4 LEFT R0 TIMES
5287 025646 072400          ASH      R0,R4          ;SHIFT R4 LEFT R0 TIMES
5288 025650 113767 177776 177767 MOVB     @#PSW,2$+1    ;SAVE CC'S IN ODD BYTE OF 2$
5289 025656 020304          CMP      R3,R4          ;COMPARE RESULTS
5290 025660 001004          BNE      3$            ;BRANCH IF THEY DO NOT COMPARE
5291 025662 126767 177756 177755 CMPB     2$,2$+1      ;CHECK CC'S AFTER ASH INSTRUCTIONS
5292 025670 001'01          BEQ      .+4
5293 025672 104000          3$:      HLT
5294                                     ;ERROR! EITHER RESULTS OF SHIFT OR
5295                                     ;RESULT CC'S ARE INCORRECT
5296 025674 005200          INC      R0            ;INCREMENT SHIFT COUNT FOR ASH R0,R4
5297 025676 005216          INC      (SP)          ;INCREMENT SHIFT COUNT FOR ASH (SP),R3
5298 025700 020200          CMP      R2,R0          ;CHECK FOR MAX SHIFT COUNT
5299 025702 001351          BNE
5300 ASHR1: CLR      (SP)          ;(SP) = SHIFT COUNT FOR ASH (SP),R4
5301 CLR      R0            ;R0 = SHIFT COUNT FOR ASH R0,R5
5302 CLR      R2            ;R2 = MAX RIGHT SHIFT COUNT (SET BY
5303                                     ;ABOVE TEST TO 16. NOW = -16.
5304 025712 005016          1$:      CLR      2$          ;CLEAR CC'S HOLDING ADDRESS
5305 025716 010704          MOV      PC,R4          ;R4,R5 = DATA TO BE SHIFTED RIGHT
5306 025720 010405          MOV      R4,R5
5307 025722 072416          ASH      (SP),R4      ;SHIFT R4 RIGHT (SP) TIMES
5308 025724 013727 177776      MOV      @#PSW,(PC)+    ;SAVE CC'S
5309 025730 000000          .WORD   0              ;CONTAINS ASH (SP),R4 CC'S IN EVEN BYTE
5310                                     ;AND ASH R0,R5 CC'S IN ODD BYTE
5311 025732 072500          ASH      R0,R5          ;SHIFT R5 RIGHT R0 TIMES
5312 025734 113767 177776 177767 MOVB     @#PSW,2$+1    ;SAVE CC'S IN ODD BYTE 2$
5313 025742 020405          CMP      R4,R5          ;CHECK RESULTS
5314 025744 001004          BNE      3$            ;CHECK RESULT CC'S
5315 025746 126767 177756 177755 CMPB     2$,2$+1      ;CHECK RESULT CC'S
5316 025754 001401          BEQ      .+4
5317 025756 104000          3$:      HLT
5318                                     ;ERROR! EITHER RESULTS OR RESULT CC'S
5319 025760 005300          DEC      R0            ;DID NOT COMPARE
5320 025762 005316          DEC      (SP)          ;DECREMENT SHIFT COUNT
5321 025764 020002          CMP      R0,R2          ;DECREMENT SHIFT COUNT FOR ASH (SP),R4
5322 025766 001351          BNE      1$            ;CHECK FOR MAX RIGHT SHIFT
5323                                     ;*****
5324 *TEST 56          DIVIDE AGAIN
5325 *          THE BELOW TEST CHECKS THE DIVIDE INSTRUCTION BY DIVIDING
5326 *          THE CURRENT PC BY ITSELF+1. THE QUOTIENT (IN R2) ALWAYS = 0,
5327 *          AND THE REMAINDER (IN R3) ALWAYS = THE CURRENT PC.
5328 *          *****
5329 †ST56: MOVB     #56,@#STSTNM ;LOAD TEST NUMBER
5330 SCOPE
5331 DIV1: MOV      PC,R3          ;CURRENT PC IS LSH DIVIDEND
5332 SXT      R2            ;EXTEND SIGN TO R2 (MSH DIVIDEND)
5333 MOV      R3,R4          ;SAVE ORIGINAL DIVIDEND
5334 MOV      R3,(SP)       ;PUT ON STACK
5335 INC      (SP)          ;ADD 1 (WILL BE DIVISOR)
5336 BPL      1$            ;BRANCH IF POSITIVE

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5337 026020 071216          15:   DIV      (SP),R2          ;DIVIDE R2 BY (SP)
5338 026022 103410          BCS      2$              ;CHECK CONDITION CODES
5339 026024 102407          BVS      2$
5340 026026 001006          BNE      2$
5341 026030 100405          BMI      2$
5342 026032 005702          TST      R2              ;CHECK QUOTIENT (R2 = 0)
5343 026034 001361          BNE      DIV1
5344 026036 010416          MOV      R4,(SP)        ;GET ORIGINAL DIVISOR
5345 026040 020316          CMP      R3,(SP)        ;CHECK REMAINDER
5346 026042 001401          BEQ      .+4
5347 026044 104000          2$:   HLT                    ;REPORT ERROR
5348
5349
5350
5351 026046 112737 000057 001202 1$TST57:  MOVB     #57,2$STSTNM    ;LOAD TEST NUMBER
5352 026054 000004          SCOPE
5353 026056 012702          SPLTST: MOV     (PC)+,R2    ;R2 CONTAINS OP CODE FOR SPL 7
5354 026060 000237          SPL      7
5355 026062 005004          CLR      R4
5356 026064 042744 000340          BIC      #PR7,-(R4)      ;CLEAR PRIORITY LEVEL BITS IN PSW
5357 026070 011403          MOV      (R4),R3        ;GET CURRENT PSW
5358 026072 042703 177757          BIC      #177757,R3     ;R3 CONTAINS CORRECT PSW AFTER SPL
5359
5360 026076 012767 000230 000010          MOV      #SPL+0,2$      ;INITIALIZE SPL INSTRUCTIONS
5361 026104 012767 000237 000050          MOV      #SPL+7,5$
5362 026112 000257          1$:   CCC                    ;CLEAR CONDITION CODES
5363 026114 000230          2$:   SPL                    ;SET PRIORITY LEVEL (NOTE: SPL=NOP IF USER/SUPER MODE)
5364 026116 121403          CMPB     (R4),R3        ;CHECK RESULT OF SPL ABOVE
5365 026120 001401          BEQ      .+4
5366 026122 104000          HLT                    ;ERROR! SPL ABOVE FAILED
5367 026124 032714 140000          BIT      #UM,(R4)      ;IF NOT IN KERNEL MODE THEN SPL
5368 026130 001002          BNE      3$            ;ACTS AS A NOP
5369 026132 062703 000040          ADD      #40,R3        ;SET NEXT CORRECT PSW RESULT
5370 026136 005267 177752          3$:   INC      2$          ;SET NEXT SPL INSTRUCTION
5371 026142 026702 177746          CMP      2$,R2         ;CHECK IF DONE
5372 026146 002761          BLT      1$            ;LOOP UNTIL DONE CHANGING SPL EACH PASS
5373 026150 012702          MOV      (PC)+,R2     ;R2 CONTAINS SPL INSTRUCTION BELOW
5374 026152 000230          SPL      0
5375 026154 052703 000017          BIS      #17,R3        ;SET CONDITION CODE RESULT INTO R3
5376 026160 000277          4$:   SCC                    ;SET CONDITION CODES
5377 026162 000237          5$:   SPL      7          ;SET PRIORITY LEVEL
5378 026164 121403          CMPB     (R4),R3        ;CHECK RESULT OF SPL ABOVE
5379 026166 001401          BEQ      .+4
5380 026170 104000          HLT                    ;ERROR! SPL ABOVE FAILED
5381 026172 032714 140000          BIT      #UM,(R4)      ;CHECK IF IN KERNEL MODE
5382 026176 001002          BNE      6$
5383 026200 162703 000040          SUB      #40,R3        ;SET NEXT CORRECT PSW RESULT
5384 026204 005367 177752          6$:   DEC      5$          ;SET NEXT SPL
5385 026210 026702 177746          CMP      5$,R2         ;CHECK IF DONE ALL SPL'S
5386 026214 002361          BGE      4$
5387
5388
5389
5390
5391
: *****
: *TEST 57 CHECK SPL INSTRUCTION
: *****
: * THIS TEST CHECKS THAT WHEN A REQUEST IS MADE AT A LEVEL = TO THE
: * CURRENT PROCESSOR PRIORITY LEVEL THAT NO INTERRUPT TAKES PLACE, AND
: * THAT WHEN A REQUEST IS MADE AT A LEVEL 1 GREATER THAN THE CURRENT PRO-

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5393 .....
5394 026216 112737 000060 001202 TST60: MOV      #60,2#STSTNM      ;LOAD TEST NUMBER
5395 026224 000004          SCOPE
5396 026226 012700 026372 PIRQ0: MOV      #45,R0      ;R0 POINTS TO A TABLE OF CORRECT PIRQ
5397          ;CONTENTS AFTER AN INTERRUPT
5398 026232 012702 000400          MOV      #400,R2      ;R2 CONTAINS INTERRUPT REQUEST LEVEL
5399 026236 005003          CLR      R3          ;R3 CONTAINS PROCESSOR PRIORITY LEVEL
5400 026240 012704 177772          MOV      #PIRQ,R4     ;R4 CONTAINS ADDRESS OF PIRQ REGISTER
5401 026244 005014          CLR      (R4)       ;INITIALZE REQUEST LEVEL TO 0
5402 026246 013737 177776 000242          MOV      2#PSW,2#PIRQVEC+2 ;RETAIN MODE & REG SET ON TRAP
5403 026254 112737 000340 000242          MOV      #PR7,2#PIRQVEC+2 ;ASSUME LEVEL 7 ON INTERRUPT
5404 026262 112737 000340 000016          MOV      #PR7,2#TBITVEC+2 ;PRIORITY LEVEL 7 ON TRAP
5405 026270 012737 026330 000240 1S:  MOV      #25,2#PIRQVEC ;SET PIRQ ERROR INTERRUPT VECTOR
5406 026276 063737 001506 000240          ADD      2#FACTOR,2#PIRQVEC ;ADD RELOCATION FACTOR
5407 026304 110337 177776          MOV      R3,2#PSW    ;SET CP PRIORITY LEVEL
5408 026310 050214          BIS      R2,(R4)     ;MAKE REQUEST AT LEVEL = TO CP LEVEL
5409 026312 100436          BMI      5$         ;BRANCH WHEN DONE
5410 026314 062737 000002 000240          ADD      #35-25,2#PIRQVEC ;SET PIRQ INTERRUPT VECTOR TO 35
5411 026322 006302          ASL      R2
5412 026324 050214          BIS      R2,(R4)     ;MAKE REQUEST AT LEVEL 1 HIGHER
5413 026326 000240          NOP
5414 026330 104000          25:  HLT
5415          ;ERROR! EITHER AN INTERRUPT OCCURED
5416          ;WHEN RQST LEVEL = CP LEVEL (PIRQVEC)=25
5417          ;OR INTERRUPT FAILED (PIRQVEC)=35
5418          ;CHECK CONTENTS OF PIRQ REGISTER
5419 026332 022014          35:  CMP      (R0)+,(R4)
5420 026334 001406          BEQ      6$
5421 026336 013737 177772 001276          MOV      2#PIRQ,2#STMPD ;SAVE PIRQ
5422 026344 005037 177772          CLR      2#PIRQ
5423 026350 104000          HLT
5424 026352 062703 000040          6$:  ADD      #40,R3      ;ERROR! INCORRECT PIRQ CONTENTS
5425 026356 040214          BIC      R2,(R4)     ;SET NEXT CP PRIORITY LEVEL
5426 026360 012716 026270          MOV      #15,(SP)    ;LOWER LEVEL BY 1
5427 026364 063716 001506          ADD      2#FACTOR,(SP) ;ADJUST RETURN ADDRESS
5428 026370 000006          30$:  RTT
5429          ;TO RETURN TO 15
5429          ;TABLE OF CORRECT PIRQ REGISTER CONTENTS ON INTERRUPT
5430 026372 001042          4$:  1042      ;PIR1+PIA1
5431 026374 003104          3104      ;PIR2+PIR1+PIA2
5432 026376 007146          7146      ;PIR3+PIR2+PIR1+PIA3
5433 026400 017210          17210     ;PIR4+PIR3+PIR2+PIR1+PIA4
5434 026402 037252          37252     ;PIR5+PIR4+PIR3+PIR2+PIR1+PIA5
5435 026404 077314          77314     ;PIR6+PIR5+PIR4+PIR3+PIR2+PIR1+PIA6
5436 026406 177356          177356    ;PIR7+PIR6+PIR5+PIR4+PIR3+PIR2+PIR1+PIA7
5437 026410 005014          5$:  CLR      (R4)       ;CLEAR PIRQ REGISTER
5438 026412 012737 000242 000240          MOV      #PIRQVEC+2,2#PIRQVEC ;RESET PIRQVEC TO HALT AT PIRQVEC+2
5439 026420 005037 000242          CLR      2#PIRQVEC+2
5440 026424 105037 177776          CLR      2#PSW
5441 026430 042737 000340 000016          BIC      #PR7,2#TBITVEC+2
5442 .....
5443 ;*TEST 61 CHECK MICRO-BREAK REGISTER
5444 ;* THIS TEST SHIFTS A '0' BIT THRU ALL BIT POSITIONS.
5445 .....
5446 026436 112737 000061 001202 TST61: MOV      #61,2#STSTNM      ;LOAD TEST NUMBER
5447 026444 000004          SCOPE

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5449 026452 011246          MOV      (R2),-(SP)      ;SAVE ORIG CONTENTS
5450 026454 012700 000376  MOV      #376,R0        ;SET DATA PATTERN
5451 026460 010012          MOV      R0,(R2)        ;LOAD REGISTER WITH PATTERN
5452 026462 021200          CMP      (R2),R0        ;AND CHECK
5453 026464 001004          BNE      3$             ;BRANCH IF INCORRECT
5454 026466 000261          SEC                     ;SET 'C'
5455 026470 006100          ROL      R0             ;SHIFT DATA
5456 026472 103772          BCS      1$
5457 026474 000402          BR       4$
5458 026476 104000          3$:      HLT                     ;ERROR DATA IN R0 NOT IN UBREAK REG
5459 026500 000772          BR       2$             ;CONTINUE TEST
5460 026502 012612          4$:      MOV      (SP)+,(R2)    ;RESTORE ORIG UBREAK CONTENTS
5461                                     ;*****
5462                                     ;*TEST 62 CHECK MFPI/MTPI INSTRUCTIONS
5463                                     ;*****
5464 026504 112737 000062 001202  ST62:  MOV      #62,#STSTNM    ;LOAD TEST NUMBER
5465 026512 000004          SCOPE
5466 026514 032737 140000 177776  MPI:   BIT      #UM,#PSW      ;KERNEL MODE?
5467 026522 001545          BEQ      ENDCP          ;YES EXIT TEST
5468 026524 010746          MOV      PC, -(SP)
5469 026526 062716 000134          ADD      #5$-.,(SP)
5470 026532 012637 000250          MOV      (SP)+,#MMVEC   ;SET MEM MGMT ABORT VECTOR
5471 026536 005046          CLR      -(SP)         ;CLEAR CHECK WORD
5472 026540 010603          MOV      SP,R3
5473 026542 010346          MOV      R3, -(SP)     ;PUT ADDRESS OF CHECK WORD ON THE STACK
5474 026544 105737 001503          TSTB    #MMON          ;CHECK IF MEM MGMT IS ENABLED
5475 026550 001417          BEQ      1$            ;BRANCH IF OFF
5476 026552 013737 177640 177654          MOV      #UIPAR0,#UIPAR6 ;SET UP USER PAGE ADDR. REG.
5477 026560 012737 006006 177614          MOV      #6006,#UIPAR6  ;SET USER PAGE DESC REG R/W UP 6 PAGES
5478 026566 013737 172240 172254          MOV      #SIPAR0,#SIPAR6
5479 026574 012737 006006 172214          MOV      #6006,#SIPAR6  ;SET SUPER PAGE DESC. REG.
5480 026602 062706 140000          10$:    ADD      #140000,SP   ;SET CURRENT MODE'S STACK POINTER
5481 026606 000240          NOP
5482 026610 010746          1$:      MOV      PC, -(SP)
5483 026612 062716 000024          ADD      #3$-.,(SP)
5484 026616 012637 000020          MOV      (SP)+,#IOTVEC  ;SET IOT TRAP VECTOR
5485 026622 000004          IOT
5486 026624 005266 000002          INC      2(SP)         ;INCREMENT CHECK WORD
5487 026630 001417          BEQ      6$
5488 026632 104000          4$:      HLT                     ;ERROR! MFPI,MTPI FAILURE-FOR BETTER
5489 026634 000415          BR       6$            ;ISOLATION SUGGEST RUNNING MFPI DIAG. DCKTD/E
5490 026636 000240          3$:      NOP
5491 026640 006506          MFPI     SP            ;PSW=KERNEL MODE,PREV USER OR SUPER MODE
5492 026642 006536          MFPI     2(SP)+       ;GET PREV. MODES STACK POINTER
5493 026644 006576 000000          MFPI     2(SP)        ;GET DATA (AN ADDRESS) ON PREV MODE'S STACK
5494 026650 000240          NOP                 ;GET DATA (=0) FROM PREV MODES ADDRESS
5495 026652 001367          BNE      4$           ;SPACE AND PUSH ONTO KERNEL STACK
5496 026654 005116          COM      (SP)         ;ERROR IF BRANCH TAKEN! SHOULD HAVE A ZERO ON THE STACK
5497 026656 006636          MTPI     2(SP)+       ;COMPLEMENT OPERAND
5498                                     ;POP OPERAND OFF KERNEL STACK AND MOVE
5499 026660 000002          RTI
5500 026662 104000          5$:      HLT                     ;RETURN TO INST FOLLOWING IOT ABOVE
5501 026664 105037 177776          CLR      #PSW         ;ERROR! MEMORY MANG. ABORT
5502 026670 012737 053274 000250  6$:      MOV      #KTABRT,#MMVEC ;SET PRIORITY LEVEL BACK TO 0
5503 026676 012737 044534 000020          MOV      #SSCOPE,#IOTVEC ;RESTORE VECTOR

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5505 ::*****
5506 ::*TEST 63 CHECK ILLEGAL HALT
5507 ::*****
5508 026710 112737 000063 001202 †ST63: MOVB #63,‡STSTNM ;LOAD TEST NUMBER
5509 026716 000004 SCOPE
5510 026720 010746 HALT1: MOV PC, -(SP) ;GET CURRENT PC
5511 026722 062716 000022 ADD #2$, -(SP)
5512 026726 011637 000004 MOV (SP), ‡ERRVEC ;SET ERROR TRAP VECTOR TO 2$ BELOW
5513 026732 012637 000010 MOV (SP)+, ‡RESVEC ;LOAD RESERVED INST TRAP VECTOR (11/40)
5514 026736 000000 HALT ;SHOULD TRAP TO 4 IN USER/SUPER MODE
5515 026740 104000 1$: HLT ;ERROR! HALT ABOVE FAILED IN USER/SUPER MODE
5516 026742 000404 BR 3$
5517 026744 010716 2$: MOV PC, (SP) ;REPLACE RETURN PC WITH
5518 026746 062716 000006 ADD #3$, -(SP) ;ADDRESS OF 3$ BELOW
5519 026752 000002 RTI ;RETURN (TO 3$)
5520
5521 026754 012737 053442 000004 3$: MOV #ERPRT, ‡ERRVEC ;RESTORE ERROR TRAP VECTOR
5522 026762 012737 053370 000010 MOV #RESEAR, ‡RESVEC
5523 026770 105037 177776 CLR ‡PSW
5524 026774 005037 177766 CLR ‡CPUERR
5525 ::*****
5526 ::*TEST 64 CHECK RESET IN SUPER/USER MODE
5527 ::*****
5528 027000 112737 000064 001202 †ST64: MOVB #64,‡STSTNM ;LOAD TEST NUMBER
5529 027006 000004 SCOPE
5530 027010 000277 RESET1: SCC
5531 027012 013700 177776 MOV ‡PSW, R0 ;GET CURRENT PSW
5532 027016 000277 SCC
5533 027020 000005 RESET
5534 027022 023700 177776 CMP ‡PSW, R0 ;CHECK THAT PSW UNCHANGED BY RESET ABOVE
5535 027026 001401 BEQ .+4
5536 027030 104000 HLT ;ERROR! RESET CLEARED MODE BITS IN PSW
5537 027032 010037 177776 MOV R0, ‡PSW ;RESTORE PSW (FOR ERROR)
5538 027036 ENDCP:
5539 027036 000004 RELE6: SCOPE
5540 027040 010702 MOV PC, R2
5541 027042 062702 000012 ADD #12, R2
5542 027046 012707 034242 MOV #RELOC, PC ;GO RELOCATE PROGRAM CODE
5543 027052 000000 REL66: .WORD 0
5544 ;66666666666666 LAST ADDRESS OF CODE TO BE RELOCATED 666666666666
5545
5546 ::*****
5547 ::*TEST 65 TEST STACK LIMIT REGISTER
5548 ::*****
5549 027054 112737 000065 001202 †ST65: MOVB #65,‡STSTNM ;LOAD TEST NUMBER
5550 027062 012767 000001 152226 MOV #1, $TIMES ;;DO 1 ITERATION
5551 027070 000004 SCOPE
5552
5553 .SBTTL START OF SECTION 7
5554 ;7777777777777777 FIRST ADDRESS TO BE RELOCATED 7777777777
5555 027072 010700 REL7: MOV PC, R0 ;GET PC
5556 027074 005740 TST -(R0) ;R0 CONTAINS THE ADDRESS OF REL7
5557 027076 010037 001512 MOV R0, ‡FRSTAD ;SAVE
5558 027102 010700 MOV PC, R0 ;GET CURRENT PC
5559 027104 162700 027104 SUB #., R0 ;SUBTRACT RELOCATION FACTOR

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5561 027114 010737 001212      MOV      PC, 0#SLPERR      ;SET LOOP ADDRESS
5562 027120 062737 000030 001212  ADD      #30, 0#SLPERR    ;ADJUST
5563 027126 013737 001212 001210  MOV      0#SLPERR, 0#SLPADR
5564 027134 105737 001502      TSTB    0#NEXEC          ;BR IF TEST CODE TO BE EXECUTED
5565 027140 001402          BEQ      +6
5566 027142 000167 001206      JMP      RELE7
5567          ;THIS TEST SHIFTS A '1' BIT THROUGH ALL BIT POSITIONS
5568 027146 012702 177774      MOV      #STKLM, R2      ;GET ADDRESS OF STACK LIM REG
5569 027152 005022          CLR      (R2)+          ;CLEAR STACK LIMIT REG
5570 027154 032712 000020      BIT      #20, (R2)      ;EXIT TEST IF 'T' BIT IS SET
5571 027160 001116          BNE     101$
5572 027162 052712 000340      BIS      #340, (R2)     ;SET PRIORITY LEVEL 7 TO PREVENT
5573          ;ANY INTERRUPTS FROM OCCURRING
5574 027166 012700 000400      MOV      #400, R0      ;SET CHECK DATA
5575 027172 010042 1$:      MOV      R0, -(R2)     ;MOVE TO STACK LIMIT REG
5576 027174 022200          CMP      (R2)+, R0     ;AND CHECK RESULT
5577 027176 001401          BEQ     2$
5578 027200 104000          HLT
5579          ;ERROR! STACK LIMIT DID NOT
5580          ;LOAD CORRECTLY. CORRECT RESULT
5581          ;IS IN R0
5582 027202 006300 2$:      ASL     R0
5583 027204 103372          BCC     1$
5584 027206 005042          CLR     -(R2)
5585          ;THIS TEST CHECKS THAT A PROPER 'RED' ZONE VIOLATION OCCURS, NOTE THAT
5586          ;NO 'RED ZONE' VIOLATION WILL OCCUR IF IN USER/SUPER MODES.
5587          ;A RED ZONE VIOLATION PUSHES THE CURRENT PSW, PC ON A STACK AT 2 AND 0
5588          ;AND TAKES THE NEXT INSTRUCTION FROM THE PC IN LOCATION 4. THE INST-
5589          ;RUCTION CAUSING THE RED ZONE VIOLATION IS 'ABORTED'.
5590 027210 010746          MOV      PC, -(SP)     ;GET CURRENT PC
5591 027212 062716 000060      ADD      #4$, -(SP)    ;FORM ADDRESS OF 4$ BELOW
5592 027216 012637 000004          MOV      (SP)+, 0#ERRVEC ;SET ERROR TRAP VECTOR TO 4$ BELOW
5593 027222 013737 177776 000006      MOV      0#PSW, 0#ERRVEC+2 ;RETAIN CURRENT STATUS ON TRAP
5594 027230 010712          MOV      PC, (R2)     ;SET STACK LIMIT TO CURRENT PC
5595          ;+400
5596 027232 011206          MOV      (R2), SP     ;AND STACK PTR = STACK LIMIT REG
5597 027234 010603          MOV      SP, R3      ;SAVE STACK PTR
5598 027236 016304 000336      MOV      336(R3), R4 ;SAVE MEMORY LOC CONTENTS
5599          ;AT 'RED ZONE' BOUNDARY
5600 027242 032737 140000 177776      BIT      #UM, 0#PSW   ;BRANCH IF IN KERNEL MODE
5601 027250 001403          BEQ     20$
5602 027252 010466 000336      MOV      R4, 336(SP)  ;SHOULD NOT CAUSE TRAP
5603 027256 000432          BR      100$
5604          ;
5605 027260 005066 000336 20$:      CLR     336(SP)      ;SHOULD CAUSE 'RED ZONE' TRAP
5606 027264 012706 000700 3$:      MOV      #SUPSTK, SP ;RESTORE THE STACK
5607 027270 104000          HLT
5608          ;ERROR! FAILED TO TRAP
5609 027272 032737 140000 000002 4$:      BIT      #UM, 0#2
5610          ;CHECK IF TRAPPED WHEN IN USER
5611          ;/SUPER MODES (2 CONTAINS OLD PSW)
5612 027300 001013          BNE     99$
5613 027302 010600          MOV      SP, R0
5614 027304 001011          BNE     99$
5615 027306 026304 000336      CMP      336(R3), R4 ;CHECK THAT INST WAS ABORTED
5616 027312 001006          BNE     99$
5617          ;GO REPORT ERRPR

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5617 027316 010705          MOV    PC,R5          ;GET CURRENT PC
5618 027320 062705 177744  ADD    #3$,R5        ;FORM ADDRESS OF 3$ ABOVE
5619 027324 020516          CMP    R5,(SP)       ;CHECK THAT RETURN PC IS ON
5620                                     ;THE STACK (AT 0)
5621 027326 001406          BEQ    100$         ;EXIT TEST
5622
5623                                     ;ERROR
5624 027330 005012          99$:  CLR    (R2)        ;CLEAR STACK LIMIT REG
5625 027332 010463 000336  MOV    R4,336(R3)    ;RESTORE MEM LOCATION
5626 027336 012706 000700  MOV    #SUPSTK,SP    ;SET STACK PTR
5627 027342 104000          HLT                                ;ERROR!
5628 027344 010463 000336  100$: MOV    R4,336(R3)    ;RESTORE MEM LOCATION
5629 027350 005022          CLR    (R2)+        ;CLEAR STACK LIM REG
5630 027352 012706 000700  MOV    #SUPSTK,SP    ;SET STACK PTR
5631 027356 042712 000340  BIC    #340,(R2)     ;SET PRIORITY LEVEL BACK TO 0
5632 027362 012737 053442 000004  MOV    #ERPAT,#ERRVEC ;RESTORE ERROR TRAP VECTOR
5633 027370 013737 177776 000006  MOV    #PSW,#ERRVEC+2
5634 027376 112737 000340 000006  MOVB   #PR7,#ERRVEC+2
5635 027404 042737 000020 000006  BIC    #BIT4,#ERRVEC+2
5636 027412 005037 177766          CLR                                ;CLEAR ERROR REG
5637 027416
5638
5639                                     ;*****
5640                                     ;*TEST 66 MEMORY MANAGEMENT REGISTER TESTS
5641                                     ;* PDR TEST - THIS TEST WRITES 64. RANDOM #'S INTO EACH PDR REGISTER
5642                                     ;* NOTE: IF MEM MGMT IS ENABLED ONLY PDR/PAR PAIRS 3-5 ARE TESTED.
5643                                     ;*****
5643 027416 112737 000066 001202 1$T66: MOVB   #66,#STSTNM ;LOAD TEST NUMBER
5644 027424 000004          SCOPE
5645
5646 027426 012702 027652  KTPDR: MOV    #PDRtbl,R2 ;SET TABLE ADDRESS OF PDR'S
5647 027432 012705 100360  MOV    #100360,R5    ;SET BIT MASK (11/45)
5648 027436 012200          1$:  MOV    (R2)+,R0    ;GET PDR ADDRESS
5649 027440 001435          BEQ    100$         ;EXIT ON '0' TERMINATOR
5650 027442 012716 000010  2$:  MOV    #8,(SP)   ;SET LOOP COUNT (FOR 8 REGS)
5651 027446 105737 001503  TSTB   #MMON        ;BRANCH IF MEM MGMT DISABLED
5652 027452 001404          BEQ    3$
5653 027454 062700 000006  ADD    #6,R0        ;SET R0 TO PDR3
5654 027460 012716 000003  MOV    #3,(SP)     ;AND LIMIT TO TEST 3 PDRS
5655 027464 012703 000040  3$:  MOV    #32.,R3    ;SET DATA COUNT
5656 027470 005004          CLR    R4          ;INITIALIZE DATA TO BE WRITTEN
5657 027472 040504          4$:  BIC    R5,R4        ;CLEAR NON-SETTABLE BITS
5658 027474 010410          MOV    R4,(R0)     ;WRITE INTO PDR
5659 027476 021004          CMP    (R0),R4     ;AND CHECK DATA READ BACK
5660 027500 001013          BNE    99$         ;GO TO ERROR CALL
5661 027502 005104          COM    R4          ;COMPLEMENT DATA
5662 027504 040504          BIC    R5,R4        ;CLEAR NON-SETTABLE BITS
5663 027506 010410          MOV    R4,(R0)     ;WRITE COMPLEMENT DATA INTO PDR
5664 027510 021004          CMP    (R0),R4     ;AND CHECK
5665 027512 001006          BNE    99$         ;GO TO ERROR CALL
5666 027514 060104          ADD    R1,R4        ;STEP DATA
5667 027516 077313          SOB   R3,4$
5668 027520 005020          5$:  CLR    (R0)+        ;STEP TO NEXT REGISTER
5669 027522 005316          DEC    (SP)         ;DECREMENT REGISTER COUNT
5670 027524 001357          BNE    3$
5671 027526 000743          BR    1$          ;GET NEXT SET OF 8 REGISTERS

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# K10

CEQKCC PDP 11/70 CPU EXERCISOR  
CEQKCC.P11 03-MAR-78 13:13

MACY11 30A(1052) 03-MAR-78 13:15 PAGE 108  
T66 MEMORY MANAGEMENT REGISTER TESTS

SEQ 0127

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5673 027530 104000          99$:   HLT                ;ERROR! INCORRECT DATA READ
5674                                     ;BACK FROM PDR. ADDRESS OF
5675                                     ;PDR IS IN R0, DATA IS IN R4
5676 027532 000772          BR      5$                ;STEP TO NEXT REGISTER
5677 027534
5678
5679
5680
5681
5682 027534 112737 000067 001202 100$: *****
5683 027542 000004          *TEST 67 PAR TEST
5684 027544 012702 027670          * PAR TEST - THIS TEST WRITES 64. COMPLEMENTING RANDOM #'S INTO EACH PAR.
5685 027550 005005          *****
5686 027552 012200          †ST67:  MOVB    #67,2#STSTNM      ;LOAD TEST NUMBER
5687 027554 001435          SCOPE
5688 027556 012716 000010          KTPAR:  MOV     #PARTBL,R2      ;GET TABLE ADDRESS OF PAR'S
5689 027562 105737 001503          CLR     R5
5690 027566 001404          1$:    MOV     (R2)+,R0          ;GET PAR ADDRESS
5691 027570 062700 000006          BEQ    100$                   ;EXIT ON '0' TERMINATOR
5692 027574 012716 000003          2$:    MOV     #8,(SP)        ;SET LOOP COUNT (FOR 8 REGS.)
5693 027600 012703 000040          TSTB   2#MMON                ;BRANCH IF MEM MGMT DISABLED
5694 027604 005004          BEQ    3$
5695 027606 040504          ADD    #6,R0                 ;SET R0 TO PAR3
5696 027610 010410          MOV    #3,(SP)              ;AND LIMIT TEST TO 3 PARS
5697 027612 021004          3$:    MOV    #32.,R3          ;SET DATA COUNT
5698 027614 001013          CLR    R4                    ;INITIALIZE DATA
5699 027616 005104          BIC   R5,R4                  ;CLEAR NON-SETTABLE BITS
5700 027620 040504          MOV    R4,(R0)              ;WRITE INTO PAR
5701 027622 010410          CMP    (R0),R4              ;AND CHECK
5702 027624 021004          BNE   99$                    ;TAKE ERROR EXIT
5703 027626 001006          COM   R4                    ;COMPLEMENT DATA
5704 027630 060104          BIC   R5,R4                  ;CLEAR NON-SETTABLE BITS
5705 027632 077313          MOV    R4,(R0)              ;WRITE COMPLEMENT DATA
5706                                     CMP    (R0),R4              ;AND CHECK
5707 027634 005020          BNE   99$                    ;TAKE ERROR EXIT
5708 027636 005316          ADD    R1,R4                ;STEP DATA
5709 027640 001357          SOB   R3,4$                 ;LOOP UNTIL FINISHED
5710 027642 000743          5$:    CLR    (R0)+              ;DECREMENT REGISTER COUNT
5711                                     DEC    (SP)                   ;BRANCH IF 8 REGS NOT DONE
5712 027644 104000          99$:   HLT                ;ERROR! INCORRECT DATA READ BACK
5713                                     ;FROM PAR. ADDRESS OF PAR IS IN
5714                                     ;R0, DATA IS IN R4
5715 027646 000772          BR      5$                ;DO NEXT REGISTER
5716 027650
5717 027650 000416          100$:  BR      TST70            ;GO TO NEXT TEST
5718
5719 027652 172300          .TABLES FOR PDR & PAR TESTS ABOVE
5720 027654 177600          PDRtbl: .WORD  KIPDR0
5721 027656 172200          .WORD  UIPDR0
5722 027660 172320          .WORD  SIPDR0
5723 027662 177620          .WORD  KDPDR0
5724 027664 172220          .WORD  UDPDR0
5725 027666 000000          .WORD  SDPDR0
5726                                     .WORD  0
5727 027670 172340          PARTBL: .WORD  KIPARO

```

;CHANGED TO '0' IF 11/40

;TERMINATOR

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5729 027674 172240 .WORD SIPARO ;CHANGED TO '0' IF 11/40
5730 027676 172360 .WORD KDPARO
5731 027700 177660 .WORD UDPARO
5732 027702 172260 .WORD SDPARO
5733 027704 000000 .WORD 0 ;TERMINATOR
5734
5735 ::*****
5736 :*TEST 70 CHECK KT ABORT LOGIC
5737 :* THIS TEST CHECKS KT ABORT LOGIC. TEST CREATES AN ABORT CONDITION
5738 :* AND INSURES THAT ABORT IS TAKEN PROPERLY. NOTE: TEST IS EXECUTED ONLY
5739 :* IF TEST IS ENTERED WITH MEM MGMT ENABLED.
5740 ::*****
5741 027706 112737 000070 001202 TST70: MOVB #70,2#STSTNM ;LOAD TEST NUMBER
5742 027714 000004 SCOPE
5743 027716 105737 001503 KTABT: TSTB 2#MMON ;BRANCH IF MEM MGMT DISABLED
5744 027722 001515 BEQ KTEX
5745 027724 005037 172350 CLR 2#KIPAR4 ;SET UP MEM MGMT REGISTERS
5746 027730 005037 172310 CLR 2#KIPDR4 ;TO ABORT IF A MEMORY
5747 027734 005037 177650 CLR 2#UIPAR4 ;REFERENCE IS MADE TO
5748 027740 005037 177610 CLR 2#UIPDR4 ;ADDRESSES (VIRTUAL) BETWEEN
5749 027744 005037 172250 CLR 2#SIPAR4
5750 027750 005037 172210 CLR 2#SIPDR4
5751 027754 013746 000250 1S: MOV 2#MMVEC, -(SP) ;SAVE MEM MGMT VECTOR
5752 027760 013746 000252 MOV 2#MMVEC+2, -(SP) ;AND PRIORITY
5753 027764 010746 MOV PC, -(SP) ;SET MEM MGMT
5754 027766 062716 000040 ADD #4, -(SP) ;VECTOR TO 4S BELOW
5755 027772 012637 000250 MOV (SP)+, 2#MMVEC
5756 027776 013737 177776 000252 MOV 2#PSW, 2#MMVEC+2
5757 030004 005000 CLR R0 ;CLEAR ABORT INDICATOR
5758 030006 010702 MOV PC, R2 ;SET R2 AND R3 NOTE:
5759 030010 012703 100000 MOV #100000, R3 ;THE REF VIA R3 CAUSES THE
5760 030014 014223 2S: MOV -(R2), (R3)+ ;ABORT
5761 030016 005700 3S: TST R0 ;BRANCH IF THE ABORT OCCURRED
5762 030020 001001 BNE .+4
5763 030022 104000 HLT ;REPORT ERROR
5764 030024 000445 BR 100S
5765 ;ABORT HERE
5766 030026 013700 177776 4S: MOV 2#PSW, R0 ;SR0 SHOULD CONTAIN
5767 030032 000300 SWAB R0 ;CAUSE FOR ABORT AND
5768 030034 006200 ASR R0 ;ALSO WHICH SEGMENT
5769 030036 042700 177637 BIC #177637, R0 ;WAS IN USE WHEN ABORT
5770 030042 062700 100011 ADD #100011, R0 ;OCCURRED.
5771 030046 020037 177572 CMP R0, 2#SR0
5772 030052 001025 BNE 99$
5773 030054 012700 030014 MOV #2S, R0 ;GET ADDRESS OF INST THAT ABORTED
5774 030060 020037 177576 CMP R0, 2#SR2 ;THAT ABORTED
5775 030064 001020 BNE 99$
5776 030066 012700 000362 MOV #362, R0
5777 030072 120037 177574 CMPB R0, 2#SR1 ;SR1 (11/45) CONTAINS REGISTER
5778 030076 001013 BNE 99$ ;MODIFICATIONS MADE
5779 030100 012700 000023 MOV #23, R0
5780 030104 120037 177575 CMPB R0, 2#SR1+1
5781 030110 001006 BNE 99$
5782 030112 012700 030014 5S: MOV #2S, R0
5783 030116 005720 TST (R0)+ ;R0=ADDRESS OF INST FOLLOWING ABORT

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5785 030122 001001      BNE      99$
5786 030124 000002      RTI
5787      ;ENTER HERE ON ERROR ;RETURN
5788 030126 104000      99$: HLT      ;REPORT ERROR
5789 030130 010716      MOV      PC,(SP)
5790 030132 062716 177664  ADD      #3$-.,(SP)
5791 030136 000002      RTI      ;RETURN
5792 030140 012637 000252 100$: MOV      (SP)+,2#MMVEC+2 ;RESTORE ABORT VECTOR
5793 030144 012637 000250      MOV      (SP)+,2#MMVEC ;& PRIORITY.
5794 030150 012737 000001 177572      MOV      #1,2#SRO ;CLEAR ERROR CONDITIONS
5795 030156
5796
5797
5798
5799
5800 030156 112737 000071 001202 KTEX:
5801 030164 000004      ;*****
5802 030166 032737 000040 172516 ;*TEST 71 MAPPING REGISTER TESTS
5803 030174 001053      ;* THIS TEST LOADS RANDOM #'S INTO EACH MAPPING REGISTER
5804 030176 012700 170200 ;*****
5805 030202 012706 000700 TST71: MOV      #71,2#STSTNM ;LOAD TEST NUMBER
5806 030206 012716 000001      SCOPE
5807 030212 012702 177700      BIT      #BITS 2#MMR3 ;IS MAP ON?
5808 030216 012703 000040      BNE      MAPTWO ;BRANCH IF YES
5809 030222 005005      MAPTST: MOV      #MAPLO,R0 ;SET ADRS OF FIRST MAP REGISTER
5810 030224 010504      MOV      #SUPSTK,SP ;SETUP THE SP
5811 030226 041604      MOV      #1,(SP) ;SET BIT MASK FOR MAPLO <15-01>
5812 030230 010410      MOV      #177700,R2 ;AND ALSO FOR MAPHO <21-16>
5813 030232 021004      1$: MOV      #32.,R3 ;SET DATA COUNT
5814 030234 001032      2$: CLR      R5 ;SET INITIAL DATA
5815 030236 005105      MOV      R5,R4 ;GET DATA
5816 030240 010504      BIC      (SP),R4 ;CLEAR UNUSED BITS
5817 030242 041604      MOV      R4,(R0) ;LOAD DATA INTO MAPLO <15-01>
5818 030244 010410      CMP      (R0),R4 ;CHECK DATA
5819 030246 021004      BNE      99$ ;BRANCH IF INCORRECT
5820 030250 001024      COM      R5 ;COMPLEMENT TEST DATA
5821 030252 005720      MOV      R5,R4 ;GET TEST DATA
5822 030254 010504      BIC      R2,R4 ;CLEAR UNUSED BITS
5823 030256 040204      MOV      R4,(R0) ;LOAD TEST DATA INTO MAPHO <21-16>
5824 030260 010410      CMP      (R0),R4 ;AND CHECK
5825 030262 021004      BNE      99$
5826 030264 001016      COM      R5 ;COMPLEMENT TEST DATA
5827 030266 005105      MOV      R5,R4 ;GET TEST DATA
5828 030270 010504      BIC      R2,R4 ;CLEAR UNUSED BITS
5829 030272 040204      MOV      R4,(R0) ;LOAD TEST DATA
5830 030274 010410      CMP      (R0),R4 ;AND CHECK
5831 030276 021004      BNE      99$
5832 030300 001010      ADD      PC,R5 ;FORM NEXT TEST DATA
5833 030302 060705      TST      -(R0) ;RESET PTR TO REGISTER <15-01>
5834 030304 005740      SOB      R3,2$ ;AND TEST UNTIL ALL #'S USED
5835 030306 077332      CMP      (R0)+,(R0)+ ;STEP TO NEXT REGISTER PAIR
5836 030310 022020 170400      CMP      #MAPLO+128.,R0 ;BRANCH IF NOT LAST PAIR
5837 030312 022700      BNE      1$
5838 030316 001337      BR
5839 030320 000401      MAPTWO

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5897 030550 004767 002116          JSR    PC,FLTMPY      ;DO THE MULTIPLY
5898 030554 174137 001306          STF    AC1,#STMP4    ;SAVE RESULT
5899 030560 013737 001404 001262  MOV    #SAC1,#SREG2  ;AND SOFTWARE EXP
5900
5901      ;NOW DO THE RIGHT HAND SIDE OF THE EQUATION
5902      ;DO THE A*A FIRST
5903 030566 013737 001256 001402  MOV    #SREG0,#SAC0  ;GET EXT EXPONENT
5904 030574 172437 001276          LDF    #STMP0,AC0    ;LOAD OPERAND A
5905 030600 013737 001402 001404  MOV    #SAC0,#SAC1  ;SET OPERAND B EXT EXPONENT
5906 030606 172500          LDF    AC0,AC1      ;LOAD B OPERAND
5907 030610 004767 002056          JSR    PC,FLTMPY    ;EXECUTE THE MULTIPLY
5908 030614 174102          STF    AC1,AC2      ;SAVE RESULT
5909 030616 013737 001404 001406  MOV    #SAC1,#SAC2
5910
5911      ;NOW DO THE B*B
5912 030624 172437 001302          LDF    #STMP2,AC0    ;LOAD B OPERAND
5913 030630 172500          LDF    AC0,AC1      ;LOAD THE A OPERAND
5914 030632 013737 001260 001402  MOV    #SREG1,#SAC0  ;AND EXT EXPONENT
5915 030640 013737 001402 001404  MOV    #SAC0,#SAC1
5916 030646 004767 002020          JSR    PC,FLTMPY    ;DO THE MULTIPLY
5917 030652 174103          STF    AC1,AC3      ;SAVE THE RESULT
5918 030654 013737 001404 001410  MOV    #SAC1,#SAC3
5919
5920      ;NOW DO THE 2*B*A
5921 030662 012701 001302          MOV    #STMP2,R1
5922 030666 172411          LDF    (R1),AC0      ;LOAD THE B OPERAND
5923 030670 172541          LDF    -(R1),AC1     ;LOAD THE A OPERAND
5924 030672 013737 001260 001402  MOV    #SREG1,#SAC0  ;AND THE EXT EXPONENTS
5925 030700 013737 001256 001404  MOV    #SREG0,#SAC1
5926 030706 004767 001760          JSR    PC,FLTMPY    ;DO THE MULTIPLY
5927 030712 172427 040000          LDF    #104000,AC0  ;SETUP TO MULTIPLY BY TWO
5928 030716 012737 000002 001402  MOV    #2,#SAC0
5929 030724 004767 001742          JSR    PC,FLTMPY    ;DO THE MULTIPLY
5930
5931      ;NOW SUM THE RESULTS
5932 030730 013737 001410 001402  MOV    #SAC3,#SAC0  ;GET RESULT OF B*B
5933 030736 172403          LDF    AC3,AC0      ;ADD THE RESULT
5934 030740 004767 001776          JSR    PC,FLTADD    ;GET RESULT OF A*A
5935 030744 172402          LDF    AC2,AC0
5936 030746 013737 001406 001402  MOV    #SAC2,#SAC0  ;ADD THIS RESULT
5937 030754 004767 001762          JSR    PC,FLTADD    ;SAVE FINAL RESULT
5938 030760 174137 001312          STF    AC1,#STMP6
5939 030764 013737 001404 001264  MOV    #SAC1,#SREG3
5940
5941      ;NOW CHECK BOTH SIDES OF THE EQUATION
5942      ;CALCULATE THE NUMBER OF CORRECT BITS
5943      ;PUT LARGEST EXPONENT OF A**2 OR B**2 IN SAC2
5944 030772 023737 001406 001410  CMP    #SAC2,#SAC3
5945 031000 002003          BGE    15           ;BRANCH IF SAC2 ALREADY HAS LARGEST
5946 031002 013737 001410 001406  MOV    #SAC3,#SAC2  ;SAC3 WAS LARGER
5947 031010 163737 001404 001406 15:  SUB    #SAC1,#SAC2  ;NOW CALCULATE NUMBER
5948 031016 162737 000024 001406  SUB    #20,#SAC2    ;OF CORRECT BITS WITHIN 2
5949 031024 005437 001406          NEG    #SAC2        ;MAKE RESULT POSITIVE
5950 031030 172437 001306          LDF    #STMP4,AC0   ;LOAD RESULT OF LEFT HAND SIDE
5951 031034 013737 001262 001402  MOV    #SREG2,#SAC0 ;AND EXTENDED EXPONENT

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C11

CEQKCC PDP 11/70 CPU EXERCISOR MACY11 30A(1052) 03-MAR-78 13:15 PAGE 113  
CEQKCC.P11 03-MAR-78 13:13 START OF SECTION 8

SEQ 0132

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5954                                     ;ACTUAL EXP'S ARE EQUAL TO 200
5955 031054 100002                       BPL 3S ;ENSURE RESULT IS POSITIVE
5956 031056 005437 001404               NEG @#SAC1
5957 031062 023737 001406 001404 3S:  CMP @#SAC2,@#SAC1 ;ANSWERS WITHIN ALLOWABLE NUMBER?
5958 031070 003401                       BLE SECT2 ;BRANCH IF YES
5959 031072 104014                       4S:  ERROR 14 ;RESULTS ARE WRONG
5960                                     ;*****
5961 031074 170127 000000               SECT2: LDFPS #0
5962                                     ;DO A+B
5963 031100 172537 001276               LDF @#STMP0,AC1 ;LOAD A OPERAND
5964 031104 172437 001302               LDF @#STMP2,AC0 ;LOAD B OPERAND
5965 031110 013737 001256 001404       MOV @#SREG0,@#SAC1
5966 031116 013737 001260 001402       MOV @#SREG1,@#SAC0
5967 031124 004767 001612               JSR PC,FLTADD ;ADD THEM
5968 031130 174102                       STF AC1,AC2 ;SAVE IN AC2
5969 031132 013737 001404 001406       MOV @#SAC1,@#SAC2 ;AND EXT EXPONENT
5970                                     ;NOW DO THE A-B
5971 031140 172537 001276 001404       LDF @#STMP0,AC1 ;LOAD OPERAND A
5972 031144 013737 001256 001404       MOV @#SREG0,@#SAC1 ;AND EXT EXPONENT
5973 031152 172437 001302 001402       LDF @#STMP2,AC0 ;LOAD OPERAND B
5974 031156 013737 001260 001402       MOV @#SREG1,@#SAC0
5975 031164 004767 001546               JSR PC,FLTSUB ;SUBTRACT THEM
5976                                     ;NOW DO (A+B)*(A-B)
5977 031170 172402                       LDF AC2,AC0 ;GET RESULT OF (A+B)
5978 031172 013737 001406 001402       MOV @#SAC2,@#SAC0
5979 031200 004767 001466               JSR PC,FLTPY ;FORM THE PRODUCT
5980 031204 174137 001306               STF AC1,@#STMP4 ;SAVE RESULT
5981 031210 013737 001404 001262       MOV @#SAC1,@#SREG2 ;AND EXT EXPONENT
5982                                     ;NOW DO THE B*B
5983 031216 172437 001302 001402       LDF @#STMP2,AC0 ;LOAD OPERAND B
5984 031222 013737 001260 001402       MOV @#SREG1,@#SAC0
5985 031230 172500                       LDF AC0,AC1 ;B OPERAND IS IN AC0
5986 031232 013737 001402 001404       MOV @#SAC0,@#SAC1 ;AND EXT EXPONENT
5987 031240 004767 001426               JSR PC,FLTPY
5988 031244 174102                       STF AC1,AC2 ;SAVE RESULT IN AC2
5989 031246 013737 001404 001406       MOV @#SAC1,@#SAC2
5990                                     ;NOW DO THE A*A
5991 031254 172437 001276 001402       LDF @#STMP0,AC0 ;LOAD OPERAND A
5992 031260 013737 001256 001402       MOV @#SREG0,@#SAC0
5993 031266 172500                       LDF AC0,AC1
5994 031270 013737 001402 001404       MOV @#SAC0,@#SAC1
5995 031276 004767 001370               JSR PC,FLTPY ;EXECUTE THE MULTIPLY
5996 031302 013737 001404 001410       MOV @#SAC1,@#SAC3 ;SAVE EXT EXPO OF A*A
5997                                     ;NOW DO A**2-B**2
5998 031310 172402                       LDF AC2,AC0 ;GET B*B
5999 031312 013737 001406 001402       MOV @#SAC2,@#SAC0 ;A*A IN AC1
6000 031320 004767 001412               JSR PC,FLTSUB
6001 031324 174137 001312               STF AC1,@#STMP6 ;SAVE IN MEMORY
6002 031330 013737 001404 001264       MOV @#SAC1,@#SREG3
6003                                     ;NOW COMPUTE THE RESULTS
6004                                     ;CALCULATE THE NUMBER OF CORRECT BITS
6005 031336 023737 001406 001410       CMP @#SAC2,@#SAC3 ;DETERMINE WHICH EXP IS LARGER
6006 031344 002003                       BGE 2S ;BRANCH IF AC2 LARGER
6007 031346 013737 001410 001406       MOV @#SAC3,@#SAC2 ;PUT LARGEST IN AC2
6008 031354 163737 001404 001406 2S:  SUB @#SAC1,@#SAC2

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6010 031370 005437 001406      NEG      2#SAC2
6011 031374 172437 001306      LDF      2#STMP4,AC0      ;GET LEFT HAND SIDE
6012 031400 013737 001262 001402      MOV      2#SREG2,2#SAC0
6013 031406 004767 001324      JSR      PC,FLTSUB      ;SUBTRACT TO SEE HOW CLOSE THEY ARE
6014 031412 163737 001264 001404      SUB      2#SREG3,2#SAC1  ;SUB EXT EXPONENTS
6015                                     ;ACTUAL EXPONENTS ARE EQUAL
6016 031420 100002                                     ;MAKE SURE RESULT IS POSITIVE
6017 031422 005437 001404      BPL      15
6018 031426 023737 001406 001404 15:      NEG      2#SAC1
6019 031434 003401                                     ;RESULTS WITHIN RANGE ALLOWED?
6020 031436 104014                                     ;BRANCH IF YES
6021                                     ;RESULTS WRONG
6022                                     ;*****

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6023 031440 172537 001276      SECT3:  LDF      2#STMP0,AC1      ;LOAD OPERAND A
6024 031444 172437 001302      LDF      2#STMP2,AC0      ;AND OPERAND B
6025 031450 013737 001256 001404      MOV      2#SREG0,2#SAC1
6026 031456 013737 001260 001402      MOV      2#SREG1,2#SAC0
6027 031464 004767 001224      JSR      PC,FLTDIV      ;GO DIVIDE THEM
6028 031470 004767 001176      JSR      PC,FLTMPY      ;MULTIPLY RESULT BY B
6029 031474 174137 001306      STF      AC1,2#STMP4      ;SAVE RESULT
6030 031500 013737 001404 001262      MOV      2#SAC1,2#SREG2
6031 031506 172437 001276      LDF      2#STMP0,AC0      ;LOAD OPERAND A
6032 031512 174037 001312      STF      AC0,2#STMP6      ;SAVE INCASE TYPE OUT
6033 031516 013737 001256 001402      MOV      2#SREG0,2#SAC0
6034 031524 013737 001256 001264      MOV      2#SREG0,2#SREG3
6035 031532 004767 001200      JSR      PC,FLTSUB      ;SUBTRACT RIGHT AND LEFT HAND SIDES
6036 031536 163737 001256 001404      SUB      2#SREG0,2#SAC1  ;SEE IF RESULT OK
6037 031544 100002                                     ;ENSURE DIFFERANCE IS POSITIVE
6038 031546 005437 001404      BPL      15
6039 031552 022737 000027 001404 15:      NEG      2#SAC1
6040 031560 003001                                     ;RESULTS WITHIN 2 BITS?
6041 031562 000401                                     ;BRANCH IF NO
6042 031564 104014                                     ;GO TO NEXT TEST
6043                                     ;RESULTS WRONG
6044                                     ;*****

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```

6045 :*TEST 73      FLOATING POINT TEST 2
6046 :*
6047 :*      THIS TEST TAKES TWO RANDOM NUMBERS (A AND B) AND
6048 :*      COMPARES THE RESULTS OF TWO EQUAL CALCULATIONS.
6049 :*      EACH SECTION EVALUATES A DIFFERENT EQUATION AS DESCRIBED BELOW:
6050 :*      SECT1      (A+B)**2=A**2+2*A*B+B**2
6051 :*      SECT2      (A+B)*(A-B)=A**2-B**2
6052 :*      SECT3      A/B*B=A
6053 :*      *****

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6054 031566 112737 000073 001202 15:      TST73:  MOV8     #73,2#STSTNM      ;LOAD TEST NUMBER
6055 031574 000004                                     ;SCOPE
6056 031576 012737 000001 001316      MOV      #1,2#STIMES
6057 031604 004737 050702 1005:      JSR      PC,2#FLTDBL      ;GET RANDOM OPERANDS
6058 031610 170127 000200      LDFPS   #200
6059 031614 172537 001276      LDF     2#STMP0,AC1      ;INIT FPS
6060 031620 172437 001306      LDF     2#STMP4,AC0      ;LOAD A OPERAND
6061 031624 013737 001256 001404      LDF     2#STMP4,AC0      ;LOAD B OPERAND
6062 031632 013737 001260 001402      MOV     2#SREG0,2#SAC1  ;SETUP EXTENDED
6063 031640 004767 001076      MOV     2#SREG1,2#SAC0  ;EXPONENTS
6064 031644 174100      JSR     PC,FLTADD      ;PERFORM THE ADD
6065                                     ;SETUP AC0 TO

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6066 031654 004767 001012 JSR PC,FLTMPY ;DO THE MULTIPLY
6067 031660 174137 001422 STF AC1,FLTMPD ;SAVE RESULT
6068 031664 013737 001404 001262 MOV ;AND SOFTWARE EXP
6069
6070 ;NOW DO THE RIGHT HAND SIDE OF THE EQUATION
6071 ;DO THE A*A FIRST
6072 031672 013737 001256 001402 MOV ;GET EXT EXPONENT
6073 031700 172437 001276 LDF ;LOAD OPERAND A
6074 031704 013737 001402 001404 MOV ;SET OPERAND B EXT EXPONENT
6075 031712 172500 LDF ;LOAD B OPERAND
6076 031714 004767 000752 JSR PC,FLTMPY ;EXECUTE THE MULTIPLY
6077 031720 174102 STF AC1,AC2 ;SAVE RESULT
6078 031722 013737 001404 001406 MOV ;
6079
6080 ;NOW DO THE B*B
6081 031730 172437 001306 LDF ;LOAD B OPERAND
6082 031734 172500 LDF ;
6083 031736 013737 001260 001402 MOV ;AND EXT EXPONENT
6084 031744 013737 001402 001404 MOV ;
6085 031752 004767 000714 JSR PC,FLTMPY ;DO THE MULTIPLY
6086 031756 174103 STF AC1,AC3 ;SAVE THE RESULT
6087 031760 013737 001404 001410 MOV ;
6088
6089 ;NOW DO THE 2*B*A
6090 031766 012701 001306 MOV ;LOAD THE B OPERAND
6091 031772 172411 LDF ;LOAD THE A OPERAND
6092 031774 172541 LDF ;LOAD THE A OPERAND
6093 031776 013737 001260 001402 MOV ;AND THE EXT EXPONENTS
6094 032004 013737 001256 001404 MOV ;
6095 032012 004767 000654 JSR PC,FLTMPY ;DO THE MULTIPLY
6096 032016 172427 040000 LDF ;SETUP TO MULTIPLY BY TWO
6097 032022 012737 000002 001402 MOV ;
6098 032030 004767 000636 JSR PC,FLTMPY ;DO THE MULTIPLY
6099
6100 ;NOW SUM THE RESULTS
6101 032034 013737 001410 001402 MOV ;
6102 032042 172403 LDF ;GET RESULT OF B*B
6103 032044 004767 000672 JSR PC,FLTADD ;ADD THE RESULT
6104 032050 172402 LDF ;GET RESULT OF A*A
6105 032052 013737 001406 001402 MOV ;
6106 032060 004767 000656 JSR PC,FLTADD ;ADD THIS RESULT
6107 032064 174137 001432 STF AC1,FLTMP1 ;SAVE FINAL RESULT
6108 032070 013737 001404 001264 MOV ;
6109
6110 ;NOW CHECK BOTH SIDES OF THE EQUATION
6111 ;CALCULATE THE NUMBER OF CORRECT BITS
6112 ;PUT LARGEST EXPONENT OF A**2 OR B**2 IN SAC2
6113 032076 023737 001406 001410 CMP ;
6114 032104 002003 BGE ;BRANCH IF SAC2 ALREADY HAS LARGEST
6115 032106 013737 001410 001406 MOV ;SAC3 WAS LARGER
6116 032114 163737 001404 001406 1S: SUB ;NOW CALCULATE NUMBER
6117 032122 162737 000065 001406 SUB ;OF CORRECT BITS WITHIN 2
6118 032130 005437 001406 NEG ;MAKE RESULT POSITIVE
6119 032134 172437 001422 LDF ;LOAD RESULT OF LEFT HAND SIDE
6120 032140 013737 001262 001402 MOV ;AND EXTENDED EXPONENT

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6122 032152 163737 001264 001404 SUB 2#SREG3,2#SAC1 ;GET DIFFERENCE IN EXT EXPONENTS
6123 032152 163737 001264 001404 BPL 35 ;ACTUAL EXP'S ARE EQUAL TO 200
6124 032160 100002 001404 001404 NEG 2#SAC1 ;ENSURE RESULT IS POSITIVE
6125 032162 005437 001404 001404 3S: CMP 2#SAC2,2#SAC1 ;ANSWERS WITHIN ALLOWABLE NUMBER?
6126 032166 023737 001406 001404 BLE SECT2D ;BRANCH IF YES
6127 032174 003401 001406 001404 4S: ERROR 16 ;RESULTS ARE WRONG
6128 032176 104016 001406 001406 *****
6129 *****
6130 032200 170127 000200 SECT2D: LDFPS #200
6131 ;DO A+B
6132 032204 172537 001276 LDF 2#STMP0,AC1 ;LOAD A OPERAND
6133 032210 172437 001306 LDF 2#STMP4,AC0 ;LOAD B OPERAND
6134 032214 013737 001256 001404 MOV 2#SREG0,2#SAC1
6135 032222 013737 001260 001402 MOV 2#SREG1,2#SAC0
6136 032230 004767 000506 JSR PC,FLTA00 ;ADD THEM
6137 032234 174102 001404 001406 STF AC1,AC2 ;SAVE IN AC2
6138 032236 013737 001404 001406 MOV 2#SAC1,2#SAC2 ;AND EXT EXPONENT
6139 ;NOW DO THE A-B
6140 032244 172537 001276 LDF 2#STMP0,AC1 ;LOAD OPERAND A
6141 032250 013737 001256 001404 MOV 2#SREG0,2#SAC1 ;AND EXT EXPONENT
6142 032256 172437 001306 LDF 2#STMP4,AC0 ;LOAD OPERAND B
6143 032262 013737 001260 001402 MOV 2#SREG1,2#SAC0
6144 032270 004767 000442 JSR PC,FLTSUB ;SUBTRACT THEM
6145 ;NOW DO (A+B)*(A-B)
6146 032274 172402 001406 001402 LDF AC2,AC0 ;GET RESULT OF (A+B)
6147 032276 013737 001406 001402 MOV 2#SAC2,2#SAC0
6148 032304 004767 000362 JSR PC,FLTMPY ;FORM THE PRODUCT
6149 032310 174137 001422 STF AC1,2#FLTMP0 ;SAVE RESULT
6150 032314 013737 001404 001262 MOV 2#SAC1,2#SREG2 ;AND EXT EXPONENT
6151 ;NOW DO THE B*B
6152 032322 172437 001306 LDF 2#STMP4,AC0 ;LOAD OPERAND B
6153 032326 013737 001260 001402 MOV 2#SREG1,2#SAC0
6154 032334 172500 001402 001404 LDF AC0,AC1 ;B OPERAND IS IN AC0
6155 032336 013737 001402 001404 MOV 2#SAC0,2#SAC1 ;AND EXT EXPONENT
6156 032344 004767 000322 JSR PC,FLTMPY
6157 032350 174102 001404 001406 STF AC1,AC2 ;SAVE RESULT IN AC2
6158 032352 013737 001404 001406 MOV 2#SAC1,2#SAC2
6159 ;NOW DO THE A*A
6160 032360 172437 001276 LDF 2#STMP0,AC0 ;LOAD OPERAND A
6161 032364 013737 001256 001402 MOV 2#SREG0,2#SAC0
6162 032372 172500 001402 001404 LDF AC0,AC1
6163 032374 013737 001402 001404 MOV 2#SAC0,2#SAC1
6164 032402 004767 000264 JSR PC,FLTMPY ;EXECUTE THE MULTIPLY
6165 032406 013737 001404 001410 MOV 2#SAC1,2#SAC3 ;SAVE EXT EXPO OF A*A
6166 ;NOW DO A**2-B**2
6167 032414 172402 001406 001402 LDF AC2,AC0 ;GET B*B
6168 032416 013737 001406 001402 MOV 2#SAC2,2#SAC0 ;A*A IN AC1
6169 032424 004767 000306 JSR PC,FLTSUB
6170 032430 174137 001432 STF AC1,2#FLTMP1 ;SAVE IN MEMORY
6171 032434 013737 001404 001264 MOV 2#SAC1,2#SREG3
6172 ;NOW COMPUTE THE RESULTS
6173 ;CALCULATE THE NUMBER OF CORRECT BITS
6174 032442 023737 001406 001410 CMP 2#SAC2,2#SAC3 ;DETERMINE WHICH EXP IS LARGER
6175 032450 002003 001410 001406 BGE 25 ;BRANCH IF AC2 LARGER
6176 032452 013737 001410 001406 MOV 2#SAC3,2#SAC2 ;PUT LARGEST IN AC2

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6178 032466 162737 000066 001406 SUB #54, 2#SAC2
6179 032474 005437 001406 NEG 2#SAC2
6180 032500 172437 001422 LDF 2#FLTMPD, ACO ;GET LEFT HAND SIDE
6181 032504 013737 001262 001402 MOV 2#SREG2, 2#SACO
6182 032512 004767 000220 JSR PC, FLTSUB ;SUBTRACT TO SEE HOW CLOSE THEY ARE
6183 032516 163737 001264 001404 SUB 2#SREG3, 2#SAC1 ;SUB EXT EXPONENTS
6184 ;ACTUAL EXPONENTS ARE EQUAL
6185 032524 100002 BPL 15 ;MAKE SURE RESULT IS POSITIVE
6186 032526 005437 001404 NEG 2#SAC1
6187 032532 023737 001406 001404 1$: CMP 2#SAC2, 2#SAC1 ;RESULTS WITHIN RANGE ALLOWED?
6188 032540 003401 BLE SECT3D ;BRANCH IF YES
6189 032542 104016 ERROR 16 ;RESULTS WRONG
6190
6191 ;*****
6192 032544 172537 001276 SECT3D: LDF 2#STMPD, AC1 ;LOAD OPERAND A
6193 032550 172437 001306 LDF 2#STMP4, ACO ;AND OPERAND B
6194 032554 013737 001256 001404 MOV 2#SREG0, 2#SAC1
6195 032562 013737 001260 001402 MOV 2#SREG1, 2#SACO
6196 032570 004767 000120 JSR PC, FLTDIV ;GO DIVIDE THEM
6197 032574 004767 000072 JSR PC, FLTMPY ;MULTIPLY RESULT BY B
6198 032600 174137 001422 STF AC1, 2#FLTMPD ;SAVE RESULT
6199 032604 013737 001404 001262 MOV 2#SAC1, 2#SREG2
6200 032612 172437 001276 LDF 2#STMPD, ACO ;LOAD OPERAND A
6201 032616 174037 001432 STF ACO, 2#FLTMP1 ;SAVE INCASE TYPE OUT
6202 032622 013737 001256 001402 MOV 2#SREG0, 2#SACO
6203 032630 013737 001256 001264 MOV 2#SREG0, 2#SREG3
6204 032636 004767 000074 JSR PC, FLTSUB ;SUBTRACT RIGHT AND LEFT HAND SIDES
6205 032642 163737 001256 001404 SUB 2#SREG0, 2#SAC1 ;SEE IF RESULT OK
6206 032650 100002 BPL 15 ;ENSURE DIFFERENCE IS POSITIVE
6207 032652 005437 001404 NEG 2#SAC1
6208 032656 022737 000067 001404 1$: CMP #55, 2#SAC1 ;RESULTS WITHIN 2 BITS?
6209 032664 003505 BLE RELEB ;BRANCH IF YES
6210 032666 104016 ERROR 16 ;RESULTS WRONG
6211 032670 000503 BR RELEB
6212
6213 ;*****
6214 .SBTTL FLOATING POINT MULTIPLY ROUTINE
6215 ;* THIS ROUTINE MULTIPLIES THE CONTENTS OF ACO AND AC1
6216 ;* AND LEAVES THE RESULT IN AC1. IT ALSO TAKES CARE OF
6217 ;* THE SOFTWARE EXPONENTS THAT ARE KEPT IN SAC0 AND SAC1.
6218 ;*****
6219 032672 063737 001402 001404 FLTMPY: ADD 2#SAC0, 2#SAC1 ;ADD SOFTWARE EXPONENTS
6220 032700 171100 MULF ACO, AC1 ;DO THE MULTIPLY
6221 032702 012746 100400 MOV #100400, -(SP) ;PUT CONTROL WORD ON STACK
6222 032706 004737 051042 JSR PC, 2#EXPEXT ;CALCULATE EXT EXPONENT
6223 032712 000207 1$: RTS PC ;RETURN
6224
6225 ;*****
6226 .SBTTL FLOATING POINT DIVIDE ROUTINE
6227 ;* THIS ROUTINE DIVIDES THE CONTENTS OF AC1 BY ACO
6228 ;* AND LEAVES THE RESULT IN AC1.
6229 ;*****
6230 032714 163737 001402 001404 FLTDIV: SUB 2#SAC0, 2#SAC1 ;ADJUST SOFTWARE EXPONENTS
6231 032722 174500 DIVF ACO, AC1 ;EXECUTE THE DIVIDE
6232 032724 012746 100400 MOV #100400, -(SP) ;PUT CONTROL WORD ON STACK

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6234 032734 000207 1S: RTS PC ;RETURN
6235
6236 ;*****
6237 ;SBTTL FLOATING POINT ADD ROUTINE
6238 ;* THIS ROUTINE ADDS THE CONTENTS OF ACO TO AC1.
6239 ;* THIS CAN ONLY BE DONE IF THE SOFTWARE EXPONENTS
6240 ;* ARE CLOSE ENOUGH TOGETHER SUCH THAT AN ADJUSTMENT
6241 ;* OF THE REAL EXPONENT LEAVES A NON-ZERO NUMBER.
6242 ;*****
6243 032736 010667 000134 001404 FLTADD: MOV SP,SUBFLG ;SET SUBTRACT FLAG
6244 032742 023737 001402 001404 FLTADD: CMP @#SAC0,@#SAC1 ;CHECK SOFTWARE EXPONENTS
6245 032750 003016 BGT 1$
6246 032752 001434 BEQ 2$
6247 ;ACCUMULATOR 1 IS LARGER THAN ACCUMULATOR 0
6248 032754 013702 001404 MOV @#SAC1,R2 ;GET OPERAND B SOFTWARE EXP
6249 032760 163702 001402 SUB @#SAC0,R2 ;GET DIFFERENCE IN SOFTWARE EXP'S
6250 032764 020227 000071 CMP R2,#57. ;EXP WITHIN DBL PREC RANGE?
6251 032770 002003 BGE 7$ ;BRANCH IF ADD NOT REQUIRED
6252 ;RESULT IS OPERAND B
6253 032772 005402 NEG R2
6254 032774 176402 LDEXP R2,ACO ;RELOAD THE EXPONENT
6255 032776 000422 BR 2$
6256 033000 176427 177703 7$: LDEXP #-75,ACO ;FAKE EXPONENT SO HARDWARE
6257 033004 000417 BR 2$ ;WILL DETECT OUT OF RANGE
6258
6259 ;ACCUMULATOR 0 IS LARGER THAN ACCUMULATOR 1
6260 033006 013702 001402 1$: MOV @#SAC0,R2 ;GET SOFTWARE EXP OF OPERAND A
6261 033012 163702 001404 SUB @#SAC1,R2 ;GET DIFFERENCE IN EXP'S
6262 033016 013737 001402 001404 MOV @#SAC0,@#SAC1 ;MAKE SOFTWARE EXP'S EQUAL
6263 033024 020227 000071 CMP R2,#57. ;EXP WITHIN DBL PREC RANGE?
6264 033030 002003 BGE 4$ ;BRANCH IF NO
6265 033032 005402 NEG R2
6266 033034 176502 LDEXP R2,AC1 ;RELOAD THE EXPONENT
6267 033036 000402 BR 2$
6268
6269 ;ACCUMULATOR 0 IS MUCH LARGER THAN ACCUMULATOR 1 SO RESULT IS 0
6270 033040 176527 177703 4$: LDEXP #-75,AC1 ;FAKE EXPONENT SO HARDWARE
6271 ;WILL DETECT OUT OF RANGE
6272 033044 005767 000026 2$: TST SUBFLG ;ADD OR SUBTRACT?
6273 033050 001402 BEQ 5$ ;BRANCH IF ADD
6274 033052 173100 SUBF ACO,AC1
6275 033054 000401 BR 6$
6276 033056 172100 5$: ADDF ACO,AC1 ;EXECUTE THE ADD
6277 033060 012746 100400 6$: MOV #100400,-(SP) ;PUT CONTROL WORD ON STACK
6278 033064 004737 051042 JSR PC,@#EXPEXT ;CALCULATE EXT EXPONENT
6279 033070 005067 000002 3$: CLR SUBFLG ;INIT SUBTRACT FLAG
6280 033074 000207 RTS PC ;RETURN
6281 033076 000000 SUBFLG: .WORD
6282 033100 000004 RELEB: SCOPE
6283 033102 010702 MOV PC,R2
6284 033104 062702 000012 ADD #12,R2
6285 033110 012707 034242 MOV #RELOC,PC ;GO RELOCATE PROGRAM CODE
6286 033114 000000 RELB: .WORD 0
6287 ;8888888888888888 LAST ADDRESS OF CODE TO BE RELOCATED 888888888888
6288

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6290 ;*TEST 74 TELETYPE AND CLOCK TESTS
6291 ;*****
6292 033116 112737 000074 001202 †ST74: MOVB #74,‡STSTNM ;LOAD TEST NUMBER
6293 033124 000240 NOP
6294 033126 113737 001202 177570 MOVB ‡STSTNM,‡SWR
6295 033134 005037 001506 TTYCHK: CLR ‡FACTOR
6296 033140 012704 000100 MOV #100,R4 ;SET R4 = CONSTANT 100
6297 033144 032737 000400 001470 BIT ‡TOPT,‡OPT.CP ;BRANCH IF TTY
6298 033152 001002 BNE 1$ ;ON SYSTEM
6299 033154 000167 000204 JMP ARBFIN ;JUMP IF NOT
6300 033160 122777 000200 146056 1$: CMPB #200,‡STPS ;CHECK IF TTY IS READY
6301 033166 001374 BNE 1$
6302 033170 012737 001525 001270 MOV #NULLS-1,‡SREGS ;SET ADDRESS OF ASCII STRING TO TYPE
6303 033176 106277 146042 ASRB ‡STPS ;SET IE BIT. SEE TPISR FOR INT SERVICE.
6304 033202 000001 WAIT ;WAIT FOR INTERRUPT
6305
6306
6307 033204 DUMMY:
6308 ;ROUTINE TO CHECK PRIORITY ARBITRATION LOGIC
6309 ;THE BELOW TEST WILL INHIBIT INTERRUPTS ON LEVEL 6 AND ABOVE (LOCKING
6310 ;OUT THE LINE CLOCK) AND THEN SET UP THE TTY TO INTERRUPT. NEXT THE
6311 ;PRIORITY LEVEL WILL BE SET TO 0 ALLOWING INTERRUPTS IN WHICH CASE
6312 ;THE LINE CLOCK (AT LEVEL 6) SHOULD INTERRUPT BEFORE THE TTY (AT LEVEL 4).
6313 033204 132737 000020 177776 1$: BITB #20,‡PSW
6314 033212 001071 BNE ARBEX ;EXIT TEST IF 'T' BIT SET
6315 033214 030477 146024 2$: BIT R4,‡STPS ;WAIT FOR TTY TO BE NOT
6316 033220 001375 BNE 2$ ;BUSY
6317 033222 112737 000300 177776 MOVB #300,‡PSW ;SET PRIORITY LEVEL 6
6318 033230 150477 146010 3$: BISB R4,‡STPS ;SET IE BIT
6319 033234 100375 BPL 3$ ;AND WAIT FOR READY
6320 033236 032737 001000 001470 BIT #LKOPT,‡OPT.CP ;LINE CLOCK AVAILABLE?
6321 033244 001447 BEQ ARBFIN ;BRANCH IF NO
6322 033246 012737 033336 000064 MOV #7$,‡TPVEC ;SET TTY VECTOR
6323 033254 012737 033350 000100 MOV #8$,‡LKVEC ;SET CLOCK VECTORS
6324 033262 012737 000340 000102 MOV #PR?,‡LKVEC+2
6325 033270 005027 CLR (PC)+ ;CLEAR CHECK WORD
6326 033272 000000 4$: .WORD 0
6327 033274 000240 NOP
6328 033276 000240 NOP
6329 033300 000240 NOP
6330 033302 010437 177546 MOV R4,‡LKS
6331 033306 113700 5$: MOVB ‡(PC)+,R0 ;GET CLOCK STATUS & BRANCH IF READY
6332 033310 177546 6$: .WORD LKS ;CONTAINS ADDRESS OF L CLOCK STAT
6333 033312 100375 BPL 5$
6334 033314 000240 NOP
6335 ;AT THIS TIME BOTH THE CLOCK
6336 033316 105037 177776 CLRB ‡PSW ;ARE READY TO INTERRUPT
6337 ;SET PRIORITY LEVEL 0
6338 ;A CLOCK INTERRUPT WILL OCCUR (8$) AND LOC 4$ WILL BE INCREMENTED
6339 ;AFTER THE CLOCK SERVICE A TTY INTERRUPT WILL OCCUR. THE TTY INT SERV
6340 ;ICE WILL SHIFT LEFT 4$.
6341 033322 022767 000002 177742 CMP #2,4$ ;CHECK THAT THE CLOCK
6342 033330 001415 BEQ ARBFIN ;& TTY INTERRUPTED IN
6343 033332 104000 HLT ;THE PROPER SEQUENCE
6344 033334 000413 BR ARBFIN

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6346 033336 005077 145702          75:  CLR      @STPS          ;CLEAR IE BIT
6347 033342 006367 177724          ASL      45              ;SHIFT INDICATOR
6348 033346 000002                   RTI                   ;RETURN
6349
6350 033350 005267 177716          85:  INC      45              ;
6351 033354 012737 044444 000100    MOV      @LKSRV,@LKVEC    ;SET CLOCK VECTORS
6352 033362 000002                   RTI
6353
6354
6355 033364 012737 052612 000064    ARBFIN: MOV      @TPISR,@TPVEC ;RESTORE TTY VECTOR
6356 033372 005077 145646          CLR      @STPS          ;CLEAR IE BIT
6357 033376
6358
6359
6360
6361
6362 033376 112737 000075 001202    *TEST 75 TURN ON UBE AND MBT
6363 033404 000240                   * TURN ON THE MASS BUS TESTER AND UNIBUS EXERCISER IF PRESENT
6364 033406 113737 001202 177570    *
6365 033414 032737 001000 001470    *
6366 033422 001411                   *
6367 033424 012737 044444 000100    *
6368 033432 012737 000340 000102    *
6369 033440 052737 000100 177546    *
6370
6371
6372
6373 033446 105737 001470          *
6374 033452 100015                   *
6375 033454 032737 010000 177570    *
6376 033462 001011                   *
6377 033464 032737 000040 172516    *
6378 033472 001050                   *
6379 033474 004737 051436 000000    *
6380 033500 012772 064545          *
6381
6382
6383
6384 033506 032737 002000 001470    *
6385 033514 001437                   *
6386 033516 032737 000010 177570    *
6387 033524 001033                   *
6388 033526 122737 000060 001532    *
6389 033534 001027                   *
6390 033536 105737 001200          *
6391 033542 001024                   *
6392 033544 105737 001503          *
6393 033550 001021                   *
6394 033552 052777 000047 146500    *
6395 033560 012777 000007 146472    *
6396 033566 005077 146456          *
6397 033572 012777 044132 146470    *
6398 033600 012777 000240 146464    *
6399 033606 112777 000161 146432    *
6400

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6402 .SBTTL STMM ROUTINE
6403 :ROUTINE TO SET UP MEMORY MANAGEMENT TO RELOCATE PROGRAM CODE ABOVE 16K
6404 :CHECK IF PROGRAM IS TO BE RELOCATED.
6405 :SW6=1=NO RELOCATION
6406
6407 :*****PLEASE NOTE: THE RELOCATION ROUTINES WILL BE REFERED TO
6408 :AS TEST 76 IN ERROR TYPEOUTS DURING
6409 :RELOCATION.
6410
6411 :*****
6412 033614 112737 000076 001202 STMM:  MOVB  #76,2#STSTNM ;LOAD TEST NUMBER
6413 033622 000240      NOP
6414 033624 113737 001202 177570      MOVB  2#STSTNM,2#SWR
6415 033632 032737 000100 177570      BIT   #SW6,2#SWR ;RELOCATION DISABLED?
6416 033640 001402      BEQ   3$ ;BRANCH IF NO
6417 033642 000167 002450      JMP   ENDM
6418
6419 :THE PROGRAM IS GOING TO RELOCATE.
6420 :RELOCATION WILL BE PERFORMED IN KERNEL MODE WITH PSW SET AT PRIORITY
6421 :LEVEL 4 (TO PREVENT TTY INTERRUPT-WHICH CHANGES DATA IN PROGRAM)
6422 :THE 'T' BIT IS CLEARED (IF SET). AFTER THE DATA HAS BEEN WRITTEN IT IS
6423 :VERIFIED BEFORE EXECUTION.
6424 033646 013727 177776      3$:  MOV  2#PSW,(PC)+ ;SAVE CURRENT PSW
6425 033652 000000      OLDPSW: .WORD 0
6426 033654 012737 000200 177776      MOV  #PR4,2#PSW
6427 033662 004767 016452      JSR  PC,CLRTBIT ;CO CLEAR 'T' BIT IF SET
6428
6429 :NOW SETUP MEMORY MANAGEMENT REGISTERS.
6430 033666 012700 077406      MOV  #77406,RO ;SET CONSTANT=R/W UP 4K WORDS
6431 033672 010037 172300      MOV  RO,2#KIPDR0 ;SET KIPDR0,1,2,3,& 7 R/W UP 4K WORDS
6432 033676 010037 172302      MOV  RO,2#KIPDR1
6433 033702 010037 172304      MOV  RO,2#KIPDR2
6434 033706 010037 172306      MOV  RO,2#KIPDR3
6435 033712 010037 172310      MOV  RO,2#KIPDR4
6436 033716 010037 172312      MOV  RO,2#KIPDR5
6437 033722 010037 172316      MOV  RO,2#KIPDR7
6438
6439 033726 005037 172340      CLR  2#KIPAR0 ;NOTE: THESE 2 INSTRUCTIONS EFFECTIVELY
6440 033732 012737 000200 172342      MOV  #200,2#KIPAR1 ;RELOCATE PROGRAM EXECUTION
6441 033740 012737 000400 172344      MOV  #400,2#KIPAR2
6442 033746 013737 001520 172346      MOV  2#NEXPAR,2#KIPAR3 ;SET UP KIPAR3 & KIPAR4 & 5
6443 033754 013737 172346 172350      MOV  2#KIPAR3,2#KIPAR4
6444 033762 062737 000200 172350      ADD  #200,2#KIPAR4
6445 033770 013737 172346 172352      MOV  2#KIPAR3,2#KIPAR5
6446 033776 062737 000400 172352      ADD  #400,2#KIPAR5
6447 034004 012737 177600 172356      MOV  #177600,2#KIPAR7;AND OF COUSE THE I/O PAGE
6448
6449 :NOW SETUP USER MEM MGMT REGISTERS
6449 034012 010037 177600      1$:  MOV  RO,2#UIPDR0 ;SET UP USER MEM MGMT REGS
6450 034016 010037 177602      MOV  RO,2#UIPDR1
6451 034022 010037 177604      MOV  RO,2#UIPDR2
6452 034026 010037 177616      MOV  RO,2#UIPDR7
6453 034032 016737 145462 177640      MOV  NEXPAR,2#UIPDR0
6454 034040 013737 177640 177642      MOV  2#UIPDR0,2#UIPAR1
6455 034046 062737 000200 177642      ADD  #200,2#UIPAR1
6456 034054 013737 177640 177644      MOV  2#UIPAR0,2#UIPAR2

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6458 034070 013737 172356 177656      MOV      @#KIPAR7,@#UIPAR7
6459
6460 034076 010037 172200      MOV      R0,@#SIPDR0      ;SET UP SUPERVISOR MEM MGMT REGS
6461 034102 010037 172202      MOV      R0,@#SIPDR1
6462 034106 010037 172204      MOV      R0,@#SIPDR2
6463 034112 010037 172216      MOV      R0,@#SIPDR7
6464 034116 016737 145376 172240      MOV      NEXPAR,@#SIPAR0
6465 034124 013737 172240 172242      MOV      @#SIPAR0,@#SIPAR1
6466 034132 062737 000200 172242      ADD      @200,@#SIPAR1
6467 034140 013737 172240 172244      MOV      @#SIPAR0,@#SIPAR2
6468 034146 062737 000400 172244      ADD      @400,@#SIPAR2
6469 034154 013737 172356 172256      MOV      @#KIPAR7,@#SIPAR7
6470 034162 012737 000001 172572      MOV      @1,@#SR0      ;ENABLE MEM MGMT
6471 034170 012737 000060 172516      MOV      @60,@#SR3      ;SETUP SR3
6472 034176 110637 001503      MOV      SP,@#MMON      ;SET MEM MGMT ON IND = ON
6473 034202 005037 000006      RETRY:  CLR      @#ERRVEC+2
6474 034206 012737 036314 000004      MOV      @ENDMEM,@#ERRVEC ;SET TIME OUT TRAP VECTOR
6475 034214 012702 060000      MOV      @60000,R2      ;SETUP GENERAL REGISTERS
6476 034220 005000      CLR      R0      ;DATA WILL BE RELOCATED FROM
6477      ;ADDRESS IN R0 TO ADDRESS IN R2
6478 034222 012703 137776      MOV      @137776,R3      ;GET 12K WORDS TO RELOCATE
6479 034226 010013      MOV      R0,(R3)      ;TRAP TO ENDMEM IF INSUFFICIENT MEMORY
6480 034230 012737 053442 000004      MOV      @ERRPT,@#ERRVEC ;RESTORE ERROR TRAP VECTOR
6481 034236 000137 034436      JMP      @#IOMON
6482
6483      ;*****
6484      ;SBTTL RELOCATION ROUTINE
6485      ;THIS ROUTINE IS USED TO RELOCATE THE 9 SUBTESTS UP TO 28K.
6486      ;IF RELOCATION BY AN I/O DEVICE IS SELECTED, CONTROL IS PASSED
6487      ;TO THE I/O MONITOR.
6488      ;ENTER WITH:
6489      ;FRSTAD=PHYSICAL ADDRESS OF FIRST CODE
6490      ;FACTOR=NUMBER OF BYTES ABOVE BASE CODE
6491      ;R2 =LAST PHYSICAL ADDRESS OF THE SECTION
6492      ;EXIT TO I/O MONITOR WITH:
6493      ;OLDBASE=FIRST PHYSICAL ADDRESS TO BE RELOCATED
6494      ;NMBASL =FIRST PHYSICAL ADDRESS TO RELOCATE TO
6495      ;IOWC =TWO'S COMPLIMENT WORD COUNT
6496      ;*****
6496 034242 032737 000100 177570 RELOC: BIT      @SW6,@#SWR      ;IS RELOCATION DISABLED?
6497 034250 001067      BNE      EXITRE      ;BRANCH IF YES
6498 034252 105737 001503      TSTB     @#MMON      ;IS MEMORY MGMT ON?
6499 034256 001064      BNE      EXITRE      ;BRANCH IF YES
6500 034260 013700 001512      MOV      @#FRSTAD,R0      ;GET FIRST ADDRESS TO BE RELOCATED
6501 034264 010005      MOV      R0,R5
6502      ;LAST ADDRESS IS IN R2
6503 034266 010203      MOV      R2,R3      ;SAVE LAST ADDRESS
6504 034270 010204      MOV      R2,R4
6505 034272 160004      SUB      R0,R4      ;R4 NOW HAS BYTE COUNT
6506 034274 010437 001510      MOV      R4,@#FACTOR      ;SAVE BYTE COUNT
6507 034300 005737 001506      TST      @#FACTOR      ;FIRST RELOC IS TO ENDTAG+2
6508 034304 001004      BNE      IS      ;BRANCH IF NOT EXECUTING BASE CODE
6509 034306 010237 034434      MOV      R2,@#RETPC      ;SAVE RETURN PC TO NEXT SECTION
6510 034312 013702 001514      MOV      @#FRSTMEM,R2      ;GET FIRST ADDRESS TO RELOCATE TO
6511 034316 060204      IS:      ADD      R2,R4      ;R4 NOW CONTAINS LAST MEM ADDRESS
6512 034320 020437 001516      CMP      R4,@#LSTMEM      ;ENOUGH MEMORY?

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6514 034326 160204          SUB      R2,R4          ;R4 NOW HAS BYTE COUNT
6515 034330 005037 001506    CLR      2#FACTOR
6516 034334 032737 000400 177570    BIT      2#SWB,2#SWR   ;INHIBIT RELOC BY I/O DEVICE?
6517 034342 001014          BNE     RELNIO        ;BRANCH IF YES
6518 034344 010037 001540    MOV     R0,2#OLDBASE  ;SAVE START ADDRESS
6519 034350 010237 001542    MOV     R2,2#NWBASL   ;SAVE NEW BASE ADDRESS
6520 034354 005037 001544    CLR     2#NWBASH
6521 034360 006204          ASR     R4             ;MAKE IT A WORD COUNT
6522 034362 005404          NEG     R4             ;GET TWO'S COMPLIMENT
6523 034364 010437 001546    MOV     R4,2#IOWC     ;SAVE R4 AS WORDCOUNT
6524 034370 000167 000120    JMP     ENTER2        ;GO TO I/O MONITOR
6525
6526 034374 012022          ;RELOCATE BY CPU-MEMORY MANAGEMENT OFF
6527 034376 020003    RELNIO: MOV     (R0)+,(R2)+ ;RELOCATE CODE
6528 034400 001375    CMP     R0,R3         ;DONE YET?
6529 034402 004737 051746    BNE     RELNIO        ;BRANCH IF NO
6530 034406 102010    JSR     PC,2#CHKDAT   ;GO CHECK DATA
6531 034410 010037 001276    BVC     EXITRE
6532 034414 010237 001474    MOV     R0,2#STMP0    ;SAVE R0 FOR TYPEOUT
6533 034420 004737 051644    MOV     R2,2#VADR     ;SAVE R2
6534 034424 104006    JSR     PC,2#CNVADR   ;CONVERT R2 TO A PHYSICAL ADR
6535 034426 000401    ERROR  6
6536 034430 010207    EXITRE: MOV    R2,PC    ;GO EXECUTE RELOCATED CODE
6537 034432 011707    NOMEM: MOV    (PC),PC ;GO TO NEXT SECTION
6538 034434 000000    RETPC: .WORD  0      ;CONTAINS PC OF NEXT SECTION
6539
6540
6541
6542
6543
6544
6545
6546 034436 012737 034444 001212 10MON: MOV    2#1S,2#SLPERR ;SETUP ERROR LOOP
6547 034444 005037 001540    CLR     2#OLDBASE
6548 034450 013705 172346    MOV    2#KIPAR3,R5
6549 034454 005004    CLR     R4
6550 034456 073427 000006    ASHC   2#6,R4        ;SETUP R2 AND R3
6551 034462 010537 001542    MOV    R5,2#NWBASL   ;TO FORM 22 BIT ADDRESS
6552 034466 010437 001544    MOV    R4,2#NWBASH   ;FORM 22 BIT ADDRESS
6553 034472 032737 000400 177570    BIT    2#SWB,2#SWR   ;SAVE LOWER 16 BITS
6554 034500 001402    BEQ    2#S           ;SAVE UPPER 6 BITS
6555 034502 000167 001436    JMP    RELOCP        ;RELOCATE VIA I/O?
6556 034506 012737 174000 001546 2#S: MOV    2#174000,2#IOWC ;BRANCH IF YES
6557 034514 005037 001302    ENTER2: CLR    2#STMP2 ;GO RELOCATE VIA CP
6558 034520 012737 177776 001556    MOV    2#-2,2#RNTBINX ;SET WORD COUNT TO 2K
6559 034526 005037 001276    CLR    2#STMP0
6560 034532 005002    CLR    R2
6561 034534 032737 000040 177570 4#S: BIT    2#SW5,2#SWR   ;CLEAR LEGAL DEV FLAG
6562 034542 001416    BEQ    5#S           ;INHIBIT ROUND ROBIN?
6563 034544 005737 001276    TST    2#STMP0
6564 034550 001027    BNE    4#S         ;BRANCH IF NO
6565 034552 113737 177570 001552    MOVB   2#SWR,2#DEVINDX ;FLAG SET?
6566 034560 042737 177770 001552    BIC    2#177770,2#DEVINDX ;BRANCH IF YES
6567 034566 006337 001552    ASL    2#DEVINDX     ;GET DEVICE FROM SWITCHES
6568 034572 005237 001276    INC    2#STMP0       ;MASK LOWER 3 BITS
;ADJUST FOR WORD INDEX
;SET FLAG

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6570	034600	012705	000010		50\$:	MOV	#10,R5		:SET SOB COUNT
6571	034604	022737	000016	001552	40\$:	CMP	#16,#DEVINDX		:LAST DEVICE YET?
6572	034612	001003				BNE	42\$		:BRANCH IF NO
6573	034614	012737	177776	001552	48\$:	MOV	#-2,#DEVINDX		:INIT DEVICE INDEX
6574	034622	062737	000002	001552	42\$:	ADD	#2,#DEVINDX		:INCREMENT INDEX
6575	034630	013703	001552		43\$:	MOV	#DEVINDX,R3		:GET INDEX
6576	034634	012737	000401	001300		MOV	#401,#STMP1		:INIT UNIT MASK
6577	034642	012704	000010			MOV	#10,R4		:SET SOB COUNT
6578	034646	133763	001300	001606	44\$:	BITB	#STMP1,SYSSIZE(R3)		:IS THIS UNIT EXISTENT?
6579	034654	001406				BEQ	52\$		:BRANCH IF NO
6580	034656	005202				INC	R2		:SET LEGAL DEVICE FLAG
6581	034660	133763	001301	001607		BITB	#STMP1+1,SYSSIZE+1(R3)		:HAS IT BEEN USED?
6582	034666	001520				BEQ	11\$		:BRANCH IF NO
6583	034670	006337	001300		52\$:	ASL	#STMP1		:SELECT NEXT UNIT
6584	034674	077414				SOB	R4,44\$		:CONTINUE
6585	034676	005737	001276			TST	#STMP0		:INHIBIT ROUND ROBIN?
6586	034702	001013				BNE	45\$		:BRANCH IF YES
6587	034704	077541				SOB	R5,40\$		:CONTINUE
6588	034706	005702				TST	R2		:ANY DEVICES AT ALL?
6589	034710	001442				BEQ	46\$		:BRANCH IF NO
6590	034712	012704	000010			MOV	#10,R4		:SET SOB COUNT
6591	034716	012701	001607			MOV	#SYSSIZE+1,R1		:GET ADR OF SIZE TABLE
6592	034722	105021			47\$:	CLRB	(R1)+		:CLEAR ALL USED BITS
6593	034724	005201				INC	R1		:IN ALL DEVICES
6594	034726	077403				SOB	R4,47\$		:CONTINUE
6595	034730	000701				BR	41\$		
6596	034732	005702			45\$:	TST	R2		:HAS IT A LEGAL DEVICE?
6597	034734	001403				BEQ	49\$		:BRANCH IF NO
6598	034736	105063	001607			CLRB	SYSSIZE+1(R3)		:CLEAR ALL USED BITS THIS DEV
6599	034742	000732				BR	43\$		
6600	034744	010367	000016		49\$:	MOV	R3,60\$		
6601	034750	062767	053612	000010		ADD	#MSGINX,60\$		:GEN MESSAGE ADR
6602	034756	017767	000004	000002		MOV	#60\$,60\$		
6603	034764	104400				TYPE			
6604	034766	000000			60\$:	WORD			
6605	034770	104400	034776			TYPE			
6606	034774	000407				BR	65\$		:TYPE ASCIZ STRING
6607						BR	64\$		:GET OVER THE ASCIZ
6608	035014				::65\$:	.ASCIZ	/UNAVAILABLE/<CRLF>		
6609	035014	000637			64\$:				
6610	035016	105737	001504		46\$:	BR	ENTER2		
6611	035022	001016				TSTB	#OV		:ACT11?
6612	035024	005227	177777			BNE	51\$		:BRANCH IF YES
6613	035030	001013				INC	#-1		
6614	035032	104400	035040			BNE	51\$		
6615	035036	000410				TYPE	67\$		:TYPE ASCIZ STRING
6616					::67\$:	BR	66\$		:GET OVER THE ASCIZ
6617	035060				66\$:	.ASCIZ	?NO I/O DEVICES?<CRLF>		
6618	035060	105737	001503		51\$:	TSTB	#MMON		:MGMT ON?
6619	035064	001012				BNE	61\$		:BRANCH IF YES
6620	035066	013700	001540			MOV	#OLDBASE,R0		:RESTORE R0
6621	035072	013702	001542			MOV	#NMBASL,R2		:RESTORE R2
6622	035076	013703	001510			MOV	#SFACOR,R3		:GET RELOCATION FACTOR
6623	035102	060003				ADD	R0,R3		:FORM LAST ADDRESS
6624	035104	010005				MOV	R0,R5		:SETUP R5

6626	035112	012702	060000		61\$:	MOV	#60000,R2	:	SETUP REGISTERS
6627	035116	012703	137776			MOV	#137776,R3	:	WITH FROM
6628	035122	005000				CLR	R0	:	AND TOO ADDRESS
6629	035124	000137	036144			JMP	@RELOCP	:	RELOCATE VIA CP
6630	035130	105763	001676		11\$:	TSTB	RP3HSTAT(R3)	:	IS HANDLER BUSY?
6631	035134	100405				BMI	R5	:	BRANCH IF NO
6632	035136	005737	001276			TST	@STMP0	:	ROUND ROBIN?
6633	035142	001372				BNE	11\$	:	BRANCH IF NO
6634	035144	000167	177364			JMP	41\$		
6635	035150	005763	001676		8\$:	TST	RP3HSTAT(R3)	:	DID HANDLER FAIL?
6636	035154	100005				BPL	62\$	:	BRANCH IF NO
6637	035156	005737	001276			TST	@STMP0	:	ROUND ROBIN
6638	035162	001402				BEQ	62\$	:	BRANCH IF YES
6639	035164	000137	036124			JMP	@15\$		
6640	035170	153763	001301	001607	62\$:	BISB	@STMP1+1,SYSSIZE+1(R3)	:	SET UNIT USED BIT
6641	035176	005002				CLR	R2		
6642	035200	006037	001300		30\$:	ROR	@STMP1	:	ENCODE THE BIT POSITION
6643	035204	005202				INC	R2	:	INTO A UNIT NUMBER
6644	035206	103374				BCC	30\$		
6645	035210	005302				DEC	R2		
6646	035212	010237	001554			MOV	R2,@UNITNO	:	SAVE UNIT NUMBER
6647	035216	013763	001546	001716	10\$:	MOV	@10MC,RP3HMC(R3)	:	GIVE WORD COUNT TO HANDLER
6648	035224	010304				MOV	R3,R4		
6649	035226	072427	000003			ASH	#3,R4	:	ENCODE DEVICE FOR RUNTABLE
6650	035232	053704	001554			BIS	@UNITNO,R4	:	ENCODE UNIT NUMBER
6651	035236	006304				ASL	R4		
6652	035240	062737	000002	001556		ADD	#2,@RANTBINX	:	INCREMENT RUN TABLE INDEX
6653	035246	013702	001556			MOV	@RANTBINX,R2	:	GET RUN TABLE INDEX
6654	035252	110462	001627			MOVB	R4,RUNTB+1(R2)	:	ENTER DEV & UNIT IN TABLE
6655	035256	013763	001554	002012		MOV	@UNITNO,RP3UNIT(R3)	:	GIVE HANDLER UNIT NUMBER
6656	035264	012737	000240	000012		MOV	@PRS,@RESVEC+2	:	SETUP RESERVED VECTOR PSW
6657	035272	016337	002026	000010		MOV	RP3HANA(R3),@RESVEC	:	SETUP RESERVED VECTOR
6658	035300	006303				ASL	R3	:	ADJUST INDEX
6659	035302	013763	001540	001732		MOV	@OLDBASE,RP3OLD(R3)	:	GIVE HANDLER OLD BASE ADDRESS
6660	035310	013763	001542	001762		MOV	@NWBASL,RP3NWL(R3)	:	GIVE HANDLER
6661	035316	013763	001544	001764		MOV	@NWBASH,RP3NWH(R3)	:	NEW BASE ADDRESS
6662	035324	005063	001734			CLR	RP3OLD+2(R3)	:	ENSURE OLD BASE HIGH IS CLR
6663	035330	000010				CALL	HANDLER		
6664	035332	105737	001503			TSTB	@MMON	:	IS MEMORY MANAGEMENT ON?
6665	035336	001416				BEQ	13\$	:	BRANCH IF NO
6666	035340	022737	000012	001556		CMP	#12,@RANTBINX	:	TRANSFERED 12K YET?
6667	035344	001412				BEQ	13\$	:	BRANCH IF YES
6668	035350	062737	010000	001540		ADD	#10000,@OLDBASE	:	ADD 2K
6669	035356	062737	010000	001542		ADD	#10000,@NWBASL	:	TO BASE
6670	035364	005537	001544			ADC	@NWBASH	:	ADDRESSES
6671	035370	000137	034534			JMP	@41\$		
6672	035374	113705	001603		13\$:	MOVB	@LTICKS+1,R5	:	GET SECOND COUNT
6673	035400	062705	000002			ADD	#2,R5	:	INCREMENT BY TWO
6674	035404	162705	000074			SUB	#60.,R5	:	ENSURE RESULT IS 59 OR LESS
6675	035410	100002				BPL	31\$		
6676	035412	062705	000074			ADD	#60.,R5	:	COUNT WAS LESS THAN 58-RESTORE
6677	035416	012700	000010		31\$:	MOV	#10,R0	:	SET SOB COUNT
6678	035422	005002				CLR	R2		
6679	035424	005003				CLR	R3		
6680	035426	005004				CLR	R4		

6682 035434 005504  
6683 035436 062702 000002  
6684 035442 077006  
6685 035444 006103  
6686 035446 005504  
6687 035450 022703 004000  
6688 035454 001406  
6689 035456 123705 001603  
6690 035462 001355  
6691 035464 104015  
6692 035466 000177 143520  
6693 035472 005704 32\$:  
6694 035474 001402  
6695 035476 000167 000422  
6696 035502 105737 001503 82\$:  
6697 035506 001012  
6698 035510 013705 001540  
6699 035514 010500  
6700 035516 063700 001510  
6701  
6702 035522 013702 001542  
6703 035526 063702 001510  
6704  
6705 035532 000406  
6706 035534 012700 057776 25\$:  
6707 035540 012702 137776  
6708 035544 012705 002100  
6709 035550 004737 051746 22\$:  
6710 035554 102413  
6711 035556 105737 001204  
6712 035562 001002  
6713 035564 000167 000412  
6714 035570 032737 001000 177570 83\$:  
6715 035576 001473  
6716 035600 000167 000244  
6717 035604 005001 81\$:  
6718 035606 010037 001276  
6719 035612 010237 001474  
6720 035616 010203  
6721 035620 005004  
6722 035622 105737 001503  
6723 035626 001406  
6724 035630 162703 010000 17\$:  
6725 035634 100403  
6726 035636 062704 000002  
6727 035642 000772  
6728  
6729 035644 116404 001627  
6730 035650 042704 177400  
6731 035654 006004  
6732 035656 005005  
6733 035660 073427 177775  
6734 035664 010500  
6735 035666 072027 177764  
6736 035672 042700 177770

ADC R4  
ADD #2,R2  
SOB R0,14\$  
ROL R3  
ADC R4  
CMP #4000,R3  
BEQ 32\$  
CMPB @#LTICKS+1,R5  
BNE 31\$  
ERROR 15  
JMP @#LPERF  
TST R4  
BEQ 82\$  
JMP 15\$  
TSTB @#MMON  
BNE 25\$  
MOV @#OLDBASE,R5  
MOV R5,R0  
ADD @#\$FACTOR,R0  
MOV @#NWBASL,R2  
ADD @#\$FACTOR,R2  
BR 22\$  
MOV #57776,R0 25\$:  
MOV #137776,R2  
MOV @2100,R5  
JSR PC,@#CHKDAT 22\$:  
BVS 81\$  
TSTB @#SERFLG  
BNE 83\$  
JMP EXIT  
BIT #SW9,@#SWR 83\$:  
BEQ 100\$+2  
JMP 20\$  
CLR R1 81\$:  
MOV R0,@#STMPD  
MOV R2,@#VAOR  
MOV R2,R3  
CLR R4  
TSTB @#MMON  
BEQ 16\$  
SUB #10000,R3 17\$:  
BMI 16\$  
ADD #2,R4  
BR 17\$  
;R4 NOW CONTAINS INDEX OF ERROR FOR RUN  
16\$: MOVB RUNTBL+1(R4),R4  
BIC #177400,R4  
ROR R4  
CLR R5  
ASHC #-3,R4  
MOV R5,R0  
ASH #-14,R0  
BIC #177770,R0

;STATUS WORDS. WHEN ALL  
;TRANSFERS ARE FINISHED  
;RESULT WILL BE 2000  
; (WITHOUT ROTATE)  
;  
; ALL DONE?  
; BRANCH IF YES  
; TWO SECONDS ELAPSED YET?  
; BRANCH IF NO  
; DEVICE HUNG  
; RESTART RELOCATION  
; ANY DEVICE ERRORS?  
; BRANCH IF NO  
; ERROR  
; MEM MGMT ON?  
; BRANCH IF YES  
; SETUP R5 FOR DATA CHECK  
;  
; GET LAST ADDRESS  
; OF GOOD DATA  
;  
; GET LAST ADDRESS  
; OF DATA TO BE CHECKED  
; CONTINUE  
; GET LAST ADR OF GOOD DATA  
; GET LAST ADR OF DATA TO BE CHECKED  
; DON'T CHECK FIRST 2100 LOCATIONS  
; GO CHECK DATA  
; BRANCH IF ERROR  
; ANY ERRORS?  
; BRANCH IF YES  
; RETURN  
; LOOP ON ERROR?  
; BRANCH IF NO  
; GO DO FUNCTION AGAIN  
;  
; SAVE ERROR ADDRESS  
;  
; IS MEM MGMT ON?  
; BRANCH IF NO  
; SUBTRACT 2K FROM ERROR ADDRESS  
; BRANCH IF BLOCK IS FOUND  
; COUNT ONE MORE BLOCK  
; CONTINUE  
; TIME TABLE  
; GET DEVICE THAT FAILED  
; ENSURE HIGH BYTE CLEAR  
; THROW AWAY LSB  
; ENSURE R5 CLEAR  
; GET UNIT NUMBER IN R5

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6738 035702 010403          MOV      R4,R3          ;AND DEVICE INDEX IN R4 & R3
6739 035704 010337 001302    MOV      R3,@STMP2
6740 035710 012737 000001 001300    MOV      #1,@STMP1          ;ENCODE 3 BIT UNIT NO INTO
6741 035716 162705 020000    19$:    SUB      #20000,R5          ;ONE BIT IN THE LOW BYTE OF STMP1
6742 035722 103403          BCS     18$              ;BRANCH IF DONE
6743 035724 006137 001300    ROL     @STMP1          ;SELECT NEXT UNIT
6744 035730 000772          BR      19$              ;CONTINUE
6745 035732 012737 036050 001212 18$:    MOV      #20$,@SLPERR      ;SETUP LOOP RETURN
6746 035740 005701          TST     R1              ;DEVICE ERROR?
6747 035742 001010          BNE     100$            ;BRANCH IF YES
6748 035744 104010          ERROR   10              ;DATA CHECK ERROR
6749 035746 105737 001503    TSTB   @MMON           ;MGMT ON?
6750 035752 001002          BNE     70$              ;BRANCH IF YES
6751 035754 000137 034514    71$:    JMP     @ENTER2
6752 035760 000137 034436    70$:    JMP     @IOMON
6753 035764 104007          100$:   ERROR   7
6754 035766 042763 100000 001676    BIC     #BIT15,RP3HSTAT(R3) ;CLEAR THE ERROR
6755 035774 022703 000002    CMP     #2,R3          ;RK05 ERROR?
6756 036000 002405          BLT     90$              ;BRANCH IF R470
6757 036002 003016          BGT     92$              ;BRANCH IF RPO3
6758 036004 112777 000001 144114    MOVB   #1,@RKCS        ;RK CONTROLLER CLEAR
6759 036012 000412          BR      92$
6760 036014 022703 000012          90$:    CMP     #12,R3
6761 036020 001004          BNE     91$              ;RS04?
6762 036022 052777 000040 144164    BIS    #BIT5,@RSCS2    ;BRANCH IF NO
6763 036030 000403          BR      92$              ;CLEAR RS CONTROLLER
6764 036032 052777 000040 144114 91$:    BIS    #BIT5,@RP4CS2
6765 036040 105737 001503 92$:    TSTB   @MMON           ;CLEAR RPO4 CONTROLLER
6766 036044 001345          BNE     70$              ;MGMT ON?
6767 036046 000742          BR      71$              ;BRANCH IF YES
6768 036050 052763 000400 001676 20$:    BIS    #BIT8,RP3HSTAT(R3) ;SET REPEAT FLAG IN HANDLER
6769 036056 016337 002026 000010    MOV     RP3HANA(R3),@RESVEC ;SETUP RESERVED INSTRUCTION VECTOR
6770 036064 000010          CALLHANDLER
6771 036066 105763 001676          21$:    TSTB   RP3HSTAT(R3)    ;HANDLER FINISHED?
6772 036072 100375          BPL     21$              ;BRANCH IF NO
6773 036074 005763 001676    TST    RP3HSTAT(R3)    ;ANY ERROR?
6774 036100 100714          BMI     18$              ;BRANCH IF YES
6775 036102 005701          TST    R1              ;DEVICE ERROR?
6776 036104 001002          BNE     80$              ;BRANCH IF YES
6777 036106 000167 177364    JMP     32$+4            ;GO CHECK DATA
6778 036112 032737 001000 177570 80$:    BIT    #BIT9,@SWR      ;STILL LOOPING?
6779 036120 001353          BNE     20$              ;BRANCH IF YES
6780 036122 000721          BR      100$+2          ;CONTINUE TEST
6781                                     ;ON RELOCATION
6782 036124 005004          15$:    CLR     R4              ;SET INDEX
6783 036126 010601          MOV     SP,R1
6784 036130 005764 001642    24$:    TST    RUNTRAK(R4)    ;SEARCH FOR DEVICE ERROR
6785 036134 100643          BMI     16$              ;BRANCH IF ERROR
6786 036136 062704 000002    ADD    #2,R4            ;INCREMENT INDEX
6787 036142 000772          BR      24$              ;CONTINUE SEARCH
6788                                     ;RELOCATE BY CPU-MEMORY MANAGEMENT ON
6789 036144 012022    RELOCP: MOV (R0)+,(R2)+ ;RELOCATE CODE
6790 036146 020302          CMP    R3,R2            ;DONE YET?
6791 036150 001375          BNE    RELOCP           ;BRANCH IF NO
6792 036152 012705 001700    MOV    #1700,R5

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6794 036162 102007
6795 036164 010037 001276
6796 036170 010237 001474
6797 036174 104006
6798 036176 000167 176000
6799 036202 105737 001503
6800 036206 001002
6801 036210 000137 034430
6802 036214 062737 000077 001520
6803 036222 013737 172346 172340
6804 036230 013737 172350 172342
6805 036236 013737 172352 172344
6806
6807
6808
6809
6810 036244 013700 172340
6811 036250 072027 177771
6812 036254 110037 001203
6813 036260 012706 001200
6814 036264 005037 177776
6815 036270 016746 175356
6816 036274 012746 005372
6817 036300 105737 001502
6818 036304 001402
6819 036306 012716 033614
6820 036312 000002
6821
6822
6823 036314 022626
6824 036316 005037 177572
6825 036322 042737 000020 172516
6826
6827
6828
6829
6830 036330 012737 000600 001520
6831 036336 005737 003244
6832 036342 001403
6833 036344 012737 001600 001520
6834 036352 105037 001503
6835
6836
6837
6838
6839
6840
6841 036356
6842 036356 012737 053442 000004
6843 036364 005037 177776
6844 036370 004767 013744
6845 036374 012706 001200
6846 036400 032777 000100 142636
6847 036406 001374
6848 036410 105237 001532

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EXIT: BVC EXIT
      MOV R0, @STMP0
      MOV R2, @VADR
      ERROR 6
      JMP RETRY
      TSTB @MMON ; MEM MGMT ON?
      BNE +6 ; BRANCH IF YES
      JMP @EXITRE
      ADD @77, @NEXPAR ; SET VALUE FOR NEXT RELOCATION
      MOV @KIPAR3, @KIPAR0
      MOV @KIPAR4, @KIPAR1
      MOV @KIPAR5, @KIPAR2
*****
: PROGRAM IS NOW EXECUTING IN KERNEL MODE RELOCATED TO ADDRESS AS SPEC-
: IFIED IN KIPAR0. FOR EX. IF KIPAR0=1600 THEN PROGRAM EXECUTING AT
: ADDRESS 16000+(PC)
      MOV @KIPAR0, R0 ; GET PAR0
      ASH @-7, R0 ; GET BITS <14:7> IN LOW BYTE
      MOVB R0, @STSTNM+1 ; PUT IN DSPLY REG HIGH BYTE
      MOV @KERSTK, SP ; SET KERNEL STACK PTR
      CLR @PSW
      MOV OLDPSW, -(SP) ; RESTORE OLD PSW
      MOV @LOOP, -(SP)
      TSTB @NEXEC ; BRANCH IF TEST CODE TO
      BEQ 1$ ; BE EXECUTED
      MOV @STMM, (SP)
1$: RTI ; RESTART PROGRAM AT LOOP

: WHEN RELOCATION ABOVE 28K IS COMPLETE PROGRAM TRAPS TO ENDMEM.
ENDMEM: CMP (SP)+, (SP)+ ; POP STACK TWICE
ENDM: CLR @SRO ; DISABLE MEM MGMT
      BIC @BIT4, @MMR3 ; CLEAR 22 BIT MODE
*****
: AT THIS TIME A 'SUB-PASS' HAS BEEN COMPLETED.
: PROGRAM NOW EXECUTING IN KERNEL MODE AT PC AS SHOWN (NO RELOCATION)
      MOV @600, @NEXPAR ; RESET NEXT VALUE FOR PAR REGISTERS
      TST @PROT
      BEQ 2$
      MOV @1600, @NEXPAR
2$: CLRB @MMON ; SET MEM MGMT ON IND = OFF
*****
: SBTTL END OF SUB-PASS ROUTINE
: * THIS ROUTINE SETSUP THE PSW AND MAINTENANCE REGISTERS
: * FOR THE NEXT SUB-PASS. IT THEN STARTS THE PRINTER
: * (IF NOT ON ACT11) FOR TYPING THE END OF SUB-PASS MESSAGE.
: *****
END:
END1: MOV @ERPRT, @ERRVEC
      CLR @PSW ; CLEAR MODE BITS IN PSW
      JSR PC, CLRTBIT ; GO CLEAR 'T' BIT IF SET
      MOV @KERSTK, SP ; SET KERNEL STACK PTR
      BIT @100, @STPS ; CHECK IF OUTPUT DEVICE IS BUSY
      BNE -6 ; IS AVAILABLE
1$: INCB @SUBPASS

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6850 036420 162702 000060      SUB      #60,R2
6851 036424 022702 000006      CMP      #6,R2
6852 036430 001013      BNE     2$ ;BRANCH :END OF TEST?
6853 036432 012737 000060 001532      MOV     #60,2#SUBPASS ;INIT SUBPASS COUNT TO ASCII 0
6854 036440 005037 177750      CLR     2#MAINT ;CLEAR MAINTENANCE REG
6855 036444 005037 001604      CLR     2#SMAINT ;CLEAR SOFTWARE VALUE
6856 036450 005046      CLR
6857 036452 012746 036560      MOV     #SEOP,-(SP)
6858 036456 000002      RTI
6859 036460 006302      2$: ASL     R2
6860 036462 012737 001472 000014      MOV     #SRTN,2#TBITVEC ;SET 'T' TRAP VECTOR
6861 036470 012737 001531 001270      MOV     #SUBPASS-1,2#$REGS
6862 036476 106277 142542      ASRB   2#STPS
6863 036502 016246 053536      MOV     PSWTAB(2),-(SP) ;PUSH NEXT PASS PSW ON STACK
6864 036506 012746 005372      MOV     #LOOP,-(SP) ;RESART PROGRAM AT LOOP
6865 036512 016237 053552 001604      MOV     MRGTAB(R2),2#SMAINT
6866 036520 016237 053552 177750      MOV     MRGTAB(R2),2#MAINT
6867 036526 105737 001504      3$: TSTB  2#OV ;OV PASS?
6868 036532 001011      BNE     RTI1 ;BRANCH IF YES
6869 036534 122777 000200 142502      CMPB   #200,2#STPS ;IS PRINTER READY?
6870 036542 001371      BNE     3$ ;BRANCH IF NO
6871 036544 012737 054602 001270      MOV     #MSG20-1,2#$REGS
6872 036552 106277 142466      ASRB   2#STPS ;TYPE END SUBPASS MESSAGE
6873 036556 000002      RTI1: RTI ;RESTART PROGRAM AT LOOP WITH NEW PSW
6874 ;(FROM TABLE BELOW)
6875
6876 ;:*****
6877
6878 .SBTTL END OF PASS ROUTINE
6879
6880 ;*INCREMENT THE PASS NUMBER ($PASS)
6881 ;*TYPE "END PASS #XXXXX TOTAL NUMBER OF ERRORS SINCE LAST REPORT YYYY"
6882 ;*WHERE XXXXX AND YYYY ARE DECIMAL NUMBERS
6883 ;*IF THERES A MONITOR GO TO IT
6884 ;*IF THERE ISN'T JUMP TO LOOP
6885
6886 SEOP: 036560
6887 036560 004737 046660      JSR     PC,2#TYPTIME
6888 036564 005067 142412      CLR     $STNM ;:ZERO THE TEST NUMBER
6889 036570 005067 142522      CLR     $TIMES ;:ZERO THE NUMBER OF ITERATIONS
6890 036574 005267 142400      INC     $PASS ;:INCREMENT THE PASS NUMBER
6891 036600 042767 100000 142372      BIC     #100000,$PASS ;:DON'T ALLOW A NEG. NUMBER
6892 036606 005327      DEC     (PC)+ ;:LOOP?
6893 036610 000001      SEOPCT: .WORD 1
6894 036612 003063      BGT     $DOAGN ;:YES
6895 036614 012737      MOV     (PC)+,2(PC)+ ;:RESTORE COUNTER
6896 036616 000001      SENDCT: .WORD 1
6897 036620 036610      SEOPCT
6898 036622 104400 036630      TYPE   65$ ;:TYPE ASCIZ STRING
6899 036626 000407      BR     64$ ;:GET OVER THE ASCIZ
6900 ;:65$: .ASCIZ <12><15>/END PASS #/
6901 036646
6902 036646 016746 142326      64$: MOV     $PASS,-(SP) ;:SAVE $PASS FOR TYPEOUT
6903 ;:TYPE PASS NUMBER
6904 036652 104410      TYPDS ;:GO TYPE--DECIMAL ASCII WITH SIGN

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6906 036660 000421          BR      66$          ;;GET OVER THE ASCIZ
6907          ;;67$: .ASCIZ / TOTAL ERRORS SINCE LAST REPORT /
6908 036724          66$:          ;;SAVE SERTTL FOR TYPEOUT
6909 036724 016746 142264    MOV     SERTTL,-(SP)  ;;TOTAL NUMBER OF ERRORS
6910          TYPDS          ;;GO TYPE--DECIMAL ASCII WITH SIGN
6911 036730 104410          TYPE          ;;TYPE CARRIAGE RETURN, LINE FEED
6912 036732 104400 001327    CLR     SERTTL      ;;CLEAR ERROR TOTAL
6913 036736 005067 142252    SGET42: MOV    J#42,RO  ;;GET MONITOR ADDRESS
6914 036742 013700 000042    BEQ    SDOAGN       ;;BRANCH IF NO MONITOR
6915 036746 001405          RESET          ;;CLEAR THE WORLD
6916 036750 000005          SENDAD: JSR   PC,(RO)  ;;GO TO MONITOR
6917 036752 004710          NOP           ;;SAVE ROOM
6918 036754 000240          NOP           ;;FOR
6919 036756 000240          NOP           ;;ACT11
6920 036760 000240          SDOAGN:      ;;
6921 036762          SDOAGN:      ;;
6922 036762 000137 005372    JMP     J#LOOP      ;;RETURN
6923 036766          377 377 000 SNULL: .BYTE  -1,-1,0  ;;NULL CHARACTER STRING
6924          036772          .EVEN
6925          ;;*****
6926          .SBTTL  RP11/RP03 HANDLER
6927          ;;*   SEE DOCUMENTATION FOR FUNCTIONAL DESCRIPTION OF HANDLER
6928          ;;*****
6929 036772 104412          RP3DRV: SAVREG
6930 036774 105037 001676    CLRB   J#RP3HSTA   ;;CLEAR DONE FLAG
6931 037000 032737 000400 001676    BIT    #BITS,J#RP3HSTA  ;;REPEAT FLAG SET?
6932 037006 001403          BEQ    8$          ;;BRANCH IF NO
6933 037010 104414          RESREG
6934 037012 000137 040644    JMP     J#RP3RPT
6935 037016 013737 001556 001566 8$:  MOV    J#RNTBINX,J#RP311  ;;SAVE RUN TABLE INDEX
6936 037024 032737 000020 177570    BIT    #SW4,J#SWR    ;;INHIBIT RND DSK ADR?
6937 037032 001403          BEQ    1$          ;;BRANCH IF NO
6938 037034 005000          CLR    RO
6939 037036 005001          CLR    R1
6940 037040 000410          BR     4$
6941 037042 004737 050604 1$:  JSR   PC,J#SRAND    ;;GO GET RANDOM NUMBER
6942 037046 013700 001524          MOV    J#$HINUM,RO  ;;GET HI NUMBER
6943 037052 013701 001522          MOV    J#$LONUM,R1  ;;GET LO NUMBER
6944 037056 073027 177771          ASHC  #-7,RO        ;;ADJUST TO FORM CYL ADR
6945 037062 042700 177000 4$:  BIC   #177000,RO    ;;GET RID OF UNUSED BITS
6946 037066 022700 000624          CMP    #624,RO      ;;LEGAL CYL?
6947 037072 100003          BFL   5$          ;;BRANCH IF YES
6948 037074 062700 000624          ADD   #624,RO      ;;MAKE IT LEGAL
6949 037100 000770          BR     4$
6950 037102 013702 001566 5$:  MOV    J#RP311,R2    ;;GET RUN TABLE INDEX
6951 037106 016203 001626          MOV    RUNTBL(R2),R3  ;;GET DEVICE ID
6952 037112 042703 000777          BIC   #777,R3      ;;ID ONLY
6953 037116 050300          BIS   R3,RO        ;;COMBINE WITH CYL ADR
6954 037120 010062 001626          MOV    RO,RUNTBL(R2)  ;;PUT BACK IN TABLE
6955 037124 072127 177775          ASH   #-3,R1       ;;GEN TRK-SECT ADR
6956 037130 010103          MOV    R1,R3       ;;SAVE
6957 037132 042701 160377 6$:  BIC   #160377,R1    ;;GET RID OF ALL BUT TRK
6958 037136 022701 011400          CMP   #11400,R1    ;;LEGAL TRAK?
6959 037142 100003          BPL   2$          ;;BRANCH IF YES
6960 037144 062701 011400          ADD   #11400,R1    ;;MAKE IT LEGAL

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6962 037152 042703 177760      2$:    BIC      #177760,R3      ;GET SECTOR ADR
6963 037156 022703 000011      CMP      #11,R3      ;IS IT LEGAL?
6964 037162 100003          BPL      3$          ;BRANCH IF YES
6965 037164 062703 000011      ADD      #11,R3      ;MAKE IT LEGAL
6966 037170 000770          BR       2$
6967 037172 050301          3$:    BIS      R3,R1      ;COMBINE TRK-SECT
6968 037174 010162 001642      MOV      R1,RUNTRAK(R2) ;PUT IN TABLE
6969 037200 010037 002044      MOV      RO,@#RP3HDC    ;SAVE DESIRED CYL
6970 037204 010137 002112      MOV      R1,@#RP3DA    ;SAVE DSK ADR
6971 037210 112737 177775 002072  MOVB     #-3,@#RP3TRY   ;INIT TRY COUNT
6972 037216 032737 000040 172516  BIT      #BITS,@#MMR3   ;MAP ON?
6973 037224 001405          BEQ      7$          ;BRANCH IF NO
6974 037226 005046          CLR      -(SP)        ;PUT DEVICE ID ON STACK
6975 037230 013746 001732      MOV      @#RP3OLD,-(SP) ;PUT ADR OF BUS ADR ON STK
6976 037234 004737 052054          JSR      PC,@#GETMAP   ;GET MAP REGISTER
6977 037240 012737 000103 001564  7$:    MOV      #103,@#RP310  ;GET FUNCTION
6978 037246 013700 001734      MOV      @#RP3OLD+2,RO ;GET BAE BITS
6979 037252 072027 000004      ASH     #4,RO         ;SHIFT TO BITS 4 & 5
6980 037256 050037 001564      BIS      RO,@#RP310    ;COMBINE WITH FUNCTION
6981 037262 010037 001734      MOV      RO,@#RP3OLD+2
6982 037266 013700 002012      MOV      @#RP3UNIT,RO
6983 037272 072027 000010      ASH     #10,RO        ;SHIFT UNIT NO TO RIGHT BITS
6984 037276 050037 001564      BIS      RO,@#RP310    ;COMBINE WITH FUNC & BAE
6985 037302 010037 002012      MOV      RO,@#RP3UNIT
6986 037306 104414          RESREG
6987 037310 005777 142564      RP3WTRY:TST @#RP3DS      ;IS DRIVE READY?
6988 037314 100375          BPL     RP3WTRY      ;BRANCH IF NO
6989 037316 053777 002012 142560  BIS     @#RP3UNIT,@#RP3CS ;SET UNIT BITS
6990 037324 004737 037356          JSR     PC,@#LDRP3   ;LOAD RP3 REGISTERS
6991 037330 012777 040674 142560  MOV     @#RP3SRV,@#RP3VEC ;SET VECTOR
6992 037336 005077 142556          CLR     @#RP3PSW
6993 037342 005037 002060          CLR     @#RP3FUN
6994 037346 013777 001564 142530  MOV     @#RP310,@#RP3CS ;SET FUNCTION TO WRITE
6995 037354 000002          RTI
6996 037356 013777 002042 142526  LDRP3: MOV @#RP3HDA,@#RP3DA ;LOAD DSK ADR
6997 037364 013777 002044 142522  MOV @#RP3HDC,@#RP3DC ;LOAD CYL ADR
6998 037372 013777 001716 142506  MOV @#RP3HWC,@#RP3WC ;LOAD WORD COUNT
6999 037400 013777 001732 142502  MOV @#RP3OLD,@#RP3BA ;LOAD BUS ADR
7000 037406 000207          RTS      PC          ;RETURN
7001
7002
7003
7004
7005
7006 037410 104412          :*****
7007 037412 105037 001700          :SBTTL RK11/RK05 HANDLER
7008 037416 032737 000400 001700  :*      SEE DOCUMENTATION FOR FUNCTIONAL DESCRIPTION OF HANDLER
7009 037424 001403          :*****
7010 037426 104414          RKDRV: SAVREG
7011 037430 000137 041462          CLR     @#RKHSTAT      ;CLEAR DONE FLAG IN HANDLER STAT
7012 037434 013737 001556 001572  5$:    BIT     #BITS,@#RKHSTAT ;REPEAT FLAG SET?
7013 037442 105037 001700          BEQ     5$            ;BRANCH IF NO
7014 037446 032737 000020 177570  RESREG
7015 037454 001403          JMP     @#RKAPT
7016 037456 005000          MOV     @#RNTBINX,@#RK11 ;SAVE RUN TABLE INDEX
          CLR     @#RKHSTAT   ;CLEAR DONE FLAG IN HANDLER STAT
          BIT     #SW4,@#SWR  ;RANDOM DSK ADDRESS?
          BEQ     6$          ;BRANCH IF YES
          CLR     RO         ;CLEAR REGISTERS

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7018	037462	000404			BR	7\$		: FOR ADDRESS CHECKING
7019	037464	004737	050604	6\$:	JSR	PC, 2#\$RAND		: GET RANDOM NUMBER
7020	037470	013700	001524		MOV	2#\$HINUM, R0		: GET HIGH NUMBER
7021	037474	072027	177775	7\$:	ASH	#-3, R0		: ADJUST TO FORM
7022								: CYLINDER ADDRESS
7023	037500	010001			MOV	R0, R1		: SAVE IN R1
7024	037502	042701	160037	4\$:	BIC	#160037, R1		: GET RID OF SURF-SECT BITS
7025	037506	022701	014300		CMP	#14300, R1		: IS IT A LEGAL CYLINDER?
7026	037512	100003			BPL	3\$		: BRANCH IF YES
7027	037514	062701	014340		ADD	#14340, R1		: ADD MAXIMUM CYLINDER
7028	037520	000770			BR	4\$		: TRY AGAIN
7029	037522	072127	177773	3\$:	ASH	#-5, R1		: ADJUST CYLINDER ADDRESS

K12

7031 037532 016203 001626  
7032 037536 042703 000777  
7033 037542 050103  
7034 037544 010362 001626  
7035 037550 072027 177770  
7036 037554 042700 177740  
7037 037560 010003

MOV RUNTBL(R2),R3  
BIC #777,R3  
BIS R1,R3  
MOV R3,RUNTBL(R2)  
ASH #-10,R0  
BIC #177740,R0  
MOV R0,R3

:GET RUN TABLE ENTRY  
:SAVE ID AND UNIT NO.  
:INSERT CYLINDER ADDR  
:ENTER CYLINDER ADR IN RUN TABLE  
:GENER SECTOR-SURF ADDRESS  
:GET RID OF EXTRA BITS  
:SAVE

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7039 037566 022700 000012          CMP      #12,RO          ; IS SECTOR ADDRESS LEGAL?
7040 037572 100004          BPL      1$             ; BRANCH IF YES
7041 037574 062700 000012          ADD      #12,RO        ; MAKE IT LEGAL
7042 037600 042700 000020          BIC      #BIT4,RO      ; GET RID OF CARRY FROM ADD
7043 037604 042703 000017          BIC      #17,R3        ; GET SURFACE ADDRESS
7044 037610 050300          BIS      R3,RO         ; GENER COMP SECT-SURF ADDRESS
7045 037612 010062 001642          MOV      RO,RUNTRAK(R2); SAVE IN RUN TRAK TABLE
7046 037616 072127 000005          ASH      #5,R1         ; ADJUST CYLINDER ADDRESS
7047 037622 050100          BIS      R1,RO         ; CONCATINATE TRK & SECT ADDR
7048 037624 013701 002014          MOV      @#RKUNIT,R1  ; GET UNIT NUMBER
7049 037630 072127 000015          ASH      #15,R1        ; ADJUST
7050 037634 050100          BIS      R1,RO         ; CONCATINATE UNIT,TRK,SURF,SECT
7051 037636 010037 002046          MOV      RO,@#RKHDA    ; SAVE
7052 037642 112737 177775 002073  MOVB     #-3,@#RKTRY   ; SET RETRY COUNT
7053 037650 032737 000040 172516  BIT      #BITS,@#MMR3  ; MAP ON?
7054 037656 001406          BEQ      2$             ; BRANCH IF NO
7055 037660 012746 000001          MOV      #1,-(SP)      ; PUT DEVICE ID ON STACK
7056 037664 012746 001736          MOV      @#RKOLD,-(SP); PUT ADDRESS OF ADR ON STACK
7057 037670 004737 052054          JSR      PC,@#GETMAP   ; GET MAP REG
7058 037674 012767 000103 141666 2$:      MOV      #103,RK10    ; SET FUNCTION
7059 037702 013700 001740          MOV      @#RKOLD+2,RO  ; GET BA EXTENDED
7060 037706 072027 000004          ASH      #4,RO         ; ADJUST
7061 037712 050037 001570          BIS      RO,@#RK10    ; PUT IN WITH FUNCTION
7062 037716 010037 001740          MOV      RO,@#RKOLD+2 ; SAVE IN MEMORY
7063 037722 104414          RESREG
7064 037724 013777 002046 142202  RKWTRY: MOV     @#RKHDA,@#RKDA ; LOAD DISK ADDRESS
7065 037732 032777 000100 142162  BIT      #BIT6,@#RKDS ; UNIT READY?
7066 037740 001774          BEQ      -6            ; BRANCH IF NO
7067 037742 013777 001720 142160  MOV      @#RKHWC,@#RKWC ; LOAD WORD COUNT
7068 037750 013777 001736 142154  MOV      @#RKOLD,@#RKBA ; LOAD BUS ADDRESS
7069 037756 012777 041512 142152  MOV      @#RKSrv,@#RKVEC ; LOAD INTERRUPT VECTOR
7070 037764 005077 142150          CLR      @#RKPSW
7071 037770 005037 002062          CLR      @#RKFUN
7072 037774 013777 001570 142124  MOV      @#RK10,@#RKCS ; SET FUNCTION TO WRITE
7073 040002 000006          RTT                    ; LOAD FUNCTION AND GO
7074                                     ; RETURN
7075                                     ; *****
7076 .SBTTL  RH70/RP04 HANDLER
7077 .*      SEE DOCUMENTATION FOR FUNCTIONAL DESCRIPTION OF HANDLER
7078 ; *****
7079 RP4DRV: SAVREG
7080 040006 105037 001706          CLR      @#RP4HSTA     ; CLEAR DONE FLAG
7081 040012 032737 000400 001706  BIT      #BIT8,@#RP4HST ; REPEAT FLAG SET?
7082 040020 001403          BEQ      6$            ; BRANCH IF NO
7083 040022 104414          RESREG
7084 040024 000137 042340          JMP      @#RP4RPT
7085 040030 013737 001556 001574 6$:      MOV      @#RNTBINX,@#RP411 ; SAVE RUN TABLE INDEX
7086 040036 105037 001706          CLR      @#RP4HSTA     ; CLEAR DONE FLAG
7087 040042 032737 000020 177570  BIT      #SW4,@#SWR    ; RANDOM DSK ADDRESS?
7088 040050 001403          BEQ      1$            ; BRANCH IF YES
7089 040052 005000          CLR      RO
7090 040054 005001          CLR      R1
7091 040056 000410          BR      4$
7092 040060 004737 050604          JSR      PC,@#SRAND    ; GET RANDOM NUMBER
7093 040064 013700 001524          MOV      @#$HINUM,RO  ; GET HI NUMBER

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7095 040074 073027 177771          ASHC      #-7,R0          ;ADJUST TO FORM CYL. ADR.
7096 040100 042700 177000          BIC      #177000,R0    ;GET RID OF UNUSED BITS
7097 040104 022700 000631          CMP      #631,R0      ;LEGAL CYLINDER
7098 040110 100003 000000          BPL      5$           ;BRANCH IF YES
7099 040112 062700 000631          ADD      #631,R0      ;MAKE IT LEGAL
7100 040116 000770 000000          BR       4$

7101
7102 040120 013702 001574          5$:      MOV      @#RP411,R2    ;GET RUN TABLE INDEX
7103 040124 016203 001626          MOV      RUNTBL(R2),R3 ;GET DEVICE ID
7104 040130 042703 000777          BIC      #777,R3      ;SAVE ID ONLY
7105 040134 050003 000000          BIS      R0,R3        ;COMBINE WITH CYL ADR
7106 040136 010362 001626          MOV      R3,RUNTBL(R2) ;PUT IN RUN TABLE
7107 040142 072127 177775          ASH      #-3,R1       ;GEN TRAK-SECT ADR
7108 040146 042701 160340          BIC      #160340,R1    ;GET RID OF UNUSED BITS
7109 040152 010103 000000          MOV      R1,R3        ;SAVE
7110 040154 042701 000037          BIC      #37,R1       ;GET RID OF SECT BITS
7111 040160 022701 011000          CMP      #11000,R1    ;LEGAL TRAK?
7112 040164 100004 000000          BPL      2$           ;BRANCH IF YES
7113 040166 062701 011000          ADD      #11000,R1    ;MAKE IT LEGAL
7114 040172 042701 020000          BIC      #BIT13,R1    ;GET RID OF ADD CARRY
7115 040176 042703 177740          2$:      BIC      #177740,R3    ;GET SECTOR ADR
7116 040202 022703 000025          CMP      #25,R3      ;LEGAL SECTOR
7117 040206 100004 000000          BPL      3$           ;BRANCH IF YES
7118 040210 062703 000025          ADD      #25,R3       ;MAKE IT LEGAL
7119 040214 042703 000040          BIC      #BITS,R3     ;GET RID OF ADD CARRY
7120 040220 050301 000000          3$:      BIS      R3,R1        ;COMBINE TRAK-SECTOR
7121 040222 010162 001642          MOV      R1,RUNTRAK(R2) ;PUT TRAK-SECT IN TABLE
7122 040226 010037 002054          MOV      R0,@#RP4HDC  ;SAVE CYLINDER ADR
7123 040232 010137 002052          MOV      R1,@#RP4HDA  ;SAVE TRAK-SECTOR ADR
7124 040236 112737 177775          002075  MOVB     #-3,@#RP4TRY  ;SET TRY COUNT
7125 040244 104414 000000          RESREG

7126 040246 004767 000026          RP4WTRY: JSR      PC,LDRP4    ;LOAD RP4 REGISTERS
7127 040252 012777 042364          141716  MOV      #RP4SRV,@#RP4VEC ;LOAD INTERRUPT VECTOR
7128 040260 005077 141714          CLR      @#RP4PSW
7129 040264 005037 002066          CLR      @#RP4FSW
7130 040270 112777 000161          141644  MOVB     #161,@#RP4CS1 ;SET FUNCTION TO WRITE
7131 040276 000002 000000          RTI

7132
7133 040300 013777 002022          141646  LDRP4:  MOV      @#RP4UNIT,@#RP4CS2 ;LOAD UNIT NUMBER
7134 040306 012777 010000          141660  MOV      #BIT12,@#RP4OF  ;SET FORMAT TO 16 BIT
7135 040314 013777 002054          141642  MOV      @#RP4HDC,@#RP4DC ;LOAD CYLINDER ADR
7136 040322 013777 002052          141622  MOV      @#RP4HDA,@#RP4DA ;LOAD TRAK-SECTOR
7137 040330 013777 001726          141606  MOV      @#RP4HWC,@#RP4WC ;LOAD WORD COUNT
7138 040336 013777 001754          141604  MOV      @#RP4OLD+2,@#RP4BAE ;LOAD EXTENDED ADR BITS
7139 040344 013777 001752          141574  MOV      @#RP4OLD,@#RP4BA ;LOAD BUS ADR
7140 040352 000207 000000          RTS      PC           ;RETURN
7141
7142
7143
7144
7145
7146 040354 104412 000000          RSDRV:  SAVREG
7147 040356 105037 001710          001710  CLRB     @#RSHSTAT      ;CLEAR DONE FLAG
7148 040362 032737 000400          000400  BIT      #BITS,@#RSHSTAT ;REPEAT FLAG SET?
7149 040370 001403 000000          BEQ     3$           ;BRANCH IF NO

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7151 040374 000137 043050          JMP      @RSRPT
7152 040400 013737 001556 001576 3S:  MOV      @RNTBINX,@RS11      ;SAVE RUN TABLE INDEX
7153 040406 032737 000020 177570  BIT      @SW4,@SWR          ;RANDOM DSK ADR?
7154 040414 001403          BEQ      1$                ;BRANCH IF YES
7155 040416 005000          CLR      R0
7156 040420 005001          CLR      R1
7157 040422 000407          BR       4$
7158 040424 004737 050604          JSR      PC,@SRAND          ;GET RANDOM NUMBER
7159 040430 013700 001524          MOV      @MINUM,R0
7160 040434 072027 177774          ASH      #-4,R0
7161 040440 010001          MOV      R0,R1            ;SAVE RANDOM NUMBER
7162 040442 042700 170077          BIC      @170077,R0        ;GET TRACK ADR
7163 040446 022700 007600          CMP      @7600,R0         ;IS IT LEGAL?
7164 040452 100003          BPL      5$                ;BRANCH IF YES
7165 040454 062700 007600          ADD      @7600,R0         ;MAKE IT LEGAL
7166 040460 000770          BR       4$
7167 040462 013702 001576          MOV      @RS11,R2         ;GET RUN TABLE INDEX
7168 040466 072027 177772          ASH      #-6,R0           ;ADJUST TRACK ADR
7169 040472 110062 001626          MOV      R0,RUNTABL(R2)   ;SAVE TRAK ADR IN RUN TBL
7170 040476 042701 177700          BIC      @177700,R1       ;GET SECTOR ADR
7171 040502 022701 000077          CMP      @77,R1           ;IS IT LEGAL?
7172 040506 100003          BPL      2$                ;BRANCH IF YES
7173 040510 062701 000077          ADD      @77,R1           ;MAKE IT LEGAL
7174 040514 000770          BR       6$
7175 040516 010162 001642          MOV      R1,RUNTRAK(R2)   ;SAVE IN RUN TRAK TABLE
7176 040522 072027 000006          ASH      @6,R0            ;ADJUST TRACK ADDR
7177 040526 050100          BIS      R1,R0            ;COMBINE SECTOR TRAK
7178 040530 010037 002056          MOV      R0,@RSHDA        ;SAVE AS DSK ADR
7179 040534 112737 177775 002076  MOV      #-3,@RSTRY       ;SET TRY COUNT
7180 040542 104414          RESREG
7181 040544 004737 040604          JSR      PC,@LDRS         ;GO LOAD REGISTERS
7182 040550 012777 043074 141446  MOV      @RSSRV,@RSVEC    ;SET INTERRUPT VECTOR
7183 040556 005077 141444          CLR      @RSPSW
7184 040562 005037 002070          CLR      @RSFUN
7185 040566 105777 141426          TSTB    @RSDS            ;SET FUNCTION TO WRITE
7186 040572 001775          BEQ      1$                ;IS DRIVE READY?
7187 040574 112777 000161 141400  MOV      @161,@RSCS1      ;BRANCH IF NO
7188 040602 000002          RTI                       ;LOAD FUNCTION AND GO
7189
7190 040604 013777 002024 141402  LDRS:  MOV      @RSUNIT,@RSCS2   ;LOAD UNIT NUMBER
7191 040612 013777 002056 141372  MOV      @RSHDA,@RSDA     ;LOAD DSK ADR
7192 040620 013777 001730 141356  MOV      @RSHWC,@RSHC     ;LOAD WORD COUNT
7193 040626 013777 001760 141354  MOV      @RSOLD+2,@RSBAE   ;LOAD EXTENDED ADDRESS
7194 040634 013777 001756 141344  MOV      @RSOLD,@RSBA     ;LOAD BUS ADDRESS
7195 040642 000207          RTS                       ;RETURN
7196
7197
7198
7199
7200
7201 040644 000005          RP3RPT: RESET
7202 040646 005337 002060          DEC      @#003FUN         ;RESTORE FUNCTION
7203 040652 022737 000001 002060  CMP      @1,@RP3FUN       ;WHAT IS IT?
7204 040660 001472          BEQ      RP31             ;BRANCH IF WC
7205 040662 002402          BLT     1$                ;BRANCH IF WRITE

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;*****
;SBTTL RP11/RP03 SERVICE ROUTINE
;* SEE DOCUMENTATION FOR FUNCTIONAL DESCRIPTION OF ROUTINE
;*****

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7207 040670 000167 000414      1S:      JMP      RP33
7208 040674 005237 002060      RP3SRV: INC      @RP3FUN      ; INCREMENT FUNCTION
7209 040700 022737 000002 002060      CMP      @2,@RP3FUN      ; WHAT IS IT?
7210 040706 001501          BEQ      RP3WCK          ; BRANCH TO WRITE CHECK
7211 040710 100002          BPL      +6
7212 040712 000137 041350          JMP      @RP3READ
7213
7214          ;FUNCTION JUST EXECUTED WAS A WRITE
7215 040716 032737 000400 001676      BIT      @BIT8,@RP3HSTAT      ; REPEAT FLAG SET?
7216 040724 001036          BNE      RP3LOOP          ; BRANCH IF YES
7217 040726 005777 141152          TST      @RP3CS          ; ANY ERRORS?
7218 040732 100045          BPL      RP31          ; BRANCH IF NO
7219 040734 105737 002072          TSTB   @RP3TRY          ; TRIED 3 TIMES?
7220 040740 001415          BEQ      RP3ERR          ; BRANCH IF YES
7221 040742 112777 000001 141134      MOVB   @BIT0,@RP3CS      ; CLEAR THE DRIVE
7222 040750 105777 141130          TSTB   @RP3CS          ; CONTROLLER READY?
7223 040754 100375          BPL      -4          ; BRANCH IF NO
7224 040756 105237 002072          INCB   @RP3TRY          ; INCREMENT TRY COUNT
7225 040762 013746 177776          MOV     @PSW,-(SP)      ; MAINTAIN SAME PSW
7226 040766 012746 037310          MOV     @RP3WTRY,-(SP)  ; SET RETRY ADDRESS
7227 040772 000002          RTI
7228 040774 012737 100200 001676      RP3ERR: MOV     @100200,@RP3HSTA  ; SET ERROR BIT IN HAND. STA
7229 041002 010046          MOV     RO,-(SP)      ; SAVE RO
7230 041004 013700 001566          MOV     @RP3I1,RO      ; GET RUNTABLE INDEX
7231 041010 052760 100000 001642      BIS     @BIT15,RUNTRAK(RO) ; SET ERROR BIT
7232 041016 012600          MOV     (SP)+,RO      ; RESTORE RO
7233 041020 000002          RTI          ; RETURN
7234
7235 041022 012737 100200 001676      RP3LOOP:MOV    @100200,@RP3HSTAT ; SET DONE AND ERROR
7236 041030 005777 141050          TST     @RP3CS          ; ANY ERRORS?
7237 041034 100403          BMI     1S          ; BRANCH IF YES
7238 041036 042737 100000 001676      1S:      BIC     @BIT15,@RP3HSTAT ; CLEAR ERROR BIT
7239 041044 000002          RTI          ; RETURN
7240
7241 041046 112737 177775 002072      ;WRITE WAS OK- NOW DO A WRITE CHECK
7242 041054 012737 000107 001564      RP31:   MOVB   @-3,@RP3TRY    ; INIT TRY COUNT
7243 041062 053737 001734 001564          MOV     @107,@RP310      ; SET FUNCTION
7244 041070 053737 002012 001564          BIS     @RP3OLD+2,@RP310  ; SET BAE BITS
7245 041076 004737 037356          BIS     @RP3UNIT,@RP310  ; SET UNIT BITS
7246 041102 013777 001564 140774      RP32:   JSR     PC,@LDRP3      ; LOAD RP3 REGISTERS
7247 041110 000002          MOV     @RP310,@RP3CS    ; LOAD FUNCTION AND GO
7248          RTI          ; RETURN
7249
7250 041112 032737 000400 001676      ;FUNCTION JUST EXECUTED WAS A WRITE CHECK
7251 041120 001340      RP3WCK: BIT     @BIT8,@RP3HSTAT      ; REPEAT FLAG SET?
7252 041122 005777 140756          BNE      RP3LOOP          ; BRANCH IF YES
7253 041126 100031          TST     @RP3CS          ; ANY ERRORS?
7254 041130 005737 001566          BPL      1S          ; BRANCH IF NO
7255 041134 001422          TST     @RP3I1          ; FIRST 2K?
7256 041136 105737 002072          BEQ     4S          ; BRANCH IF YES
7257 041142 001714          TSTB   @RP3TRY          ; TRIED 3 TIMES?
7258 041144 005337 002060          BEQ     RP3ERR          ; BRANCH IF YES
7259 041150 112777 000001 140726      DEC     @RP3FUN          ; RESTORE FUNCTION
7260 041156 105777 140722          MOVB   @BIT0,@RP3CS      ; CLEAR THE DRIVE
7261 041162 100375          TSTB   @RP3CS          ; CONTROLLER READY?
          BPL      -4          ; BRANCH IF NO
    
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7263 041170 013746 177776          MOV      @PSW,-(SP)
7264 041174 012746 041076          MOV      @RP32,-(SP)
7265 041200 000002                    RTI
7266 041202 032777 000010 140672 4S:  BIT      @BIT3,@RP3ER
7267 041210 001752                    BEQ      SS
7268
7269          :WRITE CHECK OK- NOW DO A READ
7270 041212 112737 177775 002072 1S:  MOVB    @-3,@RP3TRY
7271 041220 032737 000040 172516  BIT      @BIT5,@MMR3
7272 041226 001407                    BEQ      2S
7273 041230 005046                    CLR      -(SP)
7274 041232 004737 052320          JSR     @GIVEMAP
7275 041236 012746 001762          MOV     @RP3NL,-(SP)
7276 041242 004737 052054          JSR     @GETMAP
7277 041246 010046                    MOV     RO,-(SP)
7278 041250 013700 001764          MOV     @RP3NH,RO
7279 041254 072027 000004          ASH    @4,RO
7280 041260 010037 001764          MOV     RO,@RP3NH
7281 041264 012600                    MOV     (SP)+,RO
7282 041266 012737 000105 001564  MOV     @105,@RP310
7283 041274 053737 001764 001564  BIS     @RP3NH,@RP310
7284 041302 053737 002012 001564  BIS     @RP3UNIT,@RP310
7285 041310 013777 002042 140574  RP33:  MOV     @RP3DA,@RP3DA
7286 041316 013777 002044 140570  MOV     @RP3DC,@RP3DC
7287 041324 013777 001716 140554  MOV     @RP3HC,@RP3HC
7288 041332 013777 001762 140550  MOV     @RP3NL,@RP3BA
7289 041340 013777 001564 140536  MOV     @RP310,@RP3CS
7290 041346 000002                    RTI
7291
7292          :FUNCTION JUST EXECUTED WAS A READ
7293 041350 032737 000400 001676  RP3READ:BIT @BIT8,@RP3HSTAT
7294 041356 001221                    BNE     RP3LOOP
7295 041360 005777 140520          TST     @RP3CS
7296 041364 100022                    BPL     1S
7297 041366 105737 002072          TSTB   @RP3TRY
7298 041372 001600                    BEQ     RP3ERR
7299 041374 005337 002060          DEC     @RP3FUN
7300 041400 112777 000001 140476  MOVB   @BIT0,@RP3CS
7301 041406 105777 140472          TSTB   @RP3CS
7302 041412 100375                    BPL     -4
7303 041414 105237 002072          INCB   @RP3TRY
7304 041420 013746 177776          MOV     @PSW,-(SP)
7305 041424 012746 041310          MOV     @RP33,-(SP)
7306 041430 000002                    RTI
7307 041432 032737 000040 172516 1S:  BIT      @BIT5,@MMR3
7308 041440 001404                    BEQ      2S
7309 041442 005046                    CLR      -(SP)
7310 041444 004737 052320          JSR     @GIVEMAP
7311 041450 005726                    TST     (SP)+
7312 041452 112737 000200 001676 2S:  MOVB    @200,@RP3HSTA
7313 041460 000002                    RTI
7314
7315          :*****
7316          :SBTTL  RK11/RK05 SERVICE ROUTINE
7317          :*      SEE DOCUMENTATION FOR FUNCTIONAL DESCRIPTION OF ROUTINE
          :*****

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```

:GO TRY AGAIN
:WRITE CHECK ERROR?
:BRANCH IF NO

:RESTORE TRY COUNT
:MAP ON?
:BRANCH IF NO
:PUT DEVICE ID ON STACK
:RETURN MAP REGISTER
:PUT ADR OF BUS ADR ON STK
:GET MAP REGISTERS
:SAVE RO
:GET BAE BITS
:ADJUST
:SAVE
:RESTORE RO
:SET FUNCTION
:SET BAE BITS
:SET UNIT NUMBER
:LOAD DSK ADR
:LOAD CYL
:LOAD WORD COUNT
:LOAD BUS ADR
:LOAD FUNCTION AND GO
:RETURN

```

```

:REPEAT FLAG SET?
:BRANCH IF YES
:ANY ERRORS?
:BRANCH IF NO
:TRIED 3 TIMES?
:BRANCH IF YES
:RESTORE FUNCTION
:CLEAR THE DRIVE
:CONTROLLER READY?
:BRANCH OF NO
:INCREMENT TRY COUNT

```

```

:GO TRY AGAIN
:MAP ON?
:BRANCH IF NO
:PUT DEVICE ID IN STK
:RETURN MAP REGISTERS
:RESTORE STACK
:SET DONE FLAG
:RETURN

```



```

7319 041464 005337 002062          DEC      @@RKFUN          ;RESTORE FUNCTION
7320 041470 022737 000001 002062  CMP      @1,@@RKFUN      ;WHAT IS IT?
7321 041476 001475          BEQ      RK1             ;BRANCH IF WC
7322 041500 002402          BLT      1$             ;BRANCH IF WRITE
7323 041502 000137 037724          JMP      @@RKWTRY      ;IT WAS A WRITE
7324 041506 000137 042144          JMP      @@RK3
7325 041512 062737 000001 002062  RKSRV:  ADD      @1,@@RKFUN      ;FIND OUT WHAT FUNCTION
7326 041520 022737 000002 002062  CMP      @2,@@RKFUN      ;WAS EXECUTED
7327 041526 001507          BEQ      RKWRCK        ;WAS IT A WRITE CHECK?
7328 041530 100002          BPL      +6            ;BRANCH IF YES
7329 041532 000137 042176          JMP      @@RKREAD      ;BRANCH IF IT WAS A WRITE
7330
7331
7332          ;FUNCTION JUST EXECUTED WAS A WRITE. ANY ERRORS?
7333 041536 032737 000400 001700  BIT      @BIT@,@@RKHSTAT ;REPEAT FLAG SET?
7334 041544 001040          BNE      RKLOOP        ;BRANCH IF YES
7335 041546 005777 140354          TST      @RKCS         ;ANY ERRORS?
7336 041552 100047          BPL      RK1             ;BRANCH IF NO
7337 041554 105737 002073          TSTB    @@AKTRY        ;TRIED 3 TIMES?
7338 041560 001417          BEQ      RKERR          ;BRANCH IF YES
7339 041562 012777 000001 140336  MOV      @1,@@RKCS     ;CLEAR THE ERROR
7340 041570 004737 042322          JSR     PC,@@TIMER     ;WAIT A LITTLE
7341 041574 105777 140326          TSTB    @RKCS         ;WAIT FOR CONT CLR TO FINISH
7342 041600 100375          BPL      -4
7343 041602 105237 002073          INCB    @@AKTRY        ;INCREMENT TRY COUNT
7344 041606 013746 177776          MOV      @@PSW,-(SP)
7345 041612 012746 037724          MOV      @RKWTRY,-(SP)
7346 041616 000002          RTI
7347 041620 012737 100200 001700  RKERR:  MOV      @100200,@@RKHSTAT ;SET ERROR & DONE FLAG
7348 041626 010046          MOV      RO,-(SP)      ;SAVE RO
7349 041630 013700 001572          MOV      @@RK11,RO    ;GET SAVED RUN TABLE INDEX
7350 041634 052760 100000 001642  BIS      @BIT15,RUNTRAK(RO) ;SET ERROR BIT IN RUN TABLE
7351 041642 012600          MOV      (SP)+,RO     ;RESTORE RO
7352 041644 000002          RTI                   ;RETURN
7353
7354 041646 012737 100200 001700  RKLOOP:MOV @100200,@@RKHSTAT ;SET DONE AND ERROR BITS
7355 041654 005777 140246          TST      @RKCS         ;ANY ERRORS?
7356 041660 100403          BMI     1$             ;BRANCH IF YES
7357 041662 042737 100000 001700  BIC      @BIT15,@@RKHSTAT ;CLEAR ERROR BIT
7358 041670 000002          RTI                   ;RETURN
7359
7360 041672 112737 177775 002073  ;WRITE WAS OK, NOW DO A WRITE CHECK
7361 041700 012767 000507 137662  RK1:   MOV      @-3,@@AKTRY   ;RESTORE TRY COUNT
7362 041706 053767 001740 137654          MOV      @507,RK10    ;SET FUNCTION TO WRITE
7363 041714 013777 002046 140212  RK2:   BIS      @@RKOLD+2,RK10 ;SET BA EXT BITS
7364 041722 013777 001720 140200          MOV      @@RKHDA,@@AKDA ;LOAD DISK ADDRESS
7365 041730 013777 001736 140174          MOV      @@RKHWC,@@AKWC ;LOAD WORD COUNT
7366 041736 016777 137626 140162          MOV      @@RKOLD,@@AKBA ;LOAD BUS ADDRESS
7367 041744 000002          MOV      RK10,@@RKCS  ;START FUNCTION
7368          RTI                   ;RETURN
7369
7370 041746 032737 000400 001700  ;FUNCTION JUST EXECUTED WAS A WRITE CHECK. ANY ERRORS?
7371 041754 001334          RKWRCK:BIT @BIT@,@@RKHSTAT ;REPEAT FLAG SET?
7372 041756 005777 140144          BNE      RKLOOP        ;BRANCH IF YES
7373 041762 100033          TST      @RKCS         ;ANY ERRORS?
                          BPL      1$             ;BRANCH IF NO

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7375	041770	001424			BEQ	45			: BRANCH IF YES
7376	041772	105737	002073		TSTB	2#RKTRY			: TRYED 3 TIMES?
7377	041776	001710		5S:	BEQ	RKERR			: BRANCH IF YES
7378	042000	005337	002062		DEC	2#RKFUN			: SET FUNCTION BACK TO WC
7379	042004	012777	000001	140114	MOV	#1, 2#RKCS			: CLEAR THE ERROR
7380	042012	004737	042322		JSR	PC, 2#TIMER			: WAIT A LITTLE
7381	042016	105777	140104		TSTB	2#RKCS			: WAIT FOR CLR TO FINISH
7382	042022	100375			BPL	-4			
7383	042024	105237	002073		INCB	2#RKTRY			: INCREMENT TRY COUNT
7384	042030	013746	177776		MOV	2#PSW, -(SP)			
7385	042034	012746	041714		MOV	2#RK2, -(SP)			
7386	042040	000002			RTI				
7387	042042	032777	040000	140056	4S:	BIT	#BIT14, 2#RKCS		: HARD ERROR?
7388	042050	001350			BNE	5S			: BRANCH IF YES
7389									
7390									
7391	042052	112737	177775	002073	1S:	MOV	#-3, 2#RKTRY		: RESTORE TRY COUNT
7392	042060	032737	000040	172516	BIT	#BITS, 2#MMR3			: MAP ON?
7393	042066	001410			BEQ	2S			: BRANCH IF NO
7394	042070	012746	000001		MOV	#1, -(SP)			: PUT DEVICE ID ON STACK
7395	042074	004767	010220		JSR	PC, GIVEMAP			: RELINQUISH MAP REG
7396	042100	012746	001766		MOV	2#RKNEWL, -(SP)			: PUT ADR OF BADR ON STACK
7397	042104	004737	052054		JSR	PC, 2#GETMAP			: GET MAPREGISTER
7398	042110	010046			2S:	MOV	RO, -(SP)		: SAVE RO
7399	042112	013700	001770		MOV	2#RKNEWH, RO			: GET BA EXT
7400	042116	072027	003004		ASH	#4, RO			: ADJUST
7401	042122	010037	001770		MOV	RO, 2#RKNEWH			: SAVE
7402	042126	012600			MOV	(SP)+, RO			: RESTORE RO
7403	042130	012767	000105	137432	MOV	#105, RK10			: SET FUNCTION
7404	042136	053767	001770	137424	BIS	2#RKNEWH, RK10			: SET BA EXT BITS IN FUNCTION
7405	042144	013777	002046	137762	3S:	MOV	2#RKHDA, 2#RKDA		: LOAD DISK ADDRESS
7406	042152	013777	001720	137750	MOV	2#RKHWC, 2#RKWC			: LOAD WORD COUNT
7407	042160	013777	001766	137744	MOV	2#RKNEWL, 2#RKBA			: LOAD BUS ADDRESS
7408	042166	016777	137376	137732	MOV	RK10, 2#RKCS			: LOAD FUNCTION AND GO
7409	042174	000002			RTI				: RETURN
7410									
7411									
7412	042176	032737	000400	001700	:FUNCTION JUST EXECUTED WAS A READ. ANY ERRORS? RKREAD:	BIT	#BITS, 2#RKHSTAT		: REPEAT FLAG SET?
7413	042204	001220			BNE	RKLOOP			: BRANCH IF YES
7414	042206	005777	137714		TST	2#RKCS			: ANY ERRORS?
7415	042212	100026			BPL	1S			: BRANCH IF NO
7416	042214	105737	002073		TSTB	2#RKTRY			: TRYED 3 TIMES?
7417	042220	001002			BNE	3S			: BRANCH IF NO
7418	042222	000167	177372		JMP	RKERR			
7419	042226	005337	002062		3S:	DEC	2#RKFUN		: SET FUNCTION BACK TO READ
7420	042232	012777	000001	137666	MOV	#1, 2#RKCS			: CLEAR THE ERROR
7421	042240	004737	042322		JSR	PC, 2#TIMER			: WAIT A LITTLE
7422	042244	105777	137656		TSTB	2#RKCS			: WAIT FOR CLR TO FINISH
7423	042250	100375			BPL	-4			
7424	042252	105237	002073		INCB	2#RKTRY			: INCREMENT TRY COUNT
7425	042256	013746	177776		MOV	2#PSW, -(SP)			
7426	042262	012746	042144		MOV	2#RK3, -(SP)			
7427	042266	000002			RTI				
7428	042270	032737	000040	172516	1S:	BIT	#BITS, 2#MMR3		: MAP ON?
7429	042276	001405			BEQ	2S			: BRANCH IF NO

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7431 042304 004737 052320 JSR PC, @GIVEMAP ;RELINQUISH MAP REGISTER
7432 042310 005726 TST (SP)+ ;POP THE STACK
7433 042312 112737 000200 001700 2S: MOVB #200, @ARKHSTAT ;SET DONE FLAG
7434 042320 000002 RTI ;RETURN
7435 042322 005067 000010 TIMER: CLR 1S
7436 042326 105267 000004 2S: INCB 1S
7437 042332 001375 BNE 2S
7438 042334 000207 RTS PC
7439 042336 000000 1S: .WORD

;*****
;SBTTL RH70/RP04 SERVICE ROUTINE
; * SEE DOCUMENTATION FOR FUNCTIONAL DESCRIPTION OF ROUTINE
;*****
RP4RPT: RESET
7445 042340 000005 DEC @RP4FUN ;RESTORE FUNCTION
7446 042342 005337 002066 002066 CMP #1, @RP4FUN ;WHAT IS IT?
7447 042346 022737 000001 BEQ RP41 ;BRANCH IF WC
7448 042354 001501 BLT RP43 ;BRANCH IF READ
7449 042356 002560 JMP @RP4WTRY ;GO TO WRITE
7450 042360 000137 040246 RP4SRV: INC @RP4FUN ;FIND OUT WHAT FUNCTION
7451 042364 005237 002066 CMP #2, @RP4FUN ;WAS JUST EXECUTED
7452 042370 022737 000002 BEQ RP4WCK
7453 042376 001504 BMI RP4READ
7454 042400 100566

;WRITE FUNCTION WAS JUST EXECUTED.
7455
7457 042402 032737 000400 001706 BIT @BIT8, @RP4HSTAT ;REPEAT FLAG SET?
7458 042410 001050 BNE RP4LOOP ;BRANCH IF YES
7459 042412 032777 040000 137540 BIT @BIT14, @RP4DS ;ANY ERRORS
7460 042420 001457 BEQ RP41 ;BRANCH IF NO
7461 042422 105737 002075 TSTB @RP4TRY ;TRIED 3 TIMES?
7462 042426 001426 BEQ RP4ERR ;BRANCH IF YES
7463 042430 052777 000040 137516 BIS @BIT5, @RP4CS2 ;CLEAR ALL ERRORS
7464 042436 004737 040300 JSR PC, @LDRP4 ;RELOAD THE UNIT NO
7465 042442 105237 002075 @RP4TRY ;INCREMENT TRY COUNT
7466 042446 013746 @PSW, -(SP) ;SETUP THE STACK TO
7467 042452 012746 040246 MOV @RP4WTRY, -(SP) ;TRY WRITE AGAIN
7468 042456 032737 000400 001706 BIT @BIT8, @RP4HSTAT ;REPEAT FLAG SET?
7469 042464 001006 BNE 2S ;BRANCH IF YES
7470 042466 012777 000007 137446 MOV #7, @RP4CS1 ;RECALIBRATE
7471 042474 105777 137460 1S: TSTB @RP4DS ;DRIVE READY?
7472 042500 100375 BPL 1S ;BRANCH IF NO
7473 042502 000002 2S: RTI
7474 042504 012737 100200 001706 RP4ERR: MOV #100200, @RP4HSTA ;SET ERROR & DONE BIT
7475 042512 010046 MOV RO, -(SP) ;SAVE RO
7476 042514 013700 001574 MOV @RP4I1, RO ;GET RUN TABLE INDEX
7477 042520 052760 100000 001642 BIS @BIT15, @UNTRAK(RO) ;SET ERROR BIT
7478 042526 012600 MOV (SP)+, RO ;RESTORE RO
7479 042530 000002 RTI ;RETURN
7480
7481 042532 012737 100200 001706 RP4LOOP: MOV #100200, @RP4HSTAT ;SET DONE AND ERROR BITS
7482 042540 032777 040000 137412 BIT @BIT14, @RP4DS ;ANY ERRORS?
7483 042546 001003 BNE 1S ;BRANCH IF YES
7484 042550 042737 100000 001706 BIC @BIT15, @RP4HSTAT ;CLEAR ERROR BIT
7485 042556 000002 1S: RTI ;RETURN
    
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7487 042560 112737 177775 002075 RP41:  MOVB  #3,2#RP4TRY      ; INITIALIZE TRY COUNT
7488 042566 105777 137366          RP42:  TSTB  2#RP4DS      ; IS DRIVE READY?
7489 042572 001775                    BEQ   RP42      ; BRANCH IF NO
7490 042574 004737 040300          JSR   PC,2#LDRP4
7491 042600 112777 000151 137334  MOVB  #151,2#RP4CS1    ; LOAD FUNCTION AND GO
7492 042606 000002
7493
7494          ; FUNCTION JUST EXECUTED WAS A WRITE CHECK
7495 042610 032737 000400 001706 RP4WCK: BIT  #BIT8,2#RP4HSTAT      ; REPEAT FLAG SET?
7496 042616 001345                    BNE  RP4LOOP      ; BRANCH IF YES
7497 042620 032777 040000 137332  BIT  #BIT14,2#RP4DS  ; ANY ERRORS?
7498 042626 001421                    BEQ   1$          ; BRANCH IF NO
7499 042630 105737 002075          3$:  TSTB  2#RP4TRY      ; TRIED 3 TIMES?
7500 042634 001723                    BEQ   RP4ERR      ; BRANCH IF YES
7501 042636 005337 002062          DEC  2#RP4FUN      ; SET FUNCTION TO WC
7502 042642 052777 000040 137304  BIS  #BITS,2#RP4CS2 ; CLEAR ALL ERRORS
7503 042650 004737 040300          JSR   PC,2#LDRP4  ; RELOAD THE UNIT NO
7504 042654 105237 002075          INCB  2#RP4TRY      ; INCREMENT TRY COUNT
7505 042660 013746 177776          MOV  2#PSW,-(SP)
7506 042664 012746 042566          MOV  #RP42,-(SP)
7507 042670 000002                    RTI
7508 042672 032777 040000 137254  1$:  BIT  #BIT14,2#RP4CS2 ; TRY AGAIN
7509 042700 001404                    BEQ   2$          ; WRITE CHECK ERROR?
7510 042702 005737 001574          TST  2#RP411      ; BRANCH IF NO
7511 042706 001401                    BEQ   2$          ; FIRST 2K?
7512 042710 000747                    BR   3$
7513
7514          ; WRITE CHECK WAS OK... NOW DO A READ.
7515 042712 112737 177775 002075  2$:  MOVB  #3,2#RP4TRY      ; INITIALIZE TRY COUNT
7516 042720 105777 137234          RP43:  TSTB  2#RP4DS      ; IS DRIVE READY?
7517 042724 001775                    BEQ   RP43      ; BRANCH IF NO
7518 042726 004737 040300          JSR   PC,2#LDRP4  ; LOAD REGISTERS
7519 042732 013777 002004 137210  MOV  2#RP4NWL,2#RP4BAE ; LOAD EXTENDED ADR BITS
7520 042740 013777 002002 137200  MOV  2#RP4NWL,2#RP4BA ; LOAD BUS ADR
7521 042746 112777 000171 137166  MOVB  #171,2#RP4CS1 ; LOAD FUNCTION AND GO
7522 042754 000002                    RTI      ; RETURN
7523
7524          ; FUNCTION JUST EXECUTED WAS A READ.
7525 042756 032737 000400 001706 RP4READ: BIT  #BIT8,2#RP4HSTAT      ; REPEAT FLAG SET?
7526 042764 001262                    BNE  RP4LOOP      ; BRANCH IF YES
7527 042766 032777 040000 137164  BIT  #BIT14,2#RP4DS  ; ANY ERRORS?
7528 042774 001421                    BEQ   1$          ; BRANCH IF NO
7529 042776 105737 002075          TSTB  2#RP4TRY      ; TRIED 3 TIMES?
7530 043002 001640                    BEQ   RP4ERR      ; BRANCH IF YES
7531 043004 005337 002066          DEC  2#RP4FUN      ; SET FUNCTION TO A READ
7532 043010 052777 000040 137136  BIS  #BITS,2#RP4CS2 ; CLEAR ALL ERRORS
7533 043016 004737 040300          JSR   PC,2#LDRP4  ; RELOAD THE UNIT NO
7534 043022 105237 002075          INCB  2#RP4TRY      ; INCREMENT TRY COUNT
7535 043026 013746 177776          MOV  2#PSW,-(SP)
7536 043032 012746 042720          MOV  #RP43,-(SP)
7537 043036 000002                    RTI
7538 043040 112737 000200 001706  1$:  MOVB  #200,2#RP4HSTA ; TRY AGAIN
7539 043046 000002                    RTI      ; SET DONE FLAG
7540          ; RETURN
7541          ;*****
          ;SBTTL RH70/R504 SERVICE ROUTINE
    
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7543 .....
7544 043050 000005
7545 043052 005337 002070
7546 043056 022737 000001 002070
7547 043064 001465
7548 043066 002542
7549 043070 000137 040544
7550 043074 005237 002070
7551 043100 022737 000002 002070
7552 043106 001470
7553 043110 100550
7554
7555
7556 043112 032737 000400 001710
7557 043120 001034
7558 043122 032777 040000 137070
7559 043130 001443
7560 043132 105737 002076
7561 043136 001412
7562 043140 052777 000040 137046
7563 043146 105237 002076
7564 043152 013746 177776
7565 043156 012746 040544
7566 043162 000002
7567 043164 012737 100200 001710
7568 043172 010046
7569 043174 013700 001576
7570 043200 052760 100000 001642
7571 043206 012600
7572 043210 000002
7573
7574 043212 012737 100200 001710
7575 043220 032777 040000 136772
7576 043226 001003
7577 043230 042737 100000 001710
7578 043236 000002
7579
7580 043240 112737 177775 002076
7581 043246 105777 136746
7582 043252 001775
7583 043254 004737 040604
7584 043260 112777 000151 136714
7585 043266 000002
7586
7587
7588 043270 032737 000400 001710
7589 043276 001345
7590 043300 032777 040000 136712
7591 043306 001417
7592 043310 105737 002076
7593 043314 001723
7594 043316 005337 002070
7595 043322 052777 000040 136664
7596 043330 105237 002076
7597 043334 013746 177776

:*****
RSRPT: RESET
DEC @#RSFUN ;RESTORE FUNCTION
CMP #1,@#RSFUN ;WHAT IS IT?
BEQ RS41 ;BRANCH IF WC
BLT RS43 ;BRANCH IF WRITE
JMP @#RSWTRY
RSSRV: INC @#RSFUN ;FIND OUT WHAT FUNCTION
CMP #2,@#RSFUN ;WAS JUST EXECUTED
BEQ RSWCK
BMI RSREAD

;WRITE FUNCTION WAS JUST EXECUTED
BIT @BIT8,@#RSHSTAT ;REPEAT FLAG SET?
BNE RSLOOP ;BRANCH IF YES
BIT @BIT14,@#RSDS ;ANY ERRORS?
BEQ RS41 ;BRANCH IF NO
TSTB @#RSTRY ;TRIED 3 TIMES?
BEQ RSERR ;BRANCH IF YES
BIS @BIT5,@#RSCS2 ;CLEAR ALL ERRORS
INCB @#RSTRY ;INCREMENT TRY COUNT
MOV @#PSW,-(SP) ;SETUP THE STACK TO
MOV @#RSTRY,-(SP) ;TRY THE WRITE AGAIN
RSERR: MOV #100200,@#RSHSTAT ;SET ERROR AND DONE BIT
MOV RO,-(SP) ;SAVE RO
MOV @#RS11,RO ;GET RUN TBL INDEX
BIS @BIT15,RUNTRAK(RO) ;SET ERROR BIT
MOV (SP)+,RO ;RESTORE RO

RSLOOP: MOV #100200,@#RSHSTAT ;SET DONE AND ERROR BITS
BIT @BIT14,@#RSDS ;ANY ERRORS?
BNE 1$ ;BRANCH IF YES
BIC @BIT15,@#RSHSTAT ;CLEAR ERROR BIT
RTI ;RETURN
1$:
;WRITE OK...NOW DO A WRITE CHECK
RS41: MOVB #-3,@#RSTRY ;INIT TRY COUNT
RS42: TSTB @#RSDS ;IS DRIVE READY?
BEQ RS42 ;BRANCH IF NO
JSR PC,@#LDRS ;LOAD RS REGISTERS
MOVB #151,@#RSCS1 ;LOAD FUNCTION AND GO
RTI ;RETURN

;FUNCTION JUST EXECUTED WAS A WRITE CHECK
RSWCK: BIT @BIT8,@#RSHSTAT ;REPEAT FLAG SET?
BNE RSLOOP ;BRANCH IF YES
BIT @BIT14,@#RSDS ;ANY ERRORS?
BEQ 1$ ;BRANCH IF NO
TSTB @#RSTRY ;TRIED 3 TIMES?
BEQ RSERR ;BRANCH IF YES
DEC @#RSFUN ;SET FUNCTION BACK TO WC
BIS @BIT5,@#RSCS2 ;CLEAR THE ERROR
INCB @#RSTRY ;INCREMENT THE TRY COUNT
MOV @#PSW,-(SP)
3$:

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7599 043344 000002 RTI ;TRY AGAIN
7600
7601 043346 032777 040000 136640 1S: BIT #BIT14,RSRCS2 ;WRITE CHECK ERROR?
7602 043354 001404 BEQ 2S ;BRANCH IF NO
7603 043356 005737 001576 TST RSR11 ;FIRST 2K?
7604 043362 001401 BEQ 2S ;BRANCH IF YES
7605 043364 000751 BR 3S
7606
7607 ;WRITE CHECK WAS OK...NOW DO A READ.
7608 043366 112737 177775 002076 2S: MOVB #-3,RSRSTRY ;INIT TRY COUNT
7609 043374 105777 136620 RS43: TSTB RSDS ;IS DRIVE READY?
7610 043400 001775 BEQ RS43 ;BRANCH IF NO
7611 043402 004737 040604 JSR PC,RALDRS ;LOAD RS REGISTERS
7612 043406 013777 002010 136574 MOV RASNEWH,RSBAE ;LOAD BAE
7613 043414 013777 002006 136564 MOV RASNEWL,RSBA ;LOAD BUS ADR
7614 043422 112777 000171 136552 MOVB #171,RSRCS1 ;LOAD FUNCTION AND GO
7615 043430 000002 RTI ;RETURN
7616
7617 ;FUNCTION JUST EXECUTED WAS A READ.
7618 043432 032737 000400 001710 RSREAD: BIT #BIT8,RSRSHSTAT ;REPEAT FLAG SET?
7619 043440 001264 BNE RSLOOP ;BRANCH IF YES
7620 043442 032777 040000 136550 BIT #BIT14,RSRDS ;ANY ERRORS?
7621 043450 001417 BEQ 1S ;BRANCH IF NO
7622 043452 105737 002076 TSTB RSRSTRY ;TRIED 3 TIMES?
7623 043456 001642 BEQ RSERR ;BRANCH IF YES
7624 043460 005337 002070 DEC RRSFUN ;RESTORE FUN TO READ
7625 043464 052777 000040 136522 BIS #BITS,RSRCS2 ;CLEAR ALL ERRORS
7626 043472 105237 002076 INCB RSRSTRY ;INCREMENT TRY COUNT
7627 043476 013746 177776 MOV RPSW,-(SP)
7628 043502 012746 043374 MOV RRS43,-(SP)
7629 043506 000002 RTI ;TRY AGAIN
7630 043510 112737 000200 001710 1S: MOVB #200,RSRSHSTAT ;SET DONE FLAG
7631 043516 000002 RTI ;RETURN
7632
7633 ;*****
7634 ;SBTTL UNIBUS EXERCISER SERVICE ROUTINE
7635 ;* SEE DOCUMENTATION FOR FUNCTIONAL DESCRIPTION OF ROUTINE
7636 ;*****
7637 UBESRV: SAVREG
7638 JSR PC,RALDKT ;GO TO LOW CORE
7639 MOV #UBETBL+6,R4 ;GET ADDRESS OF UBECR1
7640 TST R(R4) ;WAS THERE AN ERROR?
7641 BMI UBE2 ;BRANCH IF YES
7642 MOV #3,-(SP) ;PUT DEVICE ID IN STACK
7643 JSR PC,RGIVEMAP ;GIVE UP MAP REG
7644 ADD #776,RUBESAV ;INCREMENT UBE BUS ADR
7645 ADC RUBESAV+2
7646 MOV RUBESAV,RUBEADR
7647 MOV RUBESAV+2,RUBEADR+2
7648 MOV RUBEADR,-(SP)
7649 JSR PC,RGETMAP
7650 MOV RUBEADR+2,R-(R4) ;LOAD UBECR2
7651 MOV RUBEADR,R-(R4) ;LOAD UBEBR
7652 MOV #170000,R-(R4) ;LOAD UBECR
7653 JSR PC,RRESKT ;GO BACK TO ORIGINAL CORE
RESREG

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7655 043636 000002 RTI ;RETURN
7656
7657 ;UBE ERROR-IS IT LAST MEMORY?
7658 043640 005037 001276 UBE2: CLR @STMP0
7659 043644 162704 000004 SUB #4,R4 ;ADJUST R4
7660 043650 017403 000002 MOV @2(R4),R3 ;GET BECR2
7661 043654 042703 000003 BIC #3,R3 ;GET RID OF ADDRESS BITS
7662 043660 022703 000400 CMP #400,R3 ;WAS ERROR A TIMEOUT?
7663 043664 001050 000000 BNE UBEERR ;BRANCH IF NO
7664 043666 017437 000000 001672 MOV @2(R4),@ERRBA ;SAVE BUS ADR OF ERROR
7665 043674 017437 000002 001674 MOV @2(R4),@ERRBA+2
7666 043702 042737 177774 001674 BIC #177774,@ERRBA+2
7667 043710 004737 051536 JSR PC,@PHYMAP ;GET PHYSICAL ADDRESS THAT TIMED OUT
7668 043714 162737 000004 001476 SUB #4,@PA1500 ;ADJUST PHYSICAL ADR THAT FAILED
7669 043722 005637 001500 SBC @PA2116 ;UBE STOPS AT ADR+4
7670 043726 023737 001476 001562 CMP @PA1500,@MXMML0 ;AT MAXIMUM MEMORY LO?
7671 043734 001022 000000 BNE MHOLE ;BRANCH IF NO
7672 043736 023737 001500 001560 CMP @PA2116,@MXMMHI ;AT MAX MEMORY HI?
7673 043744 001016 000000 BNE MHOLE ;BRANCH IF NO
7674 043746 012746 000003 MOV #3,-(SP) ;PUT DEVICE ID ON STACK
7675 043752 004737 052320 JSR PC,@GIVEMAP
7676 043756 005726 000000 TST (SP)+
7677 043760 004737 051436 JSR PC,@UBEINIT
7678 043764 004737 053216 JSR PC,@RESKT
7679 043770 104414 RESREG
7680 043772 012777 064545 136236 MOV #64545,@UBETBL+6
7681 044000 000002 RTI
7682
7683 044002 010637 001276 MHOLE: MOV SP,@STMP0
7684 044006 013737 001212 001302 UBEERR: MOV @SLPERR,@STMP2 ;SAVE LOOP ERROR ADR
7685 044014 012737 044056 001212 MOV @UBE3,@SLPERR ;SET LOOP ADR
7686 044022 012703 000022 MOV #22,R3
7687 044026 005737 001276 TST @STMP0
7688 044032 001002 BNE 1$
7689 044034 104007 ERROR 7
7690 044036 000407 BR UBE3
7691 044040 013737 001476 001226 1$: MOV @PA1500,@SGDDAT
7692 044046 013737 001500 001230 MOV @PA2116,@SBDDAT
7693 044054 104012 ERROR 12
7694
7695 ;RESTART UBE IN SAME MEMORY
7696 044056 013737 001302 001212 UBE3: MOV @STMP2,@SLPERR ;RESTORE ERROR LOOP ADR
7697 044064 010446 MOV R4,-(SP) ;SAVE R4
7698 044066 012704 002230 MOV @UBETBL,R4 ;GET ADDRESS OF UBE TABLE
7699 044072 012734 170000 MOV #170000,@(R4)+ ;SET UBECC
7700 044076 013734 001666 MOV @UBEADR,@(R4)+ ;SET UBEBA <15:00>
7701 044102 005074 000004 CLR @4(R4) ;CLEAR ALL ERRORS
7702 044106 013734 001670 MOV @UBEADR+2,@(R4)+ ;SET EXT ADR BITS
7703 044112 012774 064545 000000 MOV #64545,@(R4) ;START UBE
7704 044120 012604 MOV (SP)+,R4 ;RESTORE R4
7705 044122 004737 053216 JSR PC,@RESKT
7706 044126 104414 RESREG
7707 044130 000002 RTI ;RETURN
7708
7709 ;*****

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7711      ;*      SEE DOCUMENTATION FOR FUNCTIONAL DESCRIPTION OF ROUTINE
7712      ;:*****
7713      MBTSRV: SAVREG
7714      JSR      PC, @LDKT      ;GO TO LOW CORE
7715      CLR      @STMP0
7716      MOV      @MBTTBL, R4      ;GET ADDRESS OF ADDRESS OF CS1 REG
7717      BIT      @BIT14, @R4+      ;ANY ERRORS?
7718      BNE      1$      ;BRANCH IF YES
7719      JSR      PC, @RESKT      ;GO BACK TO ORIGINAL CORE
7720      RESREG
7721      MOV      @161, @MBTTBL
7722      RTI      ;RESTART MBT AND RETURN
7723      ADD      @10, R4      ;ADJUST R4
7724      BIT      @BIT11, @R4      ;NON-EXISTANT MEMORY ERROR?
7725      BEQ      MBTERR      ;BRANCH IF NO
7726      SUB      @6, R4      ;ADJUST R4
7727      MOV      @R4+, @PA1500      ;GET BUS ADR
7728      MOV      @R4+, @PA2116      ;GET BUS ADR EXT
7729      SUB      @4, @PA1500      ;ADJUST BUS ADR
7730      SBC      @PA2116
7731      CMP      @PA1500, @MXMML0      ;IS IT LAST MEMORY?
7732      BNE      MEMHOLE      ;BRANCH IF NO
7733      CMP      @PA2116, @MXMMHI      ;CHECK EXT ADR BITS
7734      BNE      MEMHOLE
7735      TST      (R4)+      ;INCREMENT R4
7736      BIS      @47, @R4      ;CLEAR THE ERROR
7737      MOV      @7, @R4+      ;SELECT UNIT 7
7738      CLR      @-12(R4)      ;CLEAR WORD COUNT
7739      BR       2$      ;CONTINUE
7740
7741      MEMHOLE: MOV      SP, @STMP0
7742      MBTERR: MOV      @SLPERR, @STMP2      ;SAVE LOOP ADDRESS
7743      MOV      @15, @SLPERR      ;SET NEW LOOP ADR
7744      MOV      @20, R3      ;PUT DEVICE ID IN R3
7745      TST      @STMP0
7746      BNE      2$
7747      ERROR   7
7748      BR       1$
7749      MOV      @PA1500, @SGDDAT      2$:
7750      MOV      @PA2116, @SBDDAT
7751      ERROR   13
7752      MOV      @STMP2, @SLPERR      1$:
7753      MOV      @MBTTBL+10, R4      ;RESTORE LOOP ADR
7754      MOV      @-(R4), R0      ;GET ADR OF MBTTBL+10
7755      MOV      @-(R4), R1      ;GET BUS ADR EXTENDED
7756      MOV      @-(R4), R2      ;GET BUS ADR
7757      ASL      R2      ;GET WORD COUNT
7758      SUB      R2, R1      ;ADJUST WORD COUNT
7759      SBC      R0      ;FORM START ADR OF THIS XFER
7760      BIS      @47, @10(R4)      ;CLEAR THE WORLD
7761      MOV      @7, @10(R4)      ;SELECT UNIT 7
7762      TST      (R4)+      ;ADJUST R4
7763      MOV      R1, @R4+      ;RESTORE BUS ADR
7764      MOV      R0, @R4
7765      JSR      PC, @RESKT      ;GO BACK TO ORIGINAL CORE

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7767 044434 112777 000161 135604      MOVB    #161,MBTTBL      ;START MBT AGAIN
7768 044442 000002                      RTI                    ;RETURN
7769                                     ;*****
7770 .SBTTL  LINE CLOCK SERVICE ROUTINE
7771 *      THIS ROUTINE FIRST REMAPS PROGRAM EXECUTION TO LOW
7772 *      MEMORY. IT THEN INCREMENTS AND KEEPS TRACK OF THE
7773 *      SECOND AND MINUTE COUNTS KEPT IN LOCATIONS "LTICKS"
7774 *      AND "MTICKS" RESPECTIVELY.
7775                                     ;*****
7776 044444 104412      LKSRV: SAVREG
7777 044446 004737 053130      JSR     PC,@LDKT        ;GO TO LOW CORE
7778 044452 105237 001602      INCB   @LTICKS         ;INCREMENT TICK COUNT
7779 044456 122737 000074 001602      CMPB   @60.,@LTICKS    ;ONE SECOND YET?
7780 044464 001014                      BNE    IS              ;BRANCH IF NO
7781 044466 105237 001603      INCB   @LTICKS+1       ;INCREMENT SECOND COUNT
7782 044472 105037 001602      CLRB   @LTICKS         ;CLEAR SECOND COUNT
7783 044476 122737 000074 001603      CMPB   @60.,@LTICKS+1 ;ONE MINUTE YET?
7784 044504 001004                      BNE    IS              ;BRANCH IF NO
7785 044506 105037 001603      CLRB   @LTICKS+1
7786 044512 005237 001600      INC    @MTICKS         ;INCREMENT MINUTE COUNT
7787 044516 004737 053216      1$:   JSR     PC,@RESKT  ;RESTORE THE KT
7788 044522 104414      RESREG
7789 044524 012737 000100 177546      MOV    @BIT6,@LKS     ;CLEAR READY BIT IN CLOCK
7790 044532 000002                      RTI                    ;RETURN
7791                                     ;*****
7792                                     ;*****
7793 .SBTTL  SCOPE HANDLER ROUTINE
7794
7795 *THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
7796 *AND LOAD THE TEST NUMBER($STNM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
7797 *THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
7798 *$SW14=1      LOOP ON TEST
7799 *$SW11=1      INHIBIT ITERATIONS
7800 *$SW09=1      LOOP ON ERROR
7801 *CALL        SCOPE          ;;SCOPE=IOT
7802
7803 $SCOPE:
7804 BIT     @SW14,@SWR      ;;LOOP ON PRESENT TEST?
7805 BNE    $OVER          ;;YES IF SW14=1
7806 *****START OF CODE FOR THE XOR TESTER*****
7807 $XTSTR: BR     6$      ;;IF RUNNING ON THE "XOR" TESTER CHANGE
7808                                     ;;THIS INSTRUCTION TO A "NOP" (NOP=240)
7809 044544 000416      MOV    @ERRVEC,-(SP)   ;SAVE THE CONTENTS OF THE ERROR VECTOR
7810                                     ;;SET FOR TIMEOUT
7811 044546 013746 000004      MOV    @5$,@ERRVEC    ;TIME OUT ON XOR?
7812 044552 012737 044572 000004      TST   @177060        ;RESTORE THE ERROR VECTOR
7813 044560 005737 177060      MOV    (SP)+,@ERRVEC  ;GO TO THE NEXT TEST
7814 044564 012637 000004      BR    $SVLAD         ;CLEAR THE STACK AFTER A TIME OUT
7815 044570 000453      BR    $SVLAD         ;RESTORE THE ERROR VECTOR
7816 044572 022626      5$:   CMP    (SP)+,(SP)+  ;LOOP ON THE PRESENT TEST
7817 044574 012637 000004      MOV    (SP)+,@ERRVEC ;HAS AN ERROR OCCURRED?
7818 044600 000413      BR    7$            ;;BR IF NO
7819 044602      BR    7$
7820 044602 105767 134376      6$:   *****END OF CODE FOR THE XOR TESTER*****
7821 044606 001421      2$:   TSTB   $ERFLG

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7823 044616 101015          BHI      3$          ;;BR IF NO
7824 044620 032737 001000 177570  BIT      #BIT09,2#SWR  ;;LOOP ON ERROR?
7825 044626 001404          BEQ      4$          ;;BR IF NO
7826 044630 016767 134356 134352 7$:  MOV     SLPERR,SLPADR  ;;SET LOOP ADDRESS TO LAST SCOPE
7827 044636 000441          BR       $OVER       ;;
7828 044640 105067 134340          4$:  CLRB   SERFLG       ;;ZERO THE ERROR FLAG
7829 044644 005067 134446          CLR     $TIMES      ;;CLEAR THE NUMBER OF ITERATIONS TO MAKE
7830 044650 000415          BR       1$          ;;ESCAPE TO THE NEXT TEST
7831 044652 032737 004000 177570 3$:  BIT     #BIT11,2#SWR  ;;INHIBIT ITERATIONS?
7832 044660 001011          BNE     1$          ;;BR IF YES
7833 044662 105767 134312          TSTB   $PASS       ;;IF FIRST PASS OF PROGRAM
7834 044666 001406          BEQ     1$          ;;INHIBIT ITERATIONS
7835 044670 005267 134312          INC     $ICNT       ;;INCREMENT ITERATION COUNT
7836 044674 026767 134416 134304  CMP     $TIMES,$ICNT  ;;CHECK THE NUMBER OF ITERATIONS MADE
7837 044702 002017          BGE     $OVER       ;;BR IF MORE ITERATION REQUIRED
7838 044704 012767 000001 134274 1$:  MOV     #1,$ICNT     ;;REINITIALIZE THE ITERATION COUNTER
7839 044712 016767 000054 134376  MOV     $SMXCNT,$TIMES  ;;SET NUMBER OF ITERATIONS TO DO
7840 044720 011667 134264          $SVLAD: MOV    (SP),SLPADR  ;;SAVE SCOPE LOOP ADDRESS
7841 044724 011667 134262          MOV    (SP),SLPERR    ;;SAVE ERROR LOOP ADDRESS
7842 044730 005067 134364          CLR     $ESCAPE     ;;CLEAR THE ESCAPE FROM ERROR ADDRESS
7843 044734 112767 000001 134255  MOVB   #1,$SERMAX    ;;ONLY ALLOW ONE(1) ERROR ON NEXT TEST
7844 044742 105767 134236          $OVER: TSTB   SERFLG  ;;ANY ERRORS?
7845 044746 001403          BEQ     1$          ;;BRANCH IF NO
7846 044750 116737 134230 001203  MOVB   SERFLG,2#$STSTNM+1  ;;
7847 044756 016737 134220 177570 1$:  MOV     $STSTNM,2#$DISPLAY  ;;DISPLAY TEST NUMBER
7848 044764 016716 134220          MOV     SLPADR,(SP)  ;;FUDGE RETURN ADDRESS
7849 044770 000002          RTI                    ;;FIXES PS
7850 044772 000010          $SMXCNT: 10          ;;MAX. NUMBER OF ITERATIONS
7851          ;;*****
7852          .SBTTL  ERROR HANDLER ROUTINE
7853
7854          ;;THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT,
7855          ;;SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
7856          ;;AND GO TO SERRTYP ON ERROR
7857          ;;THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
7858          ;;$SW15=1      HALT ON ERROR
7859          ;;              HALT CAN OCCUR BEFORE AND AFTER THE ERROR TYPEOUT
7860          ;;$SW13=1      INHIBIT ERROR TYPEOUTS
7861          ;;$SW10=1      BELL ON ERROR
7862          ;;$SW09=1      LOOP ON ERROR
7863          ;;CALL
7864          ;;          ERROR  N          ;;ERROR=EMT AND N=ERROR ITEM NUMBER
7865
7866          $ERROR:
7867 044774 116737 134204 001203  MOVB   SERFLG,2#$STSTNM+1  ;;
7868 044774 105267 134176 7$:  INCB   SERFLG       ;;SET THE ERROR FLAG
7869 045002 001775          BEQ     7$          ;;DON'T LET THE FLAG GO TO ZERO
7870 045006 016737 134166 177570  MOV     $STSTNM,2#$DISPLAY  ;;DISPLAY TEST NUMBER AND ERROR FLAG
7871 045010 005737 177570          TST    2#SWR        ;;HALT ON ERROR = 1?
7872 045016 100001          BPL    8$          ;;BRANCH IF NO
7873 045022 000000          HALT                    ;;YES--HALT
7874 045024 032737 002000 177570 8$:  BIT     #BIT10,2#SWR  ;;BELL ON ERROR?
7875 045026 001402          BEQ     1$          ;;NO - SKIP
7876 045034 104400          TYPE   ,SBELL       ;;RING BELL
7877

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7879 045046 011667 134146      MOV      (SP),SERRPC      ;;GET ADDRESS OF ERROR INSTRUCTION
7880 045052 162767 000002 134140  SUB      #2,SERRPC
7881 045060 117767 134134 134130  MOVB    JSERRPC,SITEMB    ;;STRIP AND SAVE THE ERROR ITEM CODE
7882 045066 032737 020000 177570  BIT     #BIT13,#SWR      ;;SKIP TYPEOUT IF SET
7883 045074 001004          BNE     2$              ;;SKIP TYPEOUTS
7884 045076 004767 000056          JSR     PC,SERRTYP      ;;GO TO USER ERROR ROUTINE
7885 045102 104400 001327          TYPE   $,SCLF
7886 045106 005737 177570 2$:      TST     #SWR            ;;HALT ON ERROR
7887 045112 100001          BPL     9$              ;;SKIP IF CONTINUE
7888 045114 000000          HALT                    ;;HALT ON ERROR!
7889 045116 022767 036752 132716 9$:      CMP     #SENDAD,42      ;;ACT-11?
7890 045124 001001          BNE     3$              ;;BRANCH IF NO
7891 045126 000000          HALT                    ;;YES
7892 045130 032737 001000 177570 3$:      BIT     #BIT09,#SWR     ;;LOOP ON ERROR SWITCH SET?
7893 045136 001402          BEQ     4$              ;;BR IF NO
7894 045140 016716 134046          MOV     SLPERR,(SP)     ;;FUDGE RETURN FOR LOOPING
7895 045144 005767 134150 4$:      TST     $ESCAPE        ;;CHECK FOR AN ESCAPE ADDRESS
7896 045150 001402          BEQ     5$              ;;BR IF NONE
7897 045152 016716 134142          MOV     $ESCAPE,(SP)   ;;FUDGE RETURN ADDRESS FOR ESCAPE
7898 045156          5$:
7899 045156 000002          RTI                    ;;RETURN
7900          ;*****
7901          .SBTTL  ERROR MESSAGE TYPEOUT ROUTINE
7902
7903          ;*THIS ROUTINE FIRST TYPES A STANDARD MESSAGE CONSISTING OF THE
7904          ;*VIRTUAL PC, THE PHYSICAL PC, THE PSW AT THE TIME OF THE ERROR CALL,
7905          ;*AND THE SUB-PASS COUNT. THE SUB-PASS COUNT CONSISTS OF THE SUB PASS COUNT IN THE
7906          ;*HIGH BYTE AND THE PASS COUNT IN THE LOW BYTE.
7907          ;*
7908          ;*IT THEN USES THE "ITEM CONTROL BYTE" (SITEMB) TO DETERMINE WHICH
7909          ;*ERROR IS TO BE REPORTED. IT THEN OBTAINS, FROM THE "ERROR TABLE"
7910          ;*THE ERROR MESSAGE POINTER AND TYPES THE ERROR MESSAGE. THE DATA
7911          ;*HEADER POINTER IS THEN OBTAINED AND A DATA HEADER IS TYPED.
7912          ;*THE DATA POINTER AND DATA FORMAT ARE THEN OBTAINED. THERE ARE
7913          ;*FOUR TYPES OF DATA FORMAT, AS FOLLOWS:
7914          ;*
7915          ;*      0      TYPE THE CONTENTS OF THE DATA TABLE WORD IN
7916          ;*              6 DIGIT OCTAL FORMAT
7917          ;*      1      CONVERT THE CONTENTS OF THE DATA TABLE WORD TO
7918          ;*              22 BITS AND TYPE AN 8 DIGIT OCTAL NUMBER
7919          ;*      2      TYPE THE CONTENTS OF THE DATA TABLE WORD AND
7920          ;*              THE WORD+2 IN 8 DIGIT OCTAL FORMAT
7921          ;*      3      USE THE CONTENTS OF THE DATA TABLE WORD AS A
7922          ;*              DEVICE ID AND TYPE THE DEVICES NAME
7923          ;*      4      CONVERT THE TWO WORDS POINTED TO BY THE DATA
7924          ;*              TABLE TO FLOATING POINT FORMAT AND TYPE.
7925          ;*      5      CONVERT THE FOUR WORDS POINTED TO BY THE DATA
7926          ;*              TABLE TO FLOATING DOUBLE FORMAT AND TYPE
7927          ;*****
7928
7929 045160 104412          SERRTYP:SAVREG
7930 045162 104400 001327          TYPE   $,SCLF          ;;"CARRIAGE RETURN" & "LINE FEED"
7931 045166 004737 046660          JSR     PC,#TYPTIME    ;GO TYPE THE TIME
7932 045172 104400 053662          TYPE   ,MSG3
7933 045176 104400 001327          TYPE   $,SCLF

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7991 045466 012067 000004      3$:  MOV      (R0)+,4$      ;; PICKUP "DATA HEADER" POINTER
7992 045472 001404              BEQ      5$              ;; SKIP TYPEOUT IF 0
7993 045474 104400              TYPE                    ;; TYPE THE "DATA HEADER"
7994 045476 000000      4$:  .WORD    0              ;; "DATA HEADER" POINTER GOES HERE
7995 045500 104400 001327      TYPE      SCRLF          ;; "CARRIAGE RETURN" & "LINE FEED"
7996 045504 012001      5$:  MOV      (R0)+,R1        ;; PICKUP "DATA TABLE" POINTER
7997 045506 001004              BNE      7$              ;; GO TYPE THE DATA
7998 045510 104414      6$:  RESREG   PC              ;; "CARRIAGE RETURN" & "LINE FEED"
7999 045512 104400 001327      TYPE      SCRLF          ;; "CARRIAGE RETURN" & "LINE FEED"
8000 045516 000207              RTS      PC              ;; RETURN
8001 045520 011002      7$:  MOV      (R0),R2        ;; GET "DATA FORMAT" POINTER
8002 045522 122712 000001      10$: CMPB    #1,(R2)        ;; DATA FORMAT 1?
8003 045526 001424              BEQ      9$              ;; BRANCH IF YES
8004 045530 122712 000002      CMPB    #2,(R2)        ;; DATA FORMAT 2?
8005 045534 001441              BEQ      11$             ;; BRANCH IF YES
8006 045536 122712 000003      CMPB    #3,(R2)        ;; DATA FORMAT 3?
8007 045542 001445              BEQ      24$             ;; BRANCH IF YES
8008 045544 122712 000004      CMPB    #4,(R2)        ;; DATA FORMAT 4?
8009 045550 001456              BEQ      40$             ;; BRANCH IF YES
8010 045552 122712 000005      CMPB    #5,(R2)        ;; DATA FORMAT 5?
8011 045556 001465              BEQ      60$             ;; BRANCH IF YES
8012                                     ;; *****
8013 :DATA FORMAT 0
8014 045560 005202              INC      R2              ;; INCREMENT FORMAT POINTER
8015 045562 013146              MOV      @R1+,-(SP)     ;; PUSH DATA TO BE TYPED
8016 045564 104402              TYPOC                    ;;
8017 045566 005711      13$: TST      (R1)          ;; ANY MORE DATA?
8018 045570 001747              BEQ      6$              ;; BRANCH IF NO
8019 045572 104400 046432      TYPE      8$            ;; TYPE TWO SPACES
8020 045576 000751              BR       10$
8021                                     ;; *****
8022 :DATA FORMAT 1
8023 045600 005202      9$:  INC      R2              ;; INCREMENT FORMAT POINTER
8024 045602 004737 051644      JSR      PC,@#CNVADR    ;; GET 22 BIT ADR
8025 045606 012746 001476      14$: MOV      @PA1500,-(SP)  ;; PUSH ADR OF 22 BIT ADR
8026 045612 004737 047712      JSR      PC,@#SDB20    ;; CONVERT TO ASCII
8027 045616 062716 000003      ADD     #3,(SP)        ;; DELETE LEADING ZEROS
8028 045622 012667 000002      MOV     (SP)+,12$      ;; GET ADR OF ASCII STRING
8029 045626 104400              TYPE                    ;;
8030 045630 000000      12$: .WORD    0              ;;
8031 045632 062701 000002      ADD     #2,R1          ;; INCREMENT R1
8032 045636 000753              BR       13$
8033                                     ;; *****
8034 :DATA FORMAT 2
8035 045640 005202      11$: INC      R2              ;; INCREMENT FORMAT POINTER
8036 045642 011100              MOV     (R1),R0
8037 045644 012037 001476      MOV     (R0)+,@#PA1500
8038 045650 011037 001500      MOV     (R0)+,@#PA2116
8039 045654 000754              BR       14$
8040                                     ;; *****
8041 :DATA FORMAT 3
8042 045656 005202      24$: INC      R2              ;; INCREMENT FORMAT POINTER
8043 045660 013167 000016      MOV     @R1+,25$      ;; GET DEVICE ID
8044 045664 062767 053612      ADD     #MSGINX,25$   ;; FORM ADR OF ASCII ADR
8045 045672 017767 000004 000002      MOV     @25$,25$     ;; GET ADR OF ASCII

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8047 045702 000000
8048 045704 000730
8049
8050
8051 045706 005202
8052 045710 012167 000002
8053 045714 104416
8054 045716 000000
8055 045720 012667 000002
8056 045724 104400
8057 045726 000000
8058 045730 000716
8059
8060
8061 045732 005202
8062 045734 012167 000002
8063 045740 104420
8064 045742 000000
8065 045744 012667 000002
8066 045750 104400
8067 045752 000000
8068 045754 000704
8069
8070
8071 045756 010300
8072 045760 062700 053612
8073 045764 011067 000002
8074 045770 104400
8075 045772 000000
8076 045774 104400 046002
8077 046000 000404
8078
8079 046012
8080 046012 010300
8081 046014 022700 000010
8082 046020 003403
8083 046022 104400 054116
8084 046026 000411
8085
8086
8087 046030 022703 000020
8088 046034 001426
8089 046036 002435
8090 046040 104400 054231
8091 046044 022700 000012
8092 046050 001140
8093
8094
8095 046052 062700 053566
8096 046056 011000
8097 046060 022703 000002
8098 046064 001404
8099 046066 100406
8100 046070 012704 000007
8101 046074 000423

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25$: .WORD 13$ ;CONTINUE
BR 13$
;*****
;DATA FORMAT 4
40$: INC R2
MOV (R1)+,44$ ;GET ADDRESS OF DATA
FL20 ;CONVERT TO FLOATING FORMAT
44$: .WORD
MOV (SP)+,45$ ;GET ADDRESS OF ASCIZ STRING
TYPE ;TYPE THE DATA
45$: .WORD
BR 13$
;*****
;DATA FORMAT 5
60$: INC R2 ;INCREMENT FORMAT POINTER
MOV (R1)+,61$ ;GET ADDRESS OF DATA
FLD20 ;CONVERT TO FLOATING ASCIZ
61$: .WORD
MOV (SP)+,62$ ;GET ADDRESS OF ASCIZ STRING
TYPE ;TYPE THE DATA
62$: .WORD
BR 13$
;*****
;ERROR 7 DECODE
15$: MOV R3,RO ;SAVE R3
ADD #MSGINX,RO ;GEN ADRS OF ASCIZ
MOV (RO),16$
TYPE
16$: .WORD
TYPE 65$ ;:TYPE ASCIZ STRING
BR 64$ ;:GET OVER THE ASCIZ
;65$: .ASCIZ /FAILED/<CRLF>
64$: MOV R3,RO ;SAVE DEVICE ID
CMP #10,RO ;:MASS BUS DEVICE?
BLE 17$ ;:BRANCH IF YES
TYPE MSG12
BR 18$
;*****
;MASS BUS ERR
17$: CMP #20,R3 ;:MBT ERROR?
BEQ 25$ ;:BRANCH IF MBT ERROR
BLT 27$ ;:BRANCH IF UBE ERROR
TYPE MSG13
CMP #12,RO ;:WAS IT RS?
BNE 29$ ;:BRANCH IF NO
;*****
;UNIBUS ERROR OR RS04 ERROR
18$: ADD #REGINX,RO ;:FORM ADR OF REG TABLE
MOV (RO),RO ;:GET ADR OF REG TABLE
CMP #2,R3 ;:RP3 OR RK?
BEQ 20$ ;:BRANCH IF RK
BMI 21$ ;:BRANCH IF NOT RPO3
MOV #7,R4 ;:SET RPO3 SOB COUNT
BR 22$

```

8103 046102 000420  
8104 046104 012704 000011  
8105 046110 000415  
8106  
8107  
8108 046112 104400 054420  
8109 046116 012704 000011  
8110 046122 062700 053566  
8111 046126 011000  
8112 046130 000405  
8113  
8114 046132 104400 054527  
8115 046136 012704 000004  
8116 046142 000767  
8117 046144 013046  
8118 046146 104402  
8119 046150 104400 046432  
8120 046154 077405  
8121  
8122  
8123 046156 022703 000022  
8124 046162 001454  
8125 046164 022703 000002  
8126 046170 002445  
8127 046172 001005  
8128  
8129 046174 104400 054723  
8130 046200 012700 002126  
8131 046204 000404  
8132  
8133 046206 012700 002104  
8134 046212 104400 054733  
8135  
8136 046216 013001  
8137 046220 005720  
8138 046222 013037 001672  
8139 046226 072127 177774  
8140 046232 042701 177774  
8141 046236 010137 001674  
8142 046242 162737 000002 001672 74S:  
8143 046250 005637 001674  
8144 046254 004737 051536  
8145 046260 012746 001476  
8146 046264 004737 047712  
8147 046270 062716 000003  
8148 046274 012667 000002  
8149 046300 104400  
8150 046302 000000  
8151 046304 104400 001327

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21S: BR 22S
      MOV 811,R4 ;SET RS04 SOB COUNT
      BR 22S
;*****
;MBT ERROR
26S: TYPE MSG16
      MOV 811,R4 ;SET MBT SOB COUNT
      ADD 8REGINX,RO
      MOV (RO),RO ;GET ADR OF MBT TABLE
      BR 22S ;GO TYPE REGISTERS
;UNIBUS EXERCISER ERROR
27S: TYPE MSG17
      MOV 84,R4 ;SET UBE SOB COUNT
      BR 28S ;GO TYPE UBE REGISTERS
22S: MOV 2(RO)+,-(SP) ;GET DATA IN REG
      TYPOC ;TYPE IT
      TYPE 8S ;TYPE TWO SPACES
      SOB R4,22S ;CONTINUE
;*****
;THIS CODE TYPES A PHYSICAL BUS ADDRESS IF THE ERROR WAS AN RPO3, RK05, OR UBE
      CMP 822,R3 ;UBE ERROR?
      BEQ 73S ;BRANCH IF YES
      CMP 82,R3 ;RK05?
      BLT 32S ;BRANCH IF NOT RK OR RPO3
      BNE 70S ;BRANCH IF RPO3
;RK05 ERROR
      TYPE MSG22
      MOV 8RKCS,RO ;GET ADR OF ADR OF RKCS REG
      BR 71S
;RPO3 ERROR
70S: MOV 8RP3CS,RO ;GET ADR OF ADR OF RP3CS REG
      TYPE MSG23
;GET, CALCULATE, & TYPE PHYSICAL BUS ADDRESS
71S: MOV 2(RO)+,R1 ;GET BUS ADR EXTENDED BITS
      TST (RO)+ ;ADJUST RO
      MOV 2(RO)+,28ERRBA ;GET BUS ADDRESS THAT FAILED
      ASH 8-4,R1 ;GET BITS 4&5 INTO BITS 0&1
      BIC 8177774,R1 ;GET RID OF UNUSED BITS
      MOV R1,28ERRBA+2 ;SAVE EXTENDED BITS
      SUB 82,28ERRBA ;DECREMENT BUS ADR
      SBC 28ERRBA+2
      JSR PC,28PHYMAP ;GO CONVERT TO 22 BIT PHYSICAL
      MOV 8PA1500,-(SP)
      JSR PC,28SDB20 ;CONVERT TO ASCIZ STRING
      ADD 83,(SP) ;GET RID OF LEADING ZEROS
      MOV (SP)+,72S
72S: .WORD
32S: TYPE ,SCLF

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8153  
8154 046314 012700 002232  
8155 046320 013037 001672  
8156 046324 013037 001674  
8157 046330 042737 177774  
8158 046336 162737 000002  
8159 046344 005637 001674  
8160 046350 000734  
8161  
8162  
8163 046352 062700 053566  
8164 046356 011000  
8165 046360 012704 000011  
8166 046364 013046  
8167 046366 104402  
8168 046370 104400 046432  
8169 046374 077405  
8170 046376 104400 001327  
8171 046402 104400 001327  
8172 046406 012704 000004  
8173 046412 104400 054341  
8174 046416 013046  
8175 046420 104402  
8176 046422 104400 046432  
8177 046426 077405  
8178 046430 000725  
8179 046432 020040 000  
8180 046436  
8181  
8182  
8183  
8184  
8185  
8186  
8187  
8188  
8189  
8190  
8191  
8192  
8193  
8194  
8195  
8196  
8197  
8198  
8199  
8200  
8201  
8202  
8203 046436 105767 132611  
8204 046442 100002  
8205 046444 000000  
8206 046446 000407  
8207 046450 010046

```

:GET UBE VIRTUAL ADDRESS
73$: MOV @UBETBL+2,RO ;GET ADR OF UBE TABLE +2
MOV @ (RO)+,@ERRBA ;GET BUS ADR THAT FAILED
MOV @ (RO)+,@ERRBA+2 ;GET BAE BITS
BIC @177774,@ERRBA+2 ;MASK OFF ADR BITS
SUB @2,@ERRBA
SBC @ERRBA+2
BR 74$ ;GO CONVERT & TYPE PHYSICAL ADR
;*****
:RPO4 ERROR
29$: ADD @REGINX,RO
MOV (RO),RO ;FORM ADR OF RPO4 TABLE
MOV @11,R4 ;SET SOB COUNT
31$: MOV @ (RO)+,-(SP) ;GET DATA TO BE TYPED
TYPCC ;TYPE DATA
TYPE BS ;CONTINUE
SOB R4,31$
TYPE ,SCLF
TYPE ,SCLF ;SET SOB COUNT
MOV @4,R4
TYPE MSG14
50$: MOV @ (RO)+,-(SP) ;GET DTA TO BE TYPED
TYPCC ;TYPE IT
TYPE BS ;CONTINUE
SOB R4,50$
BR 32$
8$: .ASCIZ / / ;TWO(2) SPACES
.EVEN
;*****
.SBTTL TYPE ROUTINE
;#ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
;#THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
;#NOTE1: #NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
;#NOTE2: #FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
;#NOTE3: #FILLC CONTAINS THE CHARACTER TO FILL AFTER.
;#
;#CALL:
;#1) USING A TRAP INSTRUCTION
;# TYPE ,MESADR ;;MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
;#OR
;# TYPE
;# MESADR
;#2) USING A JSR INSTRUCTION
;# MOV PS,-(SP) ;;PUSH PROCESSOR STATUS WORD ON THE STACK
;# JSR PC,$TYPE ;;CALL TYPE ROUTINE
;# MESADDR ;;FIRST ADDRESS OF MESSAGE
$TYPE: TSTB $TPFLG ;;IS THERE A TERMINAL?
BPL IS ;;BR IF YES
HALT ;;HALT HERE IF NO TERMINAL
BR 3$ ;;LEAVE
1$: MOV RO,-(SP) ;;SAVE RO

```



TYPE ROUTINE

```

8209 046456 112046      2$:  MOVB      (RO)+, -(SP)      ;; PUSH CHARACTER TO BE TYPED ONTO STACK
8210 046460 001005      BNE      4$                    ;; BR IF IT ISN'T THE TERMINATOR
8211 046462 005726      TST      (SP)+                  ;; IF TERMINATOR POP IT OFF THE STACK
8212 046464 012600      MOV      (SP)+, RO              ;; RESTORE RO
8213 046466 062716 000002      3$:  ADD      #2, (SP)            ;; ADJUST RETURN PC
8214 046472 000002      RTI                               ;; RETURN
8215 046474 122716 000011      4$:  CMPB      #HT, (SP)          ;; BRANCH IF <HT>
8216 046500 001426      BEQ      8$                    ;;
8217 046502 122716 000200      CMPB      #CRLF, (SP)         ;; BRANCH IF NOT
8218 046506 001004      BNE      5$                    ;;
8219 046510 005726      TST      (SP)+                  ;; POP <CR><LF> EQUIV
8220 046512 104400 001327      TYPE     $CRLF                  ;;
8221 046516 000757      BR      2$                    ;; GET NEXT CHARACTER
8222 046520 004767 000056      5$:  JSR      PC, $TYPEPC         ;; GO TYPE THIS CHARACTER
8223 046524 126726 132522      6$:  CMPB      $FILLC, (SP)+      ;; IS IT TIME FOR FILLER CHARS.?
8224 046530 001352      BNE      2$                    ;; IF NO GO GET NEXT CHAR.
8225 046532 016746 132512      MOV      $NULL, -(SP)         ;; GET # OF FILLER CHARS. NEEDED
8226                                     AND THE NULL CHAR.
8227 046536 105366 000001      7$:  DECB      1(SP)              ;; DOES A NULL NEED TO BE TYPED?
8228 046542 002770      BLT      6$                    ;; BR IF NO--GO POP THE NULL OFF OF STACK
8229 046544 004767 000032      JSR      PC, $TYPEPC         ;; GO TYPE A NULL
8230 046550 105367 000100      DECB      $CHARCNT            ;; DON'T COUNT THE NULL AS A CHARACTER
8231 046554 000770      BR      7$                    ;; LOOP
8232
8233                                     ;; HORIZONTAL TAB PROCESSOR
8234
8235 046556 112716 000040      8$:  MOVB      #' (SP)            ;; REPLACE TAB WITH SPACE
8236 046562 004767 000014      9$:  JSR      PC, $TYPEPC         ;; TYPE A SPACE
8237 046566 132767 000007 000060      BITB      #7, $CHARCNT        ;; BRANCH IF NOT AT
8238 046574 001372      BNE      9$                    ;; TAB STOP
8239 046576 005726      TST      (SP)+                  ;; POP SPACE OFF STACK
8240 046600 000726      BR      2$                    ;; GET NEXT CHARACTER
8241 046602 005737 001466      $TYPEPC: TST      $NOTYPE         ;; INHIBIT TYPING?
8242 046606 100423      BMI      $TYPEX                ;; BRANCH IF YES
8243 046610 105777 132430      TSTB      $STPS                ;; WAIT UNTIL PRINTER IS READY
8244 046614 100372      BPL      $TYPEPC                ;;
8245 046616 116677 000002 132422      MOVB      2(SP), $STPB         ;; LOAD CHAR TO BE TYPED INTO DATA REG.
8246 046624 122766 000015 000002      CMPB      #CR, 2(SP)          ;; BRANCH IF
8247 046632 001003      BNE      1$                    ;; NOT <CR>
8248 046634 105067 000014      CLRB      $CHARCNT            ;;
8249 046640 000406      BR      $TYPEX                ;; EXIT
8250 046642 122766 000012 000002      1$:  CMPB      #LF, 2(SP)         ;; BRANCH IF
8251 046650 001402      BEQ      $TYPEX                ;; <LF>
8252 046652 105227      INCB      (PC)+                ;; INC SPACE
8253 046654 000000      $CHARCNT: .WORD 0              ;; COUNT
8254 046656 000207      $TYPEX:  RTS      PC
8255
8256                                     ;; *****
8257                                     .SBTTL  ROUTINE TO TYPE THE ELAPSED RUN TIME OF THE PROGRAM
8258                                     ;; THIS ROUTINE CONVERTS THE CONTENTS OF LOCATIONS "LTICKS"
8259                                     ;; AND "MTICKS" TO SECONDS AND MINUTES/HOURS RESPECTIVELY
8260                                     ;; AND TYPES THEM IN THE FOLLOWING FORMAT:
8261                                     ;;          HH:MM:SS
8262                                     ;; *****
8263 046660 104412      $TIME: SAVREG

```

8265 046666 113701 001603  
8266 046672 005000  
8267 046674 071027 000012  
8268 046700 062701 000060  
8269 046704 110137 047116  
8270 046710 010001  
8271 046712 005000  
8272 046714 071027 000006  
8273 046720 062701 000060  
8274 046724 110137 047115  
8275 046730 013701 001600  
8276 046734 005000  
8277 046736 071027 000012  
8278 046742 062701 000060  
8279 046746 110167 000141  
8280 046752 010001  
8281 046754 005000  
8282 046756 071027 000006  
8283 046762 062701 000060  
8284 046766 110167 000120  
8285 046772 005700  
8286 046774 001434  
8287 046776 010001  
8288 047000 005000  
8289 047002 071027 000012  
8290 047006 062701 000060  
8291 047012 110167 000072  
8292 047016 005700  
8293 047020 001422  
8294 047022 010001  
8295 047024 005000  
8296 047026 071027 000010  
8297 047032 062701 000060  
8298 047036 110167 000045  
8299 047042 005700  
8300 047044 001410  
8301 047046 010001  
8302 047050 005000  
8303 047052 071027 000012  
8304 047056 062701 000060  
8305 047062 110167 000020  
8306 047066 104400 047106  
8307 047072 104400 001327  
8308 047076 004737 053216  
8309 047102 104414  
8310 047104 000207  
8311 047106 001 001 001  
8312 047111 072 001 001  
8313 047114 072 060 060  
8314 047117 000  
8315  
8316  
8317  
8318  
8319

```

MOV B J#LTICKS+1,R1 ;GET SECOND COUNT
CLR RO
DIV #10.,RO
ADD #60,R1
MOV B R1,J#TIMEBUF+10
MOV RO,R1
CLR RO
DIV #6.,RO
ADD #60,R1
MOV B R1,J#TIMEBUF+7
MOV J#MTICKS,R1 ;GET MINUTE COUNT
CLR RO
DIV #10.,RO ;GET HOURS AND MINUTES
ADD #60,R1 ;MAKE REMAINDER ASCII
MOV B R1,↑TIMEBUF+5 ;PUT IN BUFFER
MOV RO,R1
CLR RO
DIV #6.,RO
ADD #60,R1
MOV B R1,↑TIMEBUF+4
TST RO
BEQ 2$
MOV RO,R1
CLR RO
DIV #10.,RO
ADD #60,R1
MOV B R1,↑TIMEBUF+2
TST RO
BEQ 2$
MOV RO,R1
CLR RO
DIV #10.,RO
ADD #60,R1
MOV B R1,↑TIMEBUF+1
TST RO
BEQ 2$
MOV RO,R1
CLR RO
DIV #10.,RO
ADD #60,R1
MOV B R1,↑TIMEBUF
TYPE ↑TIMEBUF
TYPE ↑SCRLF
JSR PC,J#RESKT ;GO BACK TO ORIGINAL MEMORY
RESREG
RTS PC
TIMEBUF: .BYTE 1,1,1,72,1,1,72,60,60,0

```

```

.EVEN
:*****
:SBTTL ROUTINE TO TYPE THE AVAILABLE DEVICES AND UNIT NUMBERS
:* THIS ROUTINE SEARCHES THE SYSTEM SIZE TABLE FOR NON-
:* ZERO ENTRIES.WHEN IT FINDS ONE, IT TYPES THE NAME OF THE

```

8321  
8322  
8323 047120 104400 056372  
8324 047124 104400 053751  
8325 047130 012700 000010  
8326 047134 005001  
8327 047136 105761 001606  
8328 047142 001004  
8329 047144 062701 000002  
8330 047150 077006  
8331 047152 000207  
8332 047154 010102  
8333 047156 062702 053612  
8334 047162 011267 000002  
8335 047166 104400  
8336 047170 000000  
8337 047172 112767 000060 000034  
8338 047200 116102 001606  
8339 047204 012703 000010  
8340 047210 006002  
8341 047212 103002  
8342 047214 104400 047234  
8343 047220 005267 000010  
8344 047224 077307  
8345 047226 104400 001327  
8346 047232 000744  
8347 047234 000 054 040  
8348 047237 000  
8349  
8350  
8351  
8352  
8353  
8354  
8355  
8356  
8357  
8358  
8359  
8360  
8361  
8362  
8363  
8364  
8365  
8366  
8367  
8368  
8369  
8370  
8371  
8372  
8373  
8374  
8375 047240 017646 000000

```

;* AVAILABLE FOR THAT DEVICE.
;*****
TYPsiz: TYPE ,SWITCH
        TYPE ,MSG4
        MOV #10,R0 ;SET SOB COUNT
        CLR R1
        1$: TSTB SYSSIZE(R1) ;DEVICE AVAILABLE?
           BNE 2$ ;BRANCH IF YES
        7$: ADD #2,R1 ;INCREMENT INDEX
           SOB RO,1$ ;CONTINUE
           RTS PC ;RETURN
        2$: MOV R1,R2 ;GET INDEX
           ADD #MSGINX,R2 ;GET ADR OF MESSAGE ADR
           MOV (R2),3$ ;GET ADDRESS OF MESSAGE
        3$: .WORD
           MOV# #60,4$ ;INIT UNIT NO. BUFFER (ASCII)
           MOV# SYSSIZE(R1),R2 ;GET WORD WITH AVAILABLE UNITS
           MOV #10,R3 ;SET SOB COUNT
        6$: ROR R2 ;GET UNITS
           BCC 5$ ;BRANCH IF NOT A UNIT
        5$: INC 4$
           SOB R3,6$ ;CONTINUE
           TYPE #SCRLF
        4$: BR 7$
           .BYTE 0,54,40,0 ;NUMBER, COMMA, SPACE, TERMINATOR
;*****
.SBTTL BINARY TO OCTAL (ASCII) AND TYPE
;*THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT
;*OCTAL (ASCII) NUMBER AND TYPE IT.
;*STYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE
;*CALL:
;* MOV NUM,-(SP) ;:NUMBER TO BE TYPED
;* TYPOS ;:CALL FOR TYPEOUT
;* .BYTE N ;:N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE
;* .BYTE M ;:M=1 OR 0
; ;:1=TYPE LEADING ZEROS
; ;:0=SUPPRESS LEADING ZEROS
;*
;*STYPON----ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST
;*STYPOS OR STYPOC
;*CALL:
;* MOV NUM,-(SP) ;:NUMBER TO BE TYPED
;* TYPON ;:CALL FOR TYPEOUT
;*
;*STYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER
;*CALL:
;* MOV NUM,-(SP) ;:NUMBER TO BE TYPED
;* TYPOC ;:CALL FOR TYPEOUT
;*
STYPOS: MOV 2(SP),-(SP) ;:PICKUP THE MODE

```

```

8377 047252 112667 000207      MOVB      (SP)+,$OMODE+1  ;;NUMBER OF DIGITS TO TYPE
8378 047256 062716 000002      ADD       #2,(SP)        ;;ADJUST RETURN ADDRESS
8379 047262 000406                BR        $TYPON
8380 047264 112767 000001 000171 $TYP0C: MOVB      #1,$OFILL          ;;SET THE ZERO FILL SWITCH
8381 047272 112767 000006 000165      MOVB      #6,$OMODE+1    ;;SET FOR SIX(6) DIGITS
8382 047300 112767 000005 000154 $TYPON: MOVB      #5,$OCNT          ;;SET THE ITERATION COUNT
8383 047306 010346                MOV       R3,-(SP)        ;;SAVE R3
8384 047310 010446                MOV       R4,-(SP)        ;;SAVE R4
8385 047312 010546                MOV       R5,-(SP)        ;;SAVE R5
8386 047314 116704 000145      MOVB      $OMODE+1,R4    ;;GET THE NUMBER OF DIGITS TO TYPE
8387 047320 005404                NEG       R4
8388 047322 062704 000006      ADD       #6,R4          ;;SUBTRACT IT FOR MAX. ALLOWED
8389 047326 110467 000132      MOVB      R4,$OMODE      ;;SAVE IT FOR USE
8390 047332 116704 000125      MOVB      $OFILL,R4      ;;GET THE ZERO FILL SWITCH
8391 047336 016605 000012      MOV       12(SP),R5      ;;PICKUP THE INPUT NUMBER
8392 047342 005003                CLR       R3              ;;CLEAR THE OUTPUT WORD
8393 047344 006105                1$:      ROL       R5          ;;ROTATE MSB INTO "C"
8394 047346 000404                BR        3$              ;;GO DO MSB
8395 047350 006105                2$:      ROL       R5          ;;FORM THIS DIGIT
8396 047352 006105                ROL       R5
8397 047354 006105                ROL       R5
8398 047356 010503                MOV       R5,R3
8399 047360 006103                3$:      ROL       R3          ;;GET LSB OF THIS DIGIT
8400 047362 105367 000076      DECB      $OMODE         ;;TYPE THIS DIGIT?
8401 047366 100016                BPL      7$              ;;BR IF NO
8402 047370 042703 177770      BIC       #177770,R3     ;;GET RID OF JUNK
8403 047374 001002                BNE      4$              ;;TEST FOR 0
8404 047376 005704                TST      R4              ;;SUPPRESS THIS 0?
8405 047400 001403                BEQ      5$              ;;BR IF YES
8406 047402 005204                4$:      INC       R4          ;;DON'T SUPPRESS ANYMORE 0'S
8407 047404 052703 000060      BIS       #'0,R3         ;;MAKE THIS DIGIT ASCII
8408 047410 052703 000040                5$:      BIS       #' ,R3        ;;MAKE ASCII IF NOT ALREADY
8409 047414 110367 000040      MOVB      R3,$S          ;;SAVE FOR TYPING
8410 047420 104400 047460      TYPE      $S             ;;GO TYPE THIS DIGIT
8411 047424 105367 000032                7$:      DECB      $OCNT         ;;COUNT BY 1
8412 047430 003347                BGT      2$              ;;BR IF MORE TO DO
8413 047432 002402                BLT      6$              ;;BR IF DONE
8414 047434 005204                INC      R4              ;;INSURE LAST DIGIT ISN'T A BLANK
8415 047436 000744                BR        2$              ;;GO DO THE LAST DIGIT
8416 047440 012605                6$:      MOV       (SP)+,R5      ;;RESTORE R5
8417 047442 012604                MOV       (SP)+,R4      ;;RESTORE R4
8418 047444 012603                MOV       (SP)+,R3      ;;RESTORE R3
8419 047446 016666 000002 000004      MOV       2(SP),4(SP)    ;;SET THE STACK FOR RETURNING
8420 047454 012616                MOV       (SP)+,(SP)
8421 047456 000002                RTI
8422 047460                8$:      .BYTE    0              ;;RETURN
8423 047461                .BYTE    0              ;;STORAGE FOR ASCII DIGIT
8424 047462                .BYTE    0              ;;TERMINATOR FOR TYPE ROUTINE
8425 047463                .BYTE    0              ;;OCTAL DIGIT COUNTER
8426 047464 000000                .WORD    0              ;;ZERO FILL SWITCH
8427                .WORD    0              ;;NUMBER OF DIGITS TO TYPE
8428                ;;*****
8429                .SBTTL  CONVERT BINARY TO DECIMAL AND TYPE ROUTINE
8430                ;*THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 5-DIGIT
8431

```

```

8433      ;*NUMBER IS POSITIVE OR NEGATIVE A SPACE OR A MINUS SIGN WILL BE TYPED
8434      ;*BEFORE THE FIRST DIGIT OF THE NUMBER. LEADING ZEROS WILL ALWAYS BE
8435      ;*REPLACED WITH SPACES.
8436      ;*CALL:
8437      ;*      MOV      NUM,-(SP)      ;: PUT THE BINARY NUMBER ON THE STACK
8438      ;*      TYPDS      ;: GO TO THE ROUTINE
8439
8440      $TYPDS:
8441      MOV      R0,-(SP)      ;: PUSH R0 ON STACK
8442      MOV      R1,-(SP)      ;: PUSH R1 ON STACK
8443      MOV      R2,-(SP)      ;: PUSH R2 ON STACK
8444      MOV      R3,-(SP)      ;: PUSH R3 ON STACK
8445      MOV      R5,-(SP)      ;: PUSH R5 ON STACK
8446      MOV      #20200,-(SP)    ;: SET BLANK SWITCH AND SIGN
8447      MOV      20(SP),R5      ;: GET THE INPUT NUMBER
8448      BPL      1$           ;: BR IF INPUT IS POS.
8449      NEG      R5           ;: MAKE THE BINARY NUMBER POS.
8450      MOVVB   #'-,1(SP)      ;: MAKE THE ASCII NUMBER NEG.
8451      CLR      R0           ;: ZERO THE CONSTANTS INDEX
8452      MOV      #SDBLK,R3      ;: SETUP THE OUTPUT POINTER
8453      MOVVB   #' ,(R3)+      ;: SET THE FIRST CHARACTER TO A BLANK
8454      CLR      R2           ;: CLEAR THE BCD NUMBER
8455      MOV      $DTBL(R0),R1   ;: GET THE CONSTANT
8456      SUB      R1,R5        ;: FORM THIS BCD DIGIT
8457      BLT     4$           ;: BR IF DONE
8458      INC     R2           ;: INCREASE THE BCD DIGIT BY 1
8459      BR      3$
8460      ADD     R1,R5        ;: ADD BACK THE CONSTANT
8461      TST     R2           ;: CHECK IF BCD DIGIT=0
8462      BNE     5$           ;: FALL THROUGH IF 0
8463      TSTB   (SP)         ;: STILL DOING LEADING 0'S?
8464      BMI     7$           ;: BR IF YES
8465      ASLB   (SP)         ;: MSD?
8466      BCC     6$           ;: BR IF NO
8467      MOVB   1(SP),-1(R3)    ;: YES--SET THE SIGN
8468      BIS    #'0,R2        ;: MAKE THE BCD DIGIT ASCII
8469      BIS    #' ,R2        ;: MAKE IT A SPACE IF NOT ALREADY A DIGIT
8470      MOVB   R2,(R3)+      ;: PUT THIS CHARACTER IN THE OUTPUT BUFFER
8471      TST    (R0)+        ;: JUST INCREMENTING
8472      CMP    R0,#10        ;: CHECK THE TABLE INDEX
8473      BLT    2$           ;: GO DO THE NEXT DIGIT
8474      BGT    8$           ;: GO TO EXIT
8475      MOV    R5,R2        ;: GET THE LSD
8476      BR     6$           ;: GO CHANGE TO ASCII
8477      TSTB   (SP)+        ;: WAS THE LSD THE FIRST NON-ZERO?
8478      BPL    9$           ;: BR IF NO
8479      MOVB   -1(SP),-2(R3)   ;: YES--SET THE SIGN FOR TYPING
8480      CLRB   (R3)         ;: SET THE TERMINATOR
8481      MOV    (SP)+,R5      ;: POP STACK INTO R5
8482      MOV    (SP)+,R3      ;: POP STACK INTO R3
8483      MOV    (SP)+,R2      ;: POP STACK INTO R2
8484      MOV    (SP)+,R1      ;: POP STACK INTO R1
8485      MOV    (SP)+,R0      ;: POP STACK INTO R0
8486      TYPE   $DBLK        ;: NOW TYPE THE NUMBER
8487      MOV    2(SP),4(SP)    ;: ADJUST THE STACK

```

8489 047670 000002  
 8490 047672 023420  
 8491 047674 001750  
 8492 047676 000144  
 8493 047700 000012  
 8494 047702 000004  
 8495  
 8496  
 8497  
 8498  
 8499  
 8500  
 8501  
 8502  
 8503  
 8504  
 8505  
 8506  
 8507 047712 104412  
 8508 047714 016601 000002  
 8509 047720 012705 050047  
 8510 047724 012704 000014  
 8511 047730 012703 177770  
 8512 047734 012100  
 8513 047736 012101  
 8514 047740 005002  
 8515 047742 110245  
 8516 047744 010002  
 8517 047746 005304  
 8518 047750 003016  
 8519 047752 001414  
 8520 047754 005205  
 8521 047756 010566 000002  
 8522 047762 122765 000061 000003  
 8523 047770 002003  
 8524 047772 112765 000060 000003  
 8525 050000 104414  
 8526 050002 000207  
 8527 050004 006203  
 8528 050006 006001  
 8529 050010 006000  
 8530 050012 006001  
 8531 050014 006000  
 8532 050016 006001  
 8533 050020 006000  
 8534 050022 040302  
 8535 050024 062702 000060  
 8536 050030 000744  
 8537 050032 000016  
 8538  
 8539  
 8540  
 8541  
 8542  
 8543

```

RTI                                     ;;RETURN TO USER
SDTBL: 10000.
      1000.
      100.
      10.
SDBLK: .BLKW 4
;*****
.SBTTL DOUBLE LENGTH BINARY TO OCTAL ASCII CONVERT ROUTINE
;#THIS ROUTINE WILL CONVERT A 32-BIT UNSIGNED BINARY NUMBER TO AN
;#UNSIGNED OCTAL ASCII NUMBER.
;#CALL
;*   MOV     #PNTR, -(SP)           ;; POINTER TO LOW WORD OF BINARY NUMBER
;*   JSR     PC, J#SDB20          ;; CALL THE ROUTINE
;*   RETURN                                ;; THE ADDRESS OF THE FIRST ASCII CHAR. IS ON THE STACK

SDB20: SAVREG                          ;; SAVE ALL REGISTERS
      MOV     2(SP), R1             ;; PICKUP THE POINTER TO LOW WORD
      MOV     #SDBLK+13., R5        ;; POINTER TO DATA TABLE
      MOV     #12., R4              ;; DO ELEVEN CHARACTERS
      MOV     #17., R3              ;; MASK
      MOV     (R1)+, R0             ;; LOWER WORD
      MOV     (R1)+, R1             ;; HIGH WORD
      CLR     R2                     ;; TERMINATOR
1$:   MOVB    R2, -(R5)              ;; PUT CHARACTER IN DATA TABLE
      MOV     R0, R2                ;; GET THIS DIGIT
      DEC     R4                     ;; COUNT THIS CHARACTER
      BGT     3$,                   ;; BR IF NOT THE LAST DIGIT
      BEQ     2$,                   ;; BR IF IT IS THE LAST DIGIT
      INC     R5                     ;; ALL DIGITS DONE-ADJUST POINTER FOR FIRST
      MOV     R5, 2(SP)              ;; ASCII CHAR. & PUT IT ON THE STACK
      CMPB    #1, 3(R5)              ;; LAST NUMBER LEGAL?
      BGE     4$,                   ;; BRANCH IF YES
      MOVB    #0, 3(R5)              ;; MAKE IT ZERO
4$:   RESREG                          ;; RESTORE ALL REGISTERS
      RTS     PC                     ;; RETURN TO USER
2$:   ASR     R3                       ;; POSITION THE MASK FOR THE LAST DIGIT
3$:   ROR     R1                       ;; POSITION THE BINARY NUMBER FOR
      ROR     R0                       ;; THE NEXT OCTAL DIGIT
      ROR     R1
      ROR     R0
      ROR     R1
      ROR     R0
      BIC     R3, R2                  ;; MASK OUT ALL JUNK
      ADD     #'0, R2                 ;; MAKE THIS CHAR. ASCII
      BR      1$                      ;; GO PUT IT IN THE DATA TABLE
SOCTVL: .BLKB 14.                    ;; RESERVE DATA TABLE
;*****
.SBTTL SAVE AND RESTORE R0-R5 ROUTINES
;#SAVE R0-R5
;#CALL:

```

8545  
 8546  
 8547  
 8548  
 8549  
 8550  
 8551  
 8552  
 8553  
 8554  
 8555  
 8556 050050  
 8557 050050 010046  
 8558 050052 010146  
 8559 050054 010246  
 8560 050056 010346  
 8561 050060 010446  
 8562 050062 010546  
 8563 050064 016646 000022  
 8564 050070 016646 000022  
 8565 050074 016646 000022  
 8566 050100 016646 000022  
 8567 050104 000002  
 8568  
 8569  
 8570  
 8571  
 8572 050106  
 8573 050106 012666 000022  
 8574 050112 012666 000022  
 8575 050116 012666 000022  
 8576 050122 012666 000022  
 8577 050126 012605  
 8578 050130 012604  
 8579 050132 012603  
 8580 050134 012602  
 8581 050136 012601  
 8582 050140 012600  
 8583 050142 000002  
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;;UPON RETURN FROM \$\$SAVREG THE STACK WILL LOOK LIKE:

```

*
*TOP---(+16)
* +2---(+18)
* +4---R5
* +6---R4
* +8---R3
*+10---R2
*+12---R1
*+14---R0
    
```

\$\$SAVREG:

```

MOV R0,-(SP)      ;; PUSH R0 ON STACK
MOV R1,-(SP)      ;; PUSH R1 ON STACK
MOV R2,-(SP)      ;; PUSH R2 ON STACK
MOV R3,-(SP)      ;; PUSH R3 ON STACK
MOV R4,-(SP)      ;; PUSH R4 ON STACK
MOV R5,-(SP)      ;; PUSH R5 ON STACK
MOV 22(SP),-(SP)  ;; SAVE PS OF MAIN FLOW
MOV 22(SP),-(SP)  ;; SAVE PC OF MAIN FLOW
MOV 22(SP),-(SP)  ;; SAVE PS OF CALL
MOV 22(SP),-(SP)  ;; SAVE PC OF CALL
RTI
    
```

;;RESTORE RO-R5

;;CALL:

;;RESREG

\$\$RESREG:

```

MOV (SP)+,22(SP)  ;; RESTORE PC OF CALL
MOV (SP)+,22(SP)  ;; RESTORE PS OF CALL
MOV (SP)+,22(SP)  ;; RESTORE PC OF MAIN FLOW
MOV (SP)+,22(SP)  ;; RESTORE PS OF MAIN FLOW
MOV (SP)+,R5      ;; POP STACK INTO R5
MOV (SP)+,R4      ;; POP STACK INTO R4
MOV (SP)+,R3      ;; POP STACK INTO R3
MOV (SP)+,R2      ;; POP STACK INTO R2
MOV (SP)+,R1      ;; POP STACK INTO R1
MOV (SP)+,R0      ;; POP STACK INTO R0
RTI
    
```

\*\*\*\*\*

;;SBTTL CONVERT FLOATING BINARY TO OCTAL ASCIZ

;;THIS ROUTINE CONVERTS A 32 BIT FLOATING NUMBER TO AN OCTAL  
 ;;ASCIZ STRING IN THE FOLLOWING FORMAT:

```

*
*           W  XXX  YYY  ZZZZZZ
*
*
*
    
```

```

*
*   WHERE  W = SIGN BIT
*           X = 8-BIT EXPONENT (RIGHT JUSTIFIED)
*           Y = FRACTION BITS (57:51) (RIGHT JUSTIFIED)
*           Z = FRACTION BITS (50:35)
*
    
```

;;IT IS ENTERED BY A TRAP CALL WITH THE ADDRESS OF THE FLOATING  
 ;;NUMBER IN THE WORD FOLLOWING THE CALL.  
 ;;IT RETURNS WITH THE ADDRESS OF THE ASCIZ STRING ON THE STACK.

8601	050144	104412			\$FL20:	SAVREG		
8602	050146	017600	000000			MOV	2(SP),R0	:GET ADDRESS OF DATA
8603	050152	062716	000002			ADD	#2,(SP)	:ADJUST RETURN PC
8604	050156	016001	000002			MOV	2(R0),R1	:PUT SECOND DATA WORD IN R1
8605	050162	011000				MOV	(R0),R0	:PUT FIRST DATA WORD IN R0
8606	050164	012704	001357			MOV	#SFLBUFF+23,R4	:GET ADDRESS OF BUFFER END IN R4
8607	050170	112744	000000			MOVB	#0,-(R4)	:PUT TERMINATOR IN BUFFER
8608	050174	012705	000005			MOV	#5,R5	:SET SOB COUNT FOR FRACTION DIGITS
8609	050200	010103			1\$:	MOV	R1,R3	:GET LSB'S OF FRACTION
8610	050202	042703	177770			BIC	#7,R3	:SAVE LS 3 BITS
8611	050206	062703	000060			ADD	#60,R3	:MAKE THEM ASCII
8612	050212	110344				MOVB	R3,-(R4)	:STORE IN BUFFER
8613	050214	073027	177775			ASHC	#-3,R0	:SHIFT NUMBER TO NEXT 3 BITS
8614	050220	077511				SOB	R5,1\$	:CONTINUE FOR 7 DIGITS
8615	050222	010103				MOV	R1,R3	:GET NEXT DIGITS
8616	050224	042703	177776			BIC	#1,R3	:ONLY WANT 1 BIT
8617	050230	062703	000060			ADD	#60,R3	:MAKE THEM ASCII
8618	050234	110344				MOVB	R3,-(R4)	:STORE IN BUFFER
8619	050236	112744	000040			MOVB	#40,-(R4)	:PUT SPACE IN BUFFER
8620	050242	073027	177777			ASHC	#-1,R0	
8621	050246	012705	000002			MOV	#2,R5	:SET SOB COUNT
8622	050252	010103			3\$:	MOV	R1,R3	:GET LOW WORD
8623	050254	042703	177770			BIC	#7,R3	:MASK 3 BITS
8624	050260	062703	000060			ADD	#60,R3	:MAKE THEM ASCII
8625	050264	110344				MOVB	R3,-(R4)	:PUT IN BUFFER
8626	050266	073027	177775			ASHC	#-3,R0	:GET NEXT 3 BITS
8627	050272	077511				SOB	R5,3\$	:CONVERT THEM
8628	050274	010103				MOV	R1,R3	
8629	050276	042703	177776			BIC	#1,R3	:ONLY WANT 1 BIT
8630	050302	062703	000060			ADD	#60,R3	:MAKE IT ASCII
8631	050306	110344				MOVB	R3,-(R4)	:PUT IN BUFFER
8632	050310	112744	000040			MOVB	#40,-(R4)	:PUT SPACE IN BUFFER
8633	050314	112744	000040			MOVB	#40,-(R4)	
8634	050320	072127	177777			ASH	#-1,R1	:GET FIRST 3 BITS OF EXPONENT
8635	050324	012705	000002			MOV	#2,R5	:SET SOB COUNT FOR 2 DIGITS
8636	050330	010103			2\$:	MOV	R1,R3	:GET LSB'S OF EXPONENT
8637	050332	042703	177770			BIC	#7,R3	:SAVE 3 BITS
8638	050336	062703	000060			ADD	#60,R3	:MAKE THEM ASCII
8639	050342	110344				MOVB	R3,-(R4)	:STORE IN BUFFER
8640	050344	072127	177775			ASH	#-3,R1	:GET NEXT 3 BITS
8641	050350	077511				SOB	R5,2\$	:CONTINUE
8642	050352	010103				MOV	R1,R3	:GET LAST 2 BITS OF EXPONENT
8643	050354	042703	177774			BIC	#3,R3	:MAKE SURE ONLY 2 BITS
8644	050360	062703	000060			ADD	#60,R3	:MAKE THEM ASCII
8645	050364	110344				MOVB	R3,-(R4)	:STORE IN BUFFER
8646	050366	112744	000040			MOVB	#40,-(R4)	:PUT SPACE IN BUFFER
8647	050372	112744	000040			MOVB	#40,-(R4)	
8648	050376	042700	177776			BIC	#1,R0	:GET SIGN BIT (IT WAS EXTENDED)
8649	050402	062700	000060			ADD	#60,R0	:MAKE IT ASCII
8650	050406	110044				MOVB	R0,-(R4)	:PUT IT IN THE BUFFER
8651	050410	104414				RESREG		
8652	050412	011646				MOV	(SP),-(SP)	:SAVE RETURN PC
8653	050414	016666	000004	000002		MOV	4(SP),2(SP)	:AND RETURN PSW
8654	050422	012766	001334	000004		MOV	#SFLBUFF,4(SP)	:PUT BUFFER ADDRESS ON STACK
8655	050430	000006				RTT		:RETURN



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8676 050432 104412
8677 050434 017667 000000 000006
8678 050442 062716 000002
8679 050446 104416
8680 050450 000000
8681 050452 012600
8682 050454 010067 130720
8683 050460 062700 000041
8684 050464 105040
8685 050466 016701 177756
8686 050472 062701 000004
8687 050476 012102
8688 050500 012103
8689 050502 012701 000002
8690 050506 012704 000005
8691 050512 010305
8692 050514 042705 177770
8693 050520 062705 000060
8694 050524 110540
8695 050526 073227 177775
8696 050532 077411
8697 050534 010305
8698 050536 042705 177776
8699 050542 062705 000060
8700 050546 110540
8701 050550 112740 000040
8702 050554 073227 177777
8703 050560 077126
8704 050562 104414
8705 050564 011646
8706 050566 016666 000004 000002
8707 050574 016766 130600 000004
8708 050602 000006
8709
8710
8711

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```

*****
.SBTTL CONVERT FLOATING DOUBLE BINARY TO OCTAL ASCIZ
*THIS ROUTINE CONVERTS A 64 BIT FLOATING NUMBER TO AN OCTAL
*ASCIZ STRING IN THE FOLLOWING FORMAT:
*
*      U VVV WWW XXXXXX YYYYYY ZZZZZZ
*
*      WHERE  U = SIGN BIT
*             V = 8-BIT EXPONENT (RIGHT JUSTIFIED)
*             W = FRACTION BITS<57:51> (RIGHT JUSTIFIED)
*             X = FRACTION BITS <50:35>
*             Y = FRACTION BITS <34:19>
*             Z = FRACTION BITS <18:03>
*
*IT IS ENTERED BY A TRAP CALL WITH THE ADDRESS OF THE FLOATING
*NUMBER IN THE WORD FOLLOWING THE CALL.
*IT RETURNS WITH THE ADDRESS OF THE ASCIZ STRING ON THE STACK.
*****
$FLD20: SAVREG
MOV      2(SP),R15      ;GET ADDRESS OF DATA TO CONVERT
ADD      #2,(SP)       ;ADJUST RETURN PC
FL20    ;CONVERT MS 32 BITS
1$:      .WORD
MOV      (SP)+,R0      ;GET ADDRESS OF CONVERTED DATA
MOV      R0,$BUFF     ;SAVE IT
ADD      #4,R0         ;ADJUST TO END OF BUFFER
CLAB    -(R0)         ;PUT TERMINATOR IN BUFFER
MOV      1$,R1        ;GET ADDRESS OF DATA TO CONVERT
ADD      #4,R1        ;ADJUST TO LOWER 32 BITS
MOV      (R1)+,R2     ;SAVE THE DATA
MOV      (R1)+,R3
MOV      #2,R1        ;SET LOOP COUNT
MOV      #5,R4        ;SET LOOP COUNT
3$:      MOV      R3,R5 ;GET LS 32 BITS OF DATA
4$:      BIC      #7,R5 ;MASK 3 BITS
ADD      #6,R5        ;MAKE THEM ASCII
MOVB    R5,-(R0)     ;PUT IN BUFFER
ASHC   #-3,R2        ;GET NEXT 3 BITS
SOB     R4,#4        ;CONTINUE
MOV      R3,R5        ;GET LS 32 BITS
BIC     #1,R5        ;ONLY WANT 1 BIT
ADD     #6,R5        ;MAKE IT ASCII
MOVB   R5,-(R0)     ;PUT IN TABLE
MOVB   #40,-(R0)    ;PUT SPACE IN TABLE
ASHC  #-1,R2
SOB   R1,#3        ;CONVERT NEXT 16 BITS
RESREG
MOV   (SP),-(SP)   ;ADJUST STACK
MOV   4(SP),2(SP) ;TO RETURN WITH ADDRESS
MOV   $BUFF,4(SP) ;OF BUFFER ON STACK
RTT   ;RETURN
*****

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8722 050604
8723 050604 010046
8724 050606 010146
8725 050610 010246
8726 050612 016700 130704
8727 050616 016701 130702
8728 050622 012702 177771
8729 050626 006300
8730 050630 006101
8731 050632 005202
8732 050634 001374
8733 050636 066700 130660
8734 050642 005501
8735 050644 066701 130654
8736 050650 062700 001057
8737 050654 005501
8738 050656 062701 047401
8739 050662 010067 130634
8740 050666 010167 130632
8741 050672 012602
8742 050674 012601
8743 050676 012600
8744 050700 000207
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8751
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8754 050702 012767 000002 000130
8755 050710 016700 000124
8756 050714 012702 001276
8757 050720 012701 000002
8758 050724 004767 177654
8759 050730 022701 000002
8760 050734 001404
8761 050736 022767 000002 000074
8762 050744 001407
8763 050746 016703 130552
8764 050752 042703 000177
8765 050756 022703 100000
8766 050762 001760
8767 050764 016722 130534

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; *THIS ROUTINE IS A DOUBLE PRECISION PSEUDO RANDOM NUMBER GENERATOR
; *WITH A RANGE OF 0 TO 2(+33)-1.
; *CALL:
; *      JSR      PC,SRAND      ;: CALL THE ROUTINE
; *      RETURN     ;: RETURN HERE THE RANDOM
; *                  ;: NUMBER WILL BE IN
; *                  ;: SHINUM,SLONUM
; *
SRAND:
MOV      RO,-(SP)      ;: PUSH RO ON STACK
MOV      R1,-(SP)      ;: PUSH R1 ON STACK
MOV      R2,-(SP)      ;: PUSH R2 ON STACK
MOV      SLONUM,RO     ;: SET RO WITH LOW
MOV      SHINUM,R1     ;: SET R1 WITH HIGH
MOV      #7,R2        ;: SET SHIFT COUNT
1$:      ASL      RO     ;: SHIFT RO LEFT AND
        ROL      R1     ;: ROTATE CARRY INTO R1 AND
        INC      R2     ;: CHECK FOR DONE
        BNE     1$     ;: CONTINUE SHIFT LOOP
        ADD     SLONUM,RO ;: ADD NUMBER TO MAKE X 129
        ADC     R1     ;: PROPOGATE CARRY
        ADD     SHINUM,R1 ;: ADD NUMBER TO MAKE X 129
        ADD     #1057,R0 ;: ADD LOW CONSTANT
        ADC     R1     ;: PROPOGATE CARRY
        ADD     #47401,R1 ;: ADD HIGH CONSTANT
        MOV     RO,SLONUM ;: SAVE RO
        MOV     R1,SHINUM ;: SAVE R1
        MOV     (SP)+,R2 ;: POP STACK INTO R2
        MOV     (SP)+,R1 ;: POP STACK INTO R1
        MOV     (SP)+,RO ;: POP STACK INTO RO
        RTS     PC     ;: RETURN
; *****
; SBttl FLOATING POINT NUMBER GENERATOR
; * THIS ROUTINE GENERATES TWO RANDOM FLOATING POINT NUMBERS
; * IN EITHER SINGLE OR DOUBLE PRECISION. FOR SINGLE PRECISION
; * THE NUMBERS ARE STORED IN STMP0 AND STMP2. DOUBLE PRECISION
; * NUMBERS ARE STORED IN STMP0 AND STMP4.
; * IN EITHER SINGLE OR DOUBLE THE EXTENDED EXPONENT IS STORED
; * IN SREG0 AND SREG1.
; *****
FLTDBL: MOV     #2,S0B0BL ;: SET LOOP FOR 2, FOUR WORD NUMBERS
FLTSGl: MOV     S0B0BL,RO ;: SET WORD LENGTH LOOP
        MOV     #STMP0,R2 ;: GET ADDRESS TO STORE WORDS IN
2$:     MOV     #2,R1     ;: SET NUMBER OF WORDS TO 2
1$:     JSR     PC,SRAND  ;: GET RANDOM NUMBER
        CMP     #2,R1     ;: FIRST TIME?
        BEQ     3$     ;: BRANCH IF YES
        CMP     #2,S0B0BL ;: DOUBLE PRECISION?
        BEQ     4$     ;: BRANCH IF YES
3$:     MOV     SHINUM,R3 ;: GET EXPONENT PART
        BIC     #177,R3  ;: CHECK FOR MINUS ZERO
        CMP     #BIT15,R3 ;:
        BEQ     1$     ;: BRANCH IF MINUS ZERO
4$:     MOV     SHINUM,(R2)+ ;: SAVE HINUM

```

```

8769 050774 077125          SOB      R1,15          ;CONTINUE
8770 050776 077030          SOB      R0,25          ;CONTINUE FOR DOUBLE PREC
8771 051000 012746 001276    MOV      #STMP0,-(SP)    ;PUT ADDRESS OF NUMBER ON STACK
8772 051004 012746 001002    MOV      #1002,-(SP)    ;PUT CONTROL WORD ON STACK
8773 051010 022767 000002 000022  CMP      #2,S0BDBL     ;DOUBLE PREC?
8774 051016 001002          BNE      S5            ;BRANCH IF NO
8775 051020 012716 001004    MOV      #1004,(SP)     ;CHANGE CONTROL WORD
8776 051024 004767 000012 000002 55:     JSR      PC,EXPEXT     ;CALCULATE EXT EXPONENTS
8777 051030 012767 000001    MOV      #1,S0BDBL     ;INIT S0BDBL FOR SINGLE PREC
8778 051036 000207          RTS      PC            ;RETURN
8779 051040 000001          S0BDBL: .WORD 1
8780 *****
8781 :SBTTL FLOATING POINT EXPONENT EXTENSION
8782 :* THIS ROUTINE CONVERTS THE ACTUAL EXPONENT OF A FLOATING POINT
8783 :* NUMBER INTO AN ACTUAL EXPONENT OF 200 AND AN EXTENDED
8784 :* EXPONENT EQUAL TO THE DIFFERENCE BETWEEN THE ORIGINAL
8785 :* ACTUAL EXPONENT AND 200.
8786 :*
8787 :* THE ROUTINE IS ENTERED WITH A CONTROL WORD ON THE STACK.
8788 :* BIT 15 OF THE CONTROL WORD INDICATES WHETHER THE NUMBER
8789 :* IS IN MEMORY (<15>=0) OR IN AN ACCUMULATOR (<15>=1).
8790 :* IF THE NUMBER IS IN AN ACCUMULATOR, BITS <9:8> INDICATE
8791 :* THE ACCUMULATOR NUMBER. IF THE NUMBER(S) IS IN MEMORY,
8792 :* BITS <9:8> INDICATE THE NUMBER OF NUMBERS TO CONVERT AND
8793 :* BITS <2:0> INDICATE THE WORD LENGTH OF THE NUMBER(S).
8794 :* IN THE CASE OF A MEMORY CONVERSION, THE ADDRESS OF THE
8795 :* FIRST WORD TO CONVERT IS ALSO ON THE STACK (PRECEDING
8796 :* THE CONTROL WORD).
8797 :*****
8798 051042 012605          EXPEXT: MOV      (SP)+,R5      ;SAVE RETURN PC
8799 051044 012600          MOV      (SP)+,R0      ;GET CONTROL WORD
8800 051046 100437          BMI      15            ;BRANCH IF ACC CONVERSION
8801 051050 012601          MOV      (SP)+,R1      ;GET START ADDRESS
8802 051052 162700 000400    SUB      #400,R0
8803 051056 012702 001276    MOV      #STMP0,R2      ;GET OFFSET FROM STMP0
8804 051062 160102          SUB      R1,R2
8805 051064 005402          NEG      R2
8806 051066 006202          ASR      R2
8807 051070 062702 001256    ADD      #SREG0,R2      ;GEN ADDRESS OF EXT WORD
8808 051074 011103 001177 35:     MOV      (R1),R3        ;GET DATA
8809 051076 042703 100177    BIC      #100177,R3     ;GET EXPONENT
8810 051102 072327 177771    ASH      #-7,R3        ;RIGHT JUSTIFY EXPONENT
8811 051106 162703 000200    SUB      #200,R3       ;CONVERT TO 2'S COMPLIMENT
8812 051112 010312          MOV      R3,(R2)       ;ADD TO EXTENDED EXPONENT
8813 051114 042711 077600    BIC      #77600,(R1)    ;MAKE ACTUAL
8814 051120 052711 040000    BIS      #BIT14,(R1)   ;EXPONENT 200
8815 051124 162700 000400    SUB      #400,R0       ;ANY MORE WORDS?
8816 051130 100435          BMI      25            ;BRANCH IF NO
8817 051132 110003          MOV8     R0,R3         ;GET WORD LENGTH
8818 051134 006303          ASL      R3
8819 051136 060301          ADD      R3,R1         ;SELECT NEXT NUMBER ADDRESS
8820 051140 062702 000002    ADD      #2,R2         ;SELECT NEXT EXTENDED ADDRESS
8821 051144 000753          BR       35           ;CONTINUE
8822 :ACCUMULATOR CONVERSION
8823 051146 072027 177776 15:     ASH      #-2,R0        ;GET ACCUMULATOR NUMBER

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8825 051156 010002          MOV      R0,R2          ;GENERATE
8826 051160 072227 177773  ASH      #-5,R2        ;ADDRESS OF
8827 051164 062702 001402  ADD      #SAC0,R2      ;EXTENDED EXPONENT
8828 051170 042767 000300 000004  BIC      #300,S$      ;GENERATE INSTRUCTION
8829 051176 050067 000000  BIS      R0,S$        ;TO GET EXPONENT
8830 051202 175003          STEXP   AC0,R3        ;GET EXPONENT
8831 051204 060312          ADD      R3,(R2)     ;ADD TO EXTENDED EXPONENT
8832 051206 005003          CLR      R3          ;
8833 051210 042767 000300 000004  BIC      #300,4$      ;GENERATE INSTRUCTION
8834 051216 050067 000000  BIS      R0,4$        ;TO LOAD EXPONENT BACK TO ACC
8835 051222 176403          LDEXP   R3,AC0       ;LOAD EXPONENT OF 200
8836 051224 010546          MOV      R5,-(SP)    ;RESTORE RETURN PC
8837 051226 000207          RTS      PC          ;RETURN
8838
8839 ;:*****
8840 .SBTTL POWER DOWN AND UP ROUTINES
8841
8842 :POWER DOWN ROUTINE
8843 051230 012737 051356 000024 $PWRDN: MOV      #SILLUP,2#PWRVEC ;:SET FOR FAST UP
8844 051236 012737 000340 000026  MOV      #340,2#PWRVEC+2 ;:PRIO:7
8845 051244 010046          MOV      R0,-(SP)    ;:PUSH R0 ON STACK
8846 051246 010146          MOV      R1,-(SP)    ;:PUSH R1 ON STACK
8847 051250 010246          MOV      R2,-(SP)    ;:PUSH R2 ON STACK
8848 051252 010346          MOV      R3,-(SP)    ;:PUSH R3 ON STACK
8849 051254 010446          MOV      R4,-(SP)    ;:PUSH R4 ON STACK
8850 051256 010546          MOV      R5,-(SP)    ;:PUSH R5 ON STACK
8851 051260 010667 000076          MOV      SP,$SAVR6   ;:SAVE SP
8852 051264 012737 051276 000024  MOV      #SPWRUP,2#PWRVEC ;:SET UP VECTOR
8853 051272 000000          HALT
8854 051274 000776          BR      .-2          ;:HANG UP
8855
8856 :POWER UP ROUTINE
8857 051276 016706 000060 $PWRUP: MOV      $SAVR6,SP ;:GET SP
8858 051302 005067 000054          CLR      $SAVR6     ;:WAIT LOOP FOR THE TTY
8859 051306 005267 000050          INC      $SAVR6     ;:WAIT FOR THE INC
8860 051312 001375          BNE     1$          ;:OF WORD
8861 051314 012605          MOV      (SP)+,R5    ;:POP STACK INTO R5
8862 051316 012604          MOV      (SP)+,R4    ;:POP STACK INTO R4
8863 051320 012603          MOV      (SP)+,R3    ;:POP STACK INTO R3
8864 051322 012602          MOV      (SP)+,R2    ;:POP STACK INTO R2
8865 051324 012601          MOV      (SP)+,R1    ;:POP STACK INTO R1
8866 051326 012600          MOV      (SP)+,R0    ;:POP STACK INTO R0
8867 051330 012737 051230 000024  MOV      #SPWRDN,2#PWRVEC ;:SET UP THE POWER DOWN VECTOR
8868 051336 012737 000340 000026  MOV      #340,2#PWRVEC+2 ;:PRIO:7
8869 051344 104400          TYPE     $POWER     ;:REPORT THE POWER FAILURE
8870 051346 051364          SPWRMG: .WORD $POWER ;:POWER FAIL MESSAGE POINTER
8871 051350 012716          MOV      (PC)+,(SP) ;:RESTART AT START
8872 051352 003212          SPWRAD: .WORD START ;:RESTART ADDRESS
8873 051354 000002          RTI
8874 051356 000000          $SILLUP: HALT
8875 051360 000776          BR      .-2
8876 051362 000000          $SAVR6: 0
8877 051364 005015 047520 042527 $POWER: .ASCIZ <15><12>"POWER" ;:THE POWER UP SEQUENCE WAS STARTED
8878 051372 000122          ;:BEFORE THE POWER DOWN WAS COMPLETE
8879          ;:PUT THE SP HERE
          .EVEN
    
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8881  
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8889 051374 010046  
8890 051376 016600 000002  
8891 051402 005740  
8892 051404 111000  
8893 051406 016000 051414  
8894 051412 000200  
8895  
8896  
8897  
8898  
8899  
8900  
8901  
8902  
8903  
8904 051414  
8905 051414 046436  
8906 051416 047264  
8907 051420 047240  
8908 051422 047300  
8909 051424 047466  
8910 051426 050050  
8911 051430 050106  
8912 051432 050144  
8913 051434 050432  
8914  
8915  
8916  
8917  
8918  
8919 051436 012703 001662  
8920 051442 005023  
8921 051444 005023  
8922 051446 005023  
8923 051450 005013  
8924  
8925  
8926  
8927 051452 012702 002230  
8928 051456 005072 000010  
8929 051462 012772 043520 000012  
8930 051470 012772 000340 000014  
8931 051476 012732 170000  
8932  
8933 051502 012746 000003  
8934 051506 012746 001666  
8935 051512 004737 052054

.SBTTL TRAP DECODER

;\*THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION  
;\*AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS  
;\*OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL  
;\*GO TO THAT ROUTINE.

```
STRAP:  MOV    RO, -(SP)           ;; SAVE RO
        MOV    2(SP), RO          ;; GET TRAP ADDRESS
        TST    -(RO)              ;; BACKUP BY 2
        MOVB   (RO), RO           ;; GET RIGHT BYTE OF TRAP
        MOV    STRPAD(RO), RO     ;; INDEX TO TABLE
        RTS    RO                  ;; GO TO ROUTINE
```

.SBTTL TRAP TABLE

;\*THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED  
;\*BY THE "TRAP" INSTRUCTION.

```
ROUTINE
-----
STRPAD:  STYPE   ;; CALL=TYPE      TRAP+0(104400)  TTY TYPEOUT ROUTINE
        STYPOC  ;; CALL=TYPOC     TRAP+2(104402)  TYPE OCTAL NUMBER (WITH LEADING ZEROS)
        STYPOS  ;; CALL=TYPOS     TRAP+4(104404)  TYPE OCTAL NUMBER (NO LEADING ZEROS)
        STYPON  ;; CALL=TYPON     TRAP+6(104406)  TYPE OCTAL NUMBER (AS PER LAST CALL)
        STYPOS  ;; CALL=TYPOS     TRAP+10(104410) TYPE DECIMAL NUMBER (WITH SIGN)
        $SAVREG ;; CALL=SAVREG    TRAP+12(104412) SAVE RO-R5 ROUTINE
        $RESREG ;; CALL=RESREG    TRAP+14(104414) RESTORE RO-R5 ROUTINE
        $FL20   ;; CALL=FL20     TRAP+16(104416)
        $FLD20  ;; CALL=FLD20    TRAP+20(104420)
```

\*\*\*\*\*  
.SBTTL UNIBUS EXERCISER INITIALIZATION ROUTINE  
;\*THIS ROUTINE INITIALIZES THE BASE ADDRESS FOR THE  
;\*UNIBUS EXERCISER AND LOADS UP THE EXERCISER REGISTERS.  
\*\*\*\*\*

```
UBEINIT: MOV    #UBESAV, R3       ;; GET ADDRESS OF NEXT UBE ADRES
        CLR    (R3)+             ;; INITIALIZE
        CLR    (R3)+
        CLR    (R3)+
        CLR    (R3)
```

```
;; SET UP THE UBE AND START IT
MOV    #UBETBL, R2              ;; GET ADDRESS OF UBE TABLE
CLR    @10(R2)                  ;; CLEAR ALL ERRORS
MOV    #UBESRV, @12(R2)        ;; SET UP UBE VECTOR
MOV    #PR7, @14(R2)           ;; SET UP UBE VECTOR PSW
MOV    #170000, @1(R2)+        ;; SET CC FOR 2K WORD TRANSFER
                                           ;; UBE IS DOING BYTE TRANSFERS
MOV    #3, -(SP)                ;; PUT DEVICE ID IN STACK
MOV    #UBEADR, -(SP)          ;; PUT ADDRESS OF PHYSICAL BA ON STACK
JSR    PC, @#GETMAP            ;; GO GET MAP REGISTER
```

8937 051522 013732 001670  
8938 051526 052737 000040  
8939 051534 000207

172516

MOV J#UBEADR+2,2(R2)+:LOAD ADR BITS 16 & 17  
BIS #40,J#SR3 :ENABLE MAP  
RTS PC :RETURN

\*\*\*\*\*  
:SBTTL CONVERT UNIBUS VIRTUAL ADDRESS TO PHYSICAL ADDRESS  
:\* THIS ROUTINE CONVERTS THE CONTENTS OF LOCATIONS  
:\* "ERRBA" AND "ERRBA+2" FROM A VIRTUAL 18-BIT ADDRESS  
:\* TO A PHYSICAL 22-BIT ADDRESS AS MAPPED BY THE APPROPRIATE  
:\* MAP REGISTER. THE 22-BIT ADDRESS IS STORED IN LOCATIONS  
:\* "PA2116" AND "PA1500".  
\*\*\*\*\*

8948 051536 104412  
8949 051540 013703 001672  
8950 051544 013702 001674  
8951 051550 042702 177774  
8952 051554 032737 000040  
8953 051562 001005  
8954 051564 010337 001476  
8955 051570 010237 001500  
8956 051574 000421  
8957 051576 010305  
8958 051600 073227 000005  
8959 051604 042702 000003  
8960 051610 062702 170200  
8961 051614 012237 001476  
8962 051620 011237 001500  
8963 051624 042705 160000  
8964 051630 060537 001476  
8965 051634 005537 001500  
8966 051640 104414  
8967 051642 000207

172516

PHYMAP: SAVREG  
MOV J#ERRBA,R3 :GET BUS ADDRESS <15:00>  
MOV J#ERRBA+2,R2 :GET BUS ADDRESS <17:16>  
BIC #177774,R2  
BIT #BITS,J#MMR3 :MAP ON?  
BNE IS :BRANCH IF YES  
MOV R3,J#PA1500 :PHY ADR=BUS ADR  
MOV R2,J#PA2116  
BR MAPEND  
IS: MOV R3,R5 :SAVE ADR BITS <15:00>  
ASHC #5,R2 :GET MAP REG SELECT BITS  
BIC #3,R2  
ADD #MAPLO,R2 :FORM ADDRESS OF MAP REG  
MOV (R2)+,J#PA1500 :GET CONTENTS OF MAP REG LO  
MOV (R2),J#PA2116 :GET CONTENTS OF MAP REG HI  
BIC #160000,R5 :FORM PHYSICAL ADDRESS  
ADD R5,J#PA1500 :THAT TIMED OUT  
ADC J#PA2116  
MAPEND: RESREG  
RTS PC

\*\*\*\*\*  
:SBTTL CONVERT A VIRTUAL ADDRESS TO A PHYSICAL ADDRESS  
:\* THIS ROUTINE CONVERTS A 16-BIT VIRTUAL ADDRESS TO A  
:\* 22-BIT PHYSICAL ADDRESS. THE VIRTUAL ADDRESS IS  
:\* ASSUMED TO BE IN LOCATION "VADR" AND THE PHYSICAL  
:\* ADDRESS IS PLACED IN LOCATIONS "PA2116" AND "PA1500".  
:\*  
:\* IF MEMORY MANAGEMENT IS OFF THE PHYSICAL ADDRESS IS  
:\* GENERATED BY SUBTRACTING THE CONTENTS OF LOCATION  
:\* "FACTOR" FROM THE VIRTUAL ADDRESS. THIS LOCATION  
:\* CONTAINS THE BYTE OFFSET BETWEEN THE RELOCATED CODE  
:\* AND THE NON-RELOCATED CODE.  
:\*  
:\* IF MEMORY MANAGEMENT IS ON, THE CONTENTS OF THE  
:\* APPROPRIATE PAR REGISTER IS ADDED(AFTER ADJUSTMENT)  
:\* TO THE LEAST SIGNIFICANT 13 BITS OF THE VIRTUAL ADDRESS.  
\*\*\*\*\*

8986 051644 104412  
8987 051646 013703 001474  
8988 051652 105737 001503  
8989 051656 001426  
8990 051660 005002  
8991 051662 073227 000003

CNVADR: SAVREG  
MOV J#VADR,R3 :GET VIRTUAL ADDRESS TO CONVERT  
TSTB J#MMON :IS MEMORY MGMT ON?  
BEQ IS :BRANCH IF NO  
CLR R2  
ASHC #3,R2 :GET PAR SELECT BITS

# H15

CEQKCC PDP 11/70 CPU EXERCISOR  
CEQKCC.P11 03-MAR-78 13:13

MACY11 30A(1052) 03-MAR-78 13:15 PAGE 170  
CONVERT A VIRTUAL ADDRESS TO A PHYSICAL ADDRESS

SEQ 0189

```
8993 051672 042703 160000      BIC      #160000,R3      ;MAKE SURE SIGN DIDN'T EXTEND
8994 051676 006102              ROL      R2              ;MAKE R2 EVEN FOR WORD ADDRESSING
8995 051700 062702 172340      ADD      #KIPAR0,R2     ;GET ADDRESS OF PAR
8996 051704 011205              MOV      (R2),R5        ;GET PAR DATA
8997 051706 005004              CLR      R4              ;SETUP R4
8998 051710 073427 000006      ASHC     #6,R4          ;SHIFT PAR DATA
8999 051714 060305              ADD      R3,R5          ;FORM PHYSICAL ADDRESS
9000 051716 005504              ADC      R4              ;
9001 051720 010437 001500      2$:     MOV      R4,#PA2116 ;SAVE PHYSICAL
9002 051724 010537 001476      MOV      R5,#PA1500    ;ADDRESS
9003 051730 104414              RESREG
9004 051732 000207              RTS      PC              ;RETURN
9005 051734 163703 001506      1$:     SUB      #FACTOR,R3   ;FORM PHYSICAL ADDRESS
9006 051740 005004              CLR      R4              ;
9007 051742 010305              MOV      R3,R5          ;
9008 051744 000765              BR       2$             ;RETURN
```

```
9010 :*****
9011 :SBTTL ROUTINE TO CHECK RELOCATED DATA
9012 :ROUTINE TO CHECK DATA RELOCATED
9013 :CALL: R0= HIGHEST ADDRESS +2 OF SOURCE DATA
9014 :      R2= HIGHEST ADDRESS +2 OF DEST DATA
9015 :      R5= LOWEST ADDRESS OF THE SOURCE DATA
9016 :
9017 : THIS ROUTINE USES A COMPARE INSTRUCTION TO CHECK
9018 : THE DATA THAT WAS RELOCATED. IF A PARITY ERROR OCCURS
9019 : DURING THIS CHECK A SPECIAL ERROR MESSAGE IS TYPED
9020 : INSTEAD OF THE UNEXPECTED TRAP MESSAGE.
```

```
9021 :*****
9022 051746 012737 052010 000114  CHKDAT: MOV      #2$,#CACHVEC ;SETUP PARITY VECTOR
9023 051754 024042              CMP      -(R0),-(R2)   ;CHECK DATA
9024 051756 001013              BNE     99$+2          ;
9025 051760 005112              COM     (R2)           ;COMPLEMENT DEST DATA
9026 051762 005112              COM     (R2)           ;TWICE
9027 051764 021210              CMP     (R2),(R0)      ;CHECK DATA
9028 051766 001006              BNE     99$           ;
9029 051770 020005              1$:     CMP      R0,R5        ;BRANCH IF ALL DATA NOT CHECKED
9030 051772 001365              BNE     CHKDAT         ;
9031 051774 012737 052642 000114  MOV      #.PARSRV,#CACHVEC ;RESTORE CACHVEC
9032 052002 000207              RTS     PC             ;RETURN
9033 052004 000262              99$:   SEV
9034 052006 000207              RTS     PC             ;
9035 052010 013737 177744 001302  2$:     MOV      #MEMERR,#STMP2 ;SAVE ERROR REG
9036 052016 013737 177740 001304  MOV      #LOADRS,#STMP3 ;SAVE ERROR ADR
9037 052024 013737 177742 001306  MOV      #HIADRS,#STMP4 ;
9038 052032 010237 001474              MOV     R2,#VADR
9039 052036 010037 001276              MOV     R0,#STMP0
9040 052042 104005              ERROR   5
9041 052044 012737 177777 177744  MOV     #-1,#MEMERR    ;CLEAR ERROR REG
9042 052052 000754              BR      99$           ;RETURN
```

```
9043 :*****
9044 :SBTTL ROUTINE TO GET A MAP REGISTER
9045 :THIS ROUTINE TAKES AN 18 BIT RANDOM NUMBER, FINDS TWO
9046 :CONSECUTIVE MAP REGISTERS THAT ARE NOT IN USE, LOADS THE
9047
```

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9067 052054 016600 000004
9068 052060 016601 000002
9069 052064 013746 177776
9070 052070 005116
9071 052072 042716 177437
9072 052076 000237
9073 052100 104412
9074 052102 012137 001226
9075 052106 012137 001230
9076 052112 004737 050604
9077 052116 013702 001524
9078 052122 013703 001522
9079 052126 073227 177764
9080 052132 042703 177760
9081 052136 022702 000016
9082 052142 100001
9083 052144 000762
9084 052146 005737 001504
9085 052152 001403
9086 052154 122702 000000
9087 052160 001754
9088
9089 052162 010204
9090 052164 042703 100000
9091 052170 073227 177776
9092 052174 042703 000001
9093 052200 010241
9094 052202 010341
9095 052204 012705 000004
9096 052210 120465 001655
9097 052214 001435
9098 052216 077504
9099 052220 110460 001656
9100 052224 072427 000003
9101 052230 062704 170200
9102 052234 042703 160000
9103 052240 013701 001226

```

```

; *AND THE NUMBER + 4K, AND RETURNS A NEW BUS ADDRESS, BASED
; *ON THE RANDOM NUMBER.
; *
; * MAP REGISTERS 0 AND 1 ARE NOT USED IF THE PROGRAM IS
; * RUNNING ON ACT11. THIS ALLOWS "MOTHER" TO ACCESS THE
; * END OF PASS HOOKS.
; *
; * THE MAP TABLE (MAPTBL) CONTAINS 4 BYTES, ONE FOR EACH
; * UNIBUS DEVICE. IF THE UBE IS PRESENT IT USES THE
; * 4TH BYTE. WHEN A REGISTER IS ASSIGNED TO A DEVICE,
; * THE LOWER 4 ADDRESS BITS OF THAT REGISTER ARE PLACED
; * IN THE TABLE. WHEN A DEVICE REQUESTS A REGISTER PAIR
; * THIS TABLE IS THEN SEARCHED TO SEE IF THE REGISTER
; * PAIR IS IN USE.
; * ENTER WITH:
; * 4(SP)=DEVICE ID
; * 2(SP)=ADDRESS OF THE PHYSICAL ADDRESS
; * *****
GETMAP: MOV 4(SP),R0 ;GET DIVICE ID
MOV 2(SP),R1 ;GET ADR OF PHY ADR
MOV @PSW,-(SP) ;SAVE CURRENT PRIORITY
COM (SP) ;MAKE IT READY FOR RESTORE
BIC #CPR7,(SP)
SPL 7 ;IF THIS IS RK CALL, LOCK OUT UBE
SAVREG
MOV (R1)+,@SGDDAT ;SAVE PHYSICAL
MOV (R1)+,@SBDDAT ;ADDRESS
2$: JSR PC,@$RAND ;GET RANDOM NUMBER
MOV @SHINUM,R2 ;GET HIGH RANDOM NUMBER
MOV @SLONUM,R3 ;GET LOW RANDOM NUMBER
ASHC #-14,R2 ;CONVERT TO 20 BIT NUMBER
BIC #177760,R2 ;GET RID OF 11 BITS OF SIGN EXT
CMP #16,R2 ;LEGAL MAP REG SELECT?
BPL 3$ ;BRANCH IF YES
BR 2$ ;TRY AGAIN
3$: TST @QV ;ACT11 (QV OR AUTO)?
BEQ 4$ ;BRANCH IF NO
CMPB #0,R2 ;MAP SELECT 0?
BEQ 2$ ;BRANCH IF YES.(ACT MUST
;USE THIS MAP REG)
4$: MOV R2,R4 ;SAVE MAP SELECT BITS
BIC #BIT15,R3 ;CLEAR SELECT BIT 0
ASHC #-2,R2 ;FORM 18 BIT ADDRESS
BIC #BIT0,R3 ;MAKE SURE ITS EVEN
MOV R2,-(R1) ;RETURN NEW BUS ADDRESS
MOV R3,-(R1) ;TO THE APPROPRIATE HANDLER
MOV #4,R5 ;SET SOB COUNT
1$: CMPB R4,MAPTBL-1(R5) ;IS THIS MAP IN USE?
BEQ 5$ ;BRANCH IF YES
SOB R5,1$ ;CONTINUE
MOV R4,MAPTBL(R0) ;PUT MAP SELECT BITS IN TABLE
ASH #3,R4 ;FORM INDEX TO GET MAP REG ADDR
ADD @MAPLC,R4 ;GENERATE MAP ADDRESS
BIC #160000,R3 ;GET LS 13 BITS OF RAND NO.
MOV @SGDDAT,R1 ;GET PHYSICAL

```



```

9105 052250 160301          SUB      R3,R1          ;GENERATE MAP
9106 052252 005602          SBC      R2              ;REGISTER DATA
9107 052254 010124          MOV      R1,(R4)+       ;LOAD THE
9108 052256 010224          MOV      R2,(R4)+       ;FIRST MAP REGISTER
9109 052260 062701 020000  ADD      #20000,R1      ;ADD 4K
9110 052264 005502          ADC      R2              ;TO MAP DATA
9111 052266 010124          MOV      R1,(R4)+       ;LOAD THE
9112 052270 010224          MOV      R2,(R4)+       ;SECOND MAP REGISTER
9113 052272 104414          RESREG                    ;
9114 052274 042637 177776  BIC      (SP)+,2#PSW     ;RETURN PRIORITY TO ORIGINAL VALUE
9115 052300 011666 000004  MOV      (SP),4(SP)     ;SETUP RETURN PC
9116 052304 022626          CMP      (SP)+,(SP)+    ;CLEAN UP THE STACK
9117 052306 000207          RTS      PC              ;RETURN
9118                                     ;REGISTER PAIR IS IN USE, TRY ANOTHER RANDOM NUMBER
9119 052310 062701 000004  SS:     ADD      #4,R1      ;RESTORE R1
9120 052314 000137 052112  JMP      #2$             ;GET ANOTHER RANDOM NUMBER
9121                                     ;*****
9122 .SBTTL GIVE MAP SUBROUTINE
9123 ;* THIS ROUTINE TAKES THE MAP ADDRESS OUT OF THE MAP TABLE
9124 ;* FOR THE REQUESTING DEVICE AND REPLACES IT WITH 377.
9125 ;*****
9126 052320 010046          GIVEMAP:MOV  RO,-(SP)     ;SAVE RO
9127 052322 016600 000004  MOV      4(SP),RO        ;GET DEVICE ID
9128 052326 112760 000377 001656  MOVB     #377,MAPTBL(RO) ;TAKE IT OUT OF THE TABLE
9129 052334 012600          MOV      (SP)+,RO       ;RESTORE RO
9130 052336 000207          RTS      PC              ;RETURN
9131                                     ;*****
9132 .SBTTL ROUTINE TO CLEAR 'T' BIT
9133 ;*****
9134 CLRTBIT:MOV 2#PSW,-(SP) ;PUSH PSW ONTO STACK
9135 052340 013746 177776  MOV      (SP),(PC)+     ;SAVE IN RETPSW BELOW
9136 052344 011627          RETPSW: .WORD 0
9137 052346 000000          BIC      #20,(SP)      ;CLEAR T BIT IN PSW ON STACK
9138 052350 042716 000020  ;*****
9139 .SBTTL ROUTINE TO RESTORE THE T BIT
9140 ;*****
9141 RESPSW:MOV #1$,-(SP) ;SET RETURN PC FOR RTI
9142 052354 012746 052362  RTI      ;CLEAR 'T' BIT IN PSW
9143 052360 000002          1$:     RTS      PC      ;RETURN
9144 052362 000207          RESTPS: BIC      #177400,2#PSW ;SET KERNEL MODE
9145                                     MOV      RETPSW,-(SP) ;PUSH ORIG PSW ONTO STACK
9146 052364 042737 177400 177776  BR       RESPSW
9147 052372 016746 177750
9148 052376 000766
9149
9150 ;*****
9151 .SBTTL KEYBOARD INT SERV ROUTINE
9152 ;*THIS ROUTINE HANDLES INTERRUPTS FROM THE KEYBOARD
9153 ;*
9154 ;*TYPING A CONTROL 'C' WILL CAUSE THE PROCESSOR TO HALT
9155 ;*
9156 ;*TYPING A CARRAGE RETURN WILL CAUSE A CARRIAGE RETURN-LINE FEED
9157 ;*TO BE TYPED.
9158 ;*
9159 ;*TYPING A CONTROL 'O' WILL INHIBIT ANY FURTHER TYPEOUT. THE SECOND CONTROL 'O'

```

;\* ANY OTHER CHARACTER WILL JUST BE ECHOED.  
;\*\*\*\*\*

```

9161
9162
9163
9164
9165      000003      CNTRLC=3
9166      000017      CNTRLO=17
9167
9168      052400      017746      126636      TKISR:  MOV      @STKB, -(SP)      ;GET CHARACTER
9169      052404      042716      177600      BIC      #177600, (SP)      ;STRIP UNUSED BITS
9170      052410      022716      000003      CMP      @CNTRLC, (SP)      ;BRANCH IF NOT CONTROL C (↑C)
9171      052414      001010      BNE
9172      052416      012737      001326      001270  MOV      @SRLF-1, @SREGS      ;ECHO CR LF
9173      052424      106277      126614      ASRB    @STPS
9174      052430      005726      TST     (SP)+      ;POP CHARACTER OFF THE STACK
9175      052432      000000      HALT
9176      052434      000002      RTI      ;RETURN
9177
9178      052436      122716      000015      1$:     CMPB   #15, (SP)      ;BRANCH IF NOT <CR>
9179      052442      001007      BNE     2$
9180      052444      012737      001326      001270  MOV      @SRLF-1, @SREGS      ;ECHO CR LF
9181      052452      106277      126566      ASRB    @STPS
9182      052456      005726      TST     (SP)+      ;POP CHARACTER OFF STACK
9183      052460      000002      RTI      ;RETURN
9184
9185      052462      122716      000017      2$:     CMPB   @CNTRLO, (SP)      ;BRANCH IF NOT CONTROL 0 (↑0)
9186      052466      001012      BNE     3$
9187      052470      005726      TST     (SP)+
9188      052472      005167      126770      COM     NOTYPE
9189      052476      100405      BMI     7$
9190      052500      012737      001326      001270  MOV      @SRLF-1, @SREGS      ;ECHO CR LF
9191      052506      106277      126532      ASRB    @STPS
9192      052512      000002      RTI
9193
9194      052514      104412      3$:     SAVREG
9195      052516      011605      MOV     (SP), R5      ;RETRIEVE CHARACTER
9196      052520      004737      053130      JSR    PC, @LDKT      ;GO TO LOW CORE
9197      052524      013700      001442      MOV    @TKBFRP, R0      ;GET BUFFER PTR
9198      052530      110520      4$:     MOVB   R5, (R0)+      ;LOAD CHAR INTO BFR
9199      052532      105010      CLRB   (R0)      ;CLEAR NEXT LOC
9200      052534      022700      001464      5$:     CMP    @TKBFR+20, R0      ;BRANCH IF NOT END OF BFR
9201      052540      001002      BNE     6$
9202      052542      012700      001444      MOV    @TKBFR, R0      ;RESET BUFFER PTR
9203      052546      010037      001442      6$:     MOV    R0, @TKBFRP      ;RESTORE BFR PTR
9204      052552      004737      053216      JSR    PC, @RESKT      ;GO BACK TO ORIGINAL MEMORY
9205      052556      104414      RESREG
9206      052560      005737      001466      ECHO:   TST    @NOTYPE      ;TYPEOUT DISABLED?
9207      052564      100004      BPL    1$      ;BRANCH IF NO
9208      052566      005726      TST    (SP)+      ;FIX UP STACK
9209      052570      105077      126450      CLRB   @STPS      ;CLEAR IE BIT
9210      052574      000002      RTI     ;RETURN
9211      052576      105777      126442      1$:     TSTB   @STPS      ;PRINTER READY?
9212      052602      100375      BPL    -4      ;BRANCH IF NO
9213      052604      112677      126436      MOVB   (SP)+, @STPB      ;MOVE CHAR TO PRINTER
9214      052610      000002      RTI     ;RETURN
9215

```

```

9217      ;*****
9218      .SBTTL TELETYPE INTERRUPT SERVICE ROUTINE
9219      ;*THIS ROUTINE TYPES A MESSAGE POINTED TO BY THE ADR STORED
9220      ;*IN LOCATION $REGS. THIS ROUTINE IS INTERRUPT DRIVEN.
9221      ;*****
9222      TPISR: INC    @#$REGS      ;STEP MESSAGE ADDRESS PTR
9223      MOVB   @#$REGS,-(SP)    ;GET CHAR TO BE TYPED
9224      BNE    ECHO             ;GO TYPE CHAR IF NOT '0'
9225      TST    (SP)+           ;POP STACK
9226      CLR    @STPS           ;CLEAR IE BIT
9227      MOV    @NULLS,@#$REGS
9228      RTI                    ;RETURN
9229
9230      ;*****
9231      .SBTTL PARITY ERROR SERVICE
9232      ;* THIS ROUTINE FIELDS UNEXPECTED TRAPS TO 114. IT IS ASSUMED
9233      ;* THAT THE ERROR WAS IN CACHE AND WAS CAUSED BY THE "OTHER
9234      ;* WORD" RATHER THAN THE "WANTED WORD" WHICH MEANS THAT THE
9235      ;* BAD DATA IS STILL IN THE CACHE. SO, TO CLEAR THE BAD DATA
9236      ;* THE ERROR ADDRESS IS REFERENCED CAUSING THE CACHE TO GO
9237      ;* TO MAIN MEMORY TO GET THE DATA. THIS PREVENTS AN
9238      ;* ARBITRARY REFERENCE TO THE BAD WORD FROM TRAPPING.
9239
9240      ;*
9241      ;* AFTER THE ERROR IS REPORTED, BITS 2 AND 3 OF THE MEMORY
9242      ;* ERROR REGISTER ARE TESTED TO SEE IF THE BAD DATA IS IN
9243      ;* MAIN MEMORY. IF IT IS, THE PROGRAM RESTARTS SINCE THE
9244      ;* GOOD DATA IS NOW LOST FOREVER. OTHERWISE THE PROGRAM
9245      ;* RETURNS TO THE ADDRESS POINTED TO BY "SLPERR".
9246      ;*****
9247      .PARSRV:MOV    @RT1,@#CACHVEC ;PUT NEW ADDRESS IN PARITY VECTOR
9248      MOV    2(SP),@#STMP0 ;SAVER ERROR PSM
9249      MOV    (SP),@#VADR ;SAVE PC
9250      SUB    #2,@#VADR ;ADJUST ERROR PC
9251      MOV    @#MEMERR,R2 ;GET ERROR REGISTER
9252      MOV    @#LOADRS,R3 ;GET LO ADDRESS ERROR REG
9253      MOV    R3,@#STMP3 ;PUT LOW ADR IN MEMORY
9254      MOV    @#HIADRS,@#STMP4 ;GET HI ADDRESS ERROR REG
9255      BIC    #176000,R3 ;MASK OFF LOWER TEN BITS
9256      MOV    @#KIPAR6,R4 ;SAVE PAR6
9257      TSTB   @#MMON ;IS MEMORY MGMT ON?
9258      BEQ    IS ;BRANCH IF NO
9259      CLR    @#KIPAR6 ;CLEAR PAR6
9260      MOV    #77406,@#KIPDR6 ;ENSURE PDR 6 RESIDENT
9261      BIS    #140000,R3 ;SETUP R3 TO REFERENCE THRU PAR6
9262      TSTB   (R3) ;REFERENCE ADDRESS THAT TRAPPED
9263      ;SHOULD CAUSE ABORT
9264      ;GET ORIGINAL MEMORY
9265      ;ERROR REG DATA
9266      PERET:MOV    @#MEMERR,@#STMP2 ;SAVE ERROR REG FOR TYPEOUT
9267      MOV    @#SLPERR,@#$REG4 ;SAVE LOOP ADDRESS
9268      MOV    #25,@#SLPERR ;SET RETURN ADDRESS IF LOOPING
9269      ERROR 4
9270      ;RESTORE LOOP ADDRESS
9271      MOV    @#$REG4,@#SLPERR ;RESTORE PAR6
9272      MOV    R4,@#KIPAR6 ;GET MEM ERR REG
9273      MOV    @#MEMERR,R4

```

CEQKCC.P11 03-MAR-78 13:13

PARITY ERROR SERVICE

```

9273 053026 012737 052642 000114      MOV      #.PARSRV,2#CACHVEC      ;RESTORE PARITY VECTOR
9274 053034 042704 177763              BIC      #177763,R4              ;CLEAR ALL BUT BITS 2 & 3
9275 053040 001426                      BEQ      1$                      ;BRANCH IF NOT MAIN MEMORY ERROR
9276 053042 104400 053050              TYPE     65$                     ;TYPE ASCIZ STRING
9277 053046 000420                      BR       64$                     ;GET OVER THE ASCIZ
9278                                     ;:65$: .ASCIZ /FATAL PARITY ERROR-RESTARTING/<CRLF>
9279 053110 64$:                                ;
9280 053110 000005                      RESET                                ;CLEAR THE WORLD
9281 053112 000137 003212              JMP      2#START                  ;
3282 053116 012716 053124              1$:  MOV      2#X,(SP)              ;PUT ADDRESS ON STACK TO GET ORIGINAL
9283                                     ;PSW BACK
9284 053122 000002                      RT1:  RTI                          ;GET OLD PSW
9285 053124 000177 126062              X:    JMP      2#SLPERR            ;JUMP TO START OF TEST THAT HAD THE PE
9286                                     ;*****
9287                                     ;.SBTTL CONTEXT SWITCH DOWN SUBROUTINE
9288                                     ;* SUBROUTINE TO SAVE & LOAD KIPAR'S 0,1, AND 2 (IF MEM MGMT ENABLED)
9289                                     ;* THIS ROUTINE IS CALLED BY THE KEYBOARD INTERRUPT, LINE CLOCK
9290                                     ;* INTERRUPT, USE SERVICE ROUTINE, MGT SERVICE ROUTINE, AND TYPE TIME ROUTINE.
9291                                     ;*****
9292 053130 105737 001503              LDKT:  TSTB     2#MMON              ;BRANCH IF MEM MGMT DISABLED
9293 053134 001427                      BEQ      1$
9294 053136 012604                      MOV      (SP)+,R4                  ;SAVE RETURN PC
9295 053140 013737 177776 053272      MOV      2#PSW,2#SSAVPSW          ;SAVE THE CURRENT PSW
9296 053146 042737 140000 177776      BIC      #140000,2#PSW            ;GO TO KERNEL MODE
9297 053154 012700 172340              MOV      #KIPAR,R0                 ;GET ADDRESS OF PAR0
9298 053160 012001                      MOV      (R0)+,R1                  ;GET PAR0
9299 053162 012002                      MOV      (R0)+,R2                  ;GET PAR1
9300 053164 012003                      MOV      (R0)+,R3                  ;GET PAR2
9301 053166 012740 000400              MOV      #400,-(R0)                ;RELOC BACK TO LOW CORE
9302 053172 012740 000200              MOV      #200,-(R0)
9303 053176 005040                      CLR      -(R0)
9304 053200 012700 053264              MOV      #SSAVPAR,R0              ;GET ADDRESS OF SAVE BUFFER
9305 053204 010120                      MOV      R1,(R0)+                  ;PUT PAR DATA IN MEMORY
9306 053206 010220                      MOV      R2,(R0)+
9307 053210 010320                      MOV      R3,(R0)+
9308 053212 010446                      MOV      R4,-(SP)                  ;PUT RETURN PC ON STACK
9309 053214 000207              1$:  RTS      PC
9310                                     ;*****
9311                                     ;.SBTTL CONTEXT SWITCH UP SUBROUTINE
9312                                     ;SUBROUTINE TO RESTORE KIPAR, 1, AND 2 (IF MGMT ENABLED)
9313                                     ;*****
9314                                     ;RESKT:  TSTB     2#MMON              ;BRANCH IF MEM MGMT DISABLED
9315 053216 105737 001503              RESKT: TSTB     2#MMON
9316 053222 001417                      BEQ      1$
9317 053224 012604                      MOV      (SP)+,R4                  ;GET RETURN PC
9318 053226 012700 053264              MOV      #SSAVPAR,R0              ;GET ADDRESS OF SAVE BUFF
9319 053232 012001                      MOV      (R0)+,R1                  ;GET OLD PAR DATA
9320 053234 012002                      MOV      (R0)+,R2
9321 053236 012003                      MOV      (R0)+,R3
9322 053240 012700 172340              MOV      #KIPAR,R0                 ;GET ADDRESS OF PAR0
9323 053244 010120                      MOV      R1,(R0)+                  ;RELOCATE BACK
9324 053246 010220                      MOV      R2,(R0)+
9325 053250 010310                      MOV      R3,(R0)
9326 053252 013737 053272 177776      MOV      2#SSAVPSW,2#PSW
9327 053260 010446                      MOV      R4,-(SP)

```

```

9329 053264 000003          $SAVPAR:.BLKW 3
9330 053272 000000          $SAVPSW:.WORD
9331          ;*****
9332          :SBTTL  KT ABORT SUBROUTINE
9333          ;*****
9334 053274 016637 000002 001276  KTABRT: MOV      2(SP),J@STMP0          ;SAVE ERROR PSW
9335 053302 011637 001474          MOV      (SP),J@VADR          ;SAVE ERROR PC
9336 053306 162737 000002 001474          SUB      #2,J@VADR
9337 053314 013737 177572 001302          MOV      J@MMR0,J@STMP2          ;SAVE MMR0
9338 053322 013737 177576 001304          MOV      J@MMR2,J@STMP3          ;SAVE MMR2
9339 053330 013737 001212 001266          MOV      J@SLPERR,J@SREG4        ;SAVE LOOP ADDRESS
9340 053336 012737 053346 001212          MOV      #15,J@SLPERR          ;SET RETURN ADR IF LOOPING
9341 053344 104003          ERROR      3
9342 053346 013737 001266 001212 1$: MOV      J@SREG4,J@SLPERR        ;RESTORE LOOP ADR
9343 053354 042737 170000 177572          BIC      #170000,J@MMR0        ;CLEAR ERRORS
9344 053362 013716 001212          MOV      J@SLPERR,(SP)        ;GET LOOP ADDRESS
9345 053366 000002          RTI
9346
9347          ;*****
9348          :SBTTL  RESERVED INSTRUCTION ROUTINE
9349          ;*****
9350 053370 016637 000002 001276  RESERR: MOV      2(SP),J@STMP0          ;SAVE PSW
9351 053376 011637 001474          MOV      (SP),J@VADR          ;SAVE ERROR PC
9352 053402 162737 000002 001474          SUB      #2,J@VADR
9353 053410 013737 001212 001266          MOV      J@SLPERR,J@SREG4        ;SAVE LOOP ADR
9354 053416 012737 053426 001212          MOV      #15,J@SLPERR          ;SET RETURN ADR IF LOOPING
9355 053424 104002          ERROR      2
9356 053426 013737 001266 001212 1$: MOV      J@SREG4,J@SLPERR        ;RESTORE LOOP ADR
9357 053434 013716 001212          MOV      J@SLPERR,(SP)        ;GET LOOP ADDRESS
9358 053440 000002          RTI
9359
9360          ;*****
9361          :SBTTL  TRAP TO 4 SERVICE ROUTINE
9362          ;*****
9363 053442 016637 000002 001276  ERPRT: MOV      2(SP),J@STMP0          ;SAVE ERROR PSW
9364 053450 011637 001474          MOV      (SP),J@VADR          ;SAVE ERROR PC
9365 053454 162737 000002 001474          SUB      #2,J@VADR
9366 053462 012706 000700          MOV      #5,J@SUPSTK,SP        ;RESTORE SP
9367 053466 013737 177766 001302          MOV      J@CPUERR,J@STMP2        ;GET ERROR REG
9368 053474 013737 001212 001266          MOV      J@SLPERR,J@SREG4        ;SAVE LOOP ADR
9369 053502 012737 053512 001212          MOV      #15,J@SLPERR          ;SET RETURN ADR IF LOOPING
9370 053510 104001          ERROR      1
9371 053512 013737 001266 001212 1$: MOV      J@SREG4,J@SLPERR        ;SET LOOP ADR
9372 053520 005037 177766          CLR      J@CPUERR
9373 053524 013746 001276          MOV      J@STMP0,-(SP)          ;SETUP STACK TO RETURN
9374 053530 013746 001212          MOV      J@SLPERR,-(SP)
9375 053534 000002          RTI
          ;RETURN

```

```

9377
9378
9379
9380
9381
9382
9383 053536 000000
9384 053540 000020
9385 053542 140000
9386 053544 144020
9387 053546 040000
9388 053550 044020
9389
9390
9391 053552 000000
9392 053554 000004
9393 053556 000006
9394 053560 000010
9395 053562 000000
9396 053564 000012
9397
9398
9399 053566 002100
9400 053570 002122
9401 053572 000000
9402 053574 000000
9403 053576 002142
9404 053600 002202
9405 053602 000000
9406 053604 000000
9407 053606 002246
9408 053610 002230
9409 053612 054066
9410 053614 054074
9411 053616 054703
9412 053620 054703
9413 053622 054102
9414 053624 054110
9415 053626 054703
9416 053630 054703
9417 053632 054377
9418 053634 054742
9419 053636 046200 053517 046040
9420 053644 046511 000077
9421 053650 044510 044107 046040
9422 053656 046511 000077
9423 053662 051105 047522 050122
9424 053670 020103 044120 051531
9425 053676 020103 041520 020040
9426 053704 020040 051520 020127
9427 053712 020040 040515 047111
9428 053720 020124 020040 042524
9429 053726 052123 047040 020117
9430 053734 052523 026502 040520
9431 053742 051523 041440 052116

```

```

: THE BELOW TABLE REPRESENTS THE 'NEW' PSW SET BY THE PROGRAM ON
: SUCCESSIVE SUB-PASSES.
: NOTE THE BELOW TABLE MAY BE MODIFIED TO CAUSE THE PROGRAM TO RUN
: UNDER USER DEFINED PARAMETERS BY PATCHING IN THE DESIRED PASS PARAMETER
: FOR EXAMPLE TO CAUSE THE PROGRAM TO RUN WITHOUT SETTING THE 'T' BIT
: IN ALL PASSES PATCH OUT THE 'T' BIT IN THE TABLE.

```

```

PSWTAB: 000000
          000020
          140000
          144020
          040000
          044020
          : T-BIT TRAPPING
          : USER MODE
          : USER MODE, REG SET #1, T-BIT TRAPPING
          : SUPERVISOR MODE
          : SUPERVISOR MODE, REG SET #1, T-BIT TRAPPING

```

```

: THE BELOW TABLE IS USED TO SET MEMORY MARGINS
MRGTAB: .WORD 0 : NO MARGINS
         .WORD 4 : EARLY STROBE
         .WORD 6 : LATE STROBE
         .WORD 10 : LOW DRIVE CURRENT
         .WORD 0 : NO MARGINS
         .WORD 12 : HIGH DRIVE CURRENT

```

```

; MESSAGES
REGINX: .EVEN
         RP3DS
         RKDS
         .WORD
         .WORD
         RP4CS!
         RSCS!
         .WORD
         .WORD
         MBTTBL
         UBETBL
MSGINX: .WORD MSG5
         .WORD MSG6
         .WORD MSG21
         .WORD MSG21
         .WORD MSG10
         .WORD MSG11
         .WORD MSG21
         .WORD MSG21
         .WORD MSG15
         .WORD MSG24

```

```

MSG1: .ASCIZ <CRLF>'LOW LIM?'
MSG2: .ASCIZ 'HIGH LIM?'
MSG3: .ASCIZ /ERRORPC PHYSC PC PSW MAINT TEST NO SUB-PASS CNT/

```

9433	053751	124	042510	043040
9434	053756	046117	047514	044527
9435	053764	043516	042040	053105
9436	053772	041511	051505	040440
9437	054000	042116	042040	044522
9438	054006	042526	020123	044527
9439	054014	046114	041040	020105
9440	054022	051525	042105	043040
9441	054030	051117	051040	046105
9442	054036	041517	052101	047511
9443	054044	035116	200	
9444	054047	104	053105	041511
9445	054054	004507	051104	053111
9446	054062	051505	000200	
9447	054066	050122	031460	000011
9448	054074	045522	032460	000011
9449	054102	050122	032060	000011
9450	054110	051525	032060	000011
9451	054116	051104	051526	040524
9452	054124	020040	051105	051122
9453	054130	043505	020040	051503
9454	054140	042522	020107	020040
9455	054146	051127	041504	052116
9456	054154	020040	052502	040523
9457	054162	051104	020040	051504
9458	054170	040513	051104	020040
9459	054176	054503	040514	051104
9460	054204	051050	030120	024463
9461	054212	020040	044120	051531
9462	054220	041040	051525	042101
9463	054226	100122	000	
9464	054231	040	051503	020061
9465	054236	020040	053440	042122
9466	054244	047103	020124	041040
9467	054252	051525	042101	020122
9468	054260	041040	042101	042522
9469	054266	020130	042040	045523
9470	054274	042101	020122	020040
9471	054302	051503	020062	020040
9472	054310	020040	051503	020063
9473	054316	020040	042040	053122
9474	054324	052123	020101	042440
9475	054332	051122	042522	100107
9476	054340	000		
9477	054341	104	051505	054503
9478	054346	020114	020040	051105
9479	054354	020062	020040	020040
9480	054362	051105	020063	020040
9481	054370	051040	041520	100103
9482	054376	000		
9483	054377	115	051501	020123
9484	054404	052502	020123	042524
9485	054412	052123	051105	000040
9486	054420	041440	030523	020040
9487	054426	020040	051127	041504

MSG4: .ASCII /THE FOLLOWING DEVICES AND DRIVES WILL BE USED FOR RELOCATION: /<CRLF>

.ASCIZ /DEVICE DRIVES /<CRLF>

MSG5: .ASCIZ ?RPO3 ?

MSG6: .ASCIZ ?RK05 ?

MSG10: .ASCIZ ?RPO4 ?

MSG11: .ASCIZ ?RS04 ?

MSG12: .ASCIZ /DRVSTA ERRREG CSREG WRDCNT BUSADR DSKADR CYLADR(RPO3) PHYS BUSA

MSG13: .ASCIZ / CS1 WRDCNT BUSADR BADREX DSKADR CS2 CS3 DRVSTA ERRREG /

MSG14: .ASCIZ /DESCYL ER2 ER3 RPCC /<CRLF>

MSG15: .ASCIZ /MASS BUS TESTER /

MSG16: .ASCIZ / CS1 WRDCNT BUSADR BADREX MR2 CS2 ST ER CS3 /<

95299	054442	040523	051104	020040
95300	054450	040502	051104	054105
95301	054456	020040	020040	051115
95302	054464	020062	020040	020040
95303	054472	051503	020062	020040
95304	054500	020040	051440	020124
95305	054506	020040	020040	051105
95306	054514	020040	020040	020040
95307	054522	051503	100063	000
95308	054527	040	041440	020103
95309	054534	020040	041040	051525
95310	054542	042101	020122	020040
95311	054550	041440	031122	020040
95312	054556	020040	041440	030522
95313	054564	020040	044120	051531
95314	054572	041040	051525	042101
95315	054600	100122	000	
95316	054603	124	042510	050440
95317	054610	044525	045503	041040
95318	054616	047522	047127	043040
95319	054624	054117	045040	046525
95320	054632	042520	020104	053117
95321	054640	051105	052040	042510
95322	054646	046040	055101	020131
95323	054654	047504	051507	041040
95324	054662	041501	020113	030460
95325	054670	031462	032464	033466
95326	054676	034470	005015	000
95327	054703	111	046114	043505
95328	054710	046101	042040	053105
95329	054716	041511	100105	000
95330	054723	040	020040	020040
95331	054730	020040	040	
95332	054733	040	020040	020040
95333	054740	000040		
95334	054742	047125	041111	051525
95335	054750	042440	042530	041522
95336	054756	051511	051105	000040
95337	054764	050117	027124	050103
95338	054772	000075		
95339	054774	047125	054105	042520
95340	055002	052103	042105	052040
95341	055010	040522	020120	047524
95342	055016	032040	000	
95343	055021	120	047503	052106
95344	055026	020120	050040	054510
95345	055034	050123	020103	020040
95346	055042	050040	053523	020040
95347	055050	041440	052520	051105
95348	055056	000122		
95349	055060	000	001	000
95350	055063	000	000	
95351	055066			
95352	055066	001474	001474	001276
95353	055074	001302	000000	

MSG17: .ASCIZ / CC BUSADR CR2 CR1 PHYS BUSADR/<CRLF>

MSG20: .ASCIZ /THE QUICK BROWN FOX JUMPED OVER THE LAZY DOGS BACK 0123456789/<15><12>

MSG21: .ASCIZ /ILLEGAL DEVICE/<CRLF>

MSG22: .ASCII / /

MSG23: .ASCIZ / /

MSG24: .ASCIZ /UNIBUS EXERCISER /

MSG25: .ASCIZ /OPT.CP=/

EM1: .ASCIZ /UNEXPECTED TRAP TO 4/

DH1: .ASCIZ /PCOFTP PHYSPC PSW CPUERR/

DF1: .BYTE 0,1,0,0,0

DT1: .EVEN  
.WORD VADR,VADR,\$TMP0,\$TMP2,0





9601	055534	001302	000000				
9602	055540	051105	047522	020122	EM6:	.ASCIZ	/ERROR DURING DATA CHECK-RELOC WAS BY CP/
9603	055546	052504	044522	043516			
9604	055554	042040	052101	020101			
9605	055562	044103	041505	026513			
9606	055570	042522	047514	020103			
9607	055576	040527	020123	054502			
9608	055604	041440	000120				
9609	055610	051123	040503	051104	DH6:	.ASCIZ	/SRCADR DSTADR/
9610	055616	020040	051504	040524			
9611	055624	051104	000				
9612		055630					
9613	055630	001276	001474	000000	DT6:	.EVEN	
9614	055636	051105	047522	020122	EM10:	.WORD	\$TMP0,VADR,0
9615	055644	052504	044522	043516		.ASCIZ	?ERROR DURING DATA CHECK-RELOC WAS BY I/O?
9616	055652	042040	052101	020101			
9617	055660	044103	041505	026513			
9618	055666	042522	047514	020103			
9619	055674	040527	020123	054502			
9620	055702	044440	047457	000			
9621	055707	123	041522	042101	DH10:	.ASCIZ	/SRCADR DSTADR DEVICE THAT DID XFER/
9622	055714	020122	020040	051504			
9623	055722	040524	051104	020040			
9624	055730	042040	053105	041511			
9625	055736	020105	044124	052101			
9626	055744	042040	042111	054040			
9627	055752	042506	000122				
9628	055756	000	001	003	DF10:	.BYTE	0,1,3,0
9629	055761	000					
9630							
9631	055762	001276	001474	001302	DT10:	.EVEN	
9632	055770	001304	000000			.WORD	\$TMP0,VADR,\$TMP2,\$TMP3,0
9633	055774	044502	024124	024523	EM11:	.ASCIZ	/BIT(S) STUCK IN MICRO-BREAK REGISTER/
9634	056002	051440	052524	045503			
9635	056010	044440	020116	044515			
9636	056016	051103	026517	051102			
9637	056024	040505	020113	042522			
9638	056032	044507	052123	051105			
9639	056040	000					
9640	056041	107	047517	042104	DH11:	.ASCIZ	/GOODDAT BAD DATA/
9641	056046	052101	041040	042101			
9642	056054	042040	052101	000101			
9643	056062	000	000		DF11:	.BYTE	0,0
9644						.EVEN	
9645	056064	001276	001300	000000	DT11:	.WORD	\$TMP0,\$TMP1,0
9646	056072	041125	020105	047516	EM12:	.ASCIZ	/UBE NON-EXISTANT MEMORY ERROR/
9647	056100	026516	054105	051511			
9648	056106	040524	052116	046440			
9649	056114	046505	051117	020131			
9650	056122	051105	047522	000122			
9651	056130	044120	051531	041040	DH12:	.ASCIZ	/PHYS BUSADR/
9652	056136	051525	042101	000122			
9653	056144	002			DF12:	.BYTE	2
9654		056146				.EVEN	
9655	056146	001226	000000		DT12:	.WORD	\$GDDAT,0

9657	056160	026516	054105	051511				
9658	056166	040524	052116	046440				
9659	056174	046505	051117	020131				
9660	056202	051105	047522	000122				
9661	056210	044120	051531	040440	DH13:	.ASCIZ	/PHYS ADDRESS/	
9662	056216	042104	042522	051523				
9663	056224	000						
9664	056225	106	047514	052101	EM14:	.ASCIZ	/FLOATING POINT ERROR/	
9665	056232	047111	020107	047520				
9666	056240	047111	020124	051105				
9667	056246	047522	000122					
9668	056252	042011	040524	030524	DH14:	.ASCIZ	/ DTAT1	DATA2/
9669	056260	004411	004411	040504				
9670	056266	040524	000062					
9671						.EVEN		
9672	056272	001306	001262	001312	DT14:	.WORD	\$TMP4,\$REG2,\$TMP6,\$REG3,0	
9673	056300	001264	000000					
9674	056304	004	000	004	DF14:	.BYTE	4,0,4,0	
9675	056307	000						
9676	056310	042504	044526	042503	EM15:	.ASCIZ	/DEVICE HUNG/	
9677	056316	044040	047125	000107				
9678	056324	004411	040504	040524	DH16:	.ASCIZ	/ DATA1	DATA2/
9679	056332	004461	004411	020011				
9680	056340	020040	042040	052101				
9681	056346	031101	000					
9682	056351	005	000	005	DF16:	.BYTE	5,0,5,0	
9683	056354	000						
9684		056356				.EVEN		
9685	056356	001422	001262	001432	DT16:	.WORD	FLTMP0,\$REG2,FLTMP1,\$REG3,0	
9686	056364	001264	000000					
9687	056370	000000			ENDTAG:	.WORD	0	
9688					:*****			
9689					:THE FOLLOWING ASCII GETS OVERLAYED WHEN THE PROGRAM RUNS.			
9690	056372	050117	051105	052101	SWITCH:	.ASCII	/OPERATIONAL SWITCH SETTINGS/<CRLF>	
9691	056400	047511	040516	020114				
9692	056406	053523	052111	044103				
9693	056414	051440	052105	044524				
9694	056422	043516	100123					
9695	056426	053523	052111	044103		.ASCII	/SWITCH	USE/<CRLF>
9696	056434	004411	052411	042523				
9697	056442	200						
9698	056443	040	030440	004465		.ASCII	/ 15	HALT ON ERROR/<CRLF>
9699	056450	044011	046101	020124				
9700	056456	047117	042440	051122				
9701	056464	051117	200					
9702	056467	040	030440	004464		.ASCII	/ 14	LOOP ON TEST/<CRLF>
9703	056474	046011	047517	020120				
9704	056502	047117	052040	051505				
9705	056510	100124						
9706	056512	020040	031461	004411		.ASCII	/ 13	INHIBIT ERROR TYPEOUTS/<CRLF>
9707	056520	047111	044510	044502				
9708	056526	020124	051105	047522				
9709	056534	020122	054524	042520				
9710	056542	052517	051524	200				
9711	056547	040	030440	004462		.ASCII	/ 12	INHIBIT UBE/<CRLF>

9713	056562	052111	052440	042502		
9714	056570	200				
9715	056571	040	030440	004461	.ASCII / 11	INHIBIT ITERATIONS/<CRLF>
9716	056576	044411	044116	041111		
9717	056604	052111	044440	052124		
9718	056612	051105	052101	047511		
9719	056620	051516	200			
9720	056623	040	030440	004460	.ASCII / 10	BELL ON ERROR/<CRLF>
9721	056630	041011	046105	020114		
9722	056636	047117	042440	051122		
9723	056644	051117	200			
9724	056647	040	020040	004471	.ASCII / 9	LOOP ON ERROR/<CRLF>
9725	056654	046011	047517	020120		
9726	056662	047117	042440	051122		
9727	056670	051117	200			
9728	056673	040	020040	004470	.ASCII ? 8	INHIBIT RELOCATION VIA I/O DEVICE?<CRLF>
9729	056700	044411	044116	041111		
9730	056706	052111	051040	046105		
9731	056714	041517	052101	047511		
9732	056722	020116	044526	020101		
9733	056730	027511	020117	042504		
9734	056736	044526	042503	200		
9735	056743	040	020040	004467	.ASCII / 7	INHIBIT TYPEOUT OF THIS TEXT AND SYS SIZE/<CRLF>
9736	056750	044411	044116	041111		
9737	056756	052111	052040	050131		
9738	056764	047505	052125	047440		
9739	056772	020106	044124	051511		
9740	057000	052040	054105	020124		
9741	057006	047101	020104	054523		
9742	057014	020123	044523	042532		
9743	057022	200				
9744	057023	040	020040	004466	.ASCII / 6	INHIBIT RELOCATION/<CRLF>
9745	057030	044411	044116	041111		
9746	057036	052111	051040	046105		
9747	057044	041517	052101	047511		
9748	057052	100116				
9749	057054	020040	032440	004411	.ASCII / 5	INHIBIT ROUND ROBIN RELOCATION/<CRLF>
9750	057062	047111	044510	044502		
9751	057070	020124	047522	047125		
9752	057076	020104	047522	044502		
9753	057104	020116	042522	047514		
9754	057112	040503	044524	047117		
9755	057120	200				
9756	057121	040	020040	004464	.ASCII / 4	INHIBIT RANDOM DISK ADDRESS/<CRLF>
9757	057126	044411	044116	041111		
9758	057134	052111	051040	047101		
9759	057142	047504	020115	044504		
9760	057150	045523	040440	042104		
9761	057156	042522	051523	200		
9762	057163	040	020040	004463	.ASCII / 3	INHIBIT MBT/<CRLF>
9763	057170	044411	044116	041111		
9764	057176	052111	046440	052102		
9765	057204	200				
9766	057205	040	020040	004462	.ASCII / 2	THESE THREE SWITCHES/<CRLF>
9767	057212	052011	042510	042523		

9769	057226	051440	044527	041524		
9770	057234	042510	100123			
9771	057240	020040	030440	004411	.ASCII / 1	ARE ENCODED TO SELECT RELOCATION/<CRLF>
9772	057246	051101	020105	047105		
9773	057254	047503	042504	020104		
9774	057262	047524	051440	046105		
9775	057270	041505	020124	042522		
9776	057276	047514	040503	044524		
9777	057304	047117	200			
9778	057307	040	020040	004460	.ASCII / 0	ON THE FOLLOWING DEVICES:/<CRLF>
9779	057314	047411	020116	044124		
9780	057322	020105	047506	046114		
9781	057330	053517	047111	020107		
9782	057336	042504	044526	042503		
9783	057344	035123	200			
9784	057347	011	027060	027056	.ASCII ?	0...RP11/RP03?<CRLF>
9785	057354	050122	030461	051057		
9786	057362	030120	100063			
9787	057366	030411	027056	051056	.ASCII ?	1...RK11/RK05?<CRLF>
9788	057374	030513	027461	045522		
9789	057402	032460	200			
9790	057405	011	027062	027056	.ASCII ?	2...NOT USED?<CRLF>
9791	057412	047516	020124	051525		
9792	057420	042105	200			
9793	057423	011	027063	027056	.ASCII ?	3...NOT USED?<CRLF>
9794	057430	047516	020124	051525		
9795	057436	042105	200			
9796	057441	011	027064	027056	.ASCII ?	4...RH70/RP04?<CRLF>
9797	057446	044122	030067	051057		
9798	057454	030120	100064			
9799	057460	032411	027056	051056	.ASCII ?	5...RH70/RS04 OR RS03?<CRLF>
9800	057466	033510	027460	051522		
9801	057474	032060	047440	020122		
9802	057502	051522	031460	200		
9803	057507	011	027066	027056	.ASCII ?	6...NOT USED?<CRLF>
9804	057514	047516	020124	051525		
9805	057522	042105	200			
9806	057525	011	027067	027056	.ASCIZ ?	7...NOT USED?<CRLF>
9807	057532	047516	020124	051525		
9808	057540	042105	000200			

AA	001505	644#	1103#			
ADC82	011170	2265	2267#			
ADC85	012014	2495	2497#			
ADC86	012516	2651	2652#	2654#		
ADC87	013440	2874	2875	2876	2878#	
ADCO	007062	1664	1665	1666	1668#	
ADC1	007762	1893	1894	1895	1897#	
ADC2	010772	2195	2197#			
ADC5	011614	2423	2424	2426#		
ADC6	012320	2594	2595	2597#		
ADC7	013306	2832	2833	2835#		
ADD0	014134	3041	3042	3043	3045#	
ADD1	014240	3078	3079	3081#		
ADD1A	014464	3161	3162	3163	3165#	
ADD1B	014502	3170	3171	3173#		
ADD2	015124	3315	3316	3318#		
ADD3	015702	3507	3509#			
ADD6	016260	3608	3609	3611#		
ADD7	016736	3724	3725	3726	3728#	
ARBEX	033376	6314	6357#			
ARBF IN	033364	6299	6321	6342	6344	6355#
ASHCLO	025230	5144#				
ASHCRO	025306	5165#				
ASHLO	025020	5094#				
ASHL1	025616	5276#				
ASHRO	025134	5121#				
ASHR1	025704	5299#				
ASLB1	010332	2027	2028	2030#		
ASLB1A	010556	2114	2115	2117#		
ASLB3	012004	2489	2490	2492#		
ASLB4	011274	2302	2303	2304	2306#	
ASLB6	012500	2643	2644	2645	2647#	
ASLB7	013536	2906	2907	2909#		
ASLO	007204	1708	1709	1710	1711	1713#
ASL1	010136	1956	1957	1958	1960#	
ASL3	011530	2392	2393	2395#		
ASL4	011064	2227	2228	2229	2231#	
ASL6	012270	2582	2583	2585#		
ASL7	013134	2779	2780	2782#		
ASRB1	010426	2063	2065#			
ASRB1A	010442	2069	2070	2072#		
ASRB2	011240	2287	2288	2290#		
ASRB2A	011256	2295	2296	2298#		
ASRB5	011744	2470	2471	2473#		
ASRB6	012616	2683	2684	2686#		
ASRB7	013554	2913	2914	2916#		
ASRO	007232	1722	1723	1724	1726#	
ASR1	010024	1913	1914	1915	1917#	
ASR2	011006	2201	2202	2204#		
ASR3	011514	2386	2388#			
ASR6	012152	2544	2545	2547#		
ASR7	013170	2793	2794	2796#		
BICB1	014664	3233	3234	3236#		
BICB1A	014706	3244	3247#	3305	3306	
BICQ	014046	3012	3013	3014	3016#	















MAPLO = 170200	442#	5804	5837	5848*	8960	9101
MAPL00= 170200	378#	442				
MAPL01= 170204	380#	444				
MAPL02= 170210	382#	446				
MAPL03= 170214	384#	448				
MAPL04= 170220	386#	450				
MAPL05= 170224	388#	452				
MAPL06= 170230	390#	454				
MAPL07= 170234	392#	456				
MAPL1 = 170204	444#	5846*				
MAPL10= 170240	394#					
MAPL11= 170244	396#					
MAPL12= 170250	398#					
MAPL13= 170254	400#					
MAPL14= 170260	402#					
MAPL15= 170264	404#					
MAPL16= 170270	406#					
MAPL17= 170274	408#					
MAPL2 = 170210	446#					
MAPL20= 170300	410#					
MAPL21= 170304	412#					
MAPL22= 170310	414#					
MAPL23= 170314	416#					
MAPL24= 170320	418#					
MAPL25= 170324	420#					
MAPL26= 170330	422#					
MAPL27= 170334	424#					
MAPL3 = 170214	448#					
MAPL30= 170340	426#					
MAPL31= 170344	428#					
MAPL32= 170350	430#					
MAPL33= 170354	432#					
MAPL34= 170360	434#					
MAPL35= 170364	436#					
MAPL36= 170370	438#					
MAPL37= 170374	440#					
MAPL4 = 170220	450#					
MAPL5 = 170224	452#					
MAPL6 = 170230	454#					
MAPL7 = 170234	456#					
MAPTBL 001656	681#	1143*	1144*	9096	9099*	9128*
MAPTST 030176	5804#					
MAPTHO 030324	5803	5839	5844#			
MARKEX 024256	4935	4939	4943#			
MARK1 024232	4931	4933#				
MBRK 026446	5448#					
MBTAS = 160116	493#					
MBTBA = 160104	488#	881				
MBTBAE= 160174	497#	882				
MBTCS1= 160100	486#	879				
MBTCS2= 160110	490#	884				
MBTCS3= 160176	498#	887				
MBTDB = 160120	494#					
MBTDT = 160126	496#	890				
MBTER = 160114	492#	886				











# JO1

CEQKCC PDP 11/70 CPU EXERCISOR  
CEQKCC.P11 03-MAR-78 13:13

MACY11 30A(1052) 03-MAR-78 13:15 PAGE 198  
CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0216

ROR7	013210	2801	2802	2804*															
RPCC	002172	843#																	
RP3BA	002110	814#	6999*	7288*															
RP3CS	002104	812#	1328*	1329	6989*	6994*	7217	7221*	7222	7236	7246*	7252	7259*	7260					
		7289*	7295	7300*	7301	8133													
RP3DA	002112	815#	6970*	6996*	7285*														
RP3DC	002114	816#	6997*	7286*															
RP3DRV	036772	760	6929#																
RP3DS	002100	810#	1335	6987	9399														
RP3ER	002102	811#	7266																
RP3ERR	040774	7220	7228#	7257	7298														
RP3FUN	002060	783#	6993*	7202*	7203	7208*	7209	7258*	7299*										
RP3HAN	002026	760#	6657	6769															
RP3HDA	002042	771#	6996	7285															
RP3HDC	002044	772#	6969*	6997	7286														
RP3HST	001676	691#	1146	6630	6635	6681	6754*	6768*	6771	6773	6930*	6931	7215	7228*					
		7235*	7238#	7250	7293	7312*													
RP3HWC	001716	704#	6647*	6998	7287														
RP3LOO	041022	7216	7235#	7251	7294														
RP3NWH	001764	733#	6661*	7278	7280*	7283													
RP3NWL	001762	732#	6660*	7275	7288														
RP3OLD	001732	715#	6659*	6662*	6975	6978	6981*	6999	7243										
RP3PSW	002120	818#	6992*																
RP3REA	041350	7212	7293#																
RP3RPT	040644	6934	7201#																
RP3SRV	040674	6991	7208#																
RP3TRY	002072	795#	6971*	7219	7224*	7241*	7256	7262*	7270*	7297	7303*								
RP3UNI	002012	749#	6655*	6982	6985*	6989	7244	7284											
RP3VEC	002116	817#	6991*																
RP3WC	002106	813#	6998*	7287*															
RP3WCK	041112	7210	7250#																
RP3WTR	037310	6987#	6988	7206	7226														
RP31	041046	7204	7218	7241#															
RP310	001564	668#	6977*	6980*	6984*	6994	7242*	7243*	7244*	7246	7282*	7283*	7284*	7289					
RP311	001566	669#	6935*	6950	7230	7254													
RP32	041076	7245#	7264																
RP33	041310	7207	7285#	7305															
RP4BA	002146	833#	7139*	7520*															
RP4BAE	002150	834#	7138*	7519*															
RP4CS1	002142	831#	1319	1349*	7130*	7470*	7491*	7521*	9403										
RP4CS2	002154	836#	1348*	1350	1358*	6764*	7133*	7463*	7502*	7508	7532*								
RP4CS3	002156	837#																	
RP4DA	002152	835#	7136*																
RP4DC	002164	840#	7135*																
RP4DRV	040004	764	7079#																
RP4DS	002160	838#	1352	7459	7471	7482	7488	7497	7516	7527									
RP4ERR	042504	7462	7474#	7500	7530														
RP4ER1	002162	839#																	
RP4ER2	002166	841#																	
RP4ER3	002170	842#																	
RP4FUN	002066	786#	7129*	7446*	7447	7451*	7452	7531*											
RP4HAN	002036	764#																	
RP4HDA	002052	775#	7123*	7136															
RP4HDC	002054	776#	7122*	7135															
RP4HST	001706	695#	7080*	7081	7086*	7457	7468	7474*	7481*	7484*	7495	7525	7538*						









TRAP1	021402	4285	4300#				
TRAP1C	021442	4299	4312#				
TRTVEC#	000014	185#					
TSTB1	010536	2105	2106	2108#			
TSTB2	011342	2327	2329#				
TSTB2A	011352	2332	2334#				
TSTB6	012410	2616	2617	2618	2620#		
TST0	007022	1645	1646	1647	1648	1650#	
TST1	005524	1449#					
TST10	011104	2242#					
TST11	011422	2359#					
TST12	011644	2443#					
TST13	012064	2524#					
TST14	012344	2608#					
TST15	012736	2730#					
TST16	013340	2851#					
TST17	013634	2940#					
TST2	006554	1566#					
TST20	014172	3063#					
TST21	014516	3182#					
TST22	015046	3303#					
TST23	015332	3390#					
TST24	015536	3460#					
TST25	015726	3523#					
TST26	016106	3579#					
TST27	016404	3640#					
TST3	006750	1629#					
TST30	016560	3682#					
TST31	016776	3745#					
TST32	017126	3783#					
TST33	017322	3836#					
TST34	020030	3964#					
TST35	020322	4055#					
TST36	020644	4159#					
TST37	021070	4176	4225#				
TST4	007266	1745#					
TST40	021276	4280#					
TST41	021506	4328#					
TST42	022214	4448#					
TST43	022374	4494#					
TST44	022602	4542#					
TST45	023010	4608#					
TST46	023142	4656#					
TST47	023734	4829#					
TST5	007662	1867#					
TST50	024164	4918#					
TST51	024260	4968#					
TST52	024466	5011#					
TST53	024726	5076#					
TST54	025372	5194#					
TST55	025522	5245#					
TST56	025770	5328#					
TST57	026046	5351#					
TST6	010214	1990#					
TST60	026216	5394#					









F02

CEQKCC PDP 11/70 CPU EXERCISOR  
CEQKCC.P11 03-MAR-78 13:13

MACY11 30A(1052) 03-MAR-78 13.15 PAGE 207  
CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0225

\$LPERR 001212

578#	1022#	1176#	1461#	1462#	1463	1578#	1579#	1580	2742#	2743#	2744	3795#
3796#	3797	4340#	4341#	4342	4668#	4669#	4670	5088#	5089#	5090	5561#	5562#
5563	5879#	5880#	5881	6546#	6692	6745#	7684	7685#	7696#	7742	7743#	7752#
7826	7841#	7850	7894	9266	9267#	9269#	9285	9339	9340#	9342#	9344	9353
9354#	9356#	9357	9368	9369#	9371#	9374						
677#	6855#	6865#	7960									

\$MAINT 001604

\$MXCNT 044772

\$NULL 001250

\$NWTST= 000001

7839	7850#											
592#	8225	8256										
1446#	1563#	1626#	1742#	1864#	1987#	2137#	2239#	2356#	2440#	2521#	2605#	2727#
2848#	2937#	3060#	3179#	3300#	3387#	3457#	3520#	3576#	3637#	3678#	3680	3741#
3743	3780#	3833#	3961#	4052#	4154#	4156	4222#	4277#	4325#	4445#	4491#	4539#
4605#	4653#	4824#	4826	4915#	4944#	4946	5008#	5073#	5188#	5190	5238#	5240
5322#	5324	5348#	5387#	5389	5442#	5444	5461#	5505#	5525#	5546#	5638#	5640
5678#	5680	5735#	5737	5796#	5798	5857#	5859	6044#	6046	6289#	6358#	6360
8382#	8411#	8424#										
8509	8537#											
8377#	8381#	8386	8389#	8400#	8426#							
7807	7827	7837	7844#									
572#	1131#	1407	6390	6890#	6891#	6902	6923	7833	7851	7972		
8870	8877#											
8872#												
1169	8843#	8867										
8870#												
8852	8857#											
617#	7900											
6941	7019	7092	7158	8722#	8758	9076						

\$SOCNT 047462

\$SOCTL 050032

\$SMODE 047464

\$SOVER 044742

\$SPASS 001200

\$SPOWER 051364

\$SPWRAD 051352

\$SPWRDN 051230

\$SPWRMG 051346

\$SPWRUP 051276

\$SQUES 001326

\$SRAND 050604

\$SRDCHR= \*\*\*\*\*

\$SRDOEC= \*\*\*\*\*

\$SRDLIN= \*\*\*\*\*

\$SRDOCT= \*\*\*\*\*

\$SREGAD 001254

\$SREGO 001256

U  
U  
U

596#												
598#	5892	5903	5925	5965	5972	5992	6025	6033	6034	6036	6061	6072
6094	6134	6141	6161	6194	6202	6203	6205	8807				
599#	5893	5914	5924	5966	5974	5984	6026	6062	6083	6093	6135	6143
6153	6195											
600#	5899#	5951	5981#	6012	6030#	6068#	6120	6150#	6181	6199#	9672	9685
601#	5939#	5953	6002#	6014	6034#	6108#	6122	6171#	6183	6203#	9672	9685
602#	9266#	9269	9339#	9342	9353#	9356	9368#	9371				
603#	6302#	6861#	6871#	9172#	9180#	9190#	9222#	9223	9227#			
604#												
605#												
8572#	8911											
637#	1431	5004	6860									
9304	9318	9329#										
9295#	9326	9330#										
8556#	8910											
8851#	8857	8858#	8859#	8876#								
1163	4174	4180	4274	5503	7805#							
1138#	1163	1165	1167	1169	1171	1172	1173	1175	1274	6888	7886	
1138#												
7815	7840#											
555#	560											
1#	32	47	48	49	50	51	52	53	54	614	615	616
1172	1173	1175	1176	1452	1569	1631	1747	1869	1992	2142	2244	2361
2445	2526	2610	2733	2853	2942	3065	3184	3305	3392	3462	3525	3581

\$SREG1 001260

\$SREG2 001262

\$SREG3 001264

\$SREG4 001266

\$SREG5 001270

\$SREG6 001272

\$SREG7 001274

\$RESRE 050106

\$RTRN 001472

\$SAVPA 053264

\$SAVPS 053272

\$SAVRE 050050

\$SAVR6 051362

\$SCOPE 044534

\$SETUP= 000037

\$STUP = 177777

\$SVLAD 044720

\$SVPC = 000220

\$SWR = 167377



SSGET4= 000000  
SSTRP4 001416  
SSTMP6 001420  
SSTRP = 000002  
SOFILL = 042463  
          = 057544

6916#	8906	8907	8908	8909	8910	8911	8912	8913	8914	620	621#	631#
629#	8380#	8390#	8425#	555	556#	558#	560#	569#	575#	1161	1175	1176
630#	527#	529#	5325#	680#	683#	684#	685#	800#	1081#	1639	1649	1659
8895#	1459#	1465#	1579#	1580#	1583#	1601#	1609#	1616#	1624#	1765	1775	1785
8376#	1676#	1685#	1694#	1703#	1711#	1718#	1725#	1732#	1740#	1916	1923	1931
523#	1799#	1808#	1824#	1870#	1880#	1887#	1896#	1903#	1909#	2011	2017	2023
1279#	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	2094	2101	2107
1567	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	2166	2179	2184
1792	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	2211	2217	2223
1938	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	2244	2258	2262
2029	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	2273	2287	2293
1111	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	2328	2333	2339
188	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	2357	2371	2377
356	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	2414	2419	2425
351	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	2451	2456	2462
358	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	2488	2493	2499
431	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	2507	2513	2519
435	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	2544	2549	2555
535	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	2581	2586	2592
627	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	2600	2606	2612
709	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	2637	2642	2648
823	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	2674	2679	2685
221	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	2711	2716	2722
3034	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	2748	2753	2759
3145	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	2785	2790	2796
3240	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	2822	2827	2833
3383	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	2859	2864	2870
3513	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	2896	2901	2907
3623	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	2933	2938	2944
3727	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	2970	2975	2981
3872	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	3007	3012	3018
3990	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	3044	3049	3055
4232	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	3081	3086	3092
4453	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	3118	3123	3129
4682	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	3155	3160	3166
4800	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	3192	3197	3203
4937	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	3229	3234	3240
5217	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	3266	3271	3277
5559	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	3303	3308	3314
6923	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	3340	3345	3351
7900	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	3377	3382	3388
9654#	1946#	1955#	1971#	1980#	1987#	1996#	1996#	2002#	2005#	3414	3419	3425





K02

CEQKCC PDP 11/70 CPU EXERCISOR MACY11 30A(1052) 03-MAR-78 13:15 PAGE 213  
CEQKCC.P11 03-MAR-78 13:13 CROSS REFERENCE TABLE -- MACRO NAMES

SEQ 0230

.SDB20	1#		
.SDB20	1#	6#	8495
.SDIV	1#		
.SEOP	1#	6876	
.SERRO	1#	7851	
.SERRT	1#		
.SMULT	1#		
.SPOWE	1#	8838	
.SRAND	1#	7#	8710
.SRDDE	1#		
.SRDOC	1#		
.SREAD	1#		
.SSAVE	1#	8538	
.SSB20	1#		
.SSB20	1#		
.SSCOP	1#	2#	7792
.SSIZE	1#		
.SSUPR	1#		
.STRAP	1#	8880	
.STYPB	1#		
.STYPD	1#	8427	
.STYPE	1#	562#	8181
.STYPO	1#	8349	
.1170	1#	71	

. ABS. 057544 000

ERRORS DETECTED: 0

CEQKCC.BIN,CEQKCC.LST/CRF/SOL/NL:TOC=DSKZ:CEQKCC.SML,CEQKCC.P11  
RUN-TIME: 32 38 3 SECONDS  
RUN-TIME RATIO: 1039/75=13.7  
CORE USED: 32K (63 PAGES)



L02

M02