Siemens System 7.500

New Product Announcement

In May 1981, Siemens expanded the System 7.500 line with the addition of the dual-processor 7.552. Offering up to 70 percent greater performance than the 7.551, the 7.552 is aimed at the IBM 4341-2 market. Like all 7.500 models, the new machine operates under BS 2000. Main memory is composed of four modules, and each module can be outfitted with 4, 6, or 8 megabytes. Each processor has a 32K-byte cache memory.

The I/O system of the Model 7.552 has two channel control units, each with one byte multiplexor and two block multiplexor channels. The channel control units coordinate concurrent operations and switch the data paths between the processors. Each processor can access all peripherals. Each block multiplexor channel is equipped with a 2 x 16-byte exchange buffer and a 2-byte intermediate buffer. This buffer arrangement permits parallel processing of data requests at the interfaces to main memory and to the external device. The aggregate transfer rate of the I/O system is 10.2 megabytes/second.

The CPU with 4 megabytes of memory, power supply, two operator consoles, and a service processor is priced at DM 1,700,000.□

CHARACTERISTICS	Siemens 7.551	IBM 4341-2	Siemens 7.552	IBM 3032	Siemens 7.561
Relative Performance [1]	55	66	94	124	130
Main Memory (megabytes)	2-4	2-8	4-8	2-8	4-8
CPU purchase price (DM)	1,154,000 [2] (2 MB)	Not available	1,700,000 [5] (4 MB)	3,694,000 (2 MB)	3,317,000 (2 MB)
CPU monthly rental (DM)	30,100 [2] (3-yr.)	Not available	46,000 (3-yr.)	127,300 (4-yr.)	86,203 (3-yr.)
Access time (nanoseconds)	Not available	Not available	200/8 bytes	320	Not available
Cycle time (nanoseconds)	80-120	120-240	Not available	80	52
Number of channels	6-16 [3] 3-6 [4]	6	4-12 [4] 1-2 [3]	6-12	8-14 [3] 4-12 [4]
Cache memory	16K bytes	16K bytes	64K bytes [6]	32K bytes	32K bytes

[1] Based on a relative performance value of 45 for IBM 370/145-3.

[2] Basic configuration: CPU, service console, high-speed printer (600 lpm).

[3] Byte multiplexor channels.

[4] Block multiplexor channels.

[5] CPU, power supply, two operator consoles, service processor.

[6] 32K bytes per processor.