

The shortest possible design cycle
is destined to become
the industry standard design cycle.

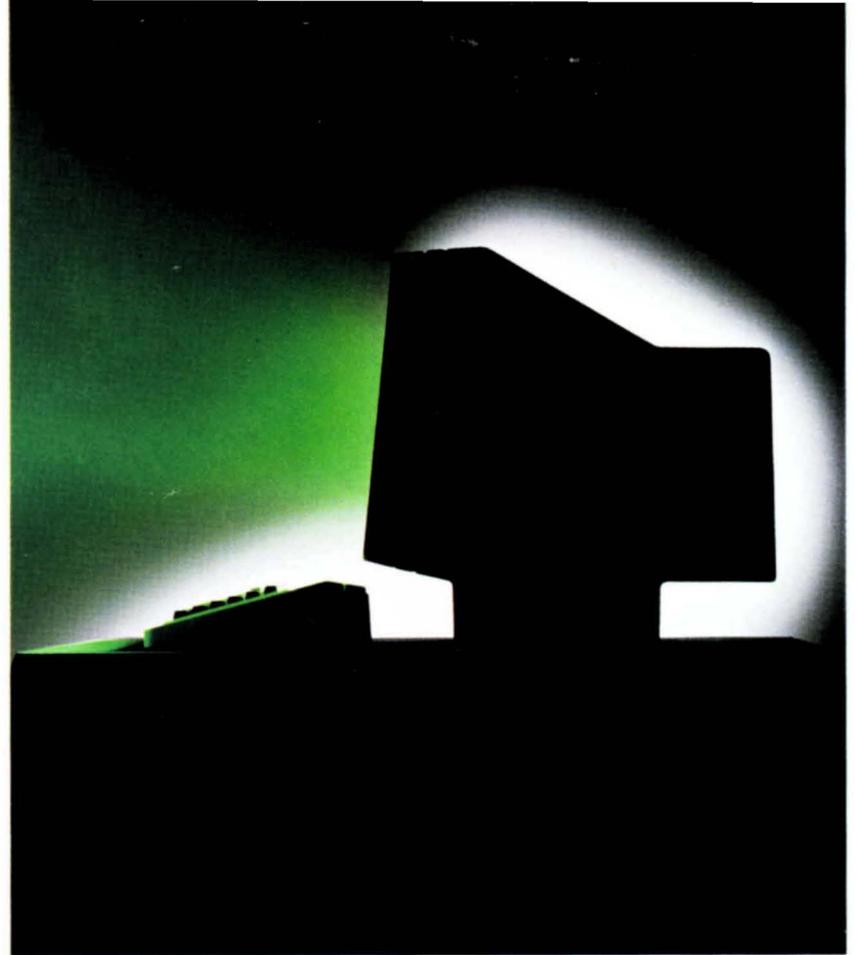
Daisy Engineering Design Stations.

**(And now one can be installed on every engineer's desk
for as little as \$25,000 per engineer.)**

DAISY SYSTEMS CORPORATION

The Daisy LOGICIAN™ Engineering Workstation.

**One year and 150 installations
after introduction.
And over 1,000 engineers
will tell you there is simply
no better way to design.**



DESIGN ENGINEERS have built the ultimate engineering workstation for design engineers.

The result is a system that redefines and re-emphasizes the value of the engineer.

Such a system can be justified easily and simply in terms of cost effectiveness through a reduction of man-hours and time to market. The time required to deliver working, debugged designs is now reduced by what can amount to an order of magnitude.

But the contribution of the LOGICIAN system is more important. It is a major step in engineering design methodology.

Quite simply, if you are an engineer, work with a LOGICIAN and you will remember why you got into engineering in the first place.

With an investment as low as \$25,000 per engineer, you can now put the state-of-the-art to work for the very designers who are creating the state-of-the-art.

State-of-the-art engineering occurs only when the engineer has the tools to maximize the impact of his creativity and thinking.

The Daisy Engineering Environment™ is an expandable system of Ethernet™-linked

engineering workstations, storage peripherals, output devices, and over one million lines of tested, proven software--all created to accelerate the process of electronic design.

The LOGICIAN workstation itself puts the power of a 1 MIPS-class computer on the desktop of each design engineer. The result is a personal, 'no waiting' workstation that delivers instantaneous computing power to each and every designer.

Design at the speed of thought. A graphic window into a comprehensive, hierarchical design database.

The LOGICIAN system's complete design software toolbox is built around a fully hierarchical design database.

The user works with familiar graphic symbols--electronic representations of standard drawing pages.

But the key advantage of the LOGICIAN is this: the drawings represent electronic circuits whose functions--even as the circuits are being created--are always (and automatically) fully described in the design database.

This full description is the basic tool for fast, fault free design. Why? Because the design database provides a 'virtual'

breadboard of the system under development. Always available for logic and circuit debugging.

The designer can quickly access any portion of the design, create additional circuitry or modify existing logic, and then verify the design (or selected modules) using advanced debugging tools.

This is true, hierarchical, top-down design.

Accelerated graphics. The maximum bandwidth between the design database and the designer's mind.

Only hands-on experience can fully convey the creative acceleration delivered by the LOGICIAN's user interface.

Drawings are now electronic.

Corrections are electronic.

Any portion of the design can be accessed and displayed within seconds.

Pan and zoom are instantaneous.

Graphic displays are crisp and bright.

Printed documentation of any drawing page or design submodule is available at any time.

The system provides access control and security. Subject to user-defined access priorities, all team members have access to the same design database.

The coordination and control

of large projects is enhanced significantly.

The LOGICIAN has not changed the way designers work so much as it has accelerated the process, by computerizing repetitive detail. That is, after all, what computers do.

Fault-free design, based on the largest array of debugging tools in the industry.

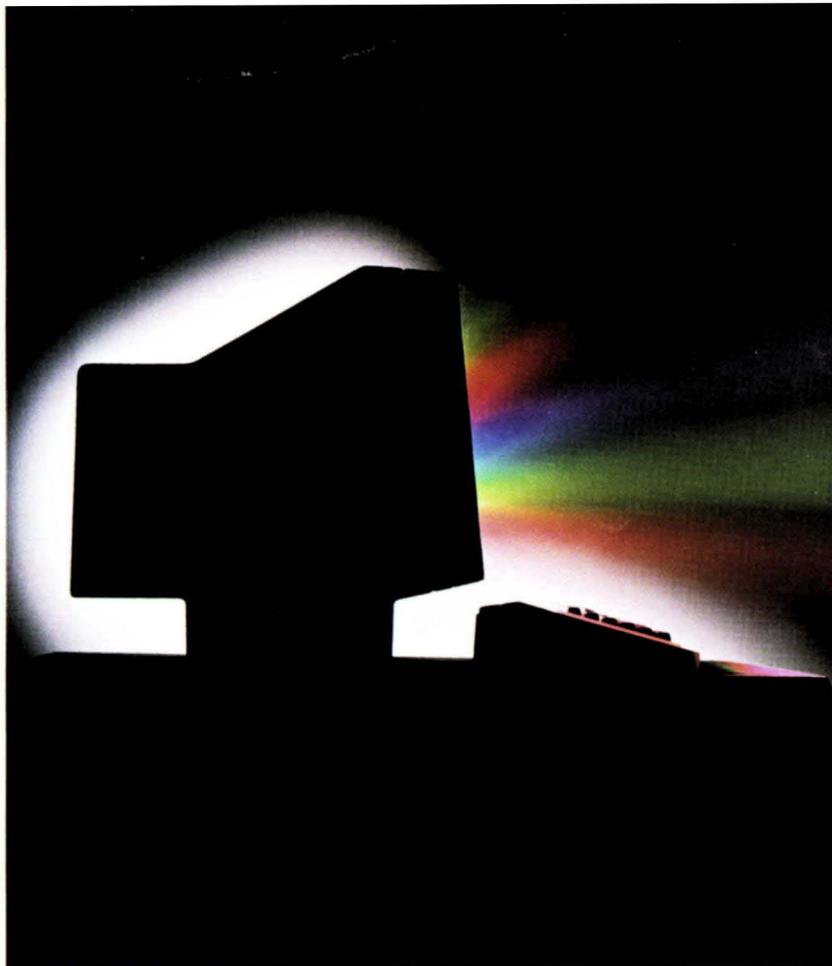
Resident on the LOGICIAN workstation and available at any time to the system designer: the Daisy Interactive Logic Simulator, Daisy SPICE Circuit Simulator, Daisy Virtual Logic Analyzer, Daisy Timing Verifier, and Daisy Modeling System.

Logic and timing verification are decoupled, allowing the designer to completely verify the timing of a prototype under all conditions. As a result, logic debugging is vastly simplified.

The debugging tools are comprehensive, and they are fast. Typically far faster, in fact, than traditional debugging tools running on mainframe systems.

As an example, the innovative 12-state logic simulator (an industry first) operates at a speed of greater than 1000 events per second.

In addition, the Daisy Modeling System allows the user to automatically format the design



Now, introducing the Daisy GATEMASTER™ Gate Array Development System.

**Suddenly, the Gatemaster puts
gate array technology within
the reach of virtually every
design engineer.**

database for electronic input to virtually any connectivity-driven design automation or debugging tool, running on any computer.

***Daisy Engineering
Workstations offer more
than the promise of state-
of-the-art engineering.
We can offer proof.***

The companies that ask the most of their design automation investments are the companies that place the highest value on the design function. Not surprisingly, they are the companies using Daisy LOGICIAN systems.

In fact, we can drop quite a few pretty impressive names.

Unfortunately, due to confidentiality agreements, we can't drop them in our advertising.

But if you are serious enough about improving engineering productivity to spend time with a Daisy field sales representative, we can provide you with a complete reference list.

Companies designing everything from 32-bit microprocessors to telecommunications equipment to computer systems to instrumentation. You name it.

***Now the Daisy GATEMASTER.
Top down design all the way
to silicon.***

State-of-the-art engineering occurs when the power of integrated, large scale circuitry-on-silicon is affordable for custom circuits.

Building on the Daisy LOGICIAN, the GATEMASTER creates an entirely new gate array engineering environment.

It's the tool that will pay off the promise of gate array technology...the promise of affordable custom circuitry.

In addition to the stand-alone power of the LOGICIAN, the GATEMASTER adds software for conversion of conventional logic to gate array logic, for interactive placement and routing, and for interactive engineering changes at any point in the design cycle.

The entire gate array design process is integrated, to provide a direct link between the electronic design and the physical implementation.

And, like the LOGICIAN, the GATEMASTER supports fault-free designs through 'virtual' breadboarding and 'correctness by construction'.

The result? A computerized engineering system that radically compresses gate array design cycles while providing the highest level of confidence in the success of the design.

For new designs, gate arrays can offer the shortest production route to working systems; for new gate arrays, the GATEMASTER offers the fastest design cycle.

The GATEMASTER can be programmed to follow the design and production rules of any number of gate array manufacturers, or customized to a company's chosen set of process rules and geometries.

The GATEMASTER connects directly to the Daisy Engineering Environment, or can operate as a stand-alone system.

It is available for your inspection now.

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The companies that bought LOGICIAN systems a year ago have a host of tremendous success stories to tell. Because in some cases, working products

--even silicon products--were brought to market in a matter of months.

The shortest route to fault-free designs.

That is the result of the Daisy LOGICIAN. That is the promise of the Daisy GATEMASTER.

We're dedicated to serving a marketplace where time is quite literally of the essence.

To find out more on how Daisy is setting the standard for engineering design, call or write today.

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International offices in the U.K., West Germany and Japan.

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Ethernet is a Trademark of Xerox Corporation.

The State of State-of-the-Art Engineering.

daisy
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