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THE LONG-AWAITED LINE "A" DOCUMENT

In order to provide "quick-and-dirty" access to the assembler-level graphics routines, ATARI engineers have set up the 68000's LINE "A" exception as an interface to several useful routines. The LINE "A" interface is faster than going through GEM's VDI and has some extra features. Also, LINE "A" calls require less application code than their VDI counterparts. Of course, LINE "A" doesn't replace the VDI completely, but if an application only needs a few primitive graphics functions (and wants maximum performance), then LINE "A" is sufficient (and optimal).

The LINE "A" interface is provided for the hacker-at-heart and no claims are made about its ease of use. The interface may seem unusually inconsistent, but it was not designed; it simply fell out as a freebie from the low-level VDI primitives interface. That is, these routines are the heart of the VDI.

The LINE "A" interface consists of 15 opcodes. The calls to LINE "A" are assembled as 1-word instructions, the highest 4 bits of which are 1010 (A in hexadecimal, hence LINE "A") and the lower 12 bits of which are used as the opcode field. Following is a description of the 15 opcodes:

- 0 = Initialization.
- 1 = Put pixel.
- 2 = Get pixel.
- 3 = Line.
- 4 = Horizontal line.
- 5 = Filled rectangle.
- 6 = Line-by-line filled polygon.
- 7 = BitBlt.
- 8 = TextBlt.
- 9 = Show mouse.
- 10 = Hide mouse.
- 11 = Transform mouse.
- 12 = Undraw sprite.
- 13 = Draw sprite.
- 14 = Copy raster form.

15 = Seedfill. (exists only in versions of TOS after the 1st release)

The LINE "A" routines have some features that the VDI doesn't support. BitBlt supports half-tone patterns on the source and TextBlt supports all 16 BitBlt logic operations, not just the 4 GEM VDI writing modes. In addition to these straight-forward extensions LINE "A" also allows the adventurous programmer to experiment with special effects. The BitBlt is especially generous in this area.

(0) Initialization

dc.w \$A000 ; Init the LINE "A".

input: none.

output: d0 = ptr to the base address of LINE "A" interface variables. a0 = ptr to the base address of LINE "A" interface variables. a1 = ptr to array of ptrs to the 3 system font headers. 845

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a2 = ptr to array of ptrs to the 15 LINE "A" routines.

- note: The value returned in a0 is the sine qua non of the LINE "A" interface. Inputs to all the other LINE "A" operations are made relative to this value, i.e., the LINE "A" interface variables are contained in a structure pointed to by a0. The offsets of these variables in the structure are given below.
- bugs: In the first TOS release, a2 is not returned as described above. Instead, it is preserved across the LINE "A" call. See Example Program #2 at the end of this document for the technique that makes a2 point to the proper place.
- (1) Put pixel

dc.w \$A001 ; Plot a pixel at x,y. ... input: INTIN[0] = pixel value. PTSIN[0] = x coordinate. PTSIN[1] = y coordinate.

output: none.

note: For a discussion of the CONTRL, INTIN, PTSIN, INTOUT, & PTSOUT arrays, see the GEM VDI manual.

(2) Get pixel

dc.w \$A002 ; Get the pixel at x,y. ... input: PTSIN[0] = x coordinate. PTSIN[1] = y coordinate.

output: d0 = pixel value.

(3) Line

dc.w	\$A003	; Draw a line between (x1,y1) and (x2,y2).
input:	Y1 = Y1 X2 = X2 Y2 = Y2 COLBIT0 COLBIT1 COLBIT2 COLBIT3 LNMASK WMODE	<pre>coordinate. coordinate. coordinate. coordinate. = bit value for plane 0. = bit value for plane 1. = bit value for plane 2. = bit value for plane 3. = line style mask. = writing mode. = always set this to -1, if using xor mode. else ignore it.</pre>

output: LNMASK is rotated to align with right-most endpoint.

quirks: 1) If the line is horizontal, LNMASK is a word-aligned pattern, not a line style. That is, a bit other than bit 15 of LNMASK may be used at the left-most endpoint.

2) As the foregoing references imply, the line is always drawn from left to right, not from (X1,Y1) to (X2,Y2). Thus, LNMASK is always applied from left to right.

note: Because of the quirks, an application cannot depend upon the phase of the LNMASK being properly updated between calls to line-drawing primitives. If the phase is critical, the application must compute and init LNMASK before each line is drawn.

> LNMASK is applied to the line-drawing DDA algorithm along the direction of greater delta. If delta Y is greater than delta X, then LNMASK is applied in the Y direction.

These line-drawing quirks and notes apply to the GEM VDI, too

(4) Horizontal line

. . . dc.w \$A004 ; Draw a line from (x1,y1) to (x2,y1). . . . • • • input: X1 = x1 coordinate. Y1 = y1 coordinate. X2 = x2 coordinate. COLBITO = bit value for plane 0. COLBIT1 = bit value for plane 1. COLBIT2 = bit value for plane 2. COLBIT3 = bit value for plane 3. WMODE = writing mode. PATPTR = ptr to the fill pattern. PATMSK = pattern index. MFILL = multi-plane pattern flag.

output: none.

(5) Filled rectangle

. \$A005 ; Draw a filled rectangle with upper left corner at dc.w ; (x1,y1) and lower right corner at (x2,y2). input: X1 = x1 coordinate. Y1 = y1 coordinate. X2 = x2 coordinate. Y2 = y2 coordinate. COLBITO = bit value for plane 0. COLBIT1 = bit value for plane 1. COLBIT2 = bit value for plane 2. COLBIT3 = bit value for plane 3. WMODE = writing mode.

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PATPTR = ptr to the fill pattern. PATMSK = fill pattern index. MFILL = multi-plane fill pattern flag. CLIP = clipping flag. XMINCL = x minimum for clipping. XMAXCL = x maximum for clipping. YMINCL = y minimum for clipping. YMAXCL = y maximum for clipping.

output: none.

(6)

Line-by-line filled polygon.

\$A006 ; Draw 1 scan-line of a filled polygon. dc.w input: PTSIN[] = array of polygon vertices. ((x1,y1), (x2,y2)..., (xn,yn), (x1,y1))CONTRL[1] = n = number of vertices.= y coordinate of scan-line to fill. Y1 COLBITO = bit value for plane 0. = bit value for plane 1. COLBIT1 COLBIT2 = bit value for plane 2. COLBIT3 = bit value for plane 3. = writing mode. WMODE = ptr to the fill pattern. PATPTR = fill pattern mask. PATMSK MFILL = multi-plane fill pattern flag. CLIP = clipping flag. XMINCL = x minimum for clipping. XMAXCL = x maximum for clipping. YMINCL = y minimum for clipping. = y maximum for clipping. YMAXCL

output: X1 and X2 are clobbered.

note: The 1st endpoint must be repeated at the end of the list of n endpoints.

(7) BitBlt

...
dc.w \$A007 ; Perform a BIT BLock Transfer.
...
input: a6 = ptr to a structure of input parameters.

output: none.

BIT BLT PARAMETER BLOCK OFFSETS

B WD	equ	+00	; width of block in pixels
B_HT	equ	+02	; height of block in pixels

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PLANE_CT	equ	+04	; number of consecutive planes to blt {D}
FG_COL BG_COL OP_TAB S_XMIN S_YMIN S_FORM	equ equ equ equ equ	+06 +08 +10 +14 +16 +18	<pre>; foreground color (logic op index:hi bit){D} ; background color (logic op index:lo bit){D} ; logic ops for all fore and background combos ; minimum X: source ; minimum Y: source ; source form base address</pre>
S_NXWD S_NXLN S_NXPL	equ equ equ	+22 +24 +26	; offset to next word in line (in bytes) ; offset to next line in plane (in bytes) ; offset to next plane from start of current
D_XMIN D_YMIN D_FORM D_NXWD D_NXLN D_NXPL	equ equ equ equ equ	+28 +30 +32 +36 +38 +40	<pre>; minimum X: destination ; minimum Y: destination ; destination form base address ; offset to next word in line (in bytes) ; offset to next line in plane (in bytes) ; offset to next plane from start of current</pre>
P_ADDR P_NXLN P_NXPL P_MASK	equ equ equ	+42 +46 +48 +50	<pre>; address of pattern buffer (0:no pattern) {D ; offset to next line in pattern (in bytes) ; offset to next plane in pattern (in bytes) ; pattern index mask</pre>
P_BLOCK_LEN	equ	76	; the parameter block must be 76 bytes long

*** notes ***

parameters marked with {D} may be altered during the course of the BIT BLT execution

contents of OP TAB

+00	byte	operation employed when foreground and background col	lor
		bits for current plane are both clear (0)	

- +01 byte operation employed when current plane's foreground color bit is clear (0) and background color bit is set (1)
- +02 byte operation employed when current plane's foreground Aco bit is set (1) and background color bit is clear (0)
- +03 byte operation employed when foreground and background color bits for current plane are both set (1)

0. PREFACE

Before one floggles one's tormented mind with this tangled nest of arcane knowledge, one ought to be intimately familiar with chapter (of the GEM VDI manual. Author assumes that one's knowledge of Raster matters is quite wide and that the rudiments of BIT BLTting are belo discussion. If the author is mistaken then he's sorry (and you're about to become lost in the sea of woe, oh ho!).

I. PARAMETER BLOCK

BIT BLT is accessed via a 76 byte parameter block. Register A6 points to the head of this block upon LINE A entry. Only the first 52 bytes of the block need be attended to by the abuser. The remaining space is maintained internally by the BLT. Note in the following explanations, parameters will be referred to by symbolic offsets into the parameter block.

II. MEMORY FORMS

memory forms are something like a cabbage patch. (a cabbage patch is a place for mentally retarded programmers). Face it, forms are nothing like a cabbage patch. if you think they are, go back and read chapter 6 in the GEM VDI manual. if you know anything at all about memory forms, you know they are almost entirely but not totally unlike a garbage can. memory forms are of two sexes, source and destination. each sex is defined by the same four parameters: form block address, block width, offset to next contiguous word, and offset to next plane.

S_FORM and D_FORM point to the first words of the source memory form and destination memory forms, respectively. addresses must fall on word boundries or severe hardships fall (as will address exceptions) like plagues upon the ancient egyptians.

S_NXWD and D_NXWD are offsets to the next word in a plane of the memory form. for example, in the monochrome mode the value is 2 while a value 4 is used in medium resolution and 8 is applicable to low resolution.

S_NXLN and D_NXLN are form widths for source and destination. (i can't remember which one belongs to source form and which one belongs to the destination form). widths must be even byte values, as you know, for they represent the offset from one row to the next and forms must be word aligned and an integral number of words wide. (hint: the hi rez screen value is 90 while lo and medium rez values are 160)

S NXPL and D NXPL are offsets from the start of one plane to start of the next plane. because of the ST screen's interleaved plane structure, this value is always two (2). alternative universes allow for a series of contiguous planes where NXPL values are number of bytes each plane. thus , it is possible to BLT from the contiguous universe into the interleaved ST universe and vice versa.

the actual bit alligned blocks of memory are defined within the form by an upper left anchor point, a pixel width, and a pixel height: (S_XMIN, S_YMIN, B_WD, and B_HT). the location in the destination form is defined by an anchor point (D_XMIN, D_YMIN). no harm will come if these two areas overlap. Note no clipping is performed andthere is no checking to determine whether bit blocks fall within the confines of the encompasing memory forms. finally, the number of planes to be transfered (the number of itterations of the BLT algorithm) is contained in the PLANE CT word.

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III. RASTER OPERATIONS

OP_TAB is a table of four RASTER OP codes. Each of byte wide entries in OP_TAB contain a code for one of sixteen logical operations between consenting source and destination blocks. For each plane, the logical operation is chosen by indexing the OP_TAB with a value derived from FG_COL and BG_COL words. given plane "n", bit "n" of FG_COL is the hi bit of the two bit index value and bit "n" of BG_COL is the lo bit of the index value.

for those with a furniture fetish, here is a table:

FG(n)	BG(n)	OP_TAB entry
0	0	finct ontro
0	U	first entry
0	1	second entry
1	0	third entry
1	1	fourth entry

IV. PATTERNS

Patterns are word wide, word aligned images that are logically anded with source prior to logical combination of source with destination.

Patterns are packed in an imaginary grid anchored left corner (0,0) of the destination memory form.

Patterns are 16 bits wide and repeated every 16 pixels horizontally.

patterns are an integral power of 2 in height and repeat vertically at that frequency.

The source is shifted into alignment with destination rectangle prior to the combination of source with pattern. Thus, the relationship between source and pattern is dependent upon the X,Y positioning of the destination rectangle.

P_ADDR points to the first word of the pattern. If this pointer is 0, a pattern is not combined with the source rectangle.

P_NXLN offset (in bytes) between consecutive words in the pattern. For reasons too inane, this number should be an integral power of 2 (such as 2,4, or 8)

P_NXPL is the offset (in bytes) from the beginning of a plane to the beginning of the next plane. In the case of a single plane pattern used in a multi plane environment, this value would be zero. thus, the same pattern is repeated through all planes.

P_MASK works with P_NXLN to specify the length of the pattern. The length (in words) of the pattern must be an integral power of 2.

if P NXLN = 2 ** n

then $P_{MASK} = (length in words -1) << n$

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... i don't know why. go ask your father.

V. BAG 'O TRICKS

Q. I want to BLT from a single plane source to multi plane destination.

- A. That's not in the form of a question. And besides, i can't think with water pick spurtin in my ear. Hey, that's my cat your puttin in the Cuisinart. Wha you think your doin bustin into my word processor like this. Hey bud, stay away from that delete key. Hey moe foe, i'm serious. How'd you like an unexpected interrupt ?
- Q. This key is loaded and it's pointed at your bonus check.
- A. ok, ok... i'll talk.

S_NXPL =0 => same source plane is BLTted to all destination planes

- Q. yea, i know that but what logic ops do i use ?
- A. to map 1's to foreground color and 0's to background color set OP_TAB to:

offset logic op

+00	00	all zeros
+01	04	$D' \leftarrow [not S]$ and D
+02	07	D' <- S or D
+03	15	all ones

load foreground color into FG COL and background color into BG COL

- Q. you wanna buy some lake bottom property?
- A. to map 1's to foreground color and make 0's transparent set OP TAB to:

offset logic op

+00	04	D' < - [not S] and D
+01	04	D' < - [not S] and D
+02	07	D' <- S or D
+03	07	D' <- S or D

load foreground color into FG_COL it doesn't matter what you put into BG COL

don't forget to set S NXPL to 0

enough smalltalk, let's get down to the core of the issue. Here are some of my Aunt Marge's flavorful BIT BLT recipes: 1. BLT a pattern without Source to the Destination.

For this number, we'll need a word of ones. Label it "ones:" next, point S_FORM at "ones". Set S_NXLN, S_NXPL, S_NXWD, S_XMIN, and S_YMIN to 0. Set up the pattern as you usually would and before you know it, you'll have a wonderful steaming pattern filled rectangle.

2. this is a nice way to make a sprite like device.

o you will need to bake a monoplane mask. everywhere there is a 1 in the mask, the background will be removed. wherever a 0 falls the background is left intact.

set OP TAB to:

offset logic op

+00	04	D'	<-	[not	S]	and D
+01	04	D '	<-	[not	S]	and D
+02	07	D '	<-	S or	D	
+03	07	D '	<-	S or	D	

load foreground color into FG COL it doesn't matter what you put into BG COL

next, take monoplane form (or multiplane form) and "or" it (OP 07 0 into the area that you just scooped out with the mask

feeds a family of four.

(8)

TextBlt

. . .

dc.w \$A008 ; Perform a TEXT BLock Transfer of 1 character. . . .

• • •

input:

• •		
	WMODE	<pre>= writing mode.(0-3 => VDI modes</pre>
	TEXTFG	= text foreground color.
	TEXTBG	= text background color. (used for modes 4-19)
	FBASE	= ptr to start of font data. (font form)
	FWIDTH	= width of font form.
		= x coord of character in font form.
	SOURCEY	= y coord of character in font form.
	DESTX	= x coord of character on screen.
	DESTY	= y coord of character on screen.
	DELX	= width of character.
	DELY	= height of character.
	STYLE	= vector of TextBlt special effects flags.
	LITEMASK	= the mask to use in lightening text.
	SKEWMASK	= the mask to use in skewing text.
	WEIGHT	= the width by which to thicken text.
	ROFF	= offset above character baseline when skewing.
	LOFF	= offset below character baseline when skewing.
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		0.0

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		XDDA = DDAINC = SCALDIR = CHUP = MONO = SCRTCHP =	<pre>scaling flag. (0 => no scaling.) accumulator for x dda. fractional amount to scale up or down scale direction flag. (0 => down) character rotation vector. monospaced font flag. ptr to start of text special effects offset of scaling buffer in above but</pre>	buffe	er.
	output:	none.			
(9)	Show mc	use			
	dc.w	\$A009 ;	Show the mouse.		
	input:	see GEM V	DI manual.	,	
	output:	none.			
(10)	Hide mo	ıse			
	dc.w	\$A00A ;]	Hide the mouse.		
	• • •	• • •			
	input:	see GEM VI	DI manual.		
	output:	none.			
(11)	Transfo	rm mouse			
	dc.w	\$A00B ; ;	Fransform the mouse's form.		
	input:	see GEM VI	DI manual.		
	output:	none.			
(12)	Undraw	sprite			
	•••	•••	Induces the providence lands and the		
	dc.w	\$A00C ; 1	Undraw the previously drawn sprite.		
		•	a aprito gave black		
	input:	$a_2 = ptr to$	o sprite save block.		
		underneath	sprite save block is used to save the the sprite. Its size is 10 bytes + 6 i.e. (10 + VPLANES * 64) bytes.		

output: clobbers a6. ("C" programmers beware.)

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(13)Draw sprite dc.w \$A00D ; Draw a sprite. input: d0 = x hot-spot.d1 = y hot spot. a0 = ptr to sprite definition block.a2 = ptr to sprite save block. SPRITE DEFINTION BLOCK LAYOUT ds.w 1 x offset of hot-spot. ds.w 1 y offset of hot-spot. format flag. (1 => VDI Format, ds.w 1 -1 => XOR Format) VDI Format bg bit action fa bit 0 0 transparent to screen 0 background color plotted 1 foreground color plotted 1 0 1 1 foreground color plotted XOR Format fg bit bg bit action 0 0 transparent to screen 0 1 background color plotted 1 0 xor screen 1 1 foreground color plotted ds.w 1 background color (color table index) foreground color (color table index) ds.w 1 ds.w 32 interleaved background/foreground image. (word 0 = background line 0. word 1 = foreground line 0. word 2 = background line 1. word 3 = foreground line 1. etc.) output: clobbers a6. ("C" programmers beware.) This function is not usable as a LINE "A" call in the 1st bugs: release of TOS. See Example Program #2 below for the technique one must adopt to use this function. (14)Copy raster form . . .

dc.w \$A00E ; Copy a raster form from source to destination.

input: See the VDI discussion of Copy Raster, Opaque & Transparent, EXCEPT, CONTRL(0), CONTRL(1), CONTRL(3), and CONTRL(6) are LINEA.DOC;2

ignored.

COPYTRAN = Opaque/Transparent mode flag. (0 => Opaque)

output: none.

note: See the BitBlt discussion above.

USING THE LINE "A" INTERFACE

The inputs to the LINE "A" routines are contained in a structure pointed to by the value returned in a0 after an initialization call (\$A000) has been made. This initialization only needs to be done once and any returned values can be saved and used as needed.

The LINE "A" interface can be used in cooperation with the VDI and AES, however, one cannot expect the variables below to be unchanged after the VDI or AES has been used. Therefore, if an application wants to mix calls to LINE "A" and VDI/AES, it must reload any variables that it uses as input to the LINE "A" routines.

The caller should assume that registers d0-d2 and a0-a2 are clobbered upon return. The rest are preserved.

The LINE"A" input variables structure:

offset	name	type	description
0	VPLANES	word	number of video planes.
2	VWRAP	word	number of bytes/video line.

note: These variables can be changed to implement special effects, e.g.,doubling VWRAP will cause the routines to skip 1 scanline between every scanline that is output to the screen. Of course, any modifications made to these variables must be undone when normal operation of the LINE "A" (or VDI) is desired.

4	CONTRL	long	ptr to the CONTRL array.
8	INTIN	long	ptr to the INTIN array.
12	PTSIN	long	ptr to the PTSIN array.
16	INTOUT	long	ptr to the INTOUT array.
20	PTSOUT	long	ptr to the PTSOUT array.

note: See the GEM VDI manual for a discussion of the above arrays.

24	COLBIT0	word	current color bit-plane 0 value.
26	COLBIT1	word	current color bit-plane 1 value.
28	COLBIT2	word	current color bit-plane 2 value.
30	COLBIT3	word	current color bit-plane 3 value.

note: current foreground writing color = 1*COLBIT0 + 2*COLBIT1 +

4*COLBIT2 + 8*COLBIT3.

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Ċ	32 34 36	LSTLIN LNMASK WMODE		word word word	equi		to VD] e. ((1	['s lin) => re L => tn 2 => xe	he style. eplace mo ransparen or mode,)
		note: s	ee VDI ma	anual fo	r dis	cussion	of wi	iting	modes.		
	38 40 42 44 46 50 52	X1 Y1 X2 Y2 PATPTR PATMSK MFILL		word word word long word word	y1 c x2 c y2 c ptr fill mult (0 =	patterr i-plane > currer	te. te. currer n "mas fill nt fil	sk". flag. 1 patt		• ingle pla: ulti-plan	
	54 56 58 60 62	CLIP XMINCL YMINCL XMAXCL YMAXCL		word word word word word	mini mini maxi	ping fla mum x c] mum y c] mum x c] mum y c]	lippir lippir lippir	ng valu ng valu ng valu	1e. 1e.)	
	64	XDDA		word	accu	mulator	for t	extblt	x dda.		
		note:	Should h of TextE		d to	8000н (.	.5) be	fore e	each invo	cation	
	66	DDAINC		word	frac	tional a	mount	to so	cale up or	r down.	
		note:	256*(Int If scali	ng up, s ended si ng down, ended siz	ize-A , set	ctual si DDAINC	.ze)/A to	ctual	size.		
	68 70	SCALDIR MONO		word word	scale 0 =>	e direct current its OK width o current	ion f font for t of the font	is no hicken curre is mo	nt font. nospaced		cenin
	72 74	SOURCEX SOURCEY		word word					font for font for		
		note:			ee App charac fnt_p	pendix G cter val ptr->fir	of V ue; st_ad	DI man e;	ual for h	ld in the neader def	Ξ)
(SOURCEY	is typic	ally	set to	0. (t	op lin	e of font	form)	
	76 78 80 82	DESTX DESTY DELX DELY		word word word word	y coo width		harac racte	ter on r.	screen. screen.	877	

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•	note:	DELX & e.g.	temp = temp -= SOURCEX DELX =	be compute character v fnt_ptr->f = fnt_ptr- fnt_ptr->of fnt_ptr->fo	alue; irst_ade >off_tab ftable(t	; le(temp) emp+1)-S	,		
84 88	FBASE FWIDTH		long word	ptr to sta width of f			(font	form)	
	note:	FBASE & e.g.	FBASE =	can be comp fnt_ptr->d = fnt_ptr->	at table	;	nt head	der.	
90	STYLE		word	Bi Bi Bi	t 0 = Th t 1 = Lic t 2 = Skc t 3 = Unc	icken fla ghten fla ewing fla	ag. ag. ag. flag.	s flags. (ignored)	
	note:			select the t be done b					
92 94 96 98 100	LITEMAS) SKEWMAS) WEIGHT ROFF LOFF		word word word word word	the mask to the mask to the width is offset about offset belo	o use in by which ve charad	skewing to thick cter base	text. ken tex eline v	xt. when skewi	
	note:	The abov header. e.g.	LITEMAS SKEWMAS WEIGHT if (skew ROFF	<pre>= fnt_ptr->: = fnt_ptr->: = 0;</pre>	->lighten ->skew; thicken; right_of:	n; fset;	l from	the font	
102 104	SCALE CHUP		word word	<pre>scaling fla character = 0 => norma 900 => rota 1800 => rot 2700 => rot</pre>	rotation 1 horizon ated 90 d tated 180	vector. htal origing legrees of degrees	entatio clockwi s clocł	ise. kwise.	
106	TEXTFG	word	text fo	reground co	lor.				
108 112	SCRTCHP SCRPT2	•	long word	ptr to star offset of a					r.
	note:	These sp before (pecial e TextBlt	ffects buffe effects can	er point be used	ers must •	be in	itialized	
114 116	TEXTBG COPYTRA	word N	text bac word	ckground co copy raste					DI.

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- 0 => Opaque type n-plane source -> n-plane dest
- BitBlt writing modes ~0 => Transparent type
- 1-plane source -> n-plane dest VDI writing modes
- 118 SEEDABORT long ptr to routine which is called within the seedfill logic to allow the fill to be aborted. Initialized to point to a dummy routine which returns FALSE. Returning TRUE aborts the seedfill.
 - note: This ptr doesn't exist in 1st release of TOS. See Example Program #2 for the technique to use to identify the 1st TOS release.

EXAMPLE LINE "A" EQUATES

equ	0
-	2
	4
equ	8
equ	12
equ	16
equ	20
equ	24
equ	26
equ	28
equ	30
equ	32
equ	34
equ	36
equ	38
equ	40
-	42
equ	44
equ	46
equ	50
_	52
equ	54
equ	56
-	58
	60
equ	62
equ	64
equ	66
	68
equ	70
equ	72
equ	74
equ	76
-	78
equ	80
equ	82
	equ equ equ equ equ equ equ equ equ equ

FBASE FWIDTH STYLE LITEMSK SKEWMSK WEIGHT ROFF LOFF SCALE CHUP TEXTFG SCRTCHP SCRPT2 TEXTBG COPYTRAN SEEDABORT	equ equ equ equ equ equ equ equ equ equ	84 88 90 92 94 96 98 100 102 104 106 108 112 114 116 118
*	-	
*		
*		
INIT	equ	\$A000
PUTPIX	equ	INIT+1
GETPIX	equ	INIT+2
ABLINE	equ	INIT+3
HABLINE	equ	INIT+4
RECTFILL	equ	INIT+5
POLYFILL	equ	INIT+6
BITBLT	equ	INIT+7
TEXTBLT	equ	INIT+8
SHOWCUR	equ	INIT+9
HIDECUR	equ	INIT+10
CHGCUR	equ	INIT+11
DRSPRITE	equ	INIT+12
UNSPRITE	equ	INIT+13
COPYRSTR SEEDFILL	equ	INIT+14
SPEDLIPP	equ	INIT+15

EXAMPLE PROGRAM #1

text

start:

dc.w move.w move.w move.w move.w move.w	#\$5555,LNMASK(a0) #0,WMODE(a0) #1,COLBIT0(a0) #1,COLBIT1(a0)	; initialize. ; once and for all. ; dithered line. ; replace mode.
move.w move.w move.w move.w dc.w	#0,COLBIT3(a0) #0,X1(a0) #0,Y1(a0) #99,X2(a0) #99,Y2(a0)	<pre>; drawing color = 7. ; X1 = 0. ; Y1 = 0. ; X2 = 99. ; Y2 = 99. ; draw line.</pre>
move.w trap end	#0 ,- (sp) #1	; exit.

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EXAMPLE PROGRAM #2 text start: clr.1 -(sp) move.w #\$20,-(sp) ; supervisor mode required to use trap #1 line "A" routines via jsr. : addq #6,sp move.l d0,stksave ; save old stack ptr. Find out which version of LINE "A" handler exists. move.1 #0,a2 ; convenient value for testing. dc.w INIT ; line "A" initialization. move.l a2,d2 ; old version? a2ok bne ; no, a2 points to array of line "A" * routine addresses. ; -4*15(a1),a2 ; yes, a2 is untouched, so use a1 plu lea displacement (15 addresses). ; * a2 now points to array of line "A" routine addresses. move.l 4*\$D(a2),drawaddr ; fetch draw routine address. a2ok: * * Bug-workaround/Initialization complete. move.w #0,d0 ; init x. move.w #0,d1 ; init y. lea sprite,a0 ; point to sprite. lea save,a2 ; point to save area. movem.w d0-d1, -(sp)loop: ; save x,y. movem.1 a0/a2,-(sp); save ptrs. move.l a6,-(sp) ; draw clobbers a6. old linea ; old or new line "A" handler? tst.w beq new ; new, branch. drawaddr,a3 ; fetch draw routine address. move.l jsr (a3) ; draw the old way. bra merge * new: dc.w DRSPRITE ; draw the new way. move.1 (sp)+,a6 merge: movem.1 (sp)+,a0/a2; restore ptrs. move.w #2000,d2 dbra d2,wait ; wait a bit. wait: movem.1 a0/a2,-(sp) ; save ptrs. ; undraw clobbers a6. move.l a6, -(sp)dc.w UNSPRITE move.1 (sp)+,a6 movem.l (sp)+,a0/a2; restore ptrs. 881 movem.w (sp)+,d0-d1; restore x,y. ; inc x. addq.w #1,d0

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	cmp.w ble	#640,d0 loop			
*	move.l move.w	stksave #\$20,-(;			
*	trap addq	#1 #6,sp		; user mode.	
	move.w trap	#0 ,- (sp #1)	; exit.	
*	data				
* sprite:	dc.w dc.w		0,0 1,0,1	; x,y offsets of hots; ; format, background,	
bob:	dc.w dc.w dc.w dc.w dc.w dc.w		SFFFF S07F0 SFFFF S0ff8 SFFFF S1fec	; background line 0. ; foreground line 0.	-
	dc.w dc.w dc.w dc.w dc.w		\$FFFF \$1804 \$FFFF \$1804 \$FFFF		
	dc.w dc.w dc.w dc.w dc.w		\$1004 \$FFFF \$1e3c \$FFFF \$1754		
	dc.w dc.w dc.w dc.w dc.w dc.w		\$FFFF \$1104 \$FFFF \$0b28 \$FFFF \$0dd8		
	dc.w dc.w dc.w dc.w dc.w		\$FFFF \$0628 \$FFFF \$07d0 \$FFFF		
	dc.w dc.w dc.w dc.w dc.w		\$2e10 \$FFFF \$39e0 \$FFFF \$3800		
	bss				
* *					
stksave: save: old_linea:	ds.l ds.b ds.w		1 10+64 1		
drawaddr:	ds.l end		1		882