

PAGE -- 0

Current memory available: 24976

0000!

.ABSOLUTE

2 blocks for procedure code 23041 words left

0000:  
Current memory available: 24451  
0000:  
0000:  
0000:

.PROC PROFILEDRIVER, 0  
.MACROLIST  
.NOPATCHLIST  
.TITLE " APPLE II PROFILE DRIVER PROM code Feb 16, 1984 "

```
0000: . PAGE
0000: . INCLUDE PROMX. A. TEXT
0000: . ORG 0C800
CB00:
CB00: 28 43 29 20 41 50 50 COPYRIGHT . ASCII "(C) APPLE COMPUTER INC. 1982"
CB07: 4C 45 20 43 4F 4D 50
CB0E: 55 54 45 52 20 49 4E
CB15: 43 2E 20 31 39 38 32
CB1C: 00D1 DEVTYPE . EQU OD1
CB1C: 0001 SUBTYPE . EQU O1
CB1C: 0001 MANUF . EQU 0001 ; Apple Computer Inc.
CB1C: 1001 RELEASE . EQU 1001
CB1C: 0000 MAXBLOCK . EQU 0000 ; variable
```

```

CB1C|                                     .PAGE
CB1C|                                     ;-----
CB1C|                                     ;
CB1C|                                     ; The macro SWITCH performs an N way branch based on a switch index.
CB1C|                                     ;
CB1C|                                     ; SWITCH [index], [bounds], adrs_table, [#]
CB1C|                                     ;-----
CB1C|
CB1C|                                     .MACRO SWITCH
CB1C|                                     .IF "%1" <> "" ; If PARM1 is present,
CB1C|                                     LDA %1 ; Load A with switch index
CB1C|                                     .ENDC
CB1C|                                     .IF "%2" <> "" ; If PARM2 is present,
CB1C|                                     CMP #%2+1 ; Perform bounds checking
CB1C|                                     BCS $010 ; on switch index
CB1C|                                     .ENDC
CB1C|                                     ASL A
CB1C|                                     TAY
CB1C|                                     LDA %3+1,Y ; Get switch address from table
CB1C|                                     PHA ; and push onto stack
CB1C|                                     LDA %3,Y
CB1C|                                     PHA
CB1C|                                     .IF "%4" <> "*" ; If PARM4 is omitted,
CB1C|                                     RTS ; Exit to code
CB1C|                                     .ENDC ; Otherwise, drop through
CB1C|                                     .ENDM
CB1C|                                     $010

```

```

CB1C|                                     . PAGE
CB1C|
CB1C|
CB1C|                                     Macro INC16.  Increments a 16 bit unsigned number.
CB1C|
CB1C|
CB1C|
CB1C|                                     . MACRO  INC16
CB1C|                                     INC      %1
CB1C|                                     BNE     $10
CB1C|                                     INC     %1+1
CB1C|                                     . ENDM
CB1C|                                     $10
CB1C|
CB1C|
CB1C|                                     Macro CMP16.  Compares two 16 bit unsigned numbers.
CB1C|                                     If %1 < %2 then carry clear, else carry set.
CB1C|
CB1C|
CB1C|                                     . MACRO  CMP16
CB1C|                                     LDA     %1
CB1C|                                     CMP     %2
CB1C|                                     LDA     %1+1
CB1C|                                     SBC     %2+1
CB1C|                                     . ENDM
CB1C|
CB1C|
CB1C|                                     Macro JSRI - JSR indirect via the address indicated by the
CB1C|                                     argument.
CB1C|
CB1C|
CB1C|                                     . MACRO  JSRI
CB1C|                                     LDA     $1+1
CB1C|                                     PHA
CB1C|                                     LDA     $1
CB1C|                                     PHA
CB1C|                                     JMP     @%1
CB1C|                                     . WORD  **1
CB1C|                                     . ENDM
CB1C|

```



```
CB1C1
CB1C1      ; Command codes:
CB1C1
CB1C1 0000      WDGTRD      .EQU 0
CB1C1 0002      WDGTRTVER   .EQU 2
CB1C1 0001      WDGTRT      .EQU 1
CB1C1
```

```
CS1C: .PAGE
CS1C: ;-----
CS1C: ;
CS1C: ; Device Information Block (DIB)
CS1C: ;
CS1C: ;-----
CS1C:
CS1C: 0000 DIBLKLO .EQU 0
CS1C: 0000 DIBLKHI .EQU 0
```



```

CB1C|                                     . PAGE
CB1C|
CB1C| ;-----
CB1C| ;
CB1C| ; zero page temps
CB1C| ;
CB1C| ;-----
CB1C| 0000 MAPTEMP1 .EQU 00 ;used in Pascal routines
CB1C| 0002 MAPTEMP2 .EQU 02 ;
CB1C| 0004 MAPTEMP3 .EQU 04 ;
CB1C| 0006 MAPTEMP4 .EQU 06 ;
CB1C| 0008 MAPTEMP5 .EQU 08 ;
CB1C| 000A MAPRET .EQU 0A ;return addr for MAP routine
CB1C| 000C SLOTNUM .EQU 0C ;ON for slot N
CB1C| 000D BUSYALL .EQU 0D ;some Profile unit is ready
CB1C| 000E MUNIT .EQU 0E ;Pascal unit number
CB1C|
CB1C| 0030 MAPENTRY .EQU 30 ;local copy of one entry of the mapping table,
CB1C| ;six bytes, 30 - 35
CB1C|
CB1C| ;Profile routines use these zero page locations
CB1C| 003A SWTCH .EQU 3A
CB1C| 003B COUNTR .EQU 3B
CB1C| 003C BCLO .EQU 3C
CB1C| 003D ERROR .EQU 3D
CB1C| 003E INDRCN .EQU 3E
CB1C|
CB1C| ;-----
CB1C| ;
CB1C| ; PRODOS PARAMETERS on zero page
CB1C| ;
CB1C| ;-----
CB1C| 0042 CMD .EQU 42
CB1C| 0042 ZBCMD .EQU 42
CB1C| 0043 UNIT .EQU 43
CB1C| 0043 SLOTX .EQU 43
CB1C| 0044 BUFADLO .EQU 44
CB1C| 0045 BUFADHI .EQU 45
CB1C| 0046 BLOCKLO .EQU 46
CB1C| 0047 BLOCKHI .EQU 47
CB1C|
CB1C| ;table of ATTACH driver pointers to driver information.
CB1C| 0048 PTRTABL .EQU 048
CB1C| 0048 MAPTABLADR .EQU PTRTABL+0 ;addr of MAPTABL mapping table
CB1C| 004A SLOTTAB .EQU PTRTABL+2 ;addr of peripheral card slot status table
CB1C| 004C CONCK .EQU PTRTABL+4 ;addr of ATTACH routine to call CONCK
CB1C| 004E INITINFOADR .EQU PTRTABL+6
CB1C|
CB1C| ;offsets of INITINFO table in ATTACH driver, pointed to by INITINFOADR.
CB1C| 0000 MAINENTRY .EQU 0 ;addr of ATTACH driver main entry
CB1C| 0002 INITIALIZE .EQU 2 ;addr of master driver init+MAPINIT routine
CB1C| 0004 ORIG4ADR .EQU 4 ;nontemporary data space for ROM code
CB1C| 0006 ROMRETURN .EQU 6 ;

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CB1C: . PAGE
CB1C: . INCLUDE PROMX. B. TEXT
CB1C:
CB1C: CB1C          PRODOS          . EQU      *
CB1C: 85 3F          STA          INDRCN+1      ;acc. has Cn
CB1E: A9 00          LDA          #0
CB20: 8D F805       STA          PASCAL          ;tell driver PRODOS called it
CB23: 85 3E          STA          INDRCN
CB25: 8D 82C0       LDA          BUSY, X
CB28: 4A            LSR          A          ;is a Profile attached?
CB29: 90**          BCC          #10          ;branch if so
CB2B: A9 28          LDA          #XNODRIVE
CB2D: 60            RTS          ;error return
CB2E: A4 42          LDY          CMD          #10
CB30: D0**          BNE          NOTSTAT      ;branch if not status command
CB32: 0A            ASL          A
CB33: 10**          BPL          BSYHI        ;branch if Profile not ready
CB35: A9 01          LDA          #01
CB37: 85 42          sta         cmd
CB39: A5 47          lda         blockhi
CB3B: 48            pha
CB3C: A5 46          lda         blocklo
CB3E: 48            pha
CB3F: A5 45          lda         bufadhi
CB41: 48            pha
CB42: A5 44          lda         bufadlo
CB44: 48            pha
CB45: A9 FF          lda         #OFF
CB47: 85 44          sta         bufadlo
CB49: 85 45          sta         bufadhi
CB4B: 85 46          sta         blocklo
CB4D: 85 47          sta         blockhi
CB4F: 20 ****        jsr         notstat
CB52: 85 3D          sta         error
CB54: A9 00          lda         #00
CB56: 85 42          sta         cmd
CB58: 68            pla
CB59: 85 44          sta         bufadlo
CB5B: 68            pla
CB5C: 85 45          sta         bufadhi
CB5E: A6 43          idx         slotx
CB60: 8D 82C0       lda         busy, x
CB63: 85 3B          sta         countr
CB65: A6 46          idx         blocklo
CB67: A4 47          ldy         blockhi
CB69: 68            pla
CB6A: 85 46          sta         blocklo
CB6C: 68            pla
CB6D: 85 47          sta         blockhi
CB6F: A5 3D          lda         error
CB71: D0**          bne         errrtn
CB73: A5 3B          lda         countr
CB75: 18            clc
CB76: 60            rts

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CB77: A9 27          BSYHI          LDA          #XIOERROR
CB79: 38             errrtn          SEC
CB7A: 60             ;                      RTS
CB7B:               ;
CB7B: C0 03          NOTSTAT          cpy          #3          ; is the command one we handle?
CB7D: B0F8          ;                      bcs          bsyhi          ; branch if not
CB7F: A2 00          ;                      LDY          #0
CB81: A0 02          ;                      LDY          #2
CB83: 86 3C          ;                      STX          BCLO
CB85: 8C 7806        ;                      STY          BCHI          ; setup to read 512 bytes
CB88: A5 42          ;                      LDA          CMD
CB8A: 48             ;                      PHA          ; save CMD for PRODOS
CB8B: 46 42          ;                      LSR          CMD
CB8D: 20 ****        ;                      JSR          RWSU1          ; do read or write command
CB90: A6 43          ;                      LDX          SLOTX
CB92: BD 83C0        ;                      LDA          CLR_PARITY, X ; turn off 9334 on card
CB95: 68             ;                      PLA
CB96: 85 42          ;                      STA          CMD          ; restore CMD for PRODOS
CB98: 18             ;                      CLC
CB99: A5 3D          ;                      LDA          ERROR
CB9B: D0DA          ;                      BNE          BSYHI          ; branch if there was an error
CB9D: 60             ;                      RTS
CB9E:               ;
CB9E:               ;
CB9E:               ; -----
CB9E:               ; PASCAL MAIN ENTRY POINT (entered via vector in CnOC ROM)
CB9E:               ; Entered with Pascal params and local return addr on stack
CB9E:               ; Areg = Pascal unitnum, Xreg = operation.
CB9E:               ; -----
CB9E:               ;
CB9E: PASCMAIN ; STA          MUNIT
CB9E:               ; STX          ZBCMD
CB9E:               ; LDA          #80
CB9E:               ; STA          PASCAL          ; tell routines Pascal called them
CB9E:               ; PLA
CB9E:               ; LDY          #ROMRETURN          ; stash return address in driver space
CB9E:               ; STA          @INITINFOADR, Y
CB9E:               ; PLA
CB9E:               ; INY
CB9E:               ; STA          @INITINFOADR, Y
CB9E:               ; CPX          #4          ; branch, based on operation
CB9E:               ; BEQ          STATIT          ; status
CB9E:               ; CPX          #2          ; init
CB9E:               ; BEQ          INITIT
CB9E:               ; BCS          BADREQ          ; >=2 (except 2,4) invalid
CB9E:               ; values 0 (read), 1 (write) fall into MAPPIT
CB9E:               ;
CB9E: MAPPIT ; map block number, then go to driver.
CB9E:               ; JSR          MAP          ; return with Drive# in Areg. IORSLT in Xreg.
CB9E:               ; CPX          #0          ; any errs?
CB9E:               ; BNE          DUMP
CB9E:               ; LDY          ZBCMD          ; no, go do read/write as indicated by ZBCMD
CB9E:               ; JMP          DOIO          ; always branch
CB9E:               ;
CB9E: DUMP ; otherwise dump stack and return.

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CB9E1 ;LDY #10.
CB9E1 ;$1 PLA
CB9E1 ;DEY
CB9E1 ;BNE $1
CB9E1
CB9E1 RTN ;enter here when no further action required. Clean up and leave the ROM.
CB9E1 ;Don't destroy Xreg (ioresult).
CB9E1 ;LDY #OFF ;indicate no further I/O required
CB9E1
CB9E1 DDIO ;enter here when further I/O action must be executed via a RAM
CB9E1 ;based call. Yreg=I/O command, Areg=Drive#. Xreg=0 to indicate no
CB9E1 ;IORESULT so far. Leaves the ROM.
CB9E1 ;STY MAPTEMP1
CB9E1 ;STA MAPTEMP2
CB9E1 ;LDY #ROMRETURN+1
CB9E1 ;LDA @INITINFOADR, Y
CB9E1 ;PHA
CB9E1 ;DEY
CB9E1 ;LDA @INITINFOADR, Y
CB9E1 ;PHA
CB9E1 ;LDY MAPTEMP1
CB9E1 ;LDA MAPTEMP2
CB9E1 ;RTS
CB9E1
CB9E1 INITIT
CB9E1 PROCESSUNIT
CB9E1
CB9E1 ;LDA MUNIT
CB9E1 ;JSR INDEX ;find drive# associated with this unit
CB9E1 ;LDX #9
CB9E1 ;BCS RTN ;set IORESULT if bad unit
CB9E1 ;JSR GETENTRY ;get maptable entry for this unit
CB9E1 ;BIT MAPENTRY+P ;is it mounted?
CB9E1 ;BPL NOTPRESENT ;branch if no
CB9E1 ;LDA MAPENTRY+MTRIVE;else go request init with Areg:= drive#
CB9E1 ;LDY ZSCMD
CB9E1 ;LDX #0
CB9E1 ;BEG DDIO
CB9E1
CB9E1 NOTPRESENT
CB9E1 ;LDX #9
CB9E1 ;BNE RTN ;always branches
CB9E1
CB9E1 STATIT ;TSX
CB9E1 ;LDA 0104, X ;fetch hi byte of control word from stack.
CB9E1 ;AND #0E0 ;get top 3 bits
CB9E1 ;CMP #040 ;top 3 bits = 010 ?
CB9E1 ;BEQ MAPSTAT ;yes, do map status and return directly to BIOS
CB9E1 ;BNE PROCESSUNIT ;otherwise do driver's status request
CB9E1
CB9E1 BADREQ LDX #XREQCODE ;Invalid request code
CB9E1 DOFC BNE RTN ;always branch
CB9E1
CB9E1

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CBA2: .PAGE
CBA2: ; status call permits retrieval or setting of the mactable.
CBA2: ; 'old12' address of unit 12 driver is also set/returned in a word immediately
CBA2: ; following the status table.
CBA2: ; Status Control Word -
CBA2: ; bit 0 - ignored.
CBA2: ; bit 1 - =0 to get status
CBA2: ; =1 to change status (update mactable)
CBA2: ; bits 13-15 - =010 to load or save the mactable. All other combinations
CBA2: ; represent unimplemented status operations.
CBA2: ; standard status return items as described in UCSD BIOS implementation guide
CBA2: ; not implemented.
CBA2: ;
CBA2: ; Stack on entry - pointer to statrec (180 bytes), control word.
CBA2: ; Return address in ROMRETURN.
CBA2: ;
CBA2: ; Pascal temp usage - MAPTEMP1, MAPTEMP3
CBA2: MAPSTAT PLA ;buffer addr
CBA3: 85 00 STA MAPTEMP1
CBA5: 68 PLA
CBA6: 85 01 STA MAPTEMP1+1
CBA8: 68 PLA ;control word lsb
CBA9: 29 02 AND #2 ;isolate get/change bit
CBA8: 85 04 STA MAPTEMP3
CBA9: 68 PLA ;control word msb
CBAE: A2 00 LDX #0 ;no errors!
CBB0: A0 B6 LDY #182.
CBB2: A5 04 LDA MAPTEMP3 ;nonzero if changing status
CBB4: D0** BNE $4
CBB6:
CBB6: ;getting status
CBB6: 88 $3 DEY
CBB7: B1 48 LDA @MAPTABLADR,Y
CBB9: 91 00 STA @MAPTEMP1,Y
CBBB: C0 00 CPY #0
CBBD: D0F7 BNE $3
CBBF: F0** BEQ STATRTN
CBC1: $4
CBC1: ;setting status
CBC1: 88 $5 DEY
CBC2: B1 00 LDA @MAPTEMP1,Y
CBC4: 91 48 STA @MAPTABLADR,Y
CBC6: C0 00 CPY #0
CBC8: D0F7 BNE $5
CBCA:
CBCA: 4C 9EC8 STATRTN JMP RTN
CBCD:

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C901: 60          RTS
C902:
C902: ;-----
C902: ;
C902: MAP          ;map read/write params to physical block # and drive#,
C902: ;           and verify write-protect and non-overflow.
C902: ;           On entry, stack contains params as follows:
C902: ;           TOS = return address to this routine 101
C902: ;           BLOCK NUMBER                        103
C902: ;           BYTE COUNT                          105
C902: ;           BUFFER ADDRESS                      107
C902: ;           DRIVE NUMBER from rsp              109
C902: ;           CONTROL WORD                       10B
C902: ;
C902: ;           Return with drive# in A register.
C902: ;
C902: ;           Pascal temp usage - MAPTEMP1, MAPTEMP2
C902: ;
C902: BA          TSX
C903: B6 02      STX          MAPTEMP2
C905: BD 0B01    LDA          010B,X          ;get lsb control word off stack
C908: 4A        LSR          A              ;see if physical sector mode (Areg bit 1)
C909: 4A        LSR          A              ;bit 1 into carry
C90A: A5 0E    LDA          MUNIT          ;Areg := Pascal unit#
C90C: 90**     BCC          $1              ;now branch if not phys sect mode
C90E:
C90E: BD 0C01    LDA          010C,X          ;it's phys sect mode, get hi byte control wd
C911: 1B        CLC
C912: 29 E0     AND          #0E0
C914: 2A        ROL          A
C915: 2A        ROL          A
C916: 2A        ROL          A
C917: 2A        ROL          A              ;move to bits 0-2
C918: 90**     BCC          OKRTN          ;always branch to put physical unit # in
C91A: ;           ;param stack & return w/o mapping
C91A:
C91A: 20 CDC8    JSR          INDEX          ;convert Areg into index into MAPTABL
C91D:
C91D: A2 09     LDX          #9              ;if error,
C91F: B0**     SCS          MAPRTN          ;carry will be set so return with Xreg=9
C921:
C921: 20 F4C8    JSR          GETENTRY         ;move this entry onto zero page
C924: 3B        SEC
C925: 24 31     BIT          MAPENTRY+P       ;check presence bit
C927: 10F4     BPL          $2              ;error return if no volume mounted
C929: A5 42     LDA          ZBCMD          ;writing?
C92B: C9 01     CMP          #1
C92D: D0**     BNE          #3
C92F: 24 31     BIT          MAPENTRY+L       ;then check write-protect bit
C931: A2 10     LDX          #16
C933: 70**     BVS          MAPRTN          ;return with IORSLT=16 if write protected
C935: A6 02     LDX          MAPTEMP2         ;restore stack ptr
C937:
C937: ;           ;check the length of the transfer, make sure no overrun.
C937: ;           ;if the last block to transfer is beyond the last block on the vol

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C937: ; then overrun exists: set IORESULT=64.
C937: BD 0601 LDA 106,X ; get hi byte of byte count
C93A: 30** BMI OVRRUN ; >32K not supported
C93C: 4A LSR A ; Areg:= # of full blocks to xfer
C93D: AB TAY
C93E: F0** BEQ $5
C940: B0** BCS $5
C942: BD 0501 LDA 105,X ; get lo byte of byte count
C945: D0** BNE $5 ; decrement #blocks to xfer if count was
C947: BB DEY ; one or more blocks even, so that...
C948: $5
C948: 98 TYA ; ..here, starting-block+Yreg=index of last block
C949: 18 CLC
C94A: 7D 0301 ADC 103,X ; add starting-block+Yreg
C94D: AB TAY
C94E: BD 0401 LDA 104,X ; calculate last block to
C951: 69 00 ADC #0 ; xfer: lo byte in Yreg, hi byte in
C953: B5 00 STA MAPTEMP1 ; MAPTEMP1
C955: B0** BCS OVRRUN ; error if last block is beyond $FFFF
C957:
C957: 98 TYA ; unsigned compare of last-block and # blocks on
C958: C5 34 CMP MAPENTRY+MTLENG ; pseudovolume
C95A: A5 00 LDA MAPTEMP1
C95C: E5 35 SBC MAPENTRY+MTLENG+1
C95E: 90** BCC NO_OVRRUN ; carry set if last-block >= volume-blocklength
C960: OVRRUN
C960: A2 40 LDX #64.
C962: D0** BNE MAPRTN
C964: NO_OVRRUN
C964:
C964: ; map the starting-block-number parameter to the physical block number
C964: BD 0301 LDA 0103,X ; get block number from stack
C967: 18 CLC
C968: 65 32 ADC MAPENTRY+MTSTART ; adjust block count
C96A: 9D 0301 STA 0103,X
C96D: BD 0401 LDA 0104,X
C970: 65 33 ADC MAPENTRY+MTSTART+1
C972: 9D 0401 STA 0104,X
C975:
C975: A5 30 LDA MAPENTRY+MTDRIVE ; get physical drive index (leave in Areg)
C977:
C977: A2 00 OKRTN LDX #0 ; IORESULT
C979: 60 MAPRTN RTS

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C97A:          PAGE
C97A:
C97A:          ;-----
C97A:          ;
C97A:          ; middle loop of map initialization. Called from
C97A:          ; ATTACH driver. Current-drive counter on TOS, below
C97A:          ; return address. Pseudovolumes directory at $6000.
C97A:          ; Loads map table with default volume entries and
C97A:          ; returns original address of driver for unit 4
C97A:          ; in ORIG4ADR.
C97A:          ;-----
C97A:          ;
C97A:          ; Pascal temp usage - MAPTEMP1, MAPTEMP3, MAPTEMP4, MAPTEMP5
C97A:
C97A: 000B          INITCNT          .EQU          MAPTEMP5
C97A:
C97A: 68          SCAN_DFMT          PLA
C97B: 85 06          STA          MAPTEMP4
C97D: 68          PLA
C97E: 85 07          STA          MAPTEMP4+1; save return address
C980: A2 00          LDX          #0
C982: BD 0260        LDA          6000+2, X; get number of vols
C985: 85 08          STA          INITCNT
C987: 8A          *C          TXA          ; loop on 8 byte records (skipping header record)
C988: 10          CLC
C989: 69 08          ADC          #8
C98B: AA          TAX
C98C: C6 08          DEC          INITCNT ; all done?
C98E: 10**         BPL          $16
C990: A5 07          LDA          MAPTEMP4+1; if so, return
C992: 40          PHA
C993: A5 06          LDA          MAPTEMP4
C995: 48          PHA
C996: 60          RTS
C997:
C997: BD 0460        *16          LDA          6000+4, X; get DEFAULT_UNIT field
C99A: F0E3          BEQ          #8          ; branch if no unit
C99C: C9 90          CMP          #144.
C99E: B0E7          BCS          #8          ; branch if out of range
C9A0:
C9A0:          ; Areg=unit#; use to index into maptable.
C9A0: 20 C0C8        JSR          INDEX ; convert into maptable index
C9A3: B0E2          BCS          #8          ; branch if invalid (NOTE: clear-carry used below)
C9A5: 48          PHA          ; save index to this entry
C9A6: 69 01          ADC          #P
C9A8: AB          TAY          ; index to field P of this entry
C9A9: B1 48          LDA          @MAPTABLADR, Y ; already have this unit present?
C9AB: 30DA          BMI          #8          ; if so, treat this mount as invalid
C9AD: BD 0560        LDA          6000+5, X ; get writeprot bit (bit 7)
C9B0: 6A          ROR          A          ; move into L field (bit 6)
C9B1: 29 40          AND          #40          ; (assumed in same byte as P!)
C9B3: 09 80          ORA          #80          ; set presence bit
C9B5: 91 48          STA          @MAPTABLADR, Y

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C9B7: 69      PLA
C9B8: 40      PHA
C9B9: 69 02    ADC      #MTSTART      ;index to next field
C9BA: AB      TAY
C9BC: BD 0060 LDA      6000,X      ;get starting block#
C9BD: 91 48    STA      @MAPTABLADR,Y
C9C1: CB      INY
C9C2: BD 0160 LDA      6000+1,X
C9C3: 91 48    STA      @MAPTABLADR,Y
C9C7: 68      PLA
C9C8: 48      PHA
C9C9: 69 04    ADC      #MTLENG      ;index to next field
C9CA: AB      TAY
C9CC: BD 0260 LDA      6000+2,X      ;get length
C9CD: 91 48    STA      @MAPTABLADR,Y
C9D1: CB      INY
C9D2: BD 0360 LDA      6000+3,X
C9D3: 91 48    STA      @MAPTABLADR,Y
C9D7: 68      PLA
C9D8: 69 00    ADC      #MTDRIVE
C9DA: AB      TAY
C9DB: 68      PLA
C9DC: 48      PHA
C9DD: 91 48    STA      @MAPTABLADR,Y
C9DF:
C9DF:          ;put driver addr in BIOS and save old addr in def mt table
C9DF: A0 00    LDY      #MAINENTRY
C9E1: B1 4E    LDA      @INITINFOADR,Y ;get our driver addr from the init table
C9E3: B5 04    STA      MAPTEMP3      ;MAPTEMP3:=our driver address
C9E5: 3B      SEC
C9E6: E9 01    SBC      #1
C9E8: B5 00    STA      MAPTEMP1      ;MAPTEMP1:=driver address - 1
C9EA: CB      INY
C9EB: B1 4E    LDA      @INITINFOADR,Y
C9ED: B5 05    STA      MAPTEMP3+1
C9EF: E9 00    SBC      #0
C9F1: B5 01    STA      MAPTEMP1+1
C9F3:
C9F3: BC 0460   LDY      6000+4,X ;get DEFAULT_UNIT field
C9F6: C0 15    CPY      #21      ;system disk unit (1-20)?
C9F8: BC**    BCS      #10      ;branch if a user unit
C9FA: 8B      DEY      ;adjust for table index
C9FB: 9B      TYA
C9FC: 0A      ASL
C9FD: AB      TAY
C9FE: B1 EA    LDA      @0EA,Y ;get disknum table entry for this unit
CA00: 1B      CLC
CA01: 69 01    ADC      #1      ;normalize to true driver address
CA03: 9D 0660 STA      6000+6,X ;save in table
CA06: A5 00    LDA      MAPTEMP1 ;get addr of ourself (-1 for disknum tbl offset)
CA08: 91 EA    STA      @0EA,Y ;save in disknum table
CA0A: CB      INY
CA0B: B1 EA    LDA      @0EA,Y
CA0D: 69 00    ADC      #0
CA0F: 9D 0760 STA      6000+6+1,X

```

CA12:	A5 01		LDA	MAPTEMP1+1
CA14:	91 EA		STA	@0EA, Y
CA16:	C0 07		CPY	#7 ;system unit (4)?
CA18:	D0**		BNE	\$9 ;if not, branch to end of loop
CA1A:	BD 0660		LDA	6000+6, X; else get old driver addr again
CA1D:	A0 04		LDY	#ORIG4ADR
CA1F:	91 4E		STA	@INITINFOADR, Y ;and save for later
CA21:	BD 0760		LDA	6000+6+1, X
CA24:	C8		INY	
CA25:	91 4E		STA	@INITINFOADR, Y
CA27:	B0**		BCS	\$9 ;always branch
CA29:		\$10		
CA29:	98		TYA	;adjust for usertable index
CA2A:	49 80		EDR	#80
CA2C:	B5 00		STA	MAPTEMP1
CA2E:	0A		ASL	A
CA2F:	38		SEC	;to add +1
CA30:	65 00		ADC	MAPTEMP1
CA32:	A8		TAY	
CA33:	B1 EB		LDA	@0EB, Y ;get usernum table entry
CA35:	9D 0660		STA	6000+6, X; save in table
CA3B:	A5 04		LDA	MAPTEMP3; our driver address, calc'd above
CA3A:	91 EB		STA	@0EB, Y ;save addr of ourself in BIOS
CA3C:	C8		INY	
CA3D:	B1 EB		LDA	@0EB, Y
CA3F:	9D 0760		STA	6000+6+1, X
CA42:	A5 05		LDA	MAPTEMP3+1
CA44:	91 EB		STA	@0EB, Y
CA46:				
CA46:	4C 87C9	\$9	JMP	\$B
CA49:				

```

CA49:                                     .PAGE
CA49:                                     ;
CA49:                                     ;
CA49:                                     ; Profile driver -- entry point from Pascal after mapping. Parameters
CA49:                                     ; same as in call from BIOS (Xreg gives command)
CA49:                                     ;
CA49:                                     ;-----
CA49:
CA49: EO 04      PROFILE          CPX      #4      ;branch, based on operation
CA4B: FO**      BEQ              $10     ;status
CA4D: EO 02      CPX              #2      ;init
CA4F: FO**      BEQ              PROINIT
CA51: 90**      BCC              RWSU
CA53:
CA53:                                     ;bad request #
CA53: A2 03      $10             LDX      #XREGCODE;set IORESULT value
CA55: 60
CA56: RTS

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CA561                                     .PAGE
CA561
CA561 ;-----
CA561 ;
CA561 ; Profile driver -- initialization routines (called from master init
CA561 ; routine in ATTACH driver)
CA561 ;-----
CA561
CA561 PRDINIT                               ; initialization of a single Profile.
CA561                                     ; No initialization necessary here since Profile gets
CA561                                     ; init'd directly by ATTACH driver
CA561 A2 00                                LDX    #0
CA581 60                                    RTS
CA591
CA591 HASCARD                               ; clear parity on card. Check to make sure it's present and online
CA591 A6 43                                LDX    SLOTX
CA5B1 A4 0C                                LDY    SLOTNUM
CA5D1 9D B3C0                              STA    CLR_PARITY,X      ; Clear any previous parity errors.
CA601 BD B2C0                              LDA    BUSY,X           ; get present, busy indicators
CA631 AA                                    TAX
CA641 29 01                                AND    #1               ; bit 0 off? (drive present & connected
CA661 D0**                                  BNE    $50              ; to card)
CA681 A9 01                                LDA    #1
CA6A1 05 0D                                ORA    BUSYALL          ; BUSYALL bit 0: some drive is present in
CA6C1 85 0D                                STA    BUSYALL          ; some slot.
CA6E1 8A                                    TXA
CA6F1 30**                                  BMI    $42             ; bit 7 on? (ready & online)
CA711 B1 4A                                $40 LDA    @SLOTTAB,Y   ; ever been online? (maybe just busy now)
CA731 10**                                  BPL    $45             ; branch if not
CA751
CA751 A9 80                                $42 LDA    #080
CA771 05 0D                                ORA    BUSYALL          ; mark that some unit was online
CA791 85 0D                                STA    BUSYALL
CA7B1 A9 80                                LDA    #80
CA7D1 11 4A                                ORA    @SLOTTAB,Y      ; mark this unit has been online
CA7F1 91 4A                                STA    @SLOTTAB,Y
CA811 60                                    $45 RTS
CA821
CA821 B1 4A                                $50 LDA    @SLOTTAB,Y
CA841 29 7F                                AND    #07F            ; drive no longer present
CA861 91 4A                                STA    @SLOTTAB,Y
CA881 60                                    RTS
CA891
CA891 INITSLOT                             ; initialize a single slot
CA891 A0 02                                LDY    #INTDSABL
CA8B1 B1 3E                                LDA    (INDRCN),Y
CA8D1 20 ****                              JSR    SETUPREAD
CA901 20 ****                              JSR    SETUPWRITE
CA931 20 ****                              JSR    SETUPREAD
CA961 60                                    RTS
CA971
CA971 SLOTVARS                             ; compute SLOTNUM, SLOTX from INDRCN.
CA971 A5 3F                                LDA    INDRCN+1

```

CA99: 29 OF  
CA9B: 85 OC  
CA9D: 0A  
CA9E: 0A  
CA9F: 0A  
CAA0: 0A  
CAA1: 85 43  
CAA3: 60

AND #OF  
STA SLOTNUM  
ASL A  
ASL A  
ASL A  
ASL A  
STA SLOTX  
RTS

;make CN --> NO

```

CAA4:                                     .PAGE
CAA4:
CAA4: ;-----
CAA4: ;
CAA4: ; Header routine for reads and writes
CAA4: ; Entered with local return addr and Pascal params on stack
CAA4: ; and XREG = 0 for read, = 1 for write
CAA4: ;
CAA4: ;-----
CAA4:
CAA4: RWSU                                     ; THIS ROUTINE GETS THE PARMS FOR
CAA4:                                     ; READS AND WRITES
CAA4: 86 42 STX ZBCMD
CAA6: 68 PLA
CAA7: 85 0A STA MAPRET ; get local return addr
CAA9: 68 PLA
CAA: 85 0B STA MAPRET+1
CAAC: 68 PLA
CAAD: 85 46 STA BLOCKLO
CAAF: 68 PLA
CAB0: 85 47 STA BLOCKHI
CAB2: 68 PLA
CAB3: 85 3C STA BCLD ; BYTE COUNT LOW
CAB5: 68 PLA
CAB6: 8D 7806 STA BCHI ; BYTE COUNT HI
CAB9: 68 PLA
CABA: 85 44 STA BUFADLO
CABC: 68 PLA
CABD: 85 45 STA BUFADHI
CABF: 68 PLA ; DRIVE NO.
CAC0: 09 C0 ORA #00
CAC2: 85 3F STA INDRCN+1
CAC4: 68 PLA
CAC5: 68 PLA ; CONTROL WORD
CAC6: 68 PLA
CAC7: A5 0B LDA MAPRET+1 ; ready to return
CAC9: 48 PHA
CACA: A5 0A LDA MAPRET
CACB: 48 PHA
CACD:
CACD: 20 97CA JSR SLOTVARS ; set up slot-dependent values
CAD0: A4 0C LDY SLOTNUM
CAD2: B1 4A LDA @SLOTTAB,Y ; check if card there
CAD4: 29 01 AND #01
CAD6: F0** BEQ $4 ; bit 1=1 if card there, branch if not
CADB:
CADB: A9 00 LDA #0
CADA: 85 0D STA BUSYALL
CADC: 20 59CA JSR HASCARD ; set up online table
CADF: A4 0C LDY SLOTNUM
CAE1: B1 4A LDA @SLOTTAB,Y ; get online indicator (bit 7)
CAE3: 30** BMI RWSUCONTINUE
CAE5:
CAE5: A5 0D LDA BUSYALL ; check which error

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CAE7: 6A          ROR      A          ;bit 0 ON = Profile is present
CAE8: 90**        BCC      $4         ;branch if no Profiles present
CAEA: A2 09          LDX      #OFFLINE   ;otherwise they were all just offline
CAEC: D0**          BNE      $5
CAEE: A2 28          LDX      #XNODRIVE
CAFO: 60           RTS
CAF1:
CAF1:             RWSUCONTINUE
CAF1: A6 3C          LDX      BCLO
CAF3: AC 7B06       LDY      BCHI
CAF6: A5 42          LDA      ZBCMD
CAF8:
CAF8:
CAF8:
CAF8: D0**          RWSU1      BNE      TSTBLKNUM   ;BRANCH IF A WRITE
CAFA: 8A           TXA
CAF8: F0**          BEQ      $10
CAF8: C8           INY
CAFE: 88           DEY      $10
CAFF: 98           TYA
CB00: 4A           LSR      A
CB01: 48           PHA
CB02: 18           CLC
CB03: 65 46        ADC      BLOCKLO
CB05: 85 46        STA      BLOCKLO
CB07: A5 47        LDA      BLOCKHI
CB09: 69 00        ADC      #0
CB0B: 85 47        STA      BLOCKHI   ;WE'LL READ BLOCKS IN
CB0D:             ;REVERSE ORDER, HIGHEST 1ST
CB0D: 68           PLA
CB0E: 0A           ASL      A
CB0F: 18           CLC
CB10: 65 45        ADC      BUFADHI
CB12: 85 45        STA      BUFADHI   ;POINT TO HIGHEST BUFFER ADDRESS
CB14:

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CB14: .PAGE
CB14: ;-----
CB14: ; This routine tests the validity of block number requested.
CB14: ;-----
CB14:
CB14: A6 43      TSTBLKNUM      LDX      SLOTX
CB16: 9D 83C0   STA      CLR_PARITY,X ;enable 9334 on card
CB19: A9 00      LDA      #0
CB1B: 85 3A      STA      SWTCH
CB1D: 85 3E      STA      INDRCN
CB1F: 85 3D      STA      ERROR
CB21: F0**      beq      $20          ; always taken -- skip TSTBLKNUM code
CB23: A9 FF      LDA      #OFF
CB25: C5 47      CMP      BLOCKHI
CB27: 18        CLC
CB28: D0**      BNE      $10
CB2A: A5 46      LDA      BLOCKLO
CB2C: C9 FE      CMP      #OFF          ;RAM or status block?
CB2E: B0**      BCS      $20          ;BRANCH IF SO
CB30: A5 46      LDA      BLOCKLO      ;Make sure Block number is
CB32: C9 00      CMP      #DIBBLKLO   ; within widget's range.
CB34: A5 47      LDA      BLOCKHI
CB36: E9 00      SBC      #DIBBLKHI   ;If carry clear then within range.
CB38: B0**      BCS      LDXBM        ;BRANCH IF INVALID BLK#
CB3A: A6 42      LDX      ZBCMD
CB3C: F0**      BEQ      RDBLOCK
CB3E: E6 42      INC      ZBCMD
CB40: 4C ****   JMP      DWRITE
CB43:
CB43: A2 03      LDXBM      LDX      #BADMODE
CB45: 4C ****   JMP      STXERR
CB48:

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CB4B:      .PAGE
CB4B: A5 44      ARBADR      LDA      BUFADLO
CB4A: 8D F804    STA      ORGADRLO
CB4D: A5 45      LDA      BUFADHI
CB4F: 8D 7805    STA      ORGADRHI
CB52: A5 3C      LDA      BCLO
CB54: A4 3F      LDY      INDRCN+1
CB56: 99 B805    STA      BCLOSV, Y
CB59: AD 7806    LDA      BCHI
CB5C: 8D 7804    STA      BCHISV      ; BUFFER ADDRESS AND BYTE COUNT SAVED
CB5F: 60        RTS
CB60:
CB60:
CB60:
CB60: ; -----
CB60: ; Profile driver -- read request.  Transfers "bytes/512" blocks from
CB60: ; disk to buffer.  buffer+bytes-1.
CB60: ; -----
CB60:
CB60: CB60      DREAD      .EQU      *
CB60: 20 48CB    RDBLOCK   JSR      ARBADR      ; move bytes parm to length var.
CB63: A9 03      LDA      #3          ; Allow 2 retries
CB65: 0D F805    ORA      BLK_RTRYCNT
CB68: 8D F805    STA      BLK_RTRYCNT
CB6B: A9 00      LDA      #0
CB6D: 8D 7807    STA      PROSTAT
CB70: CB70      RPAR_RETRY .EQU      *          ; Parity error retries enter here
CB70: A9 BF      LDA      #0BF
CB72: 25 3A      AND      SWCH
CB74: 85 3A      STA      SWCH
CB76: CB76      RCOM_RETRY .EQU      *          ; CMD-BSY bad response retries enter here
CB76: A9 03      LDA      #3          ; approx 2.1 sec. wait for BSY io
CB78: 8D F806    STA      WAITTIME
CB7B: A9 01      LDA      #1          ; expected response byte from Profile
CB7D: 20 ****    JSR      SNDCMD      ; do a CMD-BSY handshake
CB80: 90**      BCC      L70
CB82:
CB82: A5 3A      DR_ERR1   LDA      SWCH          ; has Profile already been reset?
CB84: 30**      BMI      $10         ; branch if so
CB86: 20 ****    JSR      RESET_PROFILE ; reset Profile
CB89: 38      SEC
CB8A: 66 3A      ROR      SWCH          ; set Profile was reset flag
CB8C: 30E8      BMI      RCOM_RETRY  ; try read again
CB8E: 20 ****    $10      JSR      NOTCMDLN
CB91: 38      SEC
CB92: 4C ****    LDAXIO   JMP      LDXDE
CB95:
CB95: D0DF      L70      BNE      RCOM_RETRY  ; bad response - try again.
CB97:
CB97:          Send command bytes
CB97:
CB97: 20 ****    JSR      SND_CMDBYTES
CB9A: B0**      BCS      RDRETRY     ; try again if parity error
CB9C: A9 0A      LDA      #0A

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CB9E: 8D F806          STA      WAITTIME      ;adjust timeout wait
CBA1: A9 02           LDA      #2
CBA3: 20 ****        JSR      SNDCMD        ;2nd CMD-BSY for read opn
CBA6: B0DA          BCS      DR_ERR1      ;timeout on cmd-bsy
CBA8: D0CC          BNE      RCOM_RETRY   ;bad response - try again.
CBAA:
CBAA:                ;
CBAA:                ;
CBAA:                ;
CBAA: 20 ****        JSR      GETSTAT      ;get status bytes first
CBAD: B0**          BCS      RDRETRY      ;try again if parity error
CBAF: 10**          BPL      #10
CBB1: E6 3B          INC      COUNTR      ;time to reset PIPPIN?
CBB3: F0C1          BEQ      RCOM_RETRY   ;branch if not
CBB5: D0CB          BNE      DR_ERR1      ;reset PIPPIN
CBB7: A5 44          LDA      bufadlo
CBB9: 25 45          AND      bufadhi
CBBB: 69 01          ADC      #1
CBBD: D0**          BNE      #20
CBBF: 4C ****        JMP      getsize
CBC2: 20 ****        JSR      JMPDTRNS
CBC5: B0**          BCS      RDRETRY
CBC7: A9 00          LDA      #0
CBC9: B5 3D          STA      ERROR
CBCB: B9 B803        LDA      STATUS1,Y   ;Now check status for good read
CBCE: AA            TAX
CBCF: 29 01          AND      #1
CBD1: F0**          BEQ      LDAST2      ;BRANCH IF NO ERROR
CBD3: BA            TXA
CBD4: 29 08          AND      #8
CBD6: F0**          BEQ      LDXDE      ;BRANCH IF NOT A CRC ERROR
CBD8: A2 01          LDX      #PRTYCRC
CBDA: D0**          BNE      STXERR
CBDC: A2 40          LDX      #DEVERR
CBDE: B6 3D          STX      ERROR
CBE0: D0**          BNE      EXIT
CBE2: B9 3804        LDA      STATUS2,Y
CBE5: 29 58          AND      #58
CBE7: D0A9          BNE      LDAXIO
CBE9: B9 B804        LDA      status3,Y   ; check for bad block #
CBEC: 29 40          AND      #40
CBEE: F0**          BEQ      #1          ; branch if block # ok
CBF0: 4C 43CB        JMP      ldxbm       ; tell user block # out of bounds
CBF3: A5 3C          LDA      #1
CBF5: 0D 7806        LDA      BCLO
CBF8: F0**          ORA      BCHI
CBFA: A5 46          BEQ      EXIT      ;EXIT IF NO MORE BYTES TO READ
CBFC: 29 03          LDA      BLOCKLO
CBFE: D0**          AND      #3
CC00: 20 ****        BNE      #5
CC03: 38            JSR      CCK         ;CHECK KEYBOARD EVERY FIFTH BLOCK READ
CC04: AD 7805        SEC
CC07: E9 02          LDA      ORGADRHI
CC09: B5 45          SBC      #2
CC0B: C6 46          STA      BUFADHI    ;SET BUFFER ADDRESS FOR NEXT BLOCK
CC0D: A4 46          DEC      BLOCKLO    ;NEXT BLOCK TO READ
LDY      BLOCKLO

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CC0F: C8                INY
CC10: D0**             BNE    $10
CC12: C6 47           DEC    BLOCKHI
CC14: 4C 60CB         JMP    RDBLOCK      ; GO READ NEXT (PREVIOUS) BLOCK
CC17: 20 ****        JSR    RSTORADR    ; restore 'ORGADR' and try again
CC1A: 90**           BCC    $10
CC1C: 4C 70CB         JMP    RPAR_RETRY
CC1F: 4C 82CB         JMP    DR_ERR1
CC22:
CC22: A9 03           LDA    #03          ; approx 2.1 sec wait
CC24: 8D F806         STA    WAITTIME
CC27: A9 01           LDA    #1           ; expected response from profile
CC29: 20 ****        JSR    SNDCMD       ; ANY PENDING INFO TO DISK.
CC2C: A9 FF           LDA    #OFF
CC2E: 85 42           STA    ZBCMD
CC30: 20 ****        JSR    SND_CMDBYTES ; SEND PROFILE AN INVLDI COMMAND
CC33: 20 ****        JSR    SETCMDLN
CC36: 20 ****        JSR    SETCMDLN
CC39: 20 ****        JSR    NTCMDLN1    ; LOWER COMMAND
CC3C: BD 83C0         LDA    CLR_PARITY, X ; deactivate card
CC3F: 60             RTS
CC40:
CC40: 20 22CC         JSR    FINISHIO
CC43: A6 3D           LDX    ERROR
CC45: 60             RTS
CC46:
CC46: AD F804         LDA    ORGADRLO    ; Reset addresses
CC49: 85 44           STA    BUFADLO
CC4B: AD 7805         LDA    ORGADRHI
CC4E: 85 45           STA    BUFADHI
CC50: A4 3F           LDY    INDRCN+1
CC52: B9 8B05         LDA    BCLOSV, Y
CC55: 85 3C           STA    BCLO
CC57: AD 7804         LDA    BCHISV
CC5A: 8D 7806         STA    BCHI
CC5D: 18             CLC
CC5E: AD F805         LDA    BLK_RTRYCNT
CC61: 10**           BPL    $10
CC63: 38             SEC
CC64: 6A             ROR    A
CC65: 29 83           AND    #83
CC67: 8D F805         STA    BLK_RTRYCNT
CC6A: 60             RTS
CC6B:
CC6B:                CCK      ; Console type-ahead check
CC6B:                ; From here transfer control to CONCK-caller in ATTACH routine.
CC6B:                ; Return directly to caller
CC6B:                ; TRASHES zpage $0-35, screen slot 0 temps and registers!
CC6B: 6C 4C00         JMP    @CONCK
CC6E:

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CC6E:                                     .PAGE
CC6E:                                     ;-----
CC6E:                                     ;
CC6E:                                     ; Profile driver -- write request. Transfers "bytes/512" blocks
CC6E:                                     ; from buffer to block..block+(bytes/512).
CC6E:                                     ; Error status on return from subroutines is the same as for DREAD.
CC6E:                                     ;-----
CC6E:
CC6E: A5 3C          DWRITE          LDA      BCLO
CC70: F0**          BEQ      $10
CC72: EE 7B06      INC      BCHI
CC75: AD 7B06      $10        LDA      BCHI
CC78: 4A          LSR      A
CC79: 90**          BCC      $20
CC7B: EE 7B06      INC      BCHI          ; BYTE COUNT MOD 512
CC7E: A9 00          $20        LDA      #0
CC80: B5 3C          STA      BCLO
CC82: A9 00          WRBLOCK     LDA      #0
CC84: BD 7B07      STA      PROSTAT
CC87: 20 48CB      JSR      ARBADR          ; move bytes parm to length var.
CC8A: A9 03          LDA      #3
CC8C: OD FB05      ORA      BLK_RTRYCNT
CC8F: BD FB05      STA      BLK_RTRYCNT
CC92:
CC92: A9 BF          WPAR_RETRY   LDA      #0BF
CC94: 25 3A          AND      SWTCH
CC96: B5 3A          STA      SWTCH
CC98:
CC98:                                     ; CMD-BSY retries enter here
CC98:
CC98: WCOM_RETRY
CC98: A9 03          LDA      #3          ; approx 2.1 sec wait time
CC9A: BD FB06      STA      WAITTIME
CC9D: A9 01          lds      #1          ; expected response from profile
CC9F: 20 ****      JSR      SNDCMD          ; 1st cmd-bsy handshake
CCA2: B0**          BCS      DW_ERR1          ; cmd-bsy timeout error
CCA4: D0F2          BNE      WCOM_RETRY          ; wrong response - try again
CCA6: 20 ****      JSR      SND_CMDBYTES ; send write command string
CCA9: B0**          BCS      $12          ; retry if parity error
CCAB:
CCAB:                                     ; Now set up to send write data to widget
CCAB:
CCAB: A9 04          LDA      #4
CCAD: 20 ****      JSR      SNDCMD          ; 2nd cmd-bsy handshake
CCB0: B0**          BCS      DW_ERR1          ; timeout on cmd-bsy
CCB2: D0E4          BNE      WCOM_RETRY          ; retry the handshake if taken
CCB4: 20 ****      JSR      JMPDTRNS          ; now transfer data to widget
CCB7: A9 00          LDA      #0
CCB9: B5 3D          STA      ERROR
CCBB: B0**          $12        BCS      WRRETRY          ; retry on parity error
CCBD:
CCBD:                                     ; now get status from write - 3rd cmd-bsy handshake
CCBD:

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CCBD: A9 0A          $15          LDA      #0A
CCBF: BD F806        $20          STA      WAITTIME      ;adjust timeout value
CCC2: A9 06          $20          LDA      #6
CCC4: 20 ****        $20          JSR      SNDCMD
CCC7: B0**           $20          BCS      DW_ERR1      ;timeout error - cmd-busy
CCC9: F0**           $20          BEQ      WCONT        ;continue if good return status
CCCB: 20 46CC        $20          JSR      RSTORADR     ;restore 'ORGADR' since already
CCCE:                $20          JSR      WCOM_RETRY   ;wrote bytes to zB
CCCE: 4C 98CC        $20          JMP      WCOM_RETRY   ;and retry communication
CCD1:                $20          LDA      SWTCH        ;has Profile been reset?
CCD3: 30**           $20          BMI      #10         ;branch if so
CCD5: 20 ****        $20          JSR      RESET_PROFILE
CCDB: 38             $20          SEC
CCD9: 46 3A         $20          LSR      SWTCH        ;set Profile was reset flag
CCDB: 30BB          $20          BMI      WCOM_RETRY
CCDD: 20 ****        $10          JSR      NOTCMDLN
CCE0: 4C DCCB        $10          JMP      LDXDE
CCE3:                $10          LDA      LDXDE
CCE3:                $10          JSR      GETSTAT
CCE3: 20 ****        $10          JSR      WRRETRY     ;parity error - try again
CCE6: B0**           $10          BPL      #10
CCE8: 10**           $10          INC      COUNTR      ;time to reset PROFILE?
CCEA: E6 3B         $10          BEQ      WRRETRY     ;branch if not
CCEC: F0**           $10          BNE      DW_ERR1
CCF0: 29 41         $10          AND      #41
CCF2: D0E0          $10          BNE      LDAXIDERR
CCF4: B9 3804        $10          LDA      STATUS2,Y
CCF7: 29 48         $10          AND      #48
CCF9: D0E5          $10          BNE      LDAXIDERR   ;if pippin couldnt read its status
CCFB: CE 7806        $10          DEC      BCHI
CCFE: CE 7806        $10          DEC      BCHI
CD01: D0**           $10          BNE      #20
CD03: 4C 40CC        $10          JMP      EXIT        ;IF NORE MORE BYTES TO READ
CD06: 20 6BCC        $20          JSR      CCK         ;CHECK KEYBOARD FOR INPUT
CD09: E6 46         $20          INC      BLOCKLD
CD0B: D0**           $20          BNE      #30
CD0D: E6 47         $30          INC      BLOCKHI
CD0F: 4C 82CC        $30          JMP      WRBLOCK
CD12:                $30          JSR      RSTORADR     ;restore 'ORGADR' and try again
CD12: 20 46CC        $30          BCC      DW_ERR1
CD15: 90BA          $30          JMP      WPAR_RETRY
CD1A:                $30          .equ     *
CD1A: CD1A          $30          ldx     slotx
CD1A: A6 43          $30          ldy     #13
CD1C: A0 13          $30          lda     rd_port,x
CD1E: BD 81C0        $30          dey
CD21: 88            $30          bne     #1
CD22: D0FA          $30          lda     rd_port,x
CD24: BD 81C0        $30          sta     blockhi
CD27: 85 47          $30          lda     rd_port,x
CD29: BD 81C0        $30

```

CD2C: 85 46  
CD2E: A9 00  
CD30: 8D 7B06  
CD33: A4 3F  
CD35: 4C C7CB  
CD38:  
CD38:  
CD38:  
CD38:

sta blocklo  
lda #0  
sta bchi  
ldy indircn+1  
jmp chkstat

. INCLUDE PROM. C. TEXT



```

CD38:          .PAGE
CD38: A9 E8          JMP INDRCT      LDA      #OEB
CD3A: B5 3E          STA      INDRCN
CD3C: 6C 3E00       JMP      @INDRCN
CD3F: A5 44          JMPDTRNS     LDA      BUFADLO      ; save buffer address, modified in
CD41: 4B            PHA                        ; DATRANS
CD42: A6 43          LDX      SLOTX
CD44: BD 83C0       LDA      CLR_PARITY, X      ; turn off card's 9334
CD47: 20 3BCD       JSR      JMPINDRCT        ; jsr to slot dependent DATRANS
CD4A:              ; goes to next statement when done
CD4A: 84 3C          TRANSDNE     STY      BCLO
CD4C: 6B            PLA
CD4D: B5 44          STA      BUFADLO
CD4F: A5 42          LDA      ZBCMD
CD51: D0**          BNE      L100
CD53:
CD53:              ;CHKPARITY checks the parity error line and shifts it into Carry, so
CD53:              ;Carry = 1 is a parity error on return to caller.
CD53:
CD53:
CD53: AD 7807       .CHKPARITY     LDA      PROSTAT
CD56: 29 FE          AND      #OFE
CD58: 8D 7807       STA      PROSTAT
CD5B: A4 3F          LDY      INDRCN+1
CD5D: A6 43          LDX      SLOTX
CD5F: BD 82C0       LDA      BUSY, X           ; get parity error - on bit 6
CD62: 9D 83C0       STA      CLR_PARITY, X    ; clear it for next transfer
CD65: 0A            ASL      A
CD66: 0A            ASL      A                 ; shift it into carry
CD67: 90**          BCC      #10
CD69: A9 01          LDA      #1
CD6B: 0D 7807       ORA      PROSTAT
CD6E: 8D 7807       STA      PROSTAT
CD71: 19 8504       ORA      STATUS3, Y
CD74: 99 8504       STA      STATUS3, Y      ; set parity error bit
CD77: 60            RTS
CD78:
CD78: A9 00          L100          LDA      #0
CD7A: 9D 80C0       STA      WR_PORT, X      ; this code appends a 3 byte block # and
CD7D:              ; complement to the data sent
CD7D: A5 47          LDA      BLOCKHI
CD7F: 9D 80C0       STA      WR_PORT, X
CD82: A5 46          LDA      BLOCKLO
CD84: 9D 80C0       STA      WR_PORT, X
CD87: 4B            PHA
CD88: A9 FF          LDA      #OFF
CD8A: 9D 80C0       STA      WR_PORT, X
CD8D: 45 47          EOR      BLOCKHI
CD8F: 9D 80C0       STA      WR_PORT, X
CD92: 6B            PLA
CD93: 49 FF          EOR      #OFF
CD95: 9D 80C0       STA      WR_PORT, X
CD98: 20 53CD       JSR      CHKPARITY      ; Test for parity error in transfer.
CD9B: 0B            PHP

```

CD9C: 78  
CD9D: 20 \*\*\*\*  
CDA0: 28  
CDA1: 60  
CDA2:

SEI  
JSR     SETUPREAD     ; restore read state  
PLP  
RTS

```

CDA2:                                     PAGE
CDA2:
CDA2:
CDA2: ;-----;
CDA2: ;
CDA2: ; The following are various routines for handling the communications
CDA2: ; protocol of sending commands, and receiving result codes.
CDA2: ;
CDA2: ;-----;
CDA2: ;
CDA2: ; SNDCMD performs a CMD-BSY handshake with the ZB and checks for a
CDA2: ; correct response. If the ZB responds with an incorrect code, a
CDA2: ; 'no go' code is sent by the Apple and the handshake is retried
CDA2: ; up to 2 times. On return, Carry=1 means a handshake timeout or
CDA2: ; three retries attempted. A non-zero return means an incorrect
CDA2: ; response from the ZB that may be retried.
CDA2: ;
CDA2: ;-----;
CDA2:
CDA2: 48          SNDCMD          PHA          ; save expected response
CDA3: 20 ****          JSR          WAITBSYLO
CDA6: B0**          BCS          SENDERR          ; error exit if BSY isn't low
CDA8: A0 05          LDY          #SETRD
CDA9: B1 3E          LDA          (INDRCN),Y          ; set crw hi
CDAC: A0 07          LDY          #RWHI
CDAE: B1 3E          LDA          (INDRCN),Y          ; set datarw hi
CDB0: A0 04          LDY          #SETCMD
CDB2: B1 3E          LDA          (INDRCN),Y
CDB4:          WAITBSYHI
CDB4: 18          ALOOPINIT          CLC
CDB5: A0 00          LDY          #0          ; set .7 sec timeout
CDB7: A6 43          LDX          SLOTX
CDB9: BD 82C0          ALOOP          LDA          BUSY,X
CDBC: 10**          BPL          SC1          ; done if taken
CDBE: 88          DEY
CDBF: D0F8          BNE          ALOOP
CDC1: AD FB05          LDA          PASCAL          ; was driver called from Pascal?
CDC4: 10**          BPL          $5          ; branch if not
CDC6: A5 3E          LDA          INDRCN
CDC8: 29 03          AND          #3
CDCA: D0**          BNE          $5
CDCC: 20 6BCC          JSR          CCK          ; check keyboard for input
CDCF: C6 3E          $5          DEC          INDRCN
CDD1: D0E1          BNE          ALOOPINIT
CDD3: 38          SEC          ; timeout
CDD4: 68          SENDERR          PLA
CDD5: 60          RTS
CDD6: A9 00          SC1          LDA          #0
CDD8: B5 3E          STA          INDRCN
CDDA: 68          PLA          ; get expected response
Cddb: DD 81C0          CMP          RD_PORT,X          ; is that what the ZB sent?

```

```

CDDE: DO**                BNE      NCONT      ;BRANCH IF NOT
CDE0: A9 55                CONT      LDA      #055      ;indicate good response
CDE2: 20 ****              JSR      BSYACK
CDE5: A9 00                LDA      #0        ;indicate good return to caller
CDE7: 60                    RTS
CDE8:                      NCONT
CDE8: A9 02                LDA      #2
CDEA: 0D 7807              ORA      PROSTAT
CDED: 8D 7807              STA      PROSTAT      ;set bad response bit
CDF0: A4 3F                LDY      INDRCN+1
CDF2: 19 B804              ORA      STATUS3, Y
CDF5: 99 B804              STA      STATUS3, Y
CDF8: A9 AA                LDA      #0AA      ;tell Z8 that response not OK
CDFA: 20 ****              JSR      BSYACK      ;drop cmd, wait for bsy to go lo
CDFD: B0D5                BCS      SENDERR     ;timeout on bsy going lo
CDFF: A5 3A                LDA      SWITCH
CE01: 0A                    ASL      A
CE02: 10**                 BPL      NSE
CE04: 38                    SEC
CE05: 60                    RTS      ;set error flag
CE06: 09 80                NSE      ORA      #80
CE08: 6A                    ROR      A           ;bit 7 restored, carry clear
CE09: 85 3A                STA      SWITCH
CE0B: 60                    RTS
CE0C:
CE0C:
CE0C: A0 01                WAITBSYLD LDY      #1
CE0E: AD F806              LDA      WAITTIME
CE11: 48                    PHA
CE12: 18                    BLOOPINIT CLC
CE13: A0 00                LDY      #0          ;timeout = .7 sec * waittime
CE15: A6 43                LDX      SLOTX
CE17: BD B2C0              BLOOP  LDA      BUSY, X
CE1A: 30**                 BMI      BSYLORET   ;done if taken
CE1C: 88                    DEY
CE1D: D0F8                BNE      BLOOP
CE1F: AD F805              LDA      PASCAL     ;was driver called from Pascal?
CE22: 10**                 BPL      $5         ;branch if not
CE24: A5 3E                LDA      INDRCN
CE26: 29 03                AND      #3
CE28: D0**                 BNE      $5
CE2A: 20 6BCC              JSR      CCK        ;check for input from keyboard
CE2D: C6 3E                $5      DEC      INDRCN
CE2F: D0E1                BNE      BLOOPINIT
CE31: CE F806              DEC      WAITTIME
CE34: D0DC                BNE      BLOOPINIT
CE36: 38                    SEC      ;timeout
CE37: A9 00                BSYLORET LDA      #0
CE39: 85 3E                STA      INDRCN
CE3B: 68                    PLA
CE3C: 8D F806              STA      WAITTIME
CE3F: 60                    RTS
CE40:
CE40:

```

```

CE401      ;SND_CMDBYTES sends the command string to widget.  Enter with cmd=bsy=10.
CE401      ;Error return if get parity error - Carry = 1
CE401
CE401 20 ****      SND_CMDBYTES      JSR      SETUPWRITE      ;get in proper state
CE431 A5 42          LDA      ZBCMD          ;send command string - cmd, blockhi
CE451 9D 80C0       STA      WR_PORT, X      ;blocklo, retries, retry threshold
CE481 A9 00          LDA      #0
CE4A1 A4 47          LDY      BLOCKHI
CE4C1 C8            INY
CE4D1 D0**          BNE      #10
CE4F1 A9 FF          LDA      #OFF
CE511 9D 80C0       STA      WR_PORT, X
CE541 A5 47          LDA      BLOCKHI
CE561 9D 80C0       STA      WR_PORT, X
CE591 A5 46          LDA      BLOCKLO
CE5B1 9D 80C0       STA      WR_PORT, X
CE5E1 A9 0A          LDA      #RTRYCNT
CE601 9D 80C0       STA      WR_PORT, X
CE631 A9 03          LDA      #RTRYTHRESH
CE651 9D 80C0       STA      WR_PORT, X
CE681 A0 05          LDY      #SETRD
CE6A1 B1 3E          LDA      (INDRCN), Y      ;set crw hi
CE6C1 A0 07          LDY      #RWHI
CE6E1 B1 3E          LDA      (INDRCN), Y      ;set datarw hi
CE701 4C 53CD       JMP      CHKPARITY      ;check for parity error

```

```

CE73:                                     .PAGE
CE73: ;GETSTAT retrieves the status bytes from widget.
CE73: A6 43      GETSTAT      LDX      SLOTX      ;get slot #
CE75: A4 3F      LDY      INDRCN+1
CE77: BD B1C0    LDA      RD_PORT,X
CE7A: 99 B803    STA      STATUS1,Y
CE7D: BD B1C0    LDA      RD_PORT,X
CE80: 99 3804    STA      STATUS2,Y
CE83: BD B1C0    LDA      RD_PORT,X
CE86: 4B        PHA
CE87: BD B1C0    LDA      RD_PORT,X
CE8A: 99 3805    STA      STATUS4,Y
CE8D: 20 53CD    JSR      CHKPARITY
CE90: 68        PLA
CE91: 0D 7807    ORA      PROSTAT
CE94: 99 B804    STA      STATUS3,Y
CE97: B9 B803    LDA      STATUS1,Y
CE9A: 60        RTS
CE9B:
CE9B:
CE9B:
CE9B: ;SETUPWRITE sets CRW and DATRW lo on the Apple // interface board
CE9B: ;to prepare for a write operation to Profile
CE9B:
CE9B:
CE9B: A0 01      SETUPWRITE  LDY      #SETWRT
CE9D: B1 3E      LDA      (INDRCN),Y      ;set crw lo
CE9F: A0 03      SET_WRITEDIR  LDY      #RWLD
CEA1: B1 3E      LDA      (INDRCN),Y      ;set datarw lo
CEA3: 60        RTS
CEA4:
CEA4: 9D B3C0    CSUW      STA      CLR_PARITY,X      ;turn on 9334
CEA7: 20 9BCE    JSR      SETUPWRITE
CEAA: BD B3C0    LDA      CLR_PARITY,X      ;turn off 9334
CEAD: 60        RTS      ;return to Cn00 space

```

```

CEAE1          .PAGE
CEAE1
CEAE1
CEAE1          ;BSYACK completes the cmd-busy handshake by outputting the response
CEAE1          ;byte to widget, dropping cmd, and waiting for busy to go lo.
CEAE1          ;Enter with the widget response ($55 or $AA) in A.
CEAE1
CEAE1
CEAE1 9D 80C0          BSYACK          STA      WR_PORT,X          ;store response byte
CEB11 A0 03          LDY      #RWLO
CEB31 B1 3E          LDA      (INDRCN),Y          ;set datarw lo
CEB51 A0 00          LDY      #NOTCMD
CEB71 B1 3E          LDA      (INDRCN),Y
CEB91 20 0CCE          JSR      WAITBSYLO
CEBC1
CEBC1 A0 05          SETUPREAD LDY      #SETRD
CEBE1 B1 3E          LDA      (INDRCN),Y          ;set crw hi
CEC01 A0 07          LDY      #RWHI
CEC21 B1 3E          LDA      (INDRCN),Y          ;set datarw hi
CEC41 60          RTS
CEC51
CEC51 CEC5          SETCMDLN .EQU      *
CEC51 20 BCCE          JSR      SETUPREAD
CEC81 A0 04          LDY      #SETCMD
CECA1 B1 3E          LDA      (INDRCN),Y
CECC1 60          RTS
CECD1
CECD1 20 BCCE          NOTCMDLN JSR      SETUPREAD
CED01 A0 00          NTCMDLN1 LDY      #NOTCMD
CED21 B1 3E          LDA      (INDRCN),Y
CED41 60          RTS
CED51
CED51 A9 04          RESET_PROFILE LDA     #4
CED71 0D 7807          ORA     PROSTAT
CEDA1 8D 7807          STA     PROSTAT
CEDD1 A4 3F          LDY     INDRCN+1
CEDF1 19 B804          ORA     STATUS3,Y
CEE21 99 B804          STA     STATUS3,Y
CEE51 A0 0C          LDY     #RST
CEE71 B1 3E          LDA     (INDRCN),Y
CEE91 A0 25          LDY     #25
CEEB1 88          $10     DEY
CEEC1 D0FD          BNE     $10
EEEE1 A0 08          LDY     #CLRRST
CEFO1 B1 3E          LDA     (INDRCN),Y          ;clear reset
CEF21 60          RTRN     RTS
CEF31
CEF31
CEF31

```

```

CEF3: .PAGE
CEF3: .INCLUDE BOOT.TEXT
CEF3:
CEF3: ;
CEF3: ; PROFILE BOOT CODE AND ChOO ROM
CEF3: ;
CEF3: ;
CEF3: 00 00 00 00 00 00 00 .ORG 0CF00
CF00:
CF00: A2 20 LDX #20
CF02: A9 00 LDA #0
CF04: A2 03 LDX #3
CF06: A2 55 LDX #55 ;so monitor recognizes card
CF08: ;as boot device, but pascal doesn't.
CF08: 95 41 #10 STA CMD-1,X ;status command
CF0A: 95 44 STA BUFADLO,X
CF0C: CA DEX
CF0D: D0F9 BNE #10
CF0F: 20 58FF JSR OFF58
CF12: BA TSX ;FIND WHICH SLOT...?
CF13: BD 0001 LDA O100,X
CF16: 85 3F STA INDRCN+1 ;SAVE CN.
CF18: 45 01 EOR 01 ;IS THIS AUTO BOOT?
CF1A: 05 00 ORA 00
CF1C: D0** BNE SKPL1 ;SKIP STARTUP DELAY IF NOT...
CF1E: CA L1 DEX
CF1F: CA DEX
CF20: CA DEX
CF21: D0FB BNE L1
CF23: 88 DEY ;DELAY .9 second since ProFile is
CF24: D0FB BNE L1 ;reset during Apple power up.
CF26: AD FFCF SKPL1 LDA OCFFF ;now we know what slot we're in
CF29: A5 3F LDA INDRCN+1 ;make slot CN into unitnum NO
CF2B: 0A ASL A
CF2C: 0A ASL A
CF2D: 0A ASL A
CF2E: 0A ASL A
CF2F: 85 43 STA UNIT
CF31: AA TAX
CF32: 20 1EC8 JSR PD1 ;do a status call
CF35: B0** BCS CANTBOOT ;branch if error
CF37: E6 42 INC CMD ;for a read
CF39: A9 08 LDA #8
CF3B: 85 45 STA BUFADHI
CF3D: 20 1EC8 JSR PD1 ;read block 0 into $800
CF40: B0** BCS CANTBOOT ;if error
CF42: AD 0008 LDA 0800
CF45: C9 01 CMP #01 ;is the 1st byte read a CLD ?
CF47: D0** BNE CANTBOOT ;branch if not
CF49: 4C 0108 JMP 0801 ;go execute boot code
CF4C:
CF4C: A5 3F CANTBOOT LDA INDRCN+1 ;DETERMINE IF AUTO BOOT...
CF4E: 45 01 EOR 01
CF50: 05 00 ORA 00

```



```
CF52: DO**          BNE    $10
CF54: 4C BAFB      JMP    OFABA
CF57: 4C 59FF      JMP    OFF59
CF5A:
```

```

CF5A1                                     . PAGE
CF5A1                                     ;-----
CF5A1                                     ;
CF5A1                                     ; Profile Block I/O transfer routine. This routine will transfer 512
CF5A1                                     ; bytes OR LESS to/from users buffer from/to RAM buffer of Profile's ZB.
CF5A1                                     ;-----
CF5A1
CF5A1
CF5A1 A5 3C                               DTO          LDA      BCLO
CF5C1 DO**                               BNE      DT1
CF5E1 CE 7B06                             DEC      BCHI          ; SO COUNT WILL BE ACCURATE AFTER
CF611 E6 3D                               INC      ERROR        ; 1ST DECREMENT
CF631
CF631 DO**                               BNE      JSUR
CF651 49 FF                               DT1          EOR      #OFF
CF671 AB                                  TAY
CF681 C8                                  INY
CF691 A5 3C                               LDA      BCLO
CF6B1 18                                  CLC
CF6C1 65 44                             ADC      BUFADLO
CF6E1 85 44                             STA      BUFADLO
CF701 B0**                               BCS      JSUR
CF721 C6 45                             DEC      BUFADHI
CF741
CF741 9B                               JSUR        TYA
CF751 29 03                             AND      #3
CF771 F0**                               BEQ      MOVIN
CF791 C9 03                             CMP      #3
CF7B1 F0**                               BEQ      MI4
CF7D1 C9 02                             CMP      #2
CF7F1 F0**                               BEQ      MI3
CF811 DO**                               BNE      MI2
CF831
CF831 AD FFCF                             DATRANS     LDA      OCFFF          ; turn off any other slots
CF861 A0 00                             LDY      #0
CF881 84 3D                             STY      ERROR        ; INIT PAGE COUNTER
CF8A1 84 3E                             STY      INDRCN       ; restore z page pointer
CF8C1 A5 42                             LDA      ZBCMD
CF8E1 FOCA                               BEQ      DTO          ; branch if a read
CF901 20 A4CE                             JSUW       JSR      CSUW
CF931 A0 00                             LDY      #0
CF951 C6 3D                             DEC      ERROR
CF971 B1 44                             MOVOUT     LDA      (BUFADLO), Y          ; GET A BYTE FROM USER'S BUFFER
CF991 9D B0C0                             STA      WR_PORT, X    ; SEND IT TO PROFILE
CF9C1 C8                                  INY
CF9D1 D0FB                               BNE      MOVOUT
CF9F1 E6 45                             INC      BUFADHI
CFA11 E6 3D                             INC      ERROR
CFA31 F0F2                             BEQ      MOVOUT
CFA51 60                               JTD        RTS
CFA61
CFA61 BD B1C0                             MOVIN     LDA      RD_PORT, X          ; GET A BYTE FROM PROFILE
CFA91 91 44                             STA      (BUFADLO), Y  ; PUT IT IN USER'S BUFFER
CFAB1 C8                                  INY

```

```

CFAC: BD 81C0          M12          LDA      RD_PORT, X      ; GET A BYTE FROM PROFILE
CFAD: 91 44           STA      (BUFADLO), Y    ; PUT IT IN USER'S BUFFER
CFB1: C8              INY
CFB2: BD 81C0          M13          LDA      RD_PORT, X      ; GET A BYTE FROM PROFILE
CFB5: 91 44           STA      (BUFADLO), Y    ; PUT IT IN USER'S BUFFER
CFB7: C8              INY
CFB8: BD 81C0          M14          LDA      RD_PORT, X      ; GET A BYTE FROM PROFILE
CFBB: 91 44           STA      (BUFADLO), Y    ; PUT IT IN USER'S BUFFER
CFBD: C8              INY
CFBE: DOE6           BNE      MOVIN
CFC0: E6 3D           INC      ERROR
CFC2: E6 45           INC      BUFADHI
CFC4: AD 7806         $10          LDA      BCHI
CFC7: 4A             LSR      A
CFC8: 90**           BCC      $20
CFCA: CE 7806         DEC      BCHI
CFCD: B0D7           BCS      MOVIN
CFCF: A4 3C           LDY      BCLO
CFD1: F0**           BEQ      $40             ; BRANCH IF WE HAVEN'T READ WHOLE BUF
CFD3: BD 81C0          $30          LDA      RD_PORT, X
CFD6: C8              INY
CFD7: D0FA           BNE      $30
CFD9: E6 3D           INC      ERROR
CFDB: A5 3D           LDA      ERROR
CFDD: C9 03           CMP      #3
CFDF: 90F2           BCC      $30
CFE1: B0C2           BCS      JTD
CFE3:
CFE3: 00 00 00 00 00 .ORG      0CFEB
CFE8: D099           BNE      DATRANS        ; entry branch to DATRANS
CFEA:
CFEA: AD FFCF         PENTRY          LDA      0CFFF
CFED: A5 43           LDA      UNIT          ; turn off all C800 space
CFEF: AA             TAX
CFF0: 4A             LSR      A
CFF1: 4A             LSR      A
CFF2: 4A             LSR      A
CFF3: 4A             LSR      A
CFF4: 09 C0           ORA      #0C0
CFF6: BD FB07         STA      7FB           ; save slot # for interrupts
CFF9: 4C 1CCB        JMP      PRODOS        ; jump to our cards C800 space
CFFC: 0000           .WORD    MAXBLOCK
CFFE: 47             .BYTE    47
CFFF: EA             .BYTE    OEA
D000:
D000:
D000:
D000: .END

```

AB - Absolute      LB - Label      UD - Undefined      MC - Macro  
 RF - Ref          DF - Def          PR - Proc          FC - Func  
 PB - Public        PV - Private      CS - Consts

ALDOP	LB CDB9:	ALDOPINT	LB CDB4:	ARBADR	LB CB48:	BADMDE	AB 0003:	BADOLDDA	AB 0027:	BADREQ	LB CB9E:	BCHI	AB 0678
BCH1SV	AB 0478:	BCLO	AB 003C:	BCLOS	AE 05BB:	BLKRTRYC	AB 05F8:	BLOCKHI	AB 0047:	BLOCKLO	AB 0046:	BLODP	LB CE17
BLODPINI	LB CE12:	BSYACK	LB CEAE:	BSYHI	LB C877:	BSYLORET	LB CE37:	BUFADHI	AB 0045:	BUFADLO	AB 0044:	BUSY	AB C082
BUSYALL	AB 000D:	CANTBOOT	LB CF4C:	CCK	LB CC6B:	CHKPARIT	LB CD53:	CHKSTAT	LB CBC7:	CLRPARIT	AB C083:	CLRRST	AB 0008
CMD	AB 0042:	CMP16	MC ----:	CONCK	AB 004C:	CONT	LB CDE0:	COPYRIGH	LB CB00:	COUNTR	AB 003B:	CSUW	LB CEA4
DATRANS	LB CF83:	DEVERR	AB 0040:	DEVTYPE	AB 00D1:	DIBBLKHI	AB 0000:	DIBBLKLO	AB 0000:	DIEXIT	LB CABD:	DOIO	LB CB9E
DREAD	LB CB60:	DRERR1	LB CB82:	DT0	LB CF5A:	DT1	LB CF65:	DUMP	LB CB9E:	DWERR1	LB CCD1:	DWRITE	LB CC6E
ERROR	AB 003D:	ERRRTN	LB C879:	EXIT	LB CC40:	FINISHIO	LB CC22:	GETENTRY	LB C8F4:	GETSIZE	LB CD1A:	GETSTAT	LB CE73
HASCARD	LB CA59:	INC16	MC ----:	INDEX	LB C8CD:	INDRCN	AB 003E:	INITCNT	AB 0008:	INITIALI	AB 0002:	INITINFO	AB 004E
INITIT	LB CB9E:	INITSL0T	LB CA89:	INTDSABL	AB 0002:	INTENABL	AB 0006:	JMPDTRNS	LB CD3F:	JMPINDRC	LB CD38:	JSRI	MC ----
JSUR	LB CF74:	JSUW	LB CF90:	JTD	LB CFA5:	L	AB 0001:	L1	LB CF1E:	L100	LB CD78:	L70	LB CB95
L999	LB CD9B:	LDAST2	LB CBE2:	LDAXID	LB CB92:	LDAXIOER	LB CCE0:	LDXBM	LB CB43:	LDXDE	LB CBDC:	LOSTDVC	AB 0005
MAINENTR	AB 0000:	MANUF	AB 0001:	MAP	LB C902:	MAPENTRY	AB 0030:	MAPPIT	LB CB9E:	MAPRET	AB 000A:	MAPRTN	LB C979
MAPSTAT	LB C8A2:	MAPTABLA	AB 0048:	MAPTEMP1	AB 0000:	MAPTEMP2	AB 0002:	MAPTEMP3	AB 0004:	MAPTEMP4	AB 0006:	MAPTEMP5	AB 0008
MAXBLOCK	AB 0000:	MI2	LB CFAC:	MIS	LB CFB2:	MI4	LB CFBB:	MOVIN	LB CFA6:	MOVOUT	LB CF97:	MTDRIVE	AB 0000
MTLENG	AB 0004:	MTSTART	AB 0002:	MUNIT	AB 000E:	NCONT	LB CDE8:	NOQVRRUN	LB C964:	NOTCMD	AB 0000:	NOTCMDLN	LB CEC
NOTPRESE	LB CB9E:	NOTSTAT	LB C87B:	NSE	LB CE06:	NTCMDLN1	LB CED0:	OFFLINE	AB 0009:	OKRTN	LB C977:	ORGADRHI	AB 0578
ORGADRLO	AB 04F8:	ORIG4ADR	AB 0004:	OVERRUN	LB C960:	P	AB 0001:	PASCAL	AB 05F8:	PASCMAIN	LB CB9E:	PD1	LB CB1E
PENTRY	LB CFEA:	PROCESSU	LB CB9E:	PRODOS	LB C81C:	PROFILE	LB CA49:	PROFILED	PR ----:	PROINIT	LB CA56:	PROSTAT	AB 0778
PRTYCRC	AB 0001:	PTRTABL	AB 0048:	RCOMRETR	LB CB76:	RDBLOCK	LB CB60:	RDPORT	AB C081:	RDRetry	LB CC17:	RELEASE	AB 1001
RESETPRO	LB CED5:	ROMRETUR	AB 0006:	RPARRETR	LB CB70:	RST	AB 000C:	RSTAT	AB C082:	RSTORADR	LB CC46:	RTN	LB CB9E
RTRN	LB CEF2:	RTRYCNT	AB 000A:	RTRYTHRE	AB 0003:	RWHI	AB 0007:	RWLO	AB 0003:	RWSU	LB CAA4:	RWSU1	LB CAF8
RWSUCONT	LB CAF1:	SC1	LB CDD6:	SCANDFMT	LB C97A:	SENDERR	LB CDD4:	SETCMD	AB 0004:	SETCMDLN	LB CEC5:	SETRD	AB 0005
SETUPREA	LB CEBC:	SETUPWRI	LB CE9B:	SETWRITE	LB CE9F:	SETWRT	AB 0001:	SKPL1	LB CF26:	SLOTNUM	AB 000C:	SLOTTAB	AB 004A
SLOTVARS	LB CA97:	SLOTX	AB 0043:	SNDCMD	LB CDA2:	SNDCMDBY	LB CE40:	STATIT	LB CB9E:	STATRTN	LB C8CA:	STATUS1	AB 03B8
STATUS2	AB 0438:	STATUS3	AB 04BB:	STATUS4	AB 0538:	STXERR	LB CBDE:	SUBTYPE	AB 0001:	SWITCH	MC ----:	SWTCH	AB 003A
TRANSONE	LB CD4A:	TSTBLKNU	LB CB14:	UNIT	AB 0043:	WAITBSYH	LB CDB4:	WAITBSYL	LB CE0C:	WAITTIME	AB 06F8:	WCOMRETR	LB CC98
WCONT	LB CCE3:	WDGTRD	AB 0000:	WDGTWRT	AB 0001:	WDGTWRTV	AB 0002:	WPARRETR	LB CC92:	WRBLOCK	LB CC82:	WRPORT	AB C080
WRRETRY	LB CD12:	XBADOP	AB 0026:	XBLKNUM	AB 002D:	XBYTECNT	AB 002C:	XCTLCODE	AB 0021:	XIDERROR	AB 0027:	XNODRIVE	AB 0028
XNORESRC	AB 0025:	XNOWRITE	AB 002B:	XREGCODE	AB 0009:	ZBCMD	AB 0042:						

Current minimum space is 20878 words.

Assembly complete: 1677 lines  
0 Errors flagged on this Assembly