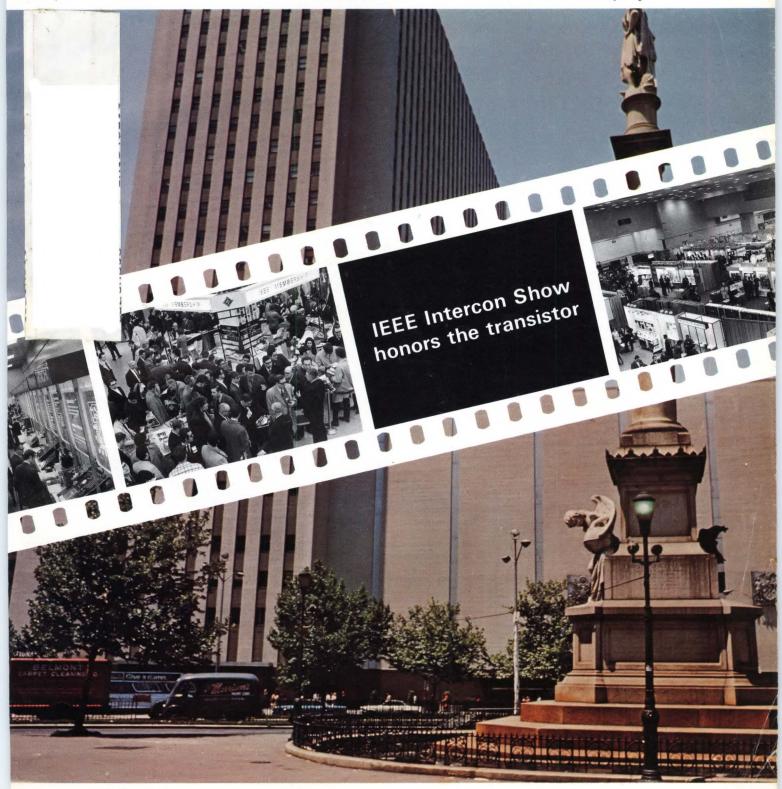
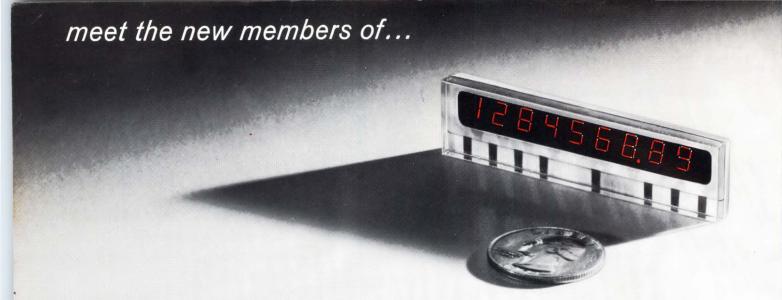
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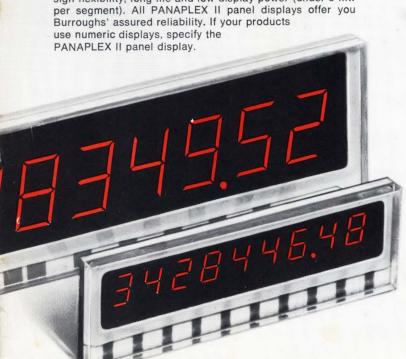
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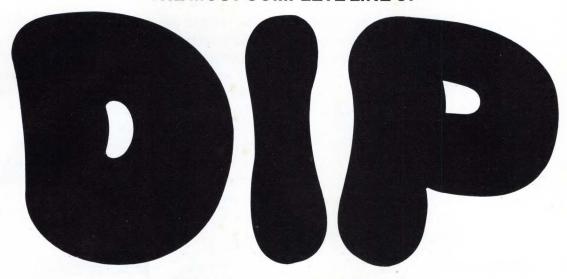
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Further details are available from our Field Sales Office/Representative in your area or from Watkins-Johnson Applications Engineering.

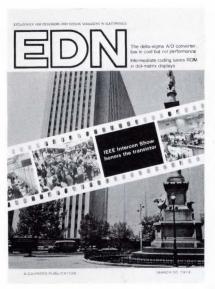
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MARCH 20, 1973 VOLUME 18, NUMBER 6





COVER

1973 IEEE INTERCON salutes the 25th anniversary of the transistor. For a rundown on the technical sessions and a preview of new products to be displayed at the show, see article starting on pg. 36.

DESIGN NEWS Force switchable diodes 20th ISSCC provides exciting glimpses of future LSI devices. DESIGN FEATURES Where to get expert help with your designs 50 An EDN editor relates his experiences with his supplier's applications engineers and how they helped him with a new small system design. Evaluate capacitors with C/V display circuit 62 Here's a circuit that allows you to display capacitance vs. voltage interactions on an oscilloscope. Designers Guide to fractional-horsepower motors (Part 1) 68 There's more than meets the eye to choosing the right motor for a given application. Here's a rundown on the things to consider when making such a choice. This novel A/D conversion technique has good performance at low speeds and medium resolutions, yet only uses two precision components. EDN DESIGN AWARDS 84 PROGRESS IN PRODUCTS Low-cost, adjustable TO-8 S/H amplifier is versatile 89 Dc-to-dc converter family features triple outputs and low-thermal impedances. **DESIGN PRODUCTS** Computer Products . . . 100 Semiconductors . . . 92 Circuits . . . 94 Components/Materials . . . 110 Equipment . . . 104 **DESIGN DEPARTMENTS** The Editor's Column . . . 9



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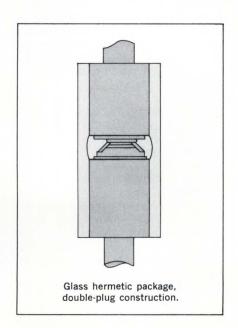


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They're proven through extensive high-reliability testing—like 168 hours in a pressure cooker at 15psi, 120°C (no failures). This high reliability is achieved by combining the best processing, construction and packaging technologies.

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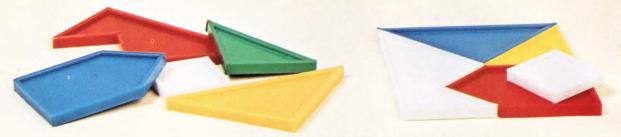
Designated 1N4001 through 1N4006, the 1-amp devices offer a reverse voltage range of 50 to 800V. Repetitive peak forward current (10 cycles, 75°C or below) is 10 amps. Peak surge current is 30 amps. Static reverse current is 10 microamps at 25°C and 50 microamps at 100°C.

For more information

For price and delivery, call your local TI sales office or authorized TI distributor. For complete data sheet covering the 1N4001-1N4006 1-amp glass series, circle 251 on the reader service card. Or write Texas Instruments Incorporated, P.O. Box 5012, MS 308, Dallas, Texas 75222.

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ECT Series. Steady-state rms current ratings (at 25°C) range from 2 to 20 amperes depending upon the method

of mounting. Coil voltages are from 5 to 48V DC. Power requirements are 290 mW for 120V, 60 Hz operation and 450 mW for 240V, 60 Hz. .250" quick-connect terminals are standard with .187" and .205" also available.

EBT Series. The solid-state "contact" is designed to carry a maximum load current of 7A rms, 60 Hz at 25°C ambient. This series provides the convenience of octal plugin terminals and may be mounted in a socket having screw terminals.

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For complete information, call your local P&B representative or Potter & Brumfield Division AMF Incorporated, Princeton, Indiana 47670. 812 385 5251

*Like to try your hand at solving the puzzle shown above? Ask your P&B representative for one.



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Try it doctor . . . you may like it

Health in the United States is big business, with billions spent annually on direct medical care and even a greater amount on peripheral areas related to health.

To the dismay of the electronics industry, only a miniscule portion of these vast sums is spent on electronic, or related, equipment. Some companies, of course, are doing quite well in the field of medical electronics. But for everyone of these there are probably four or five who are either now struggling or who didn't make it.

If we assume that the field of electronics can, indeed, make a substantial contribution to the health and medical fields—an assumption which we firmly believe to be true—what then is the solution? How can the full potential of electronics be realized?

We don't even pretend to know the answer to such a complex problem. Certain things, though, do stand out. Foremost among these is the fact that the major stumbling block is the individual (the doctor) and not the institution (the hospital). This is in contrast to electronics in education, where the stumbling block is the institution (the school) and not the individual (the teacher).

Hospitals are moving into the world of electronics on an ever-increasing scale. Electronic equipment in intensive-care units, cardiac-care units and operating rooms is commonplace. And more and more hospitals are employing engineers to evaluate, select and monitor it.

Individual doctors are another matter. Progress in convincing them to investigate and use electronic devices is painfully slow. They plead lack of familiarity, lack of confidence and lack of time. If this is the case, what can be done to accelerate things? Probably very little with present physicians. But for medical students, how about one or more courses in principles and applications of electronic instrumentation?

We don't mean a course devoted exclusively to present medical instrumentation, but one covering various types of instruments, the fields they serve and the benefits their use has brought about. It could also cover the problems and interactions that occur when connecting individual instruments into systems. Such a course would provide a perspective against which the students, as practicing physicians, would later make judgements on electronic equipment and its use.

Although such an approach would not insure confidence in medical electronics on the part of the doctor, it might replace much of the apprehension that now exists with an equal measure of open-mindedness.

Editor

Frank Egan



NEW CTS TO-8 crystal oscillators eliminate one of your biggest design headaches . . . size . . . they're in a compact .07 cu. in. package. They feature a coldweld enclosure and reliable hybrid circuitry. Frequency range is 500 KHz to 25 MHz. Available as complimentary, multiple binary related outputs capable of driving 5TTL. Temperature stability: ± 25 ppm 0°C to 70°C.

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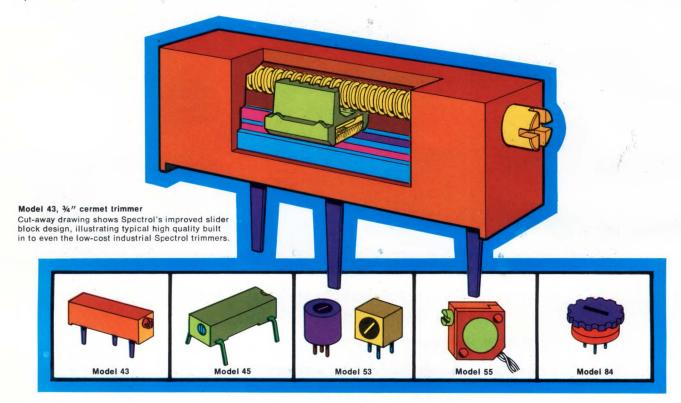
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CHECK NO. 11

Switching, plain and not so plain

The hybrid time delay relay featured below (at d) is a business-like combination of modern electronics with a reliable, inexpensive electromagnetic relay. A single package wraps up the whole deal. It is for places where you want a switching delay of from 1 to 120 sec. repeatably accurate to $\pm 3\%$, fixed or variable; and prefer not to provide the timing elsewhere. The idea is neither original nor recent. Sigma does it more compactly, reliably and *cheaper*.

10,000 volt switching doesn't sound like a subject that belongs on this page. It wouldn't under common circumstances, but item c is an elegant means of grounding the high voltage in an office copier when somebody has to reload it. Would you believe it's an internally illuminated photoconductive element dropping open circuit resistance of 10¹² ohms to 100k?

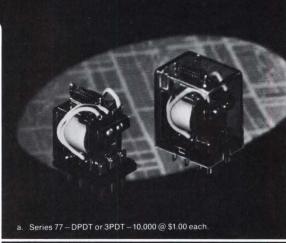
Light controlled photoconductor relays in standard packages were brainchildren of a Sigma division specializing in solid state photoconductors. Great merit and flexibility results from use of these Datacels* as we call them; viz. total isolation of input from output or outputs (1 to 4)—with outputs passive and nearly as compatible as resistors. The latest variety was invented by our lady physicist and is imaginatively known as a "Ladybug."

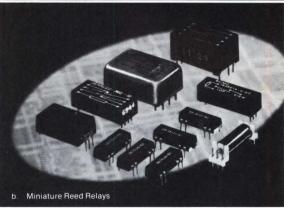
A Ladybug-relay combination solved a recent delayed alarm problem for a Sigma customer. By using a Neon light source for control, the Ladybug imposed no load on the timing circuit, operated directly from 120 volts with lock-on alarm. It makes quite a time delay device—up to 60 sec. after opening a control circuit. Ladybugs and Datacels® have the flexibility of three alternative internal light sources: Incandescent, Neon and LED.

Savvy switching is a worthwhile specialty to know about when you can use it. Sigma has it and has had it since the late thirties.

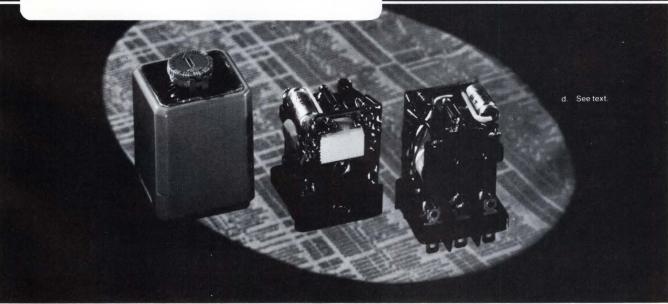
If you'll circle No.12 on the beano card, you'll get a fistful of poop. If you write on a specific question, we'll answer it carefully along with the poop; and if you make it a personal letter to relay marketing chief, Stu Knapp, with your problem and project laid bare, the action will be sudden and substantial. Write Sigma Instruments, Inc., 170 Pearl St., Braintree, Mass. 02185. Telephone 617-843-5000.











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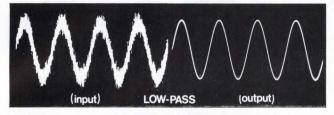
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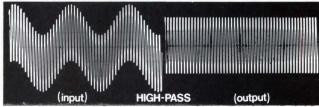


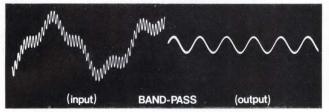
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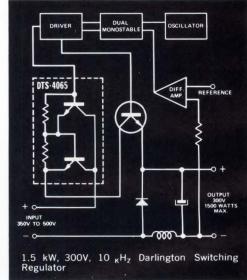
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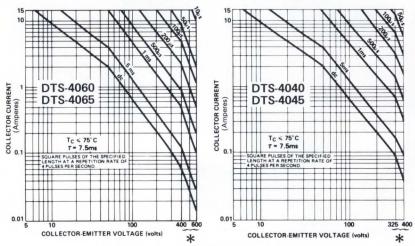
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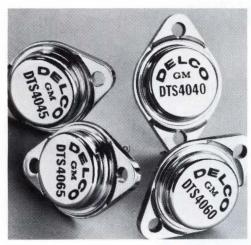


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Force-Switchable Diodes (FSDs) offer possibilities for low-cost solid-state switching

A new solid-state switch that is actuated by a predetermined force and is capable of handling a maximum of 1A at 120V ac or dc has been developed by Innovation Labs of Altadena, CA.

The new devices, called Force-Switchable Diodes (FSDs), have the characteristic of providing a virtual infinite resistance with no load and avalanching to near zero resistance when a predetermined force is applied.

Potential applications of FSDs range all the way from simple ON-OFF switches to arrays with up to 86 line-per-inch resolution for encoding written data or reading embossed cards.

Switching threshold a controlled parameter

The symbol used for FSDs by Innovation Labs and construction of a typical evaluation-prototype unit are shown in Fig. 1. Two or more opposed diodes in series block current flow at voltages up to 120V as long as no force is applied to compress the semiconductor film. When force is applied to the push rod or pressure is exerted on the silicone diaphram seal, the semiconductor film is compressed. At a certain force level determined by manufacturing parameters, the avalanche effect occurs and conduction begins. In most designs, this effect is symmetrical.

One of the interesting characteristics of the device, however, is that it may be manufactured so that

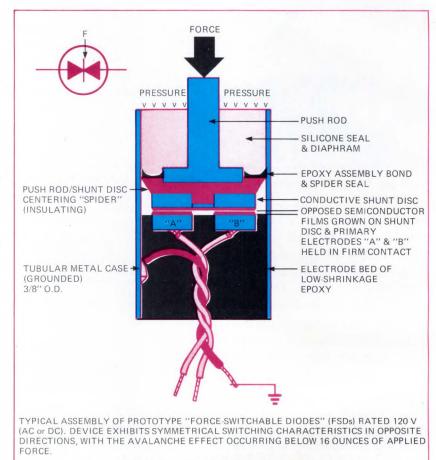


Fig. 1—Electrochemically grown films on conductors form opposing diodes that block current flow. When a predetermined force is applied, compression of the films creates an avalanche condition allowing current to flow in either direction.

UNIT MAY ALSO BE SWITCHED BY FLUID PRESSURE APPLIED TO SILICONE DIAPHRAM.

one diode avalanches before the other. The result is that at one force level, half-wave rectification is achieved. As more force is applied, the other diode avalanches, providing a virtual short circuit for current flow in both directions.

A certain amount of hysteresis is characteristic of the standard device, as shown in **Fig. 2**. Deswitching is said to occur typically at 80% to 90% of the force at avalanche. This prevents deliberate or accidental ''teasing'' of the switch. Ultimate switching times have not yet been established under best conditions, but have been observed at less than 100 nsec. For-

ward voltage drop when the device is switched on is less than 1V. Deswitching occurs when force is released to the appropriate value, independent of the voltage level across the device at that time.

FSDs have been made with various maximum voltage ratings and a wide range of force ratings. As voltage across the switch is reduced from the maximum rating, the force required to actuate is increased as indicated by Fig. 2. The manufacturer states that theoretically, switch life should also be extended with reduced voltage, but to date the only failures that have occurred have been the result of greatly exceeding the current or voltage ratings. When currents larger than 1A have to be handled, FSDs could be used in Triac, SCR or power transistor gating circuits.

By controlling the manufacturing process, a given load-bearing area can be made to avalanche at forces ranging from a fraction of an ounce to hundreds of pounds at a given voltage. It is not possible at this time to produce switches with precision avalanche points, but by combining a low-force switch with an adjustable spring, a precision assembly could be made. For example, an FSD with a 2 oz. ± 1 oz. switching force could be used to make a 50 pound pressure switch with a 1 oz. actuating tolerance by back-biasing a "static" loading diaphram or bellows with an adjustable spring to 49 lbs. 14 oz. Switching would then occur between 49 lbs. 15 oz. and 50 lbs. 1 oz.

Innovation Labs prefers to grow a film for a higher force than that ultimately needed and then mechanically bias the FSD to the desired operating range. It is claimed that this will extend switch life and that there is less likelihood that shrinking of the potting compounds will trigger the device when this approach is taken.

The switches have a slight negative temperature coefficient of change in force and an upper operating temperature limit of 100°C. No observable changes in

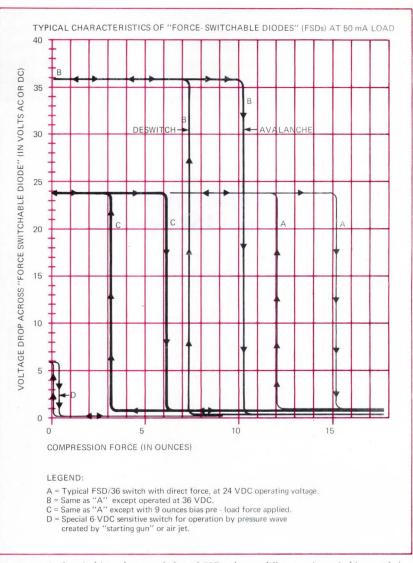


Fig. 2—Typical switching characteristics of FSDs shows difference in switching and deswitching force levels. Voltage drop in the ON condition is less than 1V and diminishes virtually to zero as additional force is applied.

characteristics with time have been reported as long as the films are sealed to eliminate moisture contamination.

Electrochemical process means low cost

While the exact nature of the FSD semiconductor film has not been disclosed, it is known that the film is grown on conductors by an electrochemical process on which patents are still pending.

The films are presently being grown with prototype production equipment under laboratory conditions. In production quantities, the developer anticipates that nominal-accuracy individual FSD barriers could ultimately be pro-

duced for a few cents each.

Total displacements required to avalanche an FSD are in the range of normal stress/strain relationships of solid materials. Because displacements are in the microinch category, a wide range of loading methods could be used, such as: static diaphrams, clamped bi-metals, piezo-ceramic transducers, thermally expanded pellets, magneto-strictive devices, etc. The extremely small displacements involved also permit stacking of FSDs in mechanical series for actuation by a single transducer. In this case, individual elements could be connected either in series or parallel.

To date, applications that have

been either developed or considered include: FSD relays actuated by tiny "bender" ceramic transducers, latching switches, binlevel indicators, electronic keyboards, electronic tablets, static

float switches, pressure switches, multi-output encoding switches, burglar-alarm sensors and trafficactuated light switches in buildings.

FSDs are presently available in a 24V or 120V, or 50 mA evaluation

prototype unit that measures 3/8 in. O.D. by 5/8 in. long including the push rod. For more information, contact Innovation Labs., PO Box 176, Altadena, CA or circle Reader's Service No. **252.**—*AS*

20th ISSCC provides exciting glimpses of future LSI devices

Philadelphia - Many of the devices presented at IEEE's International Solid State Circuit Conference in the past have been those nearing the production phase. If the same holds true for the devices discussed this year, there will be a cornucopia of exciting new semiconductor devices ready to spew forth.

The first two morning sessions, D-to-A and A-to-D conversion and FET memories, quickly aroused the interest of those attending. In the conversion session no less than three 10-bit monolithic DACs were described. As you can see from the performance characteristics in Table I, Precision Monolithics (Santa Clara, Calif.) Analog Devices (Norwood, Mass.) and Signetics (Sunnyvale, Calif.) have taken three independent paths to reach a similar goal, and at nearly the same time. Since theirs is a pre-production device, Analog Devices declined, understandably, to release performance figures for their DAC, but it seems to be in the same league as the DACs from Precision Monolithics and Signetics. It's interesting to note, too, that the superior sheet resistivity of ion implanted resistors, as used by Signetics, didn't seem to give them the overall advantage in chip size that might have been expected. In fact, they ended up with the largest chip of all. The smallest chip, that of Analog Devices, was achieved by depositing thin-film resistors on top of the glass-passivation layer. Technically, this may not be an

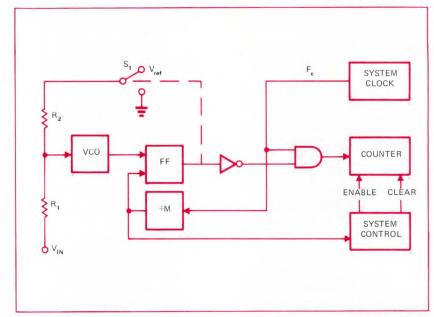


Fig. 1 – Basic CMOS A/D converter patented by General Electric uses a VCO and counting circuits to achieve conversion. The technique is reported to provide accuracy of $\pm 0.05\%$ $\pm 1/2$ LSB, and can be integrated into a monolithic CMOS IC.

"on-chip" resistor technique, but it has such obvious advantages for reducing chip area, capacitance and resistor variation, that it may well become a standard practice in the near future.

A new A/D technique

The conversion session also revealed a truly fresh approach to analog-to-digital conversion developed by M.C. Kidd of GE. While most manufacturers have been trying to develop a monolithic DAC first, with the idea that an on-chip comparator and some control logic could later be added to provide A/D conversion, Mr. Kidd has patented a technique, shown in

Fig. 1, that can be readily integrated on a single CMOS chip. Mr. Kidd's patent has been assigned to GE, who is negotiating licensing arrangements with several semiconductor manufacturers.

Single-transistor cells

A year ago, Intel reported on a 4k-bit MOS/RAM. This year, three designers from the Philips Research Laboratories, Eindhoven, Holland, presented a paper describing their one-transistor per bit 4k-RAM. R. Proebsting and N. Green of Mostek Corp., Carrollton, Texas revealed their one transistor and one capacitor memory cell, shown in **Fig. 2**, from which they

have assembled a 4k-bit RAM. This N-channel RAM, operating at 2.5 MHz, consumes only $60 \mu W/bit$.

Microsystems International, Ltd, of Ottawa, Ontario, Canada, who was the first to announce a commercially available 4k-bit RAM late in 1972, displayed more of their LSI prowess in R. F. Harland's paper describing an 8k-bit shift register. Their N-channel, silicon-gate array consumes only a 2 mil² area per bit, operates from a single +5V supply and has a data rate of 10 Mbits/sec (if operated at +8V). The device which contains an unbelievable 49,328 transistors is packaged in a standard 22-pin DIP.

More memory power

From IBM's Watson Research Center, at Yorktown Heights, NY came the report of Messrs Yu, Dennard, Chang and Hatzakis describing experimental work on high-density memory fabrication using electron-beam lithography. This technique has already allowed them to fabricate an experimental memory array with densities of 8 bits/mil²! This represents a 10:1 improvement over today's photographic techniques and is certain to play a major part in future LSI break-throughs.

IBM's coup was an 8k-bit RAM, shown in **Fig. 3**, using a single FET per memory cell.

Fabricated using a P-channel self-aligned poly-silicon gate (P-SAG) process, the chip measures 145 by 201 mils, and is internally organized 128 words by 64 bits.

The single cell area is 1.84 mils² – about half that of a conventional three-transistor unit.

In addition, the chip contains all the support circuitry needed to operate it. On-chip circuitry includes: data registers, address inverters, decoders, word line biasing, phase drivers, input biasing and chip select.

The memory chip was developed by IBM's System Products Division Laboratory at Essex Junction, Vermont, and was described by William K. Hoffman and Howard L. Kalter.



Actual scan using RL512 array. Scan rate, 2 MHz; Resolution, 6 mils; 4 bit A/D conversion provides 16 gray levels. Photo is courtesy of Recognition Equipment, Incorporated. (see Note)

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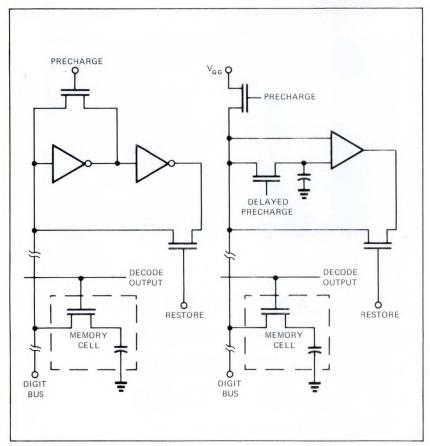


Fig. 2—**Mostek's 4k-RAM** uses one FET and one capacitor for storage of each bit. Sense amplifier techniques, shown here, provide a read-access time of less than 300 nsec, and a power consumption of 60μ W/bit at 2.5 MHz.

Evening sessions are lively

When the lectures begin at 9 AM, it really takes something worthwhile to draw a crowd at 8 PM, but ISSCC's evening sessions have always been well attended, and this year was no exception. There were informal discussion groups concerned with calculator readouts, fast auxiliary memories, auto electronics, D/A and A/D converters, bipolar vs FET for microwave, low power digital circuits, amplifier frontiers, microwave diode power amplifiers and charge-coupled devices.

The low power logic session nearly turned into a FET vs bipolar argument and really seemed to boil down to the fact that MOS devices are gaining on bipolar in speed, and bipolars are gaining on MOS in density. Silicon-gate CMOS/SOS seems capable of operating at 100 MHz at about 1/4 the power of the ECL circuits that are available today. R. Crawford of

Mostek attempted to add some sanity to the MOS vs bipolar argument when he pointed out that the technology that wins out in any particular area may not win on its own technological merits. He feels that such variables as inertia in the marketplace (i.e. PMOS) or company management may have more to do with the victory than will technical arguments. Mr. Craw-

ford's straightforward answers are very refreshing; for instance when the rest of the panel, especially the bipolar advocates, were trying to field a question from the audience on how many devices comprise an "equivalent gate", Crawford responded "I'll define a gate as one load resistor, regardless of whether there is one or twelve transistors tied to it." Not a perfect answer, perhaps, but a good place to start.

At the readout session R. D. Webb of Sigmatron, Santa Barbara, Calif. amazed the audience with the sheer beauty of a large light-emitting-film display. The bright yellow display is undoubtably the most visually attractive thing available, but the 650V p-p drive requirements are very hard to provide in hand-held calculators. Discussion also covered LEDs, gas discharge, liquid crystal and thermal printing displays.

The automotive session allowed itself to get sidetracked on catalytic converters, electric cars, propane fuels and other topics for a long time before getting into the field of electronics. There was a great deal of concern about the capabilities of semiconductor manufacturers to supply both Detroit and their traditional customers. Even the Detroit representatives seemed a little worried, but several manufacturers were very strong in their insistance that they could supply Detroit's requirements without hurting other customers. This is a gnawing problem, and only time will tell whether

mor	Table I — Comparison of nolithic DAC design and pe		
	Precision Monolithics	Analog Devices	Signetics
Chip size (mils)	82 X 148	75 X 90	113 X 124
Linearity (%)	0.05	N/A	0.05
Settling time (µsec)	1.5	N/A	2
Output slew rate	40	N/A	20
Transistors (qty)	182	77	140
Diodes (qty)	33	0	25
Capacitors (qty)	6	1	0
Total resistance ($k\Omega$)	280	1500	530
Resistor type	diffused	thin film	Ion implant
Power dissipation (mW)	300	250	



Fig. 3-8k-bit memory chip size reference, shows an amoeba (dark area), a one cell organism which is as large as 40 of the one-transistor memory cells in IBMs new device.

the electronics industry will suffer from the semiconductor-automotive industry tie-in that is presently under wav.

Informal discussions with several semiconductor manufacturers were guite revealing. Most informed EDN that they were keeping a very close eve on their customer lists and would not allow themselves to become over committed to any one market segment, whether that segment was military, automotive or anything else. Some expressed concern that Detroit wants custom MSI and LSI devices rather than assemblies of more standard units. The direction shown by Motorola and National Semiconductor with their family of low-cost guad amplifiers and comparators offers the auto makers both design versatility and ready-made second supply sources that aren't so easy to achieve in custom units. On the other hand, they offer the semiconductor manufacturer a product line that is very attractive to a broad base of customers.

CCD's cause a stir

Charge-coupled devices drew considerable attention this year, accounting for six papers and one evening session. The advances have been astounding and justify a separate report which will appear in the next issue of EDN.

Digest of papers

Digests of the technical papers delivered at the 20th ISSCC are available to IEEE members for \$15. and to nonmembers for \$20 from:

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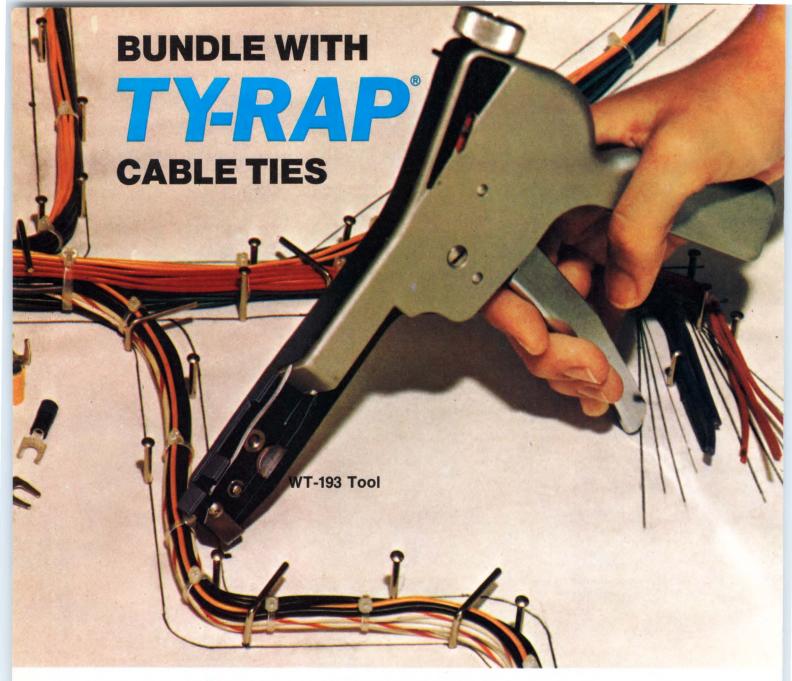
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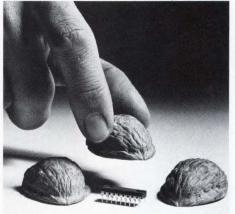
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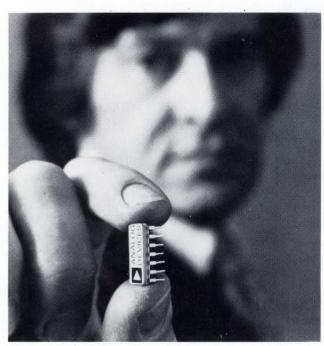
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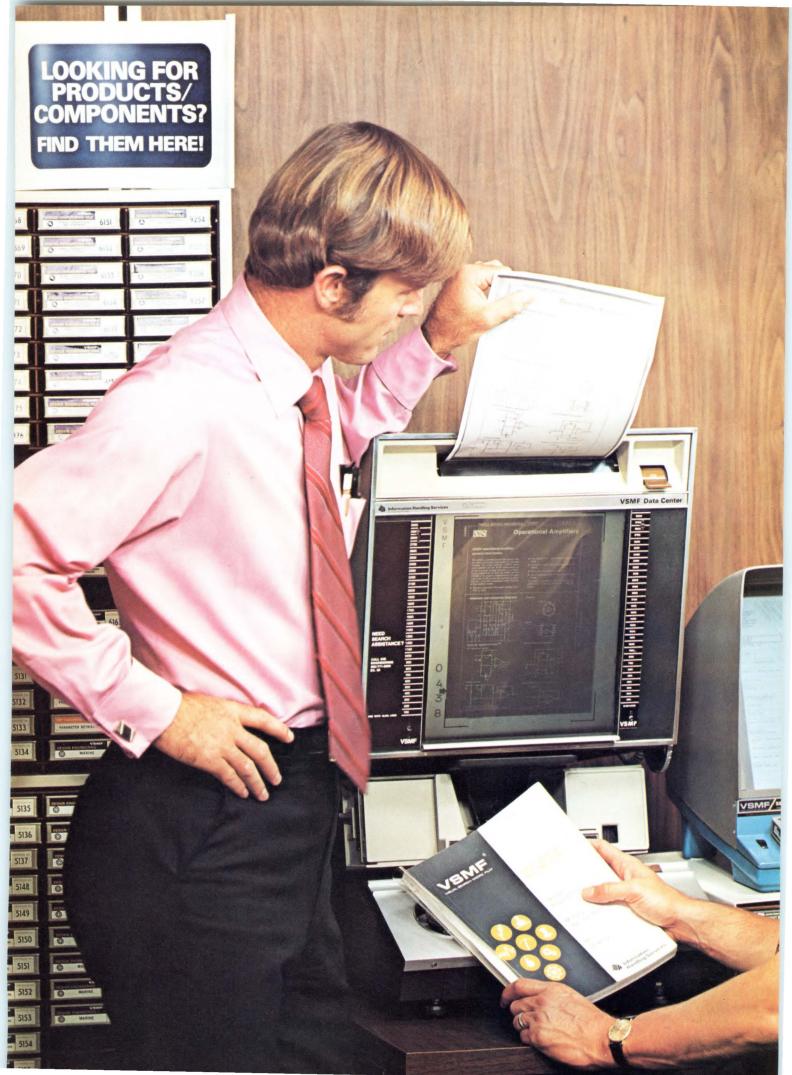
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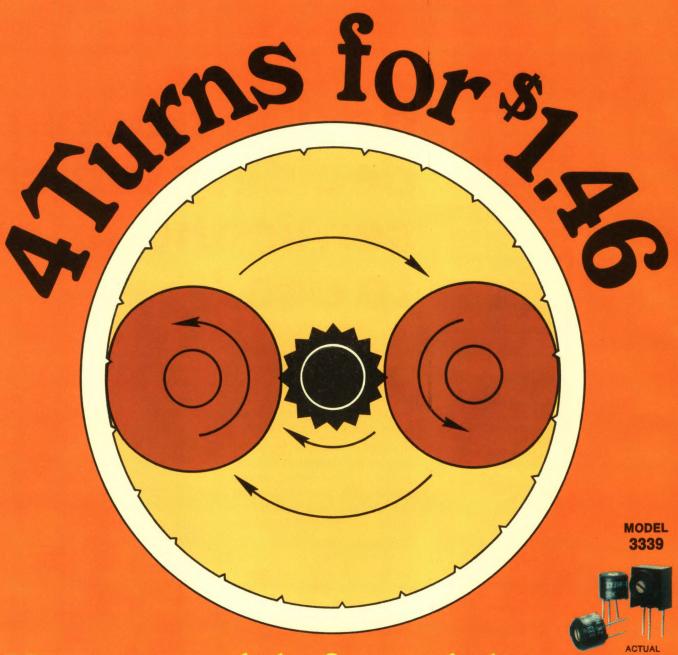
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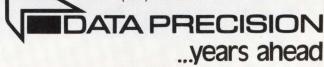
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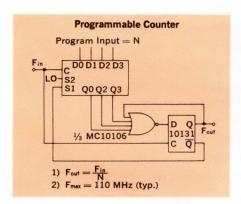


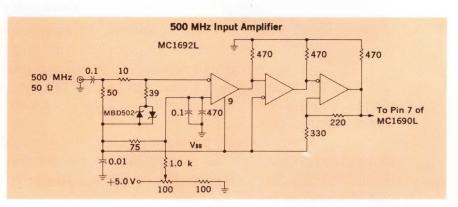
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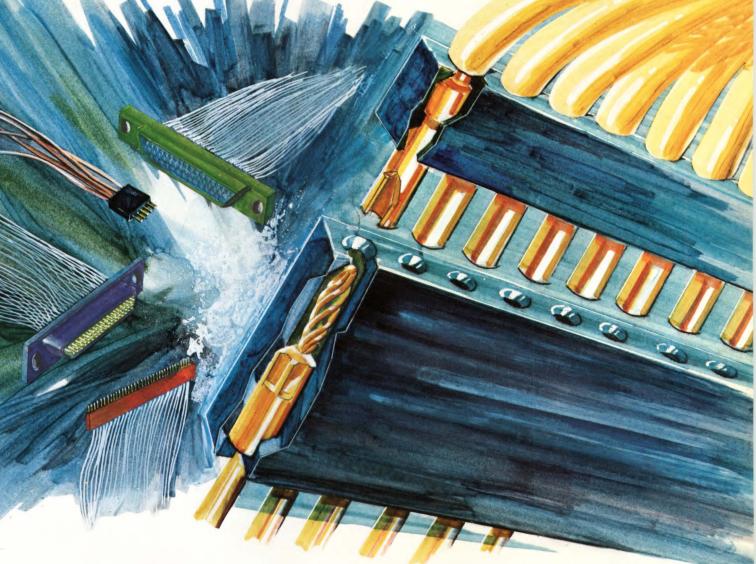
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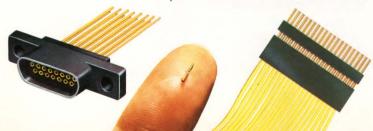
These connectors utilize the proven pin-socket contact system, where the pin, made from a precision spring cable, is the spring member. The seven cable strands are fused in a hemispherical weld, resulting in a strong flexible shock and vibration resistant contact with assured alignment and no discontinuities.

The Dura-Con pin is available in five sizes of Dura-

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The size 24 contacts with 0.050" centers are supplied factory terminated, with pigtail or wire leads. Size 22 contacts, on 0.075" and 0.100" centers, are crimp removable.

Cinch Dura-Con Micro-Miniature Connectors are described in Bulletin PBC-174, available from Cinch Connectors, an Electronic Components Division of TRW Inc., 1501 Morse Avenue, Elk Grove Village, Illinois 60007, (312) 439-8800



TRW CINCH CONNECTORS

CHECK NO. 27

IEEE INTERCON '73: "Solid State Shapes the Future"

Once again, this year's IEEE Intercon Convention and Exhibition, which is expected to draw 30,000 people, is heavily emphasizing the practical in its 59 technical sessions, all being held in one place at the Americana Hotel. Each session consists of an average of three to four papers, compared to four to five last year, as a result of an effort to concentrate more specific papers into each technical session.

The sessions cover topics in optoelectronics, computers, instrumentation, communications, medicine, marketing, consumer electronics and urban technology. Two topics that dominate the sessions are semiconductors and the growing energy crisis and what to do about it.

Approximately 40% of the technical sessions are devoted to semiconductors. These range from advances in solid-state imaging, memories for computers, MOS/LSI ICs, power semiconductors and consumer electronics to the use of solid-state devices in microwave circuits. There is even an interesting session on utilizing bipolar ICs in LSI form for high packing density.

The heavy concentration of papers on semiconductors comes on the heels of the transistor's 25th anniversary. In fact, the first floor of the exhibitor's area will have a special SIAC (semiconductor innovations and applications center) exhibit consisting of a large corridor lined with booths related to the transistor's development. There will also be a multi-media full-color film presentation on the impact the transistor and ICs are having on electronics.

Four sessions on energy focus on practical methods of energy utilization as ways to relieve future energy shortages. These sessions are being held by some of the most notable energy experts in the country. The energy crisis is receiving so much importance this year that for the first time in the IEEE's history, two sessions, a highlight session on the energy crisis and a keynote session on society's limits to growth featuring a general discussion of energy's relevance to future growth, will be open to the general public free of charge.

The exhibits, which should number about 200, down by about 10% from last year, are designed to parallel the technical sessions in content and will only use two floors of space in the Coliseum, something walk-weary individuals of previous IEEE shows are certain to appreciate.

For the first time, show attendants will be able to experience an actual pc-board line fabrication process from beginning to end in a first floor area designated for printed circuits. This will not be a demonstration by someone else, but an attempt by anyone who desires to actually make a pc board on the spot, all by himself.

One noticeable change this year is the drop in foreign exhibitors, who make up only about 8% of the total number of booths. One IEEE spokesman felt that the new and growing European Common Market may be the reason for this; foreign exhibitors are diverting their attention toward the European continent.

THE TECHNICAL SESSIONS IN REVIEW Semiconductors: Branching out in all kinds of directions

The continuing progress in solid-state device performance is reflected in papers which report the many different applications of these devices—mass memories for computers, microprocessors, microwave amplifiers and oscillators and many consumer applications such as TV and the automobile.

One session (23) devoted to MOS/LSI circuits, seeks to enhance the awareness of their present and projected future capabilities by presenting illustrative LSI circuit examples with improved performance.

Discussed will be the pros and cons of n-channel MOS, CMOS and iso-planar MOS. In

addition, the design and performance of MOSFET circuits fabricated on silicon-on-insulating substrates will be discussed. The analysis of a MOSFET read/write memory using this process combined with the n-channel process will be presented.

As an example of advances in MOS circuit design, T. De Franco, H. Bodio and W. Jordan of Intel Corp. (session 2) will describe the hardware and system design of a 1-million byte mainframe memory module using MOS dynamic memory for add-on use in IBM 370/155 and /165 computer systems.

IC designers would not want to miss one very

important session (30) entitled "High Packing Density Bipolar Technology for LSI." The five papers that make up the session will be devoted to discussions on how to produce bipolar circuits in LSI form.

Because LSI circuits are moving into prominence, with some dual-in-line package circuits utilizing more than 24 pins, the problems of packaging, reliability and servicing ICs are given attention in a session (50) on IC packaging.

Several proposed packaging schemes will be explored. One paper by Ron Boger and Jack

Norrie of Amp, Inc., discusses several of the newer IC packaging techniques in detail as evidence that packaging technology is indeed keeping pace with IC technology.

For the consumer, papers will discuss the role new ICs are having in television. Described will be digital AFC circuits that facilitate quick and easy digital pushbutton TV channel selection, and a single chroma-processing IC that combines several TV circuit functions into one package. CMOS watch circuits and a miniature IC transmitter will also be reported (session 51).

Electro-optics: charge coupled devices lead the way

Charge coupled devices (CCDs) are indeed coming into their own. They are now finding applications in image sensing, analog signal processing and digital shift-register memories.

Papers will be presented detailing CCD use in TV cameras as self scanned image sensors (**session 1**). In addition, the use of electron beam scanned silicon mosaic targets and photodiode arrays for scanning images in TV cameras will also be detailed.

Although magnetic storage dominates the mass-memory field at this time, much of the research effort is oriented toward optical, electro-optical and beam-recording technologies. These latter approaches will be examined and evaluated in the session (9) on archival and mass memories using optical devices.

The use of the hologram and a photodichromic color center as mass memories are presented in separate papers. A review is also given on recently proposed and potential future electron-beam mass-storage systems.

How to display graphic and alphanumeric computer data best has always intrigued designers. Many techniques have emerged in recent years to challenge the conventional CRT display. These include dot-matrix LED arrays, ac plasma displays, digitally addressed flat-panel CRTs and gas-discharge panel displays, all to be reviewed in session 33.

Light-valve projection-display systems with the compatibility of operating in real time at TV rates, for both color and black-and-white systems, will be the subject of a session (26) on projection

display systems. Reviewed will be the status of TV projection devices, which include the CRT-Schmidt systems, the laser projector and light valves.

Probably, one of the best applications where optoelectronic devices have been used is in circuit control. The phenomenal growth rate of optoelectronic couplers can easily attest to this. These devices are more dependable and cost less than alternate means. The use of optoelectronic couplers in industrial interface applications, in the field and in the factory, will be carefully scrutinized (session 31).



Only three tiny charge coupled devices (CCDs) make up the image sensors for this solid-state color TV camera. Developed by engineers at Bell Telephone Laboratories, it is expected to lead to a new generation of color TV cameras that are much lighter, smaller and less expensive than conventional cameras using cumbersome vacuum-tube and electron-beam-scanning systems.

Energy: a growing major crisis in need of fast solutions

Without a doubt, the growing fuel shortages of the world are causing scientists and world leaders grave concern. A session (6), devoted to ways of alleviating this, discusses several possible sources for virtually unlimited, and nearly pollution-free, energy.

Three basic themes will be covered: nuclear fusion by both magnetic confinement and laser

energy, solar power utilizing both ground based and satellite systems, and energy produced from water by either electrolytic or chemical injection of energy to produce hydrogen.

Although the concepts discussed may be decades away from commercial large-scale application, they are by far the most promising prospects for the future for satisfactory solutions to the problems posed by our all-too-finite reserves of fossil and nuclear-fission fuels.

Getting the most out of present power systems is the subject of a session (13) on energy storage. The general design, role and value of a Swedish air-storage gas turbine power station that makes use of compressed air for operating the turbine is discussed as a way of increasing power-system efficiency. Superconducting inductors and electrochemical energy are discussed in two papers as other means for storing electric power system energies.

A session (20) on power system control shows different approaches for computer control of power. Varied papers cover new equipment for on-line simulation, a centralized approach for controlling a multiple-area system and a discussion on the existing CEGB power system in England, with respect to its organization and expected developments.

A highlight session open to the general public on the energy crisis will present the viewpoints of the academic world, the power industry and the professional power societies on this matter.

Another keynote session, also open to the general public, will explore the role of the engineer and engineering in today's and tomorrow's societies. Does new technology necessarily mean a better world? Could an anti-technology attitude grow amongst the public? How can technology best serve us with our limited resources? These and other questions will be debated in this "Limits-to-Growth" keynote session.



A 10.6 μm laser amplifier operates at 1k J for 0.1 nsec pulse lengths. A prototype unit for 10⁴–J laser system is now in design at the Los Almos Scientific Laboratory, University of California.

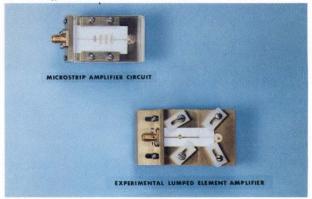
Communications: improving transmissions via satellite to extend our horizons

Satellite communication is no longer a distant dream, but an everyday reality. Methods to improve these communications are constantly being looked at and evaluated. Heavy communications requirements have resulted in the development of multiple-access transmission techniques starting with single-channel into multiple-channel schemes.

A session (5) will examine the multiple-access route as the key to effective satellite communications. In particular, communications networks having central control will be compared to ones with autonomous local control. Time-division multiple-access (TDMA) of the Intelstat satellite system and for the defense satellite communications system (DSCS) will be explored. Various multiple-access techniques planned for the Canadian domestic satellite communication network will also be presented.

A specific session (12) on satellites will review four of the new system applications likely to be in operation during this decade: the Telesat system for domestic communications, the Navy's Fleetsat system, a commercial Maritime system for ships at sea and a potential satellite system for U.S. postal communications.

A panel session (47) on the significance of satellite telecommunications for developing countries provides an international outlook with the participation of representatives from Algeria and Brazil, together with two panelists from the Comsat Corp. and one from Teleconsult, Inc., of Washington, DC.



Wideband Trapatt amplifiers, such as the microstrip (top) and an experimental lumped-element unit (bottom) from the Sperry Rand Research Center, should find applications in phased-array radars and point-to-point communication systems. These 2-terminal, circulator coupled devices operate on a negative-resistance principle, induced by the application of an external sinusoidal signal whose amplitude exceeds a critical value.

State-of-the-art microwave devices, needed to fulfill these communications requirements, will be looked at (session 48). Devices discussed include Trapatt diodes, Transferred-Electron and Impatt amplifiers and GaAs FET and silicon bipolar power transistors. Such performance parameters as power output, gain, efficiency, bandwidth and noise will be discussed and compared.

Television communications will be explored (session 39). A video recording system that utilizes a pressed disc which is read out optically will be explained. So will methods for improving

cable TV broadcasting.

Discussions are not necessarily limited to communications via the airwaves. A session (10) on interior information transfer using multiplexing is planned. A generalized time-division multiplexed (TDM) loop network is first described followed by two papers describing the application of multiplexing to two specific systems—a space station and the B-1 bomber aircraft. A final paper addresses itself to the problem of the proper selection of a multiplexing system's control element—often a very critical decision.

Bio-Engineering: a new hope for the visually handicapped

Modern medicine, safety standards and hygienic conditions have done much to reduce the incidence of blindness throughout the world. Nevertheless, with all of the miracles of medicine, a sizable number of blind people had no hope for surgical or other methods of correction. But there is hope now, thanks to sophisticated electronic, biological and computational methods being employed in a variety of ways to alleviate this age-old problem.

Four interesting papers (session 37) will be directed at implementing electronic devices to aid the visually handicapped. Jonathen Allen of MIT's Research Laboratory of Electronics and Dept. of Electrical Engineering will provide a historical review of aids for the blind, from the earliest devices up to current research efforts. The recent evolution of devices is then compared to these requirements, and future trends are predicted.

Two authors, Edward Snow of Reticon Corp. and Carter Collins of the Smith-Kettlewell Institute of Visual Sciences, will team up to describe a solid-state image-sensor system that uses tactile image perception for visual prosthesis.

Snow will report on a 32 X 32 self-scanning photodiode matrix used for image perception by the blind. Collins will discuss a 32-line, 3-mm monolithic TV camera that electronically impresses onto a blind person's skin its images,

point-for-point, with an array of 1024 electrodes. Blind subjects can locate, recognize and retrieve objects, read graphs, meters and oscilloscopes, and can even distinguish colors.

A final paper on stimulation of the human visual cortex by F.T. Hambrecht of the U.S. Dept. of Health, Education and Welfare will talk about work being done on the evaluation of potential biomaterials, the electrochemical, histopathological and neuropathological effects of long-term electrical stimulation of neural tissues and results from human visual cortex stimulation studies.



Blind seeing aid uses a miniature camera and tactile stimulation to aid the blind to locate, recognize and retrieve objects; read graphs, meters and oscilloscopes and also to distinguish colors. The camera was developed by the Smith-Kettlewell Institute for Visual Sciences and the 32 x 32-element self-scanning photodiode matrix was developed by Reticon Corp.

Computers: taking on bigger and bigger roles in all fields

A glance at some of the technical sessions will show that computers are to be found everywhere—in manufacturing, in design, in marketing, in management and in decision making, to name just a few areas. And they're getting international in character, being installed in more and more countries of the world. It is getting so that large computer systems are creating their

own set of problems—how can they be managed effectively?

The computer's role in marketing is fully explored in one session (4). Looked at will be its role in market research, surveys, planning, modeling and information systems. Another session (8) will examine computer application to the manufacturing environment. Three papers will

explore the use of analysis in the manufacturing design phase, and limitation of hardware and software in present advanced computer systems.

How can the computer help the designer? Two sessions should provide the answer. One (27) illustrates the interaction of the computer from the standpoint of the engineer's needs, and provides several examples of how he can use this means to enhance his effectiveness. Another (18) documents specific cases where the computer has helped in scientific and engineering studies for analyzing and evaluating designs, understanding and optimizing processes and analyzing human factors.

The legal considerations that must be weighed when using computers in public systems, as in cases of automatic monitoring and recording of filed access data, are examined in a special session (4). Another session (22) will analyze the current and future status of the electronic data processing (EDP) industry from the viewpoints of an economist, a venture capitalist and two EDP experts. This session will analyze the problems

and potentials of EDP as a business opportunity.

On the hardware side, a session (19) on bus organized interconnection techniques includes a discussion by proponents of both microwave and optical means of bus interconnections. The multiplexed bus organized interconnection scheme is needed where many portions of large computer systems must communicate with each other using a minimum number of hard wire interconnections.

The full capabilities of minicomputers will be detailed in a workshop panel session (15). The panel will attempt to define the areas where a minicomputer should be used as a most effective tool. The uses and abuses of the minicomputer will come under careful scrutiny.

It has been said that the state of computing outside the U.S.A. is "two years behind" in application, equipment and market development. This assumption will be examined in a session (25) that provides an overview of world market development and the status of the computer in foreign countries.

Instrumentation: sophisticated automatic testing is the byword

With IC devices reaching new highs in complexity, the need for high-speed automatic testing is becoming more and more necessary. But automatic test systems are not without problems, not to mention the expensive price tags they carry.

Just how does a computer controlled test system save money? What is its economic justification? How should resources be reallocated when it is used? What job can it do best? Should a designer build a system from the ground floor up—or buy and integrate its pieces—or purchase a "turn-key" system? These and many other questions will be answered at a session (52) on applying computer controlled test systems.

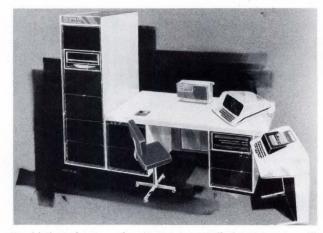
The unique aspects of testing extremely functional and analog devices, such as computers on a chip and voltage-to-frequency converters, will also be explored. Jim Cunningham of Systron-Donner Corp. provides a refreshing step-by-step guide in acquiring computer controlled automatic test systems.

A session (38) devoted to instruments for computer controlled test systems describes the testing problems and limitations of available equipment. How bench instruments can serve and survive in a system's environment will also be discussed. One paper presents a new method for interfacing the many instruments that make up an automatic test system.

As an example of how sophisticated test

systems can be, Ernst Forster of PRD Electronics presents a paper (session 38) on automatic complex-waveform analysis. He will discuss how such systems can analyze complex video-signal parameters ranging all the way from nanoseconds to seconds.

One major headache of computer controlled test systems has always been the software. Four papers (**session 45**) will deal with this subject. Two of them describe computer languages designed to make it easier for the noncomputer oriented



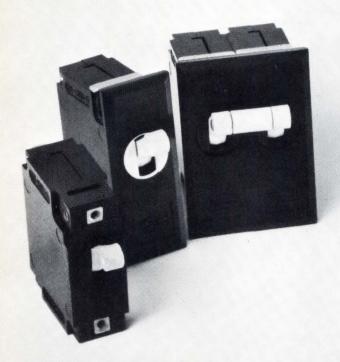
Sophisticated automatic computer controlled test systems will become more and more an economic necessity as IC and other devices become more complex. Shown above is PRD's new CAST system which can automatically analyze the most complex video waveforms ranging from nanoseconds to seconds.

Session 1		Session 14		0					
Progress in Solid State Imaging		Information Theory After 25 Years Synopsis		Session 26 Projection Display		Session 37 Electronics for the		Session 48 Microwave Solid – State Amplifiers	[
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Satellite Multiple		Maintaining the		* Session 29		Systems			
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Session 6				and Tomorrow		Trimming		Consumer	
Prospecting for	Ш	Session 18 Hybrid Simulation:	Ш	Session 30				Electronics	
Energy		What is it — and How		High Packing		Session 42 Surface			
		Viable?		Density Bipolar		Acoustic Wave		Session 52	
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Optical Computing		*Session 19 Bus Organized		Large Scale Integration		Real Systems		Controlled Test	
Session 8		Interconnection		magration		Session 43	П	Systems	
Computer Application		Techniques		* Session 31		Numeric and			
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Environment		Power System		Utilizing Light		Displays for Instruments		Multinational	
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Session 10		Microprocessors		Mechanical		Adversaries		* Session 54	
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Fransfer — An Application of		EDP as a				Session 45 Program		Technology	
Multiplexing		Business		Session 33		Generation		-3,	
		Opportunity		Matrix Displays		for Automatic		Session 55	
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The Evolution of		LSI – MOS	_	Session 34		Equipment		Instrumentation	
Large Government Computing Systems		Circuits		New Developments in Signal		* Session 46			
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Communication		Purchasing Components		Session 35		Electronics		Silicon Devices	
Satellite System Applications —		for '73		Technology		Industry		and Circuits	
extending Our			_	Assessment and Applications		* Session 47			
Horizons		Session 25 State of		Аррисацона		The Significance		* Session 57	
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user to write his own test programs. Two other papers are addressed to current problems and present achievements in automatic test-program generation.

A look at new developments in signal generators (session 34) will examine the latest in signal

generation techniques using direct digital methods and phase locked high-frequency oscillators. Newly developed test methods for instrument performance and new applications due to improvements made in signal generators will be heard.

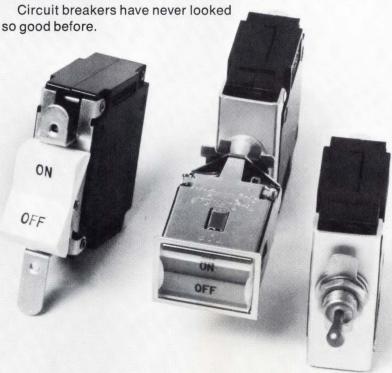


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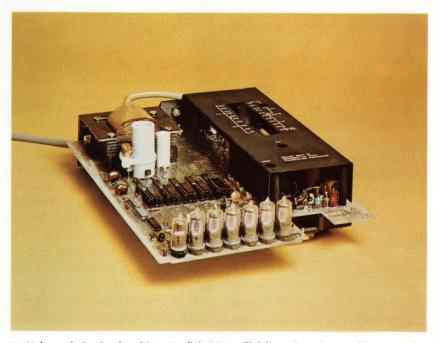
5-1/2-digit DPMs offer resolution down to $1\mu V$ dc

Data Precision has taken its recent 2540A1 digital multimeter and stripped it down to a 5-1/2-digit DPM line known as the Series 3000. Thus, the new DPMs have become the highest resolution units on the market with the most accuracy. There are no other 5-1/2-digit or even 5-digit DPMs available at the moment.

Aimed at industrial instrumentation and control markets where signals from small transducers can range down to microvolts, the series consists of five standard models offering full-scale ranges of ± 0.100000 , ± 1.00000 , ± 10.0000 , ± 100.000 and $\pm 1000.00V$ dc for resolutions of 1 μ V, 10 μ V, 100 μ V, 1 mV and 10 mV, respectively. The five models are the 3001 (\$695), 3002 (\$645), 3003 (\$645), 3004 (\$645), and 3005 (\$645), in order of decreasing resolutions.

The dual-slope integrating DPMs, which include Triphasic conversion and Isopolar reference circuits, feature 20% overranging (except for the Model 3005), autopolarity indication, bipolar, floating, true-differential and guarded inputs that are fully isolated from the outputs, and automatic zero settings.

Accuracy over 6 months at 23°C \pm 5°C for the 1- μ V DPM (Model 3001) is \pm 0.008% of reading, \pm 0.004% of full scale, \pm 1 least significant digit. For all others, it is



 $1-\mu V$ dc resolution is what this 5-1/2-digit DPM will deliver, the only one of its type on the market.

 $\pm 0.007\%$ of reading, $\pm 0.001\%$ of full scale, ± 1 least significant digit. Temperature coefficient over the range of 0°C to +50°C is $\pm 0.001\%$ of reading $\pm 0.001\%$ of full scale per °C (Model 3001 1- μ V DPM), down to $\pm 0.001\%$ of reading $\pm 0.0002\%$ of full scale per °C for all others. Input impedance for the 1, 10 and 100- μ V units is 1000 M Ω and 10 M Ω for the 1- and 10-mV units.

Other features include Nixie tube readouts, 160-dB CMRR (dc) and 120 dB (50/60Hz), 60-dB

NMRR, a reading rate of 3/sec (internal trigger), and up to 4/sec (external trigger), 1-2-4-8 BCD output and 10W power dissipation from either 105 to 125V ac or 210 to 250V ac, 47 to 430 Hz.

Dimensions are 3-in. high by 8-in. wide by 11-in. deep, and weight is 3-1/2 lbs. Numerous options are available.

Data Precision Co., Audubon Rd., Wakefield, Ma 01880. Phone (617)246-1600.

Booth No. 2628 Check No. 100

5-function lab instrument sets new concept

Look into any enginnering laboratory and you'll probably find a minimum number of basic instruments consisting of at least a power supply, frequency counter, scope, multimeter and a signal source. Each instrument has usually had to be purchased separately at a cost of a few hundred to a few thousand dollars.

With the introduction of the Versatester I from Systron-Donner's

Datapulse Div., a new concept has begun in laboratory instruments. This instrument houses five individual instrument functions in one metal case: It's a function generator, a pulse generator, a frequency counter, a multimeter and a power supply—all for a price of only \$1250.

Here's what the Versatester I can do. It generates sine and square waves from 20 Hz to 20 MHz with an accuracy of $\pm 0.1\%$ of setting. The sine wave is flat to within ± 0.25 dB and has distortion less than 0.5% to 500 kHz, -40 dB down to 2 MHz and -30 dB down to 20 MHz. Output is 3V rms, into selectable 50 or 600Ω . The square wave has 95% time symmetry, 5-nsec rise and fall times and complement outputs provided from 50Ω sources, from -5 to +5V.

As a pulse generator, the Versatester I features a repetition rate that spans 20 Hz to 20 MHz, delay and width from 20 nsec to 20 msec and 5-nsec rise and fall times. Accuracy is $\pm 0.19\%$ of setting. Output is -5 to +5V from 50Ω sources. Positive pulse, as well as complement outputs, are provided. The pulse generator portion also has external triggering from dc to 20 MHz, a sync output and pulse pairs up to 10 MHz.

As a frequency counter, the autoranging Versatester I measures frequencies from 20 Hz to 20 MHz using a 4-digit display at 100-mV sensitivity. Input impedance of the counter is 1 $M\Omega$ and time-base



Five instruments in one for \$1250. It is a 20-MHz sine-square-wave and pulse generator; a 20-MHz frequency counter; a 500V ac and dc voltmeter; an ohmmeter and a power supply.

stability is ± 1 part in 10^6 .

The multimeter section measures ac and dc voltage from 0.5 to 500V full scale in four ranges at a 0.1-mV resolution. Accuracy is $\pm 0.1\%$ of reading ± 1 count for dc and $\pm 3\%$ of reading to 1 MHz and $\pm 7\%$ of reading over 1 MHz for ac. Input impedance is 10 M Ω (dc) and 1 M Ω shunted by 25 pF (ac) Polarity (dc) is displayed automatically.

The multimeter can also measure dc resistance. It does this in four ranges from 5 k Ω to 5 M Ω full scale at a resolution that extends down to 1 Ω . Accuracy is specified as $\pm 0.5\%$ of reading ± 1 count up to 500 k Ω and $\pm 0.1\%$ of reading ± 1 count up to 5 M Ω .

As a power supply, the new instrument supplies outputs of +5V to 1A, +15V and -15V to 200 mA

each and $\pm 30V$ by the proper external grounding of the floating $\pm 15V$ outputs. Ripple is 5 mV for the $\pm 5V$ output and 10 mV for the others. All outputs are stable to within $\pm 1\%$ and feature current limiting.

The Versatester I also has some capabilities not usually found in many laboratory instruments. For example, the frequency of pulses, sine and square waves may be precisely set on its 4-digit counter readout. Output levels of each can also be precisely set on its multimeter readout.

Datapulse Div. of Systron-Donner Corp., 10150 W. Jefferson Blvd., Culver City, CA 90230. Phone(213)871-0410.

Booth No. 2326 Check No. 101

Ultra-linear 16-bit D/A converter with 1.5 ppm/°C TC

D/A converters with 16 bits of resolution are devices that yield resolutions of 1 part in 65,000, calling for ultralow nonlinearities to be effective. One such device is Datel's new DAC-HR16B which offers an excellent linearity error of ±0.00015%, accuracy of ±0.00015% of full scale and an astoundingly low temperature coefficient—just 1.5 ppm/°C! And at

a price to beat-just \$495.

Such accuracy and stability is said to be due to a precision thin-film ladder network which tracks to within ± 0.5 ppm/°C, an oven controlled zener reference with a TC of only ± 0.25 ppm/°C which is current controlled within a high-gain servo loop and the use of four highly uniform monolithic quad switches whose betas track to-

gether, both initially and with temperature.

Output settling time of the DAC-HR16B is 200 nsec to 0.025% of full scale, and 1 μ sec maximum to 0.00015% of full scale. The settling time includes switch delay, nonlinear slewing time and final exponential decay time. The converter may be used either for unipolar or bipolar operation.

For unipolar applications, full scale output is 0 to -2 mA; and for bipolar operation, it is ± 1 mA at a maximum voltage compliance of ± 1 V. Provisions are provided for the user to connect an extra op amp for scaling, sign inversion, voltage or current amplification and impedance transformation.

Terminating, feedback and bipolar offset resistors are included internally. These resistors have TCs matched to the ladder network. The values of the internal feedback and bipolar resistors are set to produce a unipolar (0 to $\pm 10V$) or bipolar (± 5 or $\pm 10V$) output from the external amplifier which should be selected to suit particular applications.

Input digital coding is straight binary for unipolar output and offset binary for a bipolar output. All digital inputs are compatible with standard TTL/DTL logic levels and



TC of 1.5 ppm/°C and 0.00015% linearity characterize this 16-bit D/A converter. The fully repairable encapsulated unit has a 1- μ sec maximum settling time to 0.00015% of full scale.

are positive logic. Input power requirements are $\pm 15V$ dc at ± 35 mA.

To compensate for long-term drifts, offset and gain adjustments are provided. The $2 \times 4 \times 0.4$ in.

encapsulated converter module is fully repairable.

Datel Systems, Inc., 1020 Turnpike St., Canton, MA 02021. Phone (617)828-6395.

Booth No. 2609 Check No. 102

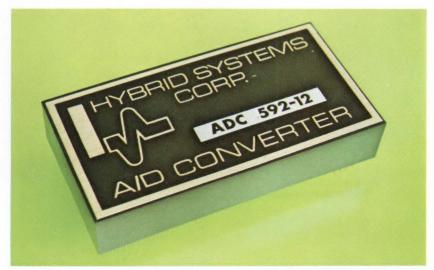
A 12-bit A/D converter with 3.5- μ sec conversion time

Hybrid Systems has introduced a new high-speed \$395 12-bit A/D converter known as the Model ADC592-12. It features a maximum sampling rate of more than 30,000 samples/sec, converting all 12 bits of input data in under $3.5 \ \mu sec$.

The ADC592-12 features an accuracy vs. temperature of 50 ppm/°C over an operating temperature range of 0°C to 70°C. Linearity vs. temperature is specified as 30 ppm/°C.

The high speed/accuracy combination is said to be the result of a precision D/A converter used in conjunction with a proprietary variation of the successive-approximation conversion technique. This is claimed to allow the D/A converter to settle in 50 nsec with an accuracy of 0.0125%.

Complete with all references, logic and timing circuits, the ADC592-12 accepts input signals in three different ranges: 0 to +1V, 0 to +10V and ±5V. Range selec-



This 12-bit A/D converter features a 3.5-µsec conversion time. Packaged in a DIP-compatible module, the 50-ppm/°C unit requires no trimming or external components.

tion is through a simple pin interconnection. The output coding is binary and is fully compatible with TTL/DTL logic levels.

For packaging convenience, the new A/D converter has dual-inline pin spacing for plugging into standard IC socket cards, and is available in a 2 \times 0.4 \times 0.8 in. module. It operates on $\pm 15V$ and +5V. No trimming or external components are required.

Hybrid Systems Corp., 87 Second Ave., Burlington, MA 01803. Phone(617)272-1522.

Booth No. 2632 Check No. 103

IEEE/INTERCON – PRODUCTS

Portable lightweight 2-MHz function generator.



Model 5600 portable function generator is a lightweight unit (only 7 lbs including battery pack) that can be operated from a battery as well as the ac line, and provides sine, square and triangular waveforms over a frequency range of 0.002 Hz to 2 MHz. The instrument's internal rechargeable batteries are rated to operate continuously for up to 10 hrs.

Priced at \$395 (\$70 for the battery kit),

the Model 5600 provides both a 50Ω single ended and 600Ω balanced output at either 15V p-p or 30V p-p, respectively. The output is controlled by a 3-position attenuator and amplitude vernier. An additional 1V auxiliary output is provided. The tuning dial features a 1000:1 tuning ratio. Kron-Hite Corp., 580 Massachusetts Ave., Cambridge, MA 02139. Phone(617)491-3211.

Booth No. 2427 Check No. 104

Low-cost 50-MHz pulse-generator pair

Both the Phillips \$765 PM5715 and \$575 PM5712 offer pulse repetition rates from 1 to 50 MHz with a variable output amplitude from 200 mV to 10V. A separate fixed-amplitude auxiliary output on each unit provides full TTL compatibility without any adjustments. The dc baseline is vernier controlled (the PM5715 has a ±2.5V range; and the PM5712, a range of -5 to +2V).

The PM5715 offers variable transition

times from 6 nsec to 500 nsec while the PM5712 offers a fixed 4-nsec transition time. Each has independently settable pulse delay and duration via selector switches and verniers from 10 nsec to 100 msec. Selectable operating modes include external trigger, gated output, single and double pulse, and square wave. Test & Measuring Instruments, Inc., 224 Duffy Ave., Hicksville, NY 11802. Phone(516)433-8800.

Booth No. 2108 Check No. 105



50-MHz pulse generators with constant duty cycle



The P23 and P25 pulse generators feature constant duty cycle modes that allow the setting and maintaining of an established timing relationship between pulse width and period that is not affected by changing frequencies. Both instruments cover 1 Hz to 50 MHz and provide simultaneous ± 10 V pulses into 50Ω , as well as their positive and negative complements.

The P23 offers 3.5-nsec rise and falltime pulses, and the P25 offers independently adjustable rise- and falltimes down to 5 nsec. Repetition-rate, rise/fall, delay and width controls for both units feature a continuous, 2-decade operating range. An extra 10:1 coarse/fine control is included for fine tuning. \$625 (P23) and \$995 (P25). Interstate Electronics Corp., 707 E. Vermont Ave., Anaheim, CA 92803. Phone(714) **Booth No. 2617** Check No. 106

Rechargeable standby "super-power" batteries

The rechargeable line of Solid-Gel (patent pending) leakproof batteries are said to be "super-power" batteries that are specifically engineered to provide reliable standby power. Available in either 6 or 12V versions, they are reported to have long shelf-life and can be connected in series or parallel and operate in any position at temperatures from -40°F to $+140^{\circ}\text{F}$.

Specific ratings and prices (depending on quantity) follow: 6V, 3 AH, from \$5.50 to \$11.21; 6V, 4.5 AH, \$6.50 to \$12.95; 6V,



7.5 AH, \$7 to \$14; 6V, 8 AH, \$7 to \$14; 12V, 4.5 AH, \$10 to \$17.95; 12V, 8 AH, \$12.50 to \$24.14. In view of their leakproof characteristics, the Solid-Gel batteries are reported to be approved for shipment without precautionary packaging by the U.S. Postal Service. Elpower Corp., 2117 S. Anne St., Santa Ana, CA 92704. Phone(714) 540-6155.

Booth No. 1625 Check No. 107

Ac/dc/ohms 4-1/2-digit microvoltmeter for \$895



Keithley's Model 171 digital multimeter for \$895 features 1- μ V dc and 10- μ V ac resolution, and ac and dc current plus resistance-measurement capabilities. It measures 1 μ V to 1 kV dc full scale in 6 ranges at $\pm 0.02\%$ accuracy, and the same voltage span for ac in 5 ranges from 40 Hz to 100 kHz, at $\pm 0.3\%$ midband accuracy.

Ac and dc current are both measured from 1 μ A to 2A full scale in seven ranges. Resistance measurements can be made in seven ranges from 1 k Ω to 2 M Ω full scale. Input resistance is as high as 1000 M Ω , and line-noise rejection is 70 dB. The Model 171 may have its input circuit floated up to $\pm 500V$ with respect to chassis ground. Keithley Instruments, Inc., 28775 Aurora Rd., Cleveland, OH 44139. Phone(216) 248-0400.

Booth No. 2309 Check No. 108

4-bit A/D converter with 25-MHz conversion time

Model ADC-4B-25MHz is a 3-in, wide by 5-in. long by 1-in. high module that can perform 4-bit conversion in 40 nsec (throughput rate of 25 MHz). Costing only \$1250, it utilizes a parallel/serial/parallel conversion scheme and boasts $\pm 0.8\%$ accuracy and differential linearity. The temperature coefficient for differential linearity is ± 50 ppm/°C and ± 25 ppm/°C of reading for full scale and gain.

Specified accuracy for 4 bits of resolution is maintained over an operating temperature range of 0°C to $+70^{\circ}\text{C}$. A larger operating range of -25°C to $+85^{\circ}\text{C}$ is available at extra cost. Output is straight binary and TTL/DTL compatible. Input is 0 to -2.55V and long-term stability is $\pm 0.4\%/\text{year}$. Datel Systems, Inc., 1020 Turnpike St., Canton, MA 02021. Phone (617)828-6395.

Booth No. 2609 Check No. 109



Frequency agile phase locked signal sources

Units in this series of signal sources are available to cover two bands of frequencies: 4.5 to 4.8 GHz and 4.5 to 5.3 GHz (Models 461-F5-001 and 502-F5-002, respectively), with approximately +10 dBm of output. The input reference may be provided either by a switched crystal bank or a frequency synthesizer.

Each unit's source will track a sweeping input signal, or if switched to a new input

signal, lock it to within 0.1 sec. Output VSWR is 1.5:1; residual FM is 55 Hz rms in any 3-kHz slot between 10 and 100 kHz away from the carrier; spurious rf output is 75 dB down (in band) and 50 dB down (out of band). Narda Microwave Corp., 75 Commercial St., Plainview, NY 11803. Phone(516)433-9000.

Booth No. 2426 Check No. 110



High-accuracy microwave power meter

The 435A power meter/8481A power sensor system measures power over a 55-dB dynamic range (300 nW to 100 mW) from 10 MHz to 18 GHz. The sensor's SWR is under 1.2 from 30 MHz to 12.4 GHz, and under 1.3 from 12.4 to 18 GHz. The system consists of the power sensor which contains the thermocouple and a FET chopper stabilized amplifier and the power meter with a built-in rf calibration source.

Features include over a 300-mW thermocouple burnout level, a precise 1.00-mW



50-MHz calibration source, remote power monitoring, sensor calibration data that consists of a chart displaying calibration factor vs. frequency on each sensor plus computer calibration magnitude and phase data at 17 frequencies and automatic pushbutton zeroing. \$350 (sensor); \$550 (meter); and \$100 (optional battery). Hewlett-Packard Co., 1501 Page Mill Rd., Palo Alto, CA 94304. Phone(415)493-1501.

Booth No. 2400 Check No. 111

IEEE/INTERCON – PRODUCTS

Recorder/converter system for live TV images



Live TV images are stored in a storage tube and later reprinted on demand in 45 sec or transmitted over dial-up phone lines and printed out as permanent hard-copy records on a frame-by-frame basis at voice-band speeds. The system includes an Alden 400 or 600 "push-to-print" recorder interfaced to a scan converter.

Two systems are available: one with a resolution of 1300 TV lines/diameter at 50% modulation orthagonal read right (80 characters/line) and another with 750 TV lines/diameter at 50% modulation orthagonal read right (50 characters/line). From \$795 to \$2500 (without converter). Alden Electronic & Impulse Recording Equipment Co., Inc., Alden Research Center, Westboro, MA 01581. Phone(617)366-8851.

Booth No. 2519 Check No. 112

Dry rf load handles 0.5 kW of power



The Model 8431 Termaline rf load resistor has a continuous input power rating of 500W in the horizontal position and 600W standing vertically. The new load is used for terminating 50Ω transmission systems during design, measurement and maintenance operations. It has a high-conductivity ceramic substrate.

Specifications include low VSWR of 1.1

from dc to 1 GHz, 1.25 from 1 to 2.5 GHz, continuous power dissipation of 500/600W in ambients from -40°C to $+45^{\circ}\text{C}$ and 50Ω impedances. The Model 8431 is normally supplied with a female N connector and costs \$250. Bird Electronic Corp., 30303 Aurora Rd., Cleveland, OH 44139. Phone(216)248-1200.

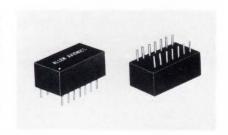
Booth No. 2509 Check No. 113

14-pin DIP delay line offers delays to 1 μ sec

LC DIP is a tiny 14-pin dual-in-line tapped lumped-constant delay line that offers delay times up to 1 μ sec. It incorporates 10 equal delay taps and is available in standard units with time delays from 10 to 1000 nsec. These units are available in time-delay-to-rise-time ratios of 5:1.

Because of the dual-in-line packaging of the LC DIP and its low silhouette, it is compatible with semiconductor and other assemblies where circuit board height is restricted. Each unit has a working voltage of 50V dc and is epoxy encapsulated. Cost is approximately \$20 in small quantities. Allen Avionics, Inc., 224 E. 2nd St., Mineola, NY 11501. Phone(516)248-8080.

Booth No. 1722 Check No. 114



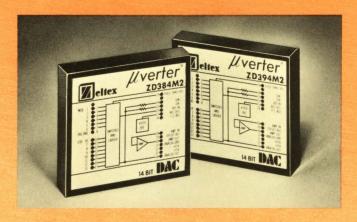
4-digit DMM features a 2-speed clock

Model DMM-40 clock increases conversion speed while maintaining high accuracy and high noise rejection. An additional feature of this digital multimeter is an overrange capability that extends up to 140%. The Model DMM-40 autoranges over all 20 ranges of its five measurement functions. These functions include ac and dc voltage, ac and dc current and resistance. The price for the basic unit is \$695. Lear Siegler, Inc., 714 N. Brookhurst St., Anaheim, CA 92803. Phone(714)774-1010.

Booth No. 2302 Check No. 115



from ZELTEX... 14-Bit DAC's from 85



LOW COST 14-BIT DAC's—The new ZD300 Series include ten new models that offer excellent linearity, fast settling, current and voltage outputs, bipolar and unipolar coding, slaveable reference, and two quadrant multiplication. Prices range from \$85 to \$179 in single quantities.

FAST SETTLING TIMES (1 us)—Settling times as fast as 1 us for current output models and 2 us for voltage output DAC's makes the ZD300 Series useful in a variety of data conversion applications.

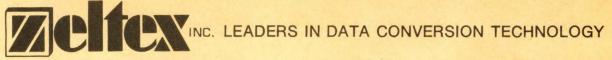
UNIPOLAR, BIPOLAR OUTPUTS—Unipolar and bipolar operation is specified at 0 to ±10V, and ±10V, respectively. Current output models are rated at 0-2 mA. Voltage output models also feature current output capability as well as two-quadrant multiplication-up to 100 kHz.

LINEARITY 0.005%—High performance units feature linearity error of only 0.005% of full scale. Moreover, the extremely low linearity temperature coefficient of only 0.0005%/°C ensures high resolution accuracy.

SMALLEST 14-BIT DAC's—The ZD300 Series modular DAC's measure only 1.96 x 1.76 x 0.40 inches high—less than 1.4 cubic inches in volume. Low profile, DIP pinning compatibility and interchangeability of models make the series highly desirable for OEM usage.

APPLICATIONS—The versatility of the ZD300 Series makes them an excellent choice for use in process-control systems, automated test equipment, servo/synchro/resolver systems, and biomedical instrumentation.

Multiplying capabilities further enhance their use for CRT character generation, digital modulation, and polar-to-rectangular coordinate conversion.



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You can get expert help with your designs if you know where to look

An EDN editor relates his experiences with his supplier's applications engineers and how they helped him with a new small system design.

Robert H. Cushman, Special Features Editor

The process of design often seems like a race against one's stupidity. You may know your goal and have some pretty good initial ideas about how to get there, but as your design progresses, you find there are so many little (and big) details that are just beyond your knowledge and experience. You wonder how you can ever get the product out on schedule and be confident in its performance.

There is one place you can go for help—to the savvy experts among your suppliers. These guys know the details of the components you're using a lot better than you, and they are motivated to help you. It is their job, and besides, many of them have gravitated into their jobs because they enjoy helping people. There are a few who will put you down with a haughty sneer at the pathetic quality of your design, but we found the majority of these fellows bend over backwards to politely and patiently hear your problem out, carefully

examine your circuits, and then freely and ungrudgingly give of their own hard-won expertise. In fact, we've found the more they are truly experts, the more likely they are to want to sincerely help you.

Our case history

To prove our point, we want to give you a running account of the help we've received from specific application engineers—and we'll be naming names—on a design project that seems important to us. This was the digital transducer system shown in breadboard form in **Fig. 1** and circuit diagram form in **Fig. 3**. The basic concept was described briefly at the end of a previous article (**Ref. 1**), and this version's operation is explained in terms of the waveforms in **Fig. 4**. We think it might provide a widely needed building block for industrial systems. It's a self-contained unit complete with its own power supply. It needs

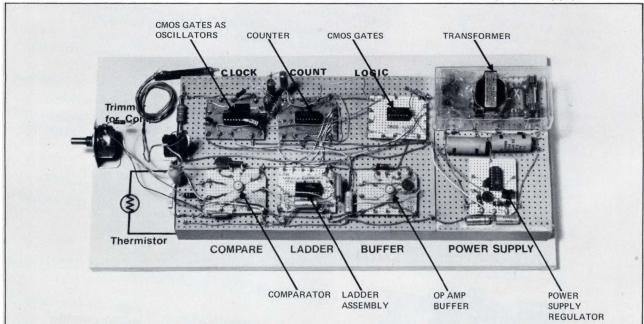


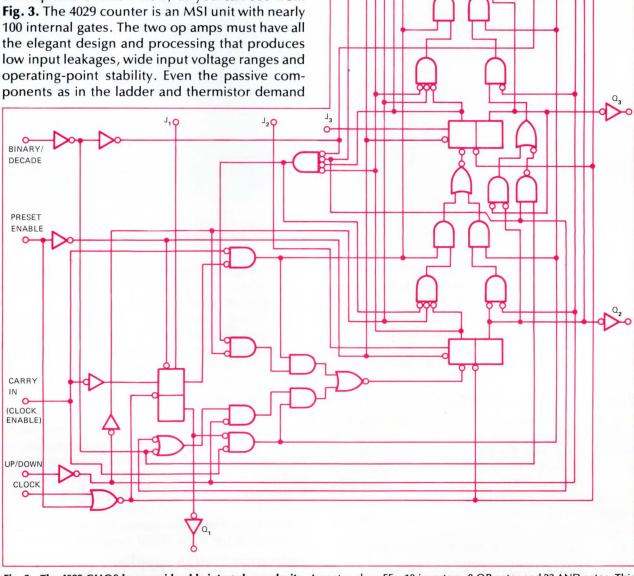
Fig. 1—Breadboard proved that the system would work and that the power supply was adequate. The author likes to first mount all the ICs on small $2\times1-1/2$ -in. carrier boards that bring all the IC terminations out to standard positions, regardless of

the IC pinouts. Then these carriers make the whole poresemble a block diagram which helps troubleshooting and visualization of system improvements.

less than 50 mW power and will provide up to 8 bits resolution (only 4 bits are shown in Fig. 3 to save space). We see it as especially useful for remote monitoring of analog variables over telephone lines.

The complexity of even a seemingly small and simple system like this in these days of high levels of component integration is one reason why you should have your thinking reviewed by outside experts. For example, even though our system consists of just a half dozen or so DIP packages, as can be seen in Fig. 1, each of these DIPs represents a whole little system in itself. There are a total of about 500 transistors in the circuit shown in Fig. 3, and our final design will probably grow another DIP or two to include 1000 transistors.

Furthermore, quite a few different technologies are contained in the half dozen DIPs, each with its own sphere of know-how, as you can see from Fig. 3. The 4029 counter is an MSI unit with nearly 100 internal gates. The two op amps must have all the elegant design and processing that produces low input leakages, wide input voltage ranges and operating-point stability. Even the passive com-



CARRY

Fig. 2—The 4029 CMOS has considerable internal complexity: 4 master-slave FFs, 10 inverters, 8 OR gates and 22 AND gates. This complexity need not concern the user, as long as he obeys the logic and timing rules for using the device. One internal feature that is important for the user for this D/A application is that all the Q outputs are buffered. Buffering helps insure that the 4029 outputs will provide relatively low impedance drives for the D/A ladder.

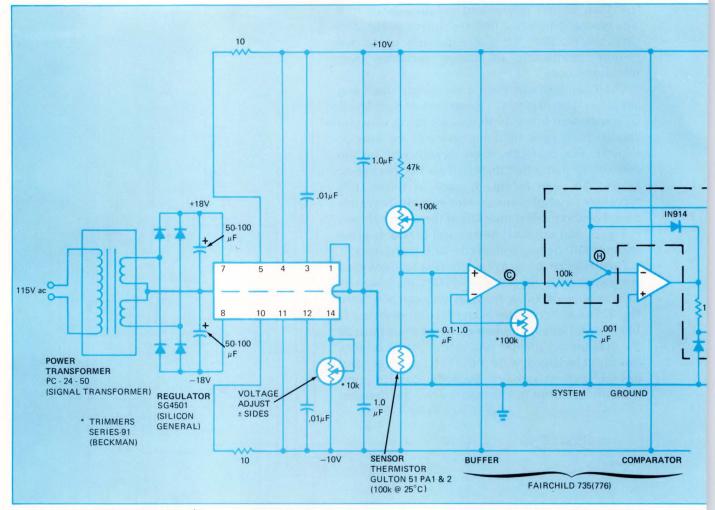


Fig. 3—Complete circuit for the digital temperature transducer: the aim was to produce a self-contained building block that would convert variable-voltage (and therefore also

variable resistance) signals into digital form. The four main sections are shown here left to right, the power supply, the transducer, the A/D and the control.

considerable supplier technical contribution, for they must have accurate and predictable characteristics over a temperature range, and be interchangeable. This circuit places unusual demands on its power supply also, for the power supply must serve as balanced precision reference for the analog "front end" and the D/A converter.

The communication steps

Before launching into a component-bycomponent account of the help we received, I'd like to summarize the procedure I think best for getting the most out of these outside experts:

First, you must break your system down into the regions that will be of interest to various suppliers. You can only expect a given application engineer to feel justified in commenting on those parts of your system of potential sales interest to his employer. He won't mind if you are not using his product in your initial design, as long as it is a "socket" his product could fit into.

Second, you must be willing to show your design to him. If company secrecy prevents you

from doing so, you will end up only talking in generalities and you won't really have him zeroing in on your problem.

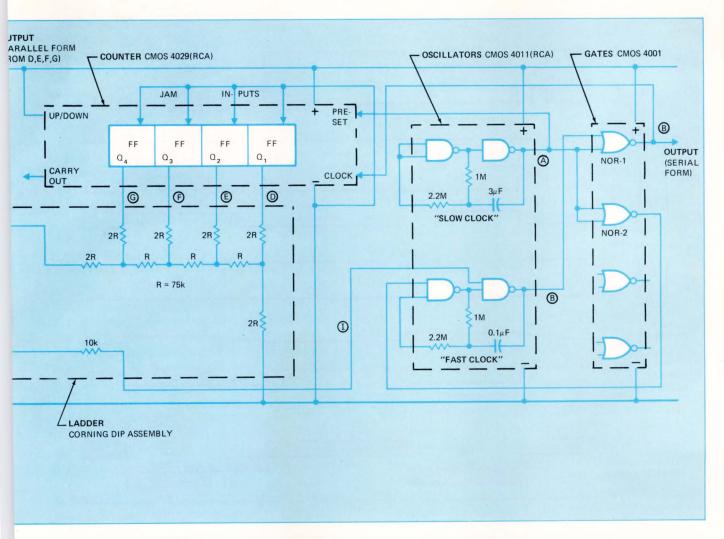
Third, you should listen very, very carefully to everything he tells you. It may seem like he is rambling, but later, if you've taken careful notes (and I personally like to tape record their verbal comments), you'll find out he was, in fact, giving you pearls of wisdom that you just did not understand or appreciate.

Finally, you should not be upset if the backand-forth communication takes time, because in the end, it will save you embarrassment, and your company's money will be catapulted towards dependable perfection much quicker. It is a matter of taking *time* to save *time*.

Now for some of the comments we sought out and received in each of our component areas.

Help from CMOS supplier

The D/A calls for a CMOS counter to also serve as the analog switches that feed the 0 and 10V precision reference voltages to the ladder. The concept is to put the clean rail-to-rail output



switching of CMOS logic gates to work and thus simplify the system. We chose the 4029 counter because of its flexibility, versatility and expandability. It is a 4-bit "slice" of a counter that can be "programmed" by logic levels to count up or down and to count in binary or BCD (**Fig. 2**). There are carry-in and carry-out terminals that permit 4029s to be cascaded for higher counts. It even has presetting inputs that, when tied to ground (as in **Fig. 3**), can be used to reset the counter via a preset-enable input.

Our first concern about the 4029 was whether it was the best choice among the growing number of counters in the 4000 family. Dick Funk of RCA, after reviewing our design and our statement of its purpose, assured us the 4029 was an acceptable choice, but that for our information there were others. He suggested longer counters like the 7-bit 4024 or the 12-bit 4040 (which would give spare counts we might use for timing purposes). He shot down our thought that we might use the 4017 because, he explained, internally it uses Johnson coding and that would be incompatible with our standard binary weighted ladder—to his knowledge nobody was using Johnson counters in D/As.

Dick agreed that we could use the Motorola 4510 and 4516s, which are specialized versions of the 4029 with essentially the same pinouts. (We've found we can sometimes plug them into a 4029 socket.)

Our next concern was whether the 4029s' output resistances would be low enough to accurately drive the ladder. The specifications and our measurements have shown this to be under 1k and satisfactory for our high-resistance (R=75k) ladder, but what safety margin would our design have over all the variables of alternate counter types, production spreads, operating voltage changes, temperature, etc.? It had come to our attention that Otto Schade of RCA had written an application note (Ref. 2) on just that subject, so we showed him a copy of our design. Otto set our mind at ease saying that he had found little difficulty in building up 8-bit systems of this sort with 1% metal-film resistors selected from the parts bin. He also opinioned that he thought we might be overly concerned about tempco's, unless we intended our system for the full military temperature range.

Dick Funk came along with the best suggestion of all, and one we plan to try as soon as we can.

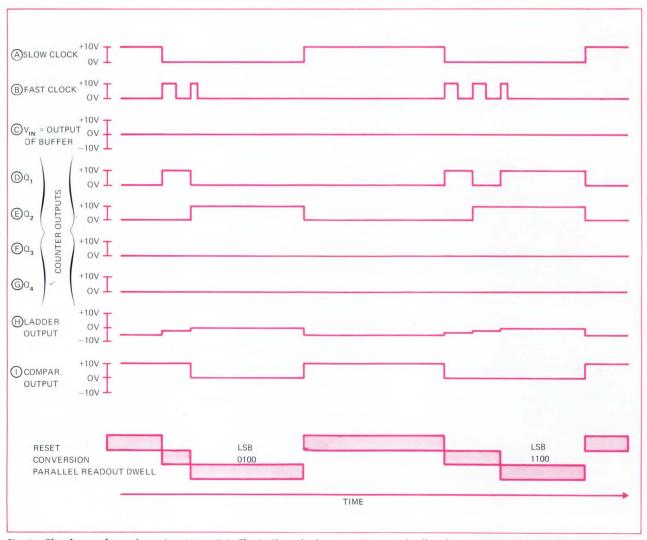


Fig. 4—Circuit waveforms for points (A) to (I) in Fig. 3. Slow clock output (A) periodically releases 4029 counter and starts fast clock whose buffered output (B) causes counter to count up. Outputs of counter (D,E,F,G) drive ladder positive, and when ladder counteracts unknown negative input (C) and pulls comparator input (H) up through system ground, comparator flips and its output (I) stops the fast clock. Counter holds state until slow clock output (A) again resets it.

"If you are worried about the fact that regular CMOS devices don't have as low an output resistance in their ON state as you'd like, why don't you drive the ladder via our recently announced 4041 quad buffer. This has both true and complementary outputs, and the true outputs are made with large area, symmetrical inverters that have specified ON resistances of 100Ω , and typically run as low as 50Ω at the 10V of your circuits."

The more we think about this option, the more we're glad it is there in case we need it. We can use it in two ways. We could lower the ladder resistance from our present R=75k to, say 10k, and impose less of a performance penalty on our comparator op amp (as will be discussed later on) and have faster conversion speed. Or we could use it to raise the accuracy margin of our present system and possibly extend our system to 9 or 10 bits.

We don't think we will want to be too hasty to

lower our ladder resistance because that would raise the power consumption of our system, and this is one of its unique attributes—the basic A/D needs only 10mW. However, we definitely want to evaluate whether we can raise our accuracy reserve. A side benefit from using the 4041 will come when the application calls for taking the output off the counter in parallel. Then we can use the complementary outputs from the 4041 for added logical flexibility in driving readouts. Some of the BCD-to-7-segment display drivers we've been looking at need this signal inversion, and it would be nice to know that our display drive was not loading our ladder switches. In other situations, we can use the availability of both true and complementary outputs to simplify following decoding logic.

Help from ladder supplier

We really got concrete help from Corning. It was in the most tangible form of three custom DIP

Table Cost comparisons: discretes vs. DIP assembly (Corning AO 1276 for EDN)									
Quantity	100	pieces	1000 p	pieces					
Approach	Discrete	DIP	Discrete	DIP					
Components		100 @ \$3.15 = \$315.		1k @ \$1.70=\$1700					
R-NA55	1200 @ \$0.11 = \$132		12k @ \$0.088 = \$1056						
D-IN914	200 @ \$0.17 = \$ 34		2k @ \$0.10 = \$200						
Insertion cost	1400 @ \$0.06 = \$ 84	100 @ \$0.06 = \$6	14k @ \$0.06 = \$840	1k @ \$0.06 = \$60					
Total cost	\$250	\$321	\$2096	\$1760					
Savings?	DIP costs \$7	1 or 28% more	DIP saves \$3	36 or 16% less					

ladder assemblies for 4, 6 and 8 bits (**Fig. 5**). We got involved with Corning because we had been using their RN55D 1% metal-oxide film resistors in our earliest discrete-resistor ladders. Mark Slifkin of Corning, who is on the marketing side, but quite knowledgeable, and who has a team of application design engineers, told us that Corning's investigations had shown that 1% metal-film resistors with 100 ppm/°C tempcos would handle 8-bit ladders "without any problem," but that "they just can't make it at 10 bits."

Mark then asked us whether we had considered using these resistors assembled in Corning's CORDIPtm component network packages and volunteered to make up several samples to our designs. We jumped at this opportunity because it had been one of our goals all along to put as much of the circuitry as possible into DIPs. Though it might be said that Mark did this only because he knew his product would be getting publicity in this article, we got the distinct impression that Corning will make up custom samples without charge for any designer whose plans seem serious. Corning seems very proud of the way they can turn out these custom CORDIPs on short notice.

When you go custom, you owe it to yourself to learn as much as you can about your supplier's method of fabrication. Then you can optimize your use of his technology. In the case of this component, we had the opportunity to visit "our supplier" at his factory and let him show us his technology firsthand. Mark first showed us Corning's discrete resistor production line, and then took us step by step through the process by which they design and assemble their resistors and other components (note in Fig. 3 and 5 that there are also diodes in our 4-bit network and, in addition, Corning can put their ceramic and small tantalum capacitors into DIPs). We saw for ourselves how Corning's investment in assembly automation for these packages could provide the savings that Mark had calculated for our design at the 1000-piece level (see table in **Fig. 5**).

Just like it helps communication if the expert sees your circuit, so it helps if you can see his process.

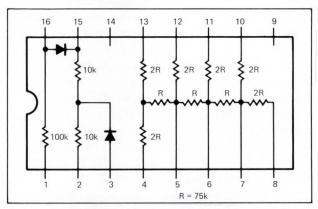


Fig. 5—The Corning custom DIP network contains a 4-bit ladder and some neighboring circuitry (note the diodes). A feature of this configuration that was appreciated later was the fact that so many of the network nodes are brought out to pins. This made it easy to investigate design modifications during breadboard phase and yet have a compact DIP package. It suggested flexible patterns for the final production DIPs.

Corning is of course not the only supplier making custom DIP networks. Sprague, Dale, Allen-Bradley, Beckman, Centralab, CTS, Mepco, etc., are among the many others (**Ref. 3**). What we like about Corning at this initial stage of our product's evolution is that their construction has such quick turnaround and allows such easy changes.

Corning puts small discrete components into a "Swiss Cheese" matrix (like pegs in a pegboard) and then machine molds a plastic case around them. Your final part looks like any other DIP, but as it is composed of discrete components, you can readily ask for changes in component values. Further, Corning's clever manner of printing the cured-silver plastic conductive patterns (not epoxy) on the top and bottom surfaces of the Swiss Cheese matrix allows them to make up new circuit interconnections in hours.

The one question a designer always has about getting help from a supplier is, "how long does it take?" We sent Corning the circuit diagram of **Fig. 3**, in which we indicated the discrete components we wanted in the 4-bit CORDIP by the dashed line. Within two weeks they sent back the quote that specified their CORDIP, of which the circuit and pricing is shown in **Fig. 5**. Then after we gave

them the go-ahead, they delivered the samples within two to three weeks.

When we put the DIP networks into our breadboard, we found out that through a mistake on our part, we had called for a 1R rather than a 2R resistor in the final tail of the ladder going to the comparator. We called Mark on this, and he told us it could be fixed by simply dropping the correct resistance value in the next lot. Meanwhile, we began to find some other unexpected virtues in our supplier's product. The Corning method of construction forces them to bring many circuit nodes out to DIP pins. At first, this struck us as not too elegant, but we soon found it very helpful. For example, as will be described more fully later in the section on the comparator, we began to have doubts as to the utility of the clamping diode in the comparator op amp's feedback. Because of the access to circuit nodes, we found it easy to wire this out of the circuit. We've found similar unexpected flexibility in using the 6- and 8-bit ladder DIPs.

The point is sometimes there is no communication quite like that which you get from the supplier's product itself. There may be designers so smart they never need this interchange, but one doubts it.

Help on ladder details

So far as the ladder circuitry itself was concerned, we had a little fallout of luck in going to the DIP assembly. We had originally planned to use an R=100k ladder to be sure of not loading the 4029. But Mark told us Corning could not make the miniature resistors that would fit into their CORDIPs with values greater than 150k—there just wasn't room to machine the long, fine spirals needed to make higher values. This meant we could not use an R greater than 75k.

But this limitation has turned out to be a blessing, because it now appears that R=75k is about as high in ladder resistance as you can go before you ask too much of a moderately priced comparator and also start to limit your system operating speed. At R=75k, you are at the upper limit of source resistance that any bipolar op amp should be looking at, and you are getting into the region where you ought to be using one of the more expensive FET-input op amps.

Another happy accident was that in drawing our dashed line (Fig. 3) to show what the DIP should include, we had encircled the 100k resistor on the unknown input side of the comparator input. We now see that it makes a lot of sense to have this resistor also "locked" into the DIP because then it is certain that this resistor will be made by the same factory and by the same process that made the rest or the ladder, and so this whole analog

network that governs the system accuracy will temperature-track better. We asked Mark about our thinking here, and he agreed. He said that while the resistors themselves were individually good to just 100 ppm/°C tempcos, they would track each other to 50 ppm/°C. Now we are thinking that perhaps we ought to try to make this resistor on the negative side, 3R or 225k, to match the value seen looking back into the ladder, and thus obtain even better tracking. However, as we mentionned earlier, Otto Schade of RCA said he thought we were letting ourselves become overly concerned with tempcos, for after all, we are only shooting for 8 bits resolution.

More to come

In a following issue we'll go on to the comparator, buffer, power supply and thermistor sensing sections of the design. We'll tell you of the help we've received from experts at Silicon General, Victory Engineering, Signal Transformer and other companies.

But by now you should be at least partially convinced that there is expert help out there; you only have to go after it in the right manner. Consider what it has done for our humble design. Just think how much more uncertain we'd be about it now if we had not had it looked over and commented upon by these experts.

Why go it alone when such help exists? □

Any comments other suppliers or readers may have about the process of obtaining help from experts or about this design example will be greatly appreciated. We would like to work such comments into future parts of this "rambling saga of a design's progress."

References

1. "Elementary A/D Converters Can Be Efficiently Implemented in CMOS," Cushman, R.H., EDN, July 15, 1972, pg. 26. Fig. 3 introduces the A/D circuit that is further developed in this article. (The final 2R resistor in that figure should be labeled 50k rather than 100k, and there are a number of places where signal lines at the op amp input were mistakenly shown shorted to ground.)

2. "Digital-to-Analog Conversion Using the RCA-CD4007A COS/MOS IC," (ICAN-6080), available from RCA Solid State, Somerville, NJ (or in RCA Databook SSD-203A). It contains specific data on selection of resistors for CMOS-driven ladders up to 9 bits. Also described is a novel voltage-follower op amp that operates with a wide swing off a single supply. It is claimed that conversions as fast as 250 nsec (settling time) are possible.

3, "Dip Resistor Networks Can Save You Time and Money," Slifkin, M. A., available as reprint from author Slifkin at Corning Glass Works, Corning, NY 14830. Presents general arguments for assembling discrete components into custom DIPs, as arguments apply to all makes of passive DIPs, not just Corning's.

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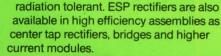
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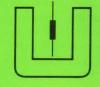
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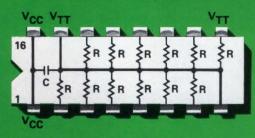


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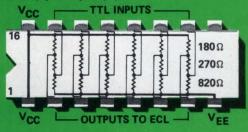
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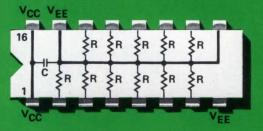
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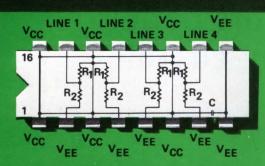


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Getting to know capacitors better with a simple C/V display circuit

Here's a circuit that allows you to display capacitance vs. voltage interactions on an oscilloscope.

Jerald Graeme, Burr-Brown Research Corp.

If you are dealing with a capacitance that is voltage sensitive, the output voltage waveshape displays a capacitance vs. voltage (C/V) response. By being able to display this response readily on an oscilloscope, one can examine varactor diodes, measure voltage sensitivities of fixed capacitors or monitor MOS oxide purity.

For example, representative C/V responses for these three applications are illustrated in **Fig. 1**. From the varactor response, the capacitance variation that will be produced by a given drive voltage can be seen. The C/V display for a fixed capacitor permits prediction of filter detuning by signal voltage, and also prediction of the resultant distortion. The third C/V response illustrated is representative of MOS capacitors used in monitoring monolithic fabrication processes. Ions in the MOS oxide are detected by observing bias induced shifts in the transition point of this response.

Three op amps do the trick

Low-frequency capacitance measurement with a capacitance vs. voltage display is provided by a three op-amp circuit. A measure of fixed capacitance is provided by the dc output voltage from the circuit.

To make both the capacitance measurement and the C/V display, only a ramp generator and a differentiator are needed, as in **Fig. 2**, followed by a dc voltmeter or an oscilloscope.

When the capacitance to be measured is inserted as the differentiating capacitor, an output voltage is developed that is a measure of the capacitance. This output voltage is

$$e_0 = -RC \frac{de_r}{dt}$$

Since the slope of the ramp, de_r/dt , is a constant other than during reset, the above voltage is directly proportional to the capacitance C. If C is independent

of voltage, the output is a dc level interrupted only by short reset pulses. A dc voltmeter then serves as a capacitance readout. If C is voltage sensitive, the output voltage will vary as the ramp changes the capacitor voltage. The ramp produces a linear rate-of-change in capacitor voltage, so the output wave-shape is linearly related to the C/V response.

Using two op amps to generate the ramp, the complete circuit can be realized as in Fig. 3. A gain-

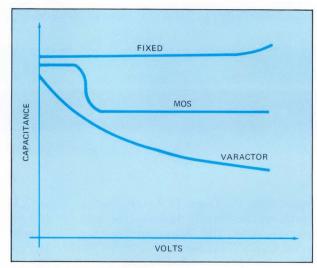


Fig. 1—C/V displays of varactor responses, voltage sensitivities of fixed capacitors and MOS C/V response shifts caused by oxide ions.

limiting resistor, R₁, is added to the differentiator shown to control transient response. This resistor is chosen to eliminate ringing following reset transients when measuring the lowest intended capacitance. To generate the ramp, a comparator with hysteresis is connected as feedback to an integrator. When the comparator output is positive, positive feedback thru

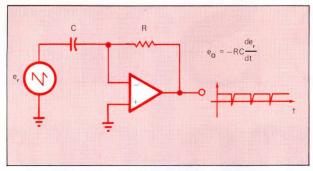


Fig. 2—Response of a differentiator to a ramp input voltage is linearly related to the capacitor since the slope of the ramp is essentially constant.

 R_5 shifts the trip point. This sets the ramp voltage peak at

$$e_{rP} = -\frac{R_4}{R_5} V_Z$$

where V_z is the zener voltage. However, when the comparator output is negative, D_2 disconnects the positive feedback, and the integrator output alone causes switching when it crosses zero. The result is a unipolar ramp, as is sometimes convenient for varactor diodes where one ramp polarity would simply forward bias the diode. For a bipolar ramp, D_2 is not used.

The frequency of the ramp is set by the zener level and the integrator response. By means of D_1 and R_3 , the time duration of the positive output integration is made much shorter than that of the negative integration. This produces a ramp instead of a triangle wave, and makes the signal frequency es-

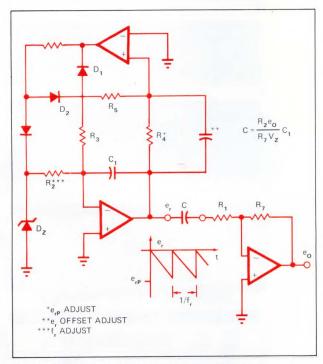


Fig. 3—Only three op amps are needed to form this capacitance measuring, C/V response-generating circuit.

sentially determined by the slower integration. In developing the greatest difference in integration times, a time delay produces an amplitude error that must be corrected by selecting a bypass capacitor for R_4 . When this is done, the frequency of the ramp train will be

$$\mathfrak{f}_{\mathrm{r}} = \left(\frac{\mathsf{R}_{5}}{\mathsf{R}_{4}}\right) \left(\frac{1}{\mathsf{R}_{2}\mathsf{C}_{1}}\right)$$

From this frequency and the previously defined amplitude e_{rp}, the slope of the ramp is

$$\frac{\Delta e_r}{\Delta t} = \frac{e_{rP}}{1/f_r} = \frac{V_Z}{R_2 C_1}$$

The resulting differentiator output, e_0 , defines the capacitance by

$$C = \frac{R_2 e_0}{R_7 V_Z} C_1$$

Errors associated with this circuit are primarily due to the dc errors of the differentiator and the response limitations of all three op amps. During the reset time of the ramp, the differentiator output is momentarily interrupted. This reduces the average output voltage measured by a voltmeter readout, unless this interruption due to amplifier response limits is a negligible fraction of the ramp period. A low-frequency ramp reduces this error but simultaneously makes dc errors greater. With general purpose op amps, a 100-Hz ramp frequency is a good compromise for errors limited to 1%.

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Author's biography

Jerald G. Graeme is manager, monolithic engineering, at Burr-Brown Research Corp., Tucson, AZ. Jerry received his BSEE from the Univ. of Arizona and his MSEE from Stanford Univ. He joined Burr-Brown in 1965 as an amp circuit design-



er. Since that time he has developed bipolar, FET and chopper stabilized op amps, as well as instrumentation amplifiers. He now directs the development of monolithic analog and analog/ digital circuits. During this time, he has had two patents issued to him and has been a frequent contributor of circuit design material to electronic trade publications.

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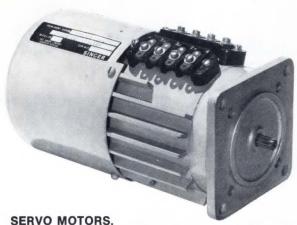


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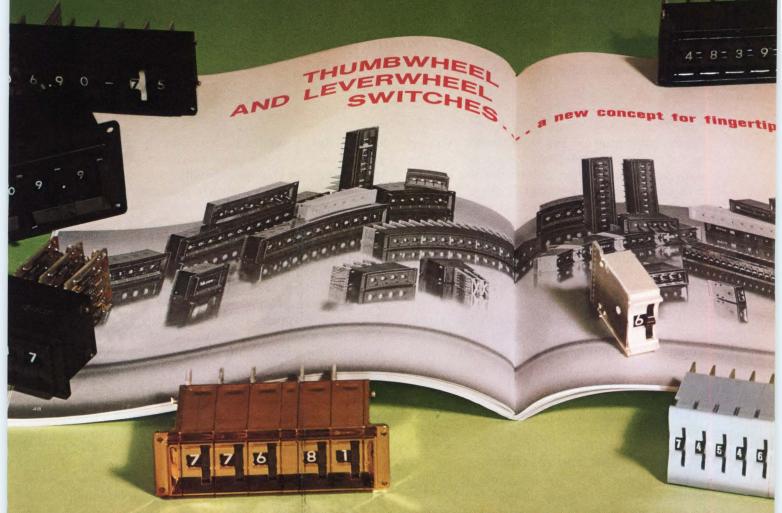
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Designers Guide to:

Fractional-horsepower motors (Part 1)

There's more than meets the eye to choosing the right motor for a given application. Here's a rundown on the things to consider.

Engineering Staff, Bodine Electric Co.

Proper application of any small motor requires careful preliminary selection considerations. The factor which most often determines the success or failure of the motor-driven device is the initial care exercised in matching load characteristics of the machine to be driven with performance characteristics of the motor to be used as the driving member. A motor too large or too complex is unnecessarily expensive, while a motor too small might fail to drive the load under abnormal operating conditions.

The characteristic chart shown in **Fig. 1** provides a good preliminary guide for selecting the proper type of motor for a given set of application characteristics. Many other factors though must also be taken into consideration before the final selection is made. The most important are:

- · Matching motor to machine
- Cost

Matching motor and machine

The first step in motor selection is to fully define the characteristics of the device to be driven as they relate to the motor parameters.

Power source: The power available for a motor is, in most cases, a fixed factor. Hence, it must be known if ac or dc current is to be used and the line voltage or voltages available. Furthermore, if the source of power is ac, the frequency and phase must also be known. Sometimes there is a choice of currents and voltages; and in other cases, unusual or fluctuating voltages or frequencies must be contended with. It is essential, therefore, that the power source be fully defined and understood before proceeding with the next step in the solution of the problem.

Machine: An analysis of the requirements of the machine to be driven must precede any consideration of the various types of motors.

One way of doing this is to start by preparing a detailed chart listing in sequence the various operations of the machine. In some cases, there would be only one operation—i.e., a shaft turning at constant speed under a steady load; in

other cases, it would be a constantly changing pattern with no cycle quite the same; in others, there would be changing conditions of, say, load and speed, but a definite cycle as to stopping and starting. In many applications, reversing would be required. The variables making up the operational pattern would be direction of rotation, speed, load, length of the operation, continuous or intermittent duty and mechanical factors.

Speed: Here it should be known if it is desirable that speed remain constant with load or if a variable speed is desirable or permissible. Also, the amount of variation allowable and the necessity for adjusting the speed while the motor is running or while standing still should be determined.

Direction of rotation: Is unidirectional operation sufficient or is reversing required? And if reversing is necessary, must it be while the motor is running or standing still?

Horsepower: What horsepower is required to operate the machine? In what manner is the power to be applied? Will the torque required be maximum during starting, running or acceleration? Will the torque applied to the shaft stay constant or will it fluctuate at different points during the operation of the machine? Will the machine run at constant speed or will it be required to run at different speeds under varying conditions of operation?

One of the most important considerations in regard to the above is an analysis of the starting conditions. In some machines, the initial load is primarily one of friction: as speed increases, the power requirements increase rapidly. In other applications the motor is brought up to speed before a load is imposed. Here, friction and inertia comprise the initial load. If the motor is of sufficient capacity, no trouble is likely to be encountered in meeting the requirements of the working load.

Some applications require a motor that is equipped to handle a heavy starting torque. The heaviest demands on the motor are therefore during starting and acceleration. After full speed of the rotating member is achieved, less power is needed to keep the unit in operation.

Length of the duty cycle: Is the machine to be operated continuously over long periods of time

without shutdown? Or is the machine to be operated intermittently?

Mechanical factors: Mechanical factors to be considered are: space available for the motor, weight limitations, coupling requirements,

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		SPLIT PHASE	SHUNT OR COMPOUND	SHADED POLE	POLYPHASE	SYNCHRONOUS SPLIT PHASE	SYNCHRONOUS POLYPHASE	SYNCHRONOUS CAPACITOR.START	SYNCHRONOUS PERMANENT CAPACITOR SPLIT	SYNCHRONOUS 2 VALUE CAPACITOR	HYSTERESIS SYNCHRONOUS PERMANENT SPLIT CAPACITOR	CAPACITOR START	PERMANENT SPLIT CAPACITOR	2 VALUE CAPACITOR	SERIES 2 LEAD	SERIES 4 LEAD	SERIES SPLIT FIELD	SERIES GOVERNOR	2 PHASE SERVO
	A.C.	•		•	•	•	•	•	•	•	•	•	•	•					•
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	A.C. OR D.C.			70											•	•	•	•	
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	LOW			•					•		•		•			4 14			•
	NORMAL	•	•		•	•	•	3		3		3		3					
	нідн													,	•	•	•	•	

Fig. 1—The characteristics of a particular motor type determine whether or not it is suited for a specific application. The most important motor characteristics are shown here as they relate to specific motor types.

mounting requirements, changeability of position, exposure, climatic and atmospheric conditions, ambient air temperature surrounding the motor, requirements as to quietness of operation, provisions for lubrication, end thrust on shaft, restriction of shaft end play, vibration of machine or effect of motor vibration on functioning of machine, safety requirements and other mechanical requirements such as terminal leads, connectors, cords, plugs and switches.

Motor drive: Once the power source and driven machine have been fully defined, it is time to investigate the types of motors available and their characteristics. The broad categories to consider are ac vs. dc, speed reducers vs. nonspeed reducers and constant speed vs. variable speed motors.

In all cases, a motor must be selected with sufficient capacity to do the job without overloading it. Overloading may result in the motor being heated beyond safe limits. The safe allowable temperature rise, of course, varies depending upon whether the motor is an open frame or a totally enclosed motor. Excessive motor temperature also plays havoc with good lubrication causing friction and shorter life.

Where a reduction in speed is needed, built-in speed reducer motors should be considered. By combining the motor and speed reducer in one unit, all cumbersome and complicated speed reducer transmissions are eliminated. In selecting the proper gear ratio, due regard to the strength of the gears and their adaptability for the application intended must be carefully considered. Where conditions specify and strength warrants, laminated or molded nonmetalic gears may be used to achieve quietness in operation.

Cost considerations are subtle

The cost considerations involved in selecting small motors are extremely important, particularly because of the subtle nature of many of them. Purchase price is the first, but by no means the only, cost. Frequently it is a lesser factor than some of the associated costs. Some important economic considerations in motor selection are:

- Life expectancy
- Associated components required
- Installation cost
- Cost of rejections
- Cost of incorrect application
- Cost of servicing
- Purchasing policy
- Special motor features

Life expectancy: Some machines are expected to operate quite continuously for a number of years with very little servicing. Others operate infrequently and are used for a rather limited

time. An example of the former would be the motor for an on-line chart recorder, while the latter could be the motor for a one-shot experimental set-up. You would pay top prices for a motor you could expect to give a number of trouble-free years of service in a chart recorder, but it would be poor economy to invest in any other but a low priced unit for an experimental set-up which will be discarded after operating only a few months.

Associated components: Adapting a motor to a machine always requires the use of one or more other components such as a coupling, belt and pulleys, chain and sprockets, gears, brake, clutch, mechanical or electrical speed control device, limit switch, etc. These components are obtainable from different sources. Most of them are also obtainable as part of the motor drive. Some of these components represent a considerable initial investment. Whether to purchase a motor which incorporates them within itself or to purchase separate units requires a comparison of the following factors:

- 1. Package price vs. sum of prices of separate components.
- 2. Cost of one purchase order vs. several.
- 3. Assembly cost of package vs. assembly and adjustment of individual components.
- 4. Guaranteed performance of one unit vs. a number of components.
- 5. Cost of designing around a standard package vs. flexibility of combining individual components to best advantage.

The decision on these points may be dictated by other than economic factors; but when economic factors predominate, the number of machines produced usually enters into the consideration. Economy generally favors a packaged unit where production quantities are limited, and separate components where the quantities are sufficient to support a single-purpose design. In the latter case, of course, the manufacturer of the end item must possess the required engineering and specialized manufacturing facilities.

Installation cost: The time and precision required to install a packaged unit is usually insignificant compared with assembling and adjusting components. Frequently separate components mean extra precision machine work on parts of the driven machine, as well as extra supporting parts such as castings, brackets, bearings, shafts, gears, enclosures, etc. Special motor features, such as special shaft extensions, flanged end shields and special machined surfaces, often reduce installation time. Usually, however, it is less costly to adapt to standard motor features where only a few machines are to be built.

						ARAMETERS AD AMEPLATE DEVI		pul true								
					7	se male	STARTING DEVICES									
NAMEPLATE RATINGS	TORQUE	SPEED	TEMPERATURE	NOISE	VIBRATION	THERMAL O/L PROTECTORS	CURRENT SENSITIVE	CENTRIFUGAL CUT - OUTS	CAPACITOR LIFE	MOTOR LIFE						
VOLTAGE	•	•	•	•	•	•	•		•	•						
FREQUENCY	•	•	•	•	•	•	•	•	•	•						
TORQUE (HP, RPM)	•	•	•	•	•	•	•			•						
TEMPERATURE RISE	•	•	•	2		•	•			•						
CAPACITOR	•	•	•	•	•	•			•	•						
DUTY			•			•	•	•	•	•						

Fig. 2—Deviation from the nameplate ratings of a motor can seriously degrade the motor's operating performance.

Rejections: Rejections of delivered motors can be caused by marginal performance in matching motor to machine, by critical tolerances on dimensions, by vibration, noise, etc., or by loose production control of motors or machine elements, or both. Rejections can be more costly than one might expect and can seriously reduce the profits from the final product.

Application: As noted previously, faulty or marginal application of motors can result in costly rejections. It also results in more fussiness than is justified in the assembly and adjustment of the driven machine. This affects the labor component of cost, and reduces efficiency and output. Probably the most serious effect of misapplication is the cost of servicing the end product on location. The greatest insurance against this is to take a little extra time getting into full production by first producing a pilot run of machines for field testing.

Servicing: Cost of servicing is reduced by thorough and conservative motor application engineering, by selection of all components (including the motor) so that they meet expectations of durability and reliability, and by motor and machine design which accommodates replacement and adjustment of components. For instance, commutator-type motors should be so located in the machine that brushes can be replaced without removing the motor or any of the other components. When standard motors are used, location of service stations affects service cost. Special motors usually require factory attention. Service cost is reduced in the case of special motors, and time is saved, by including extra motors with quantity production orders.

Purchase policy: Purchasing policy must of course be tuned to the particular nature of a business. It is difficult, especially in a large operation, to maintain adequate control of the purchasing function, yet keep it flexible enough

to take advantage of the savings obtainable through most effective use of the supplier's facilities. Good sales forecasting and inventory control can result in considerable savings. Where the nature of a business permits setting a production pattern, it should be integrated into the motor supplier's pricing structure to obtain the most favorable motor price. Where special motors are used, it is poor economy to risk running out of stock. On the other hand, it is false economy to overorder just to fit into a price bracket. The planning of motor purchases should also include an allowance for the motor manufacturer's lead time. In addition, consideration should be given to the economy of including the quantity of motors required for service replacements in the production run order.

Special motor features: For the purpose of this discussion, a special motor is defined as one which embodies components the supplier does not manufacture for use in his standard units, or one whose components are assembled in an other than standard orientation. Such a motor may incorporate only a very minor special feature, or it can be entirely special as to basic design. Somewhere between these two extremes lies the most economical special motor for a particular machine application. In some instances, a standard motor may prove to be the most economical, even for a special application.

When should special motor features be considered? The premium cost of producing special motors varies with the quantity of the production run and with the degree of off-standard modifications. Economically, the added cost of a special motor over that of a standard motor should be considered only where the additional cost is offset by associated economies, such as time savings or elimination of additional machine operations. A feature which is considered a minor modification by one motor manufacturer could be considered a major one by another. It all

depends on the nature of his facilities.

The following modifications are sufficiently common to be considered minor by those motor manufacturers who regularly produce special motors. They can usually be produced in minimum quantities somewhere between ten and one hundred.

- 1. Special winding
 - for increased or reduced output for odd voltage
 - for special performance characteristics
- 2. Special rotors to modify performance (limited)
- 3. Special shaft extensions
 - diameter
 - length
 - special material
 - hardened
 - drilled and/or tapped
 - · milled flat
 - keyway (rectangular or woodruff)
 - · groove or shoulder
 - threaded
 - pinion or spline (using available tools)
 - tapered
 - · square or tongue
 - worm or spiral groove
- 4. Special terminal leads
 - number
 - length
 - material
 - cord sets with or without plugs and switches
 - · with eyelet or spade terminals
 - · terminal box
- 5. Off-standard bearings (including end thrust provisions)
- 6. Special end shields (designs already tooled and produced in limited quantities)
- 7. Special stator housings (designs already tooled and produced in limited quantities)
- 8. Special mounting features and bases (designs already tooled and produced in limited quantities)
- Special machine work on motor frames (operations which utilize existing tools and gauges)
- 10. Special oilers, seals and hardware (only items readily available and practical to assemble)
- 11. Thermal overload protection
- 12. Special lubricant (limited)
- 13. Dynamic balance to special limits

Major modifications are those which usually require extensive engineering and/or tooling or those which require manufacturing facilities that lend themselves to only large quantity production. Major modifications are generally economically practical for the machine manufacturer as well as the motor supplier only when production quantities are in multiples of 1000. A customer should expect to pay tooling charges for major modifications. When the special features involve basic motor design (requiring special lamination punching dies or casting dies), it is generally advisable to anticipate ordering quantities of the nature of 10,000, and assure steady production for at least several years.

Some special motor features which are generally considered major modifications are:

- 1. Special rotors (involving special tooling and/or extensive engineering)
- 2. Special end shields not produced before
- 3. Special stator housings not produced before
- 4. Special mounting features and bases not produced before
- 5. Special machine work requiring new tools, fixtures and gauges
- 6. Some forms of climatic and/or high-voltage protection
- 7. Special finishes
- 8. Special gear ratios
- 9. Completely new special purpose motor frame
- 10. New basic motor design

Importance of nameplate ratings

A primary purpose in placing a nameplate on a motor is to convey sufficient information to the user to assure that it will be applied and operated in accordance with its design criteria. In general, the criteria used by reputable motor manufacturers is based upon established motor industry standards and recognized safety standards.

Satisfactory running performance of a motor will be achieved within the normal expected line variations of $\pm 10\%$ of nameplate voltage and $\pm 5\%$ of nameplate frequency even though other nameplate values may not be met. The hazards of operating motors at other than nameplate values do not apply with the normal variations in voltage and frequency, but rather to abnormal values such as those created by uncondoned and deliberate deviations taken in adapting a motor to an application. (**Fig. 2**).

Original equipment manufacturers can avoid such hazards by seeking the assistance of the motor manufacturer.

The purpose of the following paragraphs is to create an awareness of the effects of deviations from nameplate ratings on the performance of both alternating and direct current motors. Judging from problems commonly encountered, some of these effects are often overlooked by design engineers.

HORSEPOWER		WATTS				FULL LOAD TORQUES IN. OZ.					
FRACTION	DECIMAL	1-1/2 LOAD	FULL LOAD	3/4 LOAD	1/2 LOAD	1125 RPM	1725 RPM	3450 RPM	4000 RPM	5000 RPM	6000 RPM
1/200	.005	5.59	3.73	2.79	1.86	4.47	2.91	1.45	1.26	1.01	0.85
1/100	.010	11.19	7.46	5.59	3.73	8.96	5.84	2.92	2.52	2.01	1.71
1/90	.011	12.42	8.28	6.21	4.14	9.96	6.49	3.24	2.80	2.24	1.90
1/80	.012	13.98	9.32	6.99	4.66	11.19	7.29	3.64	3.15	2.52	2.13
1/70	.014	15.97	10.65	7.98	5.32	12.80	8.35	4.17	3.60	2.88	2.44
1/60	.016	18.63	12.42	9.31	6.21	14.94	9.74	4.87	4.30	3.32	2.85
1/50	.020	22.38	14.92	11.19	7.46	17.93	11.69	5.84	5.04	4.02	3.42
1/40	.025	27.97	18.65	13.98	9.32	22.38	14.61	7.30	6.30	5.04	4.27
1/30	.033	37.29	24.86	18.64	12.43	29.88	19.47	9.73	8.60	6.65	5.70
1/20	.050	55.95	37.30	27.97	18.65	44.82	29.18	13.61	12.60	10.08	8.55
1/10	.100	111.90	74.60	55.95	37.30	89.64	58.46	29.23	25.21	20.17	17.10

Fig. 3—Torque and wattage ratings are shown for various horsepower motors.

Do not operate motors at other than $\pm 10\%$ of nameplate voltage. Higher voltages produce adverse effects on motor temperature, noise, vibration, operation of current-sensitive starting relays, motor life and capacitor life, and could result in nuisance operation of thermal overload protectors. Lower voltages create starting problems with current-sensitive starting relays and could cause thermal overload protectors having internal heater coils to operate at winding temperatures which exceed the maximum allowable limits as specified in U.L. Standard #547.

Do not operate motors on nominal power source frequencies other than specified on the nameplate! With the exception of brush type motors, it is a well recognized fact that the motor speed will vary directly with frequency. The use of a motor on more than one frequency is an understandable goal of original equipment design engineers. Often overlooked, however, is that a change in operating speed will have an adverse effect on the operation of centrifugal cutout switches (when present).

At high frequencies, output torque is reduced, current-sensitive starting relays may fail to engage the auxiliary winding, capacitor voltage ratings may be exceeded (resulting in shorter capacitor life) and fan noise may become objectionable. Additionally, the amount of noise and vibration emanating from a motor will generally change directly with frequency. These changes could create undesirable levels of noise and vibration if the frequency is coincident with the natural resonant frequency of the mounting surface, or enclosure, or falls within a more sensitive hearing range of the human ear.

At lower frequencies, motor winding temperature will increase with corresponding decrease in motor life, and nuisance "trip-outs" could be expected from thermal overload protectors.

Do not overload motor in excess of nameplate output rating! Where nameplate rating is in horsepower, the rated output torque can readily be determined from Fig. 3 or from the following equations relating speed, torque and horsepower.

HP =
$$\frac{2\pi R \times N \times W}{33,000}$$
 = $\frac{T \text{ (in. -lb.)} \times N}{63,025}$

HP = T (in. -oz.) x 9.917 x N x
$$10^{-7}$$

= (approx.) T (in. -oz.) x N x 10^{-6}

$$P = EI \times power factor = \frac{HP \times 746}{motor efficiency}$$

T = torque or twisting moment

N = revolutions per minute

HP = horsepower (33,000 ft. -lbs. per min.)

E = input voltage

I = current in amperes

P = power input in watts

In the case of gearmotors, rated torque is usually stated directly and is a function of either the capacity of the motor portion or the gearhead portion of the gearmotor. Operation at higher torque loads will result in lower speeds, higher winding temperatures, shorter motor, gear, and bearing life and nuisance operation of thermal overload protectors (if present).

At very light torque loads, permanent split capacitor motors will generally exhibit higher winding temperatures, with possible nuisance operation of thermal overload protectors and could result in improper operation of current-sensitive starting relays with 2-capacitor start, 1-capacitor run-type designs.

Noise and vibration are also a function of

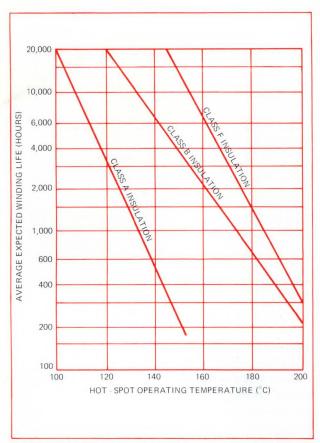


Fig. 4— Motor winding life is approximately cut in half for every 10°C increase in motor operating temperature over the recommended hottest-spot temperature limit. The rated hottest-spot temperature for the different insulation classes are 105°C for class A, 130°C for class B and 155°C for class F.

torque loading. An in-depth discussion on any of the above points would have to be associated with a specific motor or gearmotor design. Technical assistance should be requested from the motor/gearmotor manufacturer.

Do not exceed nameplate temperature rise. The temperature-rise rating is based on the motor winding temperature when operating in a specific ambient temperature in accordance with the nameplate parameters, and represents a safe value for long motor life. Specifically, the temperature-rise rating represents the number of degrees centigrade that the winding temperature may safely exceed the ambient temperature and is based on a maximum ambient of 40°C. Lack of air intake, obstructions to the ventilation flow and excessive deviations from the nameplate parameters will result in a higher temperature rise.

Operation at higher temperatures will reduce motor insulation (**Fig. 4**) and lubricant life and, in general, will also result in decreases in motor torque and speed. High temperatures could also result in nuisance operation of thermal overload protectors and motor start failures where current-sensitive starting relays are employed. These hazards can sometimes be avoided by designing

equipment to provide heat sinks (for enclosed motors) or adequate air intakes and exhausts (for ventilated motors). Assistance should be requested from the motor manufacturer when unusual problems arise. Proper selection from the wide variety of motor constructions available will often solve temperature problems.

Do not change the value of capacitance indiscriminately. This precaution applies mainly to permanent split capacitor motors. (Motor start capacitors, used with split-phase motors, are normally specified to achieve maximum starting torque and/or minimum locked current, and deviations are not usually made by the user.) Deviating to a higher value of capacitance will provide increases in starting torque and in some instances speed, but at the risk of certain hazards. Higher values of capacitance will result in higher winding temperatures, shorter motor life, nuisance operation of thermal overload protectors and increases in the level of noise and vibration. The voltage rating of the applied capacitor must also be consistent with the voltage across it.

Another side effect is that problems may be encountered with safety testing laboratories when the applied capacitor is of a different rating than that specified on the nameplate. The proper approach would be to obtain assistance from the motor manufacturer to evaluate the proposed deviation with the possibility of changing the nameplate rating or developing a more satisfactory motor design.

Do not subject the motor to duty cycles for which it was not designed. Continuous (cont.) or intermittent (int.) duty as stamped on the nameplate indicates the designed mode of operation of the motor and is generally based on the insulation class system of the motor and the watts that the motor must dissipate when energized. Adverse effects will develop from operating a continuous duty motor in an application requiring a high frequency of starts and stop. Adverse effects will also be encountered if an intermittent duty motor is operated continuously or at other than the designed mode of operation.

Generally, an adverse deviation in duty will result in higher winding temperatures with a shortening of motor life and the possibility of nuisance operation of thermal overload protectors. Increased frequency of starts could result in failure of electrolytic motor start capacitors and a reduction in the life of motor starting switches or relays.

The performance characteristics of the various motor types available to the designer will be covered in Part 2 of this Designers Guide, to be published in the April 5 issue.



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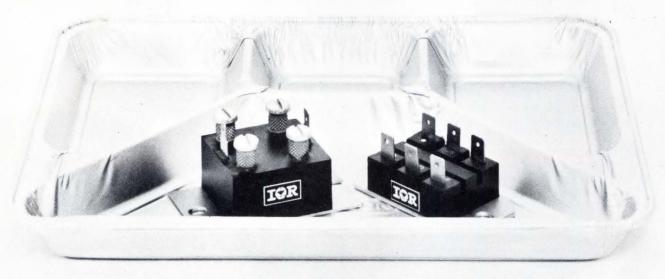
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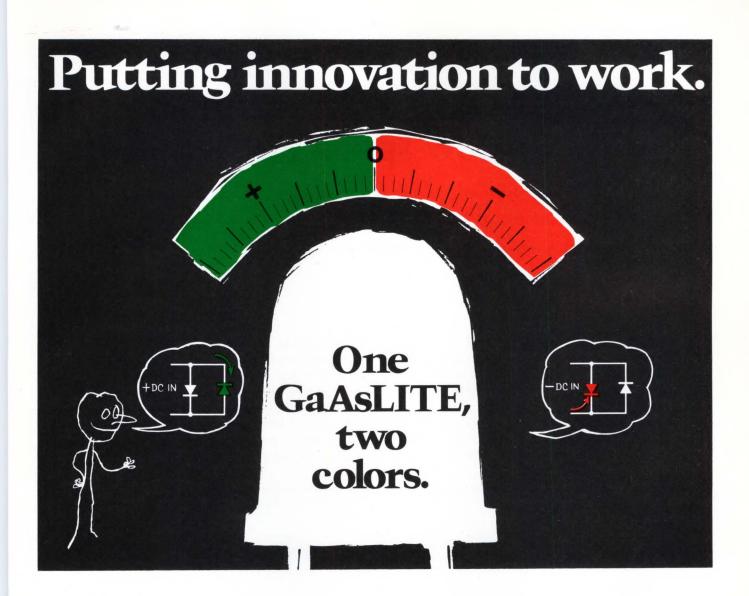
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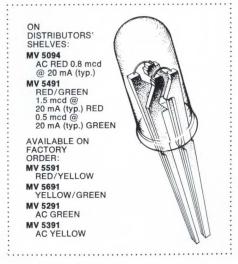
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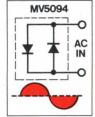
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Delta-sigma A/D conversion can save \$\$ in slow-speed applications

This novel A/D conversion technique has good performance at low speeds and medium resolutions, yet only uses three precision components.

Thomas Harrison, George Hellwarth and Richard Jaeger, IBM Corp.

There are many analog-to-digital (A/D) conversion applications where speed is not a significant requirement. The designer is then able to trade off the speed parameter for others, such as resolution and cost. The delta-sigma technique of A/D conversion is ideally suited to such applications. It provides a low-cost converter that has good performance at a resolution of 10 bits plus sign and a speed of 100 con-

versions/second.

A delta-sigma modulator is used as the basis of the delta-sigma A/D converter (Ref. 1 and 2), which is shown in block diagram form in **Fig. 1**. In the circuit, the comparator, switchable reference and filter network form a delta-sigma modulator. The unknown voltage, V_x , is applied to one input of the filter, and a reference voltage, either $+V_r$ or $-V_r$, is connected

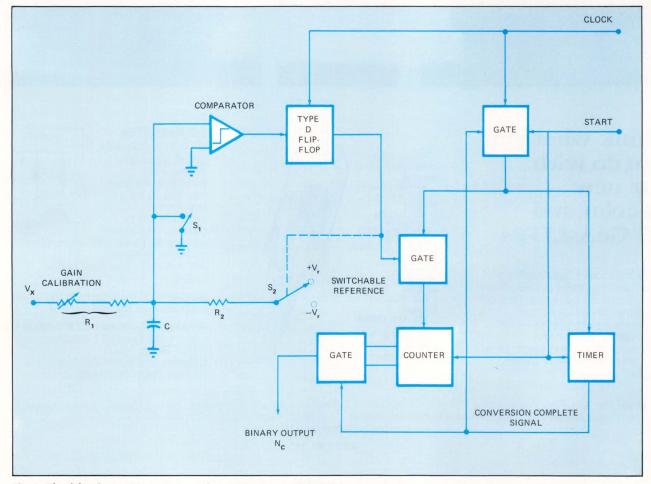


Fig. 1—The delta-sigma A/D converter makes maximum use of digital circuits and minimum use of precision components.

to the second filter input. The operation of the delta-sigma modulator is such that the reference voltage selected will be that which tends to force the voltage on capacitor C, and hence the comparator input, to zero. Changes in the reference voltage polarity are permitted only at fixed clock times. The number of clock periods during which the negative reference is used, compared to the total number of clock cycles in the measurement period, is related to the unknown value of $V_{\rm r}$.

In operation, the circuit of **Fig. 1** functions as follows: Prior to the start of conversion, switch S_1 is closed to provide a zero input to the comparator, and switch S_2 is connected to $+V_r$. When the START CONVERT signal is given, switch S_1 is opened and capacitor C charges toward a positive potential at a rate determined by the magnitudes of V_x , V_r , R_1 , R_2 and C. If V_c is positive at the end of the first clock cycle, the state of the comparator output causes the flip-flop to change state which, in turn, causes S_2 to switch to the reference source $-V_r$. The clock pulse is counted in the timer circuit but not the counter.

At the end of the second clock interval, the capacitor voltage, V_c , may be either positive or negative. If it is positive, the state of the flip-flop and switch S_2 remain unchanged. The clock pulse is again accumulated only by the timer. If V_c is negative, the flip-flop changes state, causing S_2 to change state to connect $\pm V_r$, and a clock count is accumulated by both the timer and the counter.

This cycle of events continues with clock pulses being accumulated in the timer on every occurrence and in the counter whenever switch S_2 is in the $-V_r$ position. When 2^{N+1} clock cycles have occurred, where N+1 is the number of binary stages in the timer, the timer overflows, providing a signal that the conversion is complete. The overflow signal inhibits further clock pulses from entering either the timer or the counter, resets switch S_1 to the closed position and activates the read-out gate of the counter. The number in the counter, N_c , at the end of conversion is equal to the number of clock cycles during which the reference $-V_r$ was used.

A detailed analysis of the circuit shows that the number N_c is related to the circuit parameters by the expression:

$$N_c = 2^N \left[\frac{R_2}{R_1} \frac{V_x}{V_r} + 1 \right] \tag{1}$$

From this equation, the unknown voltage V_x is determined as:

$$V_x = \left(\frac{N_c}{2^N} - 1\right) \frac{R_1}{R_2} V_r \tag{2}$$

Since all values on the right side of **Eq. 2** are known, V_x is uniquely determined. Examination of the expression also shows that V_x will be represented by an offset binary coded value of N_c . Thus, the cir-

cuit provides bipolar analog-to-digital conversion with a resolution of N bits plus sign.

Two adjustments are required for calibration of the converter. One attenuates either the input voltage, V_x , or the reference voltage, V_r , to obtain full-

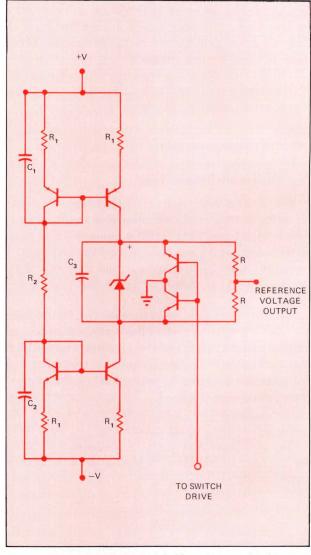


Fig. 2—One version of the switchable reference circuit uses a zener reference biased by a pair of current sources.

scale or gain calibration. A second is used to adjust the symmetry of the modulator waveform to obtain zero or offset calibration. The counter, timer and gating circuitry of **Fig. 1** may be constructed with any suitable logic family.

Application of the signal V_x and reference voltages $\pm V_r$ to the same input of the comparator, together with starting switch S_1 , remove the necessity of an initialization interval which would be required in the case where V_x and the delta-sigma modulator output are applied to opposite comparator inputs (Ref. 3). The converter yields an output which represents the average of the input during the conversion period. The conversion period is fixed and independent

dent of the magnitude of unknown input signal V_x .

What are the performance limitations?

The prime performance limitation of the deltasigma converter is the required adjustment and stable maintenance of the reference waveform timing and symmetry. In order to introduce an offset error of less than 1/2 a least significant bit (LSB), the allowable error, ϵ , in the timing of the changes in polarity of the reference voltage is:

$$\epsilon \le \frac{\mathsf{R}_1}{\mathsf{R}_2} \frac{\mathsf{V}_{f8}}{\mathsf{V}_r} \frac{\mathsf{T}}{2^{2N+2}} \tag{3}$$

where

T = conversion period

 ϵ = timing error

 V_{fs} = full-scale input voltage

For a converter with T=0.01 sec, N=10, $R_1=R_2$ and $V_{fs}=V_r$, the allowable timing error is less than 2.4 nsec. This small a timing tolerance can be achieved with carefully designed logic and analog switching circuits. Conversion rate may be traded for resolution, as shown by **Eq. 3**, for given timing errors.

To insure linearity, several other conditions must be met. The bias current of the comparator must be negligible with respect to the charging current from the switchable reference. There is a minimum size for capacitor C, which is determined by the conversion period and resistor R_2 . To prevent nonlinearity of the converter for input voltages near zero, the capacitor ripple voltage developed for zero input voltage (a 50-50 duty cycle of $+V_r$ and $-V_r$) must be less than 1 LSB. For zero input, the peak ripple is approximately:

$$V = \frac{V_r}{2R_oC} \times \frac{T}{2^{N+1}}$$
 (4)

Insuring this voltage to be less than 1 LSB requires:

$$C \ge \frac{\mathsf{T}}{4\mathsf{R}_2} \times \frac{\mathsf{V}_r}{\mathsf{V}_{fs}} \tag{5}$$

When operating at a conversion rate of 100 conversions per second and a resolution of 10 bits plus sign (a clock frequency of about 100 kHz is required at this frequency), double buffering of the comparator output has been found helpful in reducing nonlinearity caused by metastable flip-flop conditions.

One implementation of a polarity-switchable reference with accurate timing is shown in **Fig. 2**. A floating reference voltage is obtained by biasing a zener diode with a pair of current sources. The reference potentials are obtained by switching either the positive or negative terminal of the floating source to ground through inverted complementary bipolar transistors. The resistive voltage divider and zener

diode are the only precision components used in the converter. \Box

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Authors' biographies

Dr. Thomas Harrison is manager of advanced development for sensor based computing systems at IBM's General Systems Div. in Boca Raton, FL. He also participates in language development and standards activity sponsored by the ISA. Dr. Harrison holds a BS



and MS in electrical engineering from Carnegie-Mellon Univ. and a PhD in electrical engineering from Stanford Univ.

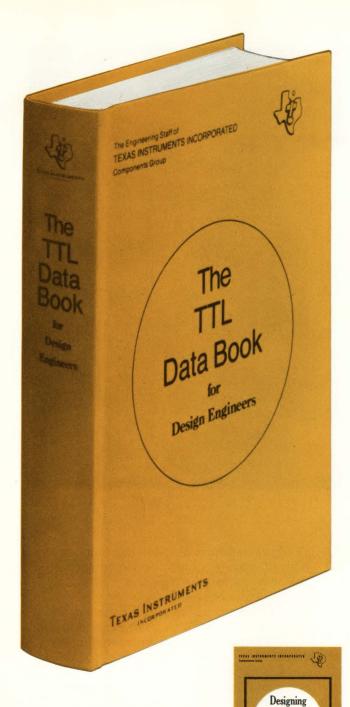
Dr. George Hellwarth is a senior engineer at IBM's General Systems Div. in Boca Raton, FL. He is currently involved in advanced development work for sensor based computing systems. Dr. Hellwarth holds a BSE in electrical engineering, an MSE in nu-



clear engineering and a PhD in electrical engineering, all from the Univ. of Michigan.

Dr. Richard Jaeger is an advisory engineer at IBM's General Systems Div. in Boca Raton, FL. He is also involved in advanced development of sensor based computing systems. Dr. Jaeger holds a BSEE, MEE and PhD in electrical engineering, all from the Univ. of Florida.





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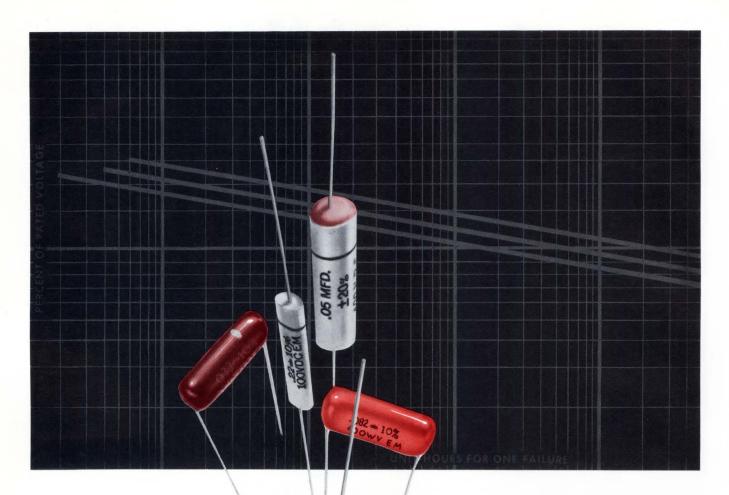
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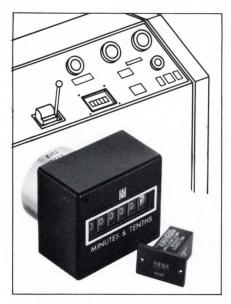
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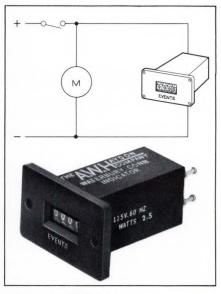


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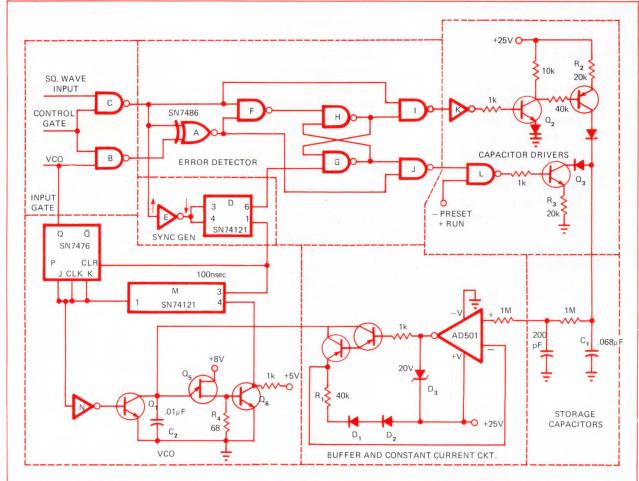
Robert Bohlken Honeywell Inc., Hopkins, MN

A problem that exists in measuring the frequency of short-signal bursts is the long acquisition time of phase locked loops.

A short time from signal acquisition to phase lock was obtained by synchronization of the VCO to the input phase. This allows correction pulses to be developed in the correct polarity only. Lockup times of less than 10 cycles of input are achieved with a 1 to 10 kHz using an idling frequency of 12 kHz for the VCO (PRESET). A high idle frequency is used because it is easier to slow the VCO from a high to low frequency.

Input signals are compared to the synchronized VCO at exclusive-OR A, gating of the error pulses by gate F and flip-flop G-H allows I or J to drive current pulses of the correct polarity into storage capacitor C_1 . The voltage correction on C_1 is proportional to the width of the error pulses and can be controlled by the value of R_2 and R_3 . The voltage on C_1 is buffered with a FET input op amp, AD501, which drives the VCO with a constant current inversely proportional to C_1 voltage. When the control gate is turned OFF, Q_3 and Q_4 are turned OFF allowing the VCO to run at lockup frequency which is equal to input frequency.

The VCO in provided with a fast discharge time. When voltage first appears across $R_{4\prime}$ one-shot M



Phase locking in less than 10 cycles of the input is achieved by idling a high frequency and "pulling down" the VCO, rather than the more common approach of idling at mid range.

turns on Q_1 which dumps C_2 much faster than Q_5 could. The sawtooth of the VCO is synchronized by use of pulses derived from the positive edge of the input, into the OR gate of one-shot M. The output of M is also fed into J-K flip-flop P which is synchronized into the proper state. The output of P is at VCO frequency which can be fed into a counter, or

the analog voltage on C_1 can be ultilized. Range of frequency can be changed by varying R_1 . D_1 , D_2 and D_3 provide limiting VCO voltage. \Box

To Vote For This Circuit Check 160

True RMS measurements using IC multipliers

Karl Huehne and Don Aldridge Motorola Semiconductor Products Div. Phoenix, AZ

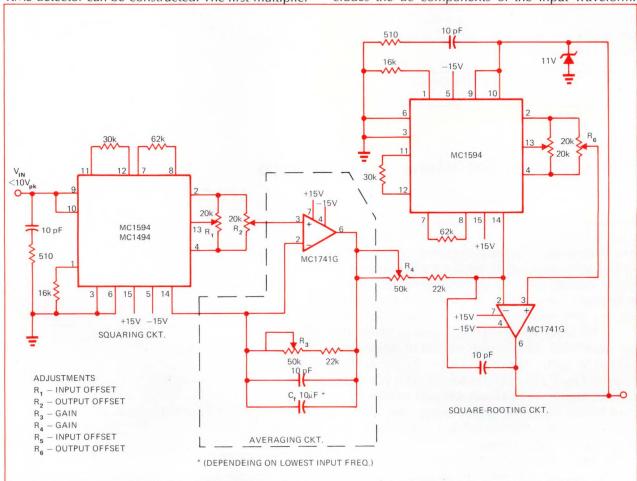
Mathematically, the RMS value of a function is obtained by squaring the function, averaging it over a time period T and then taking the square root:

$$V_{RMS} = \sqrt{\frac{1}{T} \int_{o}^{t} V^{2} dt}$$

In a practical sense this same technique can also be used to find the RMS value of a waveform. Using two MC1594 multipliers and a pair of op amps, an RMS detector can be constructed. The first multiplier is used to square the input waveform. Since the output of the multiplier is a current, an op amp is customarily used to convert this output to a voltage. The same op amp may also be used to perform the averaging function by placing a capacitor in the feedback path. The second op amp is used with a multiplier as the feedback element to produce the square root configuration.

This method eliminates the thermal-response time that is prevalent in most RMS measuring circuits.

The input-voltage range for this circuit is from 2 to 10V pk. For other ranges, input scaling can be used. Since the input is dc coupled, the output voltage includes the dc components of the input waveform.



This RMS circuit follows math procedures of squaring instantaneous input values, averaging over time and taking the square root.

The maximum frequency for a sine-wave input is about 600 kHz and the accuracy over the input voltage range is about 1 to 2%. The overall calculation of the output voltage is

$$V_o = \sqrt{\frac{1}{RC} \int_o^t V_{IN}^2 dt}$$

where
$$R = R_3 + 22 \text{ k}\Omega$$

 $C = C_f + 10 \text{ pF}$

To Vote For This Circuit Check 161

Three ICs monitor pulse width

Joseph Kish, Jr.

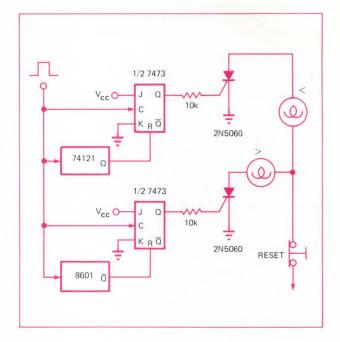
Diebold, Inc., Canton, OH

Very often in pulse circuits, it is necessary to monitor pulse width in terms of "greater than" but "less than" given specified values. The circuit described here measures both limits constantly, and a fault indication will latch until manually reset.

The 74121 is set to coincide with the minimum pulse width spec. Should the pulse under observation fall to ground before the one-shot recovers, the J-K is clocked and the "less than" SCR is triggered.

The 8601 is set to coincide with the maximum pulse width spec. Should the pulse under observation fall to ground after the one-shot recovers, the second half of the J-K is clocked and the "greater than" SCR is triggered.

To Vote For This Circuit Check 162



Phase-shifter for phase locked loops

J. S. Krikorian, Jr. Warwick, RI

Phase locked loops can be utilized as synchronous AM demodulators or tone detectors. Other phase locked loop applications require a measurement of signal strength or a means of determining signal acquisition. The quadrature detector illustrated in **Fig. 1** is employed to obtain such signals. Frequently, an RC phase-shift network is used to obtain the 90° phase-shift. An alternate method of phase-shifting can be implemented using logic circuits, as illustrated in **Fig. 2**. The output of this circuit is especially suitable for use with FET gate-chopper multipliers or hard-limiter logic multipliers, while the input is compatible with a VCO that has a square wave output within its voltage limits.

The logic phase-shift circuit uses a flip-flop to divide the frequency by two. The input, f, and the output, f/2, of this flip-flop are fed into an equivalence

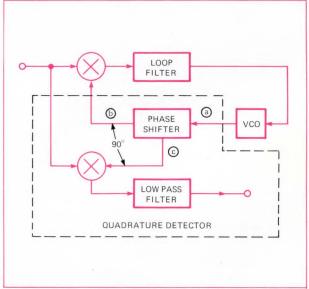


Fig. 1—**Logic phase-shifter** provides 90° phase-shift signal for use by the quadrature detector.

logic circuit to obtain a square wave, f/2-90°, that is shifted in phase by 90° with respect to the output, f/2, of the flip-flop. An exclusive-OR in place of the equivalence logic circuit would also provide a 90° phase-shift. To compensate for the frequency division by two, the VCO center frequency has to be doubled.

Because of its insensitivity to frequency changes, this circuit is useful for wideband phase locked loop applications. High-frequency accuracy is dependent on the speed of logic circuitry used in the implementation. Along with the associated circuitry for the quadrature detector, the logic phase-shifter can be incorporated into a self contained phase locked loop IC package.

To Vote For This Circuit Check 163

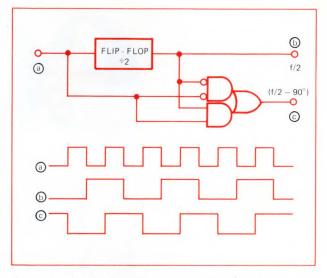


Fig. 2—Details of logic phase-shifter showing flip-flop, equivalence circuit and associated signals.

Simple ±15V regulated supply provides tracking

Hank Olson

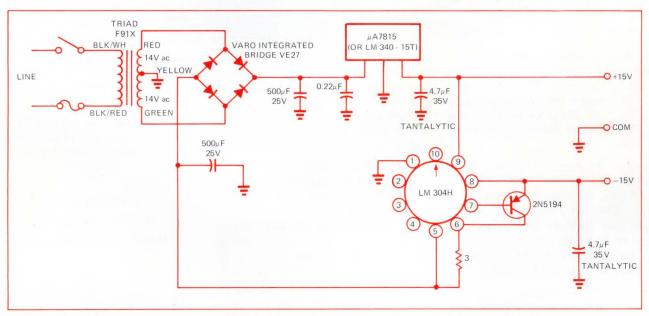
Stanford Research Inst., Menlo Park, CA

A simple ±15V regulated supply which provides tracking can be built using only four semiconductor packages. The output current capability as shown is 100 mA, but this can easily be increased to about 200 mA using a larger rectifier-filter section.

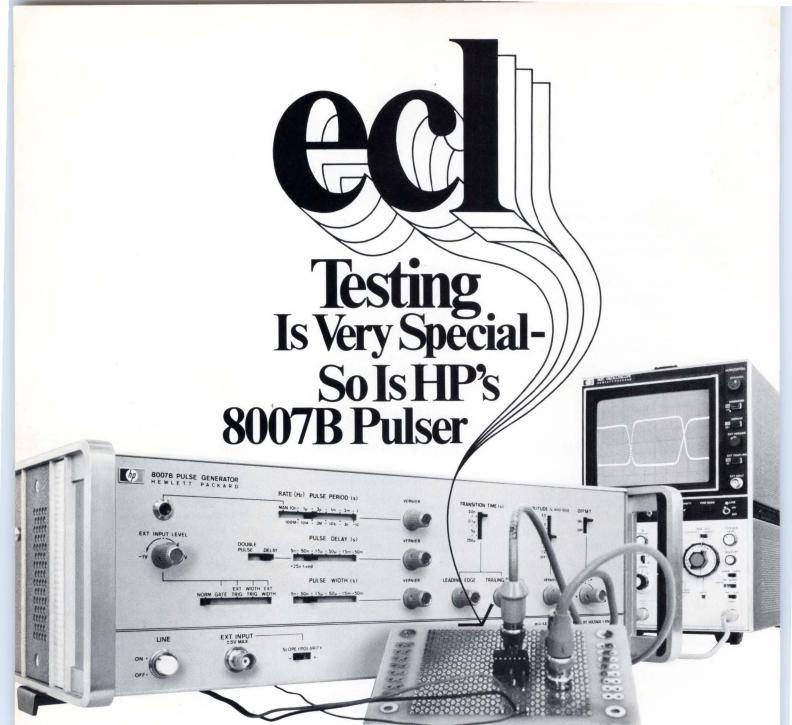
The μ A7815 is a positive, fixed output regulator with built-in current limiting at about 230 mA. It would seem a simple task to use two of these power ICs to obtain ± 15 V, but this requires two isolated transformer windings. The circuit shown uses only

one center tapped transformer winding and an integrated bridge (as two full-wave rectifiers). By using the National LM304 and an outboard current-carrying PNP, and the +15V from the μ A7815 as an external reference, a simple -15V companion regulator is obtained. The LM304 does not even require compensation, as do most IC regulators. Depending on the application and construction methods, the 0.22 μ F and two 4.7 μ F capacitors may not be needed. \Box

To Vote For This Circuit Check 164



Tracking 100 mA regulator provides \pm 15V and can be boosted to 200 mA output by using a larger rectifier section.



Leading IC manufacturers use it — and they can't afford to take chances in testing. They know that the 8007B delivers the pulses they need. And they know that the \$1,750 price is below competitive models.

Look at the specs of this fast pulse generator: rep rates from 10Hz to 100MHz, variable transition times from 2.0ns to $250\mu\text{s}$, $\pm5\text{V}$ amplitude and $\pm4\text{V}$ dc offset. It's this kind of capability that you'll need for testing ECL II, ECL 10,000, and other comparable families.

Also note the generator's 50 ohm source impedance — mighty important for minimizing reflections when you're working with fast

ECL. Today's testing also calls for a wide span of linear transition times, like the 2.0ns to $250\mu s$ of the 8007B. You can really use this when you're measuring propagation delay to a manufacturer's specs (even test linear devices).

In addition to ECL, the 8007B equips you to test most other IC families. You can, for example, measure the sensitivity of a flipflop; or determine the noise immunity of TTL circuits by adding pulses onto as much as a 4V dc level. These types of tests are made possible because of the continuously variable ±4V offset.

If your test needs call for even higher repetition rates (to 200

MHz) or faster transition time (to 1.2ns), you may also want to look into its companion generator, the 8008A.

For more information on either the 8007B or the 8008A fast pulse generators, contact your HP field engineer.

For standards in pulse generators, think Hewlett-Packard.

083/3



Sales, Service and support in 172 centers in 65 countries.

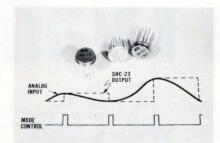
Low-cost adjustable TO-8 S/H amplifier is versatile

PROGRESS IN PACKAGED CIRCUITS

Designed for applications requiring a broad range of acquisition times, Burr-Brown's new SHC23 sample-and-hold (S/H) amplifier in a hermetically sealed TO-8 can have its acquisition time adjusted from as low as 25 μ sec with the selection of an appropriate external storage capacitor.

A 0.005-µF storage capacitor provides the 25-µsec acquisition time (to 0.01%) while a 1-µsec storage capacitor allows the user to hold the output for more than 15 minutes with less than 1% error.

Costing only \$31 (100-up quantities) and \$45 in single quantities, the SHC23 amplifier offers an accuracy spec that is surprising for a device in its price class: maximum total dynamic nonlinearity from input to output is $\pm 0.01\%$ for a



A 25-µsec acquisition time (to 0.01%) is available from a new \$45 TO-8 S/H amplifier, and is set by the addition of an external capacitor.

20V step. This is the total nonadjustable error.

Droop rate is 20 μ V/msec max., and throughput nonlinearity is 1 mV. Other characteristics include an 80-kHz, 3-dB bandwidth, a 20-kHz full-power bandwidth and output slew rate of 1V/ μ sec. The maximum aperture time is specified as 50 nsec. Noise is 100 μ V rms max., and unity gain error is

 $\pm 0.01\%$ max.

Input voltage range of the SHC23 is $\pm 10\text{V}$ at $100\text{ M}\Omega$ minimum input impedance. Bias current is 30 nA. The output is $\pm 10\text{V}$ at 5 mA. The unit is TTL/DTL input compatible and operates over 0°C to +70°C from $\pm 15\text{V}$ dc at 30 mA. A military version that operates over the temperature range of -55°C to +125°C is also available (Model SHC23ET) at a slightly higher cost.

Needless to say, the quality of the external capacitor to be used should be high, preferably of teflon or polystyrene, since it controls the length of acquisition time and effects droop and charge offset.

Burr-Brown Research Corp., International Airport Industrial Park, Tucson, AZ 85706. Phone(602) 294-1431

Dc-to-dc converter family features triple outputs and low-thermal impedance

PROGRESS IN CIRCUIT MODULES

Aimed specifically at the commercial and industrial markets, the 1000 Series dc-to-dc converters offer the user a choice of single, dual or triple outputs. All three versions of the new series—namely, 1, 3 and 6W—are available with single or dual outputs, while the 3 and

6W versions can be had with single, dual or triple outputs.

Stock units are available with input voltages of 5, 6, 12, 24, 28 and 48V dc, with other input voltages available on request. Output voltages available are 3.6 to 24V dc for single-output units, 10 to 24V dc for dual-output units and either 5V dc and ±15V dc or 5V dc and ±12V dc for the triple-out-

put units.

All units are fully encapsulated and have 0.1% max. line regulation, 0.3% load regulation and typical temperature regulation of 0.007% per °C. Short-circuit and overload protection is incorporated, and the converters are reverse-polarity protected to 100V dc, except for the 5 and 6V input models.

Dialight sees a need:

(Need: A large 5/8" high LED readout at a low \$4.95* price.)

See Dialight.

Each digit in this bezel assembly contains a Dialight lightemitting diode, decoder/driver, and resistor network that produces a bright, highly visible readout that can be easily installed in a panel. The readout display is supplied with discrete gallium phosphide or gallium arsenide phosphide diodes arrange: in a seven-segment format. These generate a bright, highly legible red character (0.625 inch

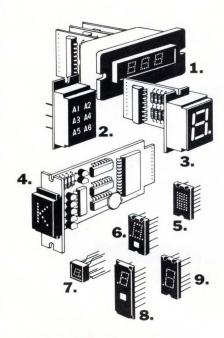


high—the largest size character in the industry) with the lowest power consumption for a character of this size. Ideal for mounting on a control panel, or in a digital clock, meter, credit-card verifier, TV channel indicator, or hospital room status-board indicator. The contrast ratio between the illuminated and non-illuminated segments is further enhanced by a one-piece red nonglare window.



Dialight is a company that looks for needs...and develops solutions. That's how we developed the industry's broadest line of LED light sources, indicator lights and readouts. No other company offers you one-stop shopping in LED visual displays. And no one has more experience in the visual display field. Dialight can help you do more with LEDs than anyone else because we have done more with them. Talk to the specialists at Dialight first. You won't have to talk to anyone else.

Here are a few products in this family: 1. Multidigit readout assembly in 0.205" character height 2. Status display module with 6 LEDs with adjustable light cells 3. LED readout in character height 0.625"4. Alphanumeric display complete with code generator/driver character height 0.300" 5.5 x 7 dot matrix alphanumeric display in character height 0.300"6. Hexadecimal display with logic character height 0.270" 7. Single digit LED readout module in 0.125" character height. **8.** Numeric display with integral TTL MSI circuit chip with counter character height 0.270" **9.** Single digit LED readout module in 0.270" character height (MAN 1 equiv.). *1000 lot quantity for 730-1003



NAME	14.7
TITLE	
COMPANY	
ADDRESS	
CITY	STATE

DC-DC POWER SUPP the bottom 6W. A unique packaging concept is and vibration applications.



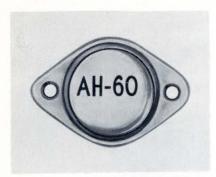
used with the Series 1000. The converters are built around an integral heat sink, with all heat-producing components, such as pass transistors and inverter transistors, bonded to the heat sink. This keeps thermal impedance to a minimum within the converters. However, if external heat sinking is needed, the mounting plate offers an easy way to accomplish it. On the 3 and 6W models, the heat sink can also be used as a mounting plate. All of the 1000 Series converters may also be mounted directly to a printed circuit board by means of pc pin terminals. On 3 and 6W

units, there are inserts which allow the converters to be bolted to either a heat sink or a pc board. This assures a rigid mount in high shock

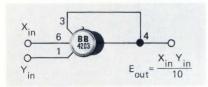
Delivery of most of the 1000 Series modules is from stock. Maximum delivery is four weeks after receipt of the order. Prices are: 1W units with single output \$44, dual output \$49; 3W units with single output \$69, dual output \$79 and triple output \$89; 6W units with single output \$79, dual output \$89 and triple output \$99.

Tecnetics, P.O. Box 910, Boulder Industrial Park, Boulder, CO 80302. Phone(303)442-3837. 166

SEMICONDUCTORS



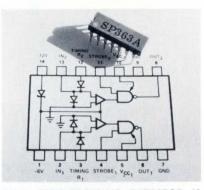
AMPLIFIER PROVIDES OUTPUT POWER OF +20 dBm. Optimax Model AH-60 modular amplifier, is designed for direct insertion into microstrip circuitry and comes in a TO-3 package. Operating frequency is 5-300 MHz and it has a nominal gain of 9 dB at +24V dc. The amplifier has a noise figure of 8 dB and an impedance of 50Ω . The amplifier presents economies of time and cost to the design engineer because it eliminates the need for customized design and development of a comparable amplifier. The plug-in model may be used for a variety of applications including: ECM receivers, RF amplifiers and test equipment. \$75 in quantities from 1-9. Optimax Inc., P.O. Box 105, Advance Lane, Colmar, PA 18915. Phone(215)822-1311. 170



IC MULTIPLIER/DIVIDER NEEDS NO EX-TERNAL COMPONENTS. Model 4203, hermetically sealed and internally laser trimmed in a TO-100 package, delivers a guaranteed accuracy of 1% (4203K). In addition to division and 4-quadrant multiplication, square-rooting may also be performed. The 4203 has a 1-MHz bandwidth and a slew rate of 25V/µsec. Unit prices in 1-24 quantities are as follows: 42031, 2% accuracy, \$29; 4203K, 1% accuracy, \$39; 4203S, 1% accuracy, Mil temperature, \$51; and 4203SQ, 1% accuracy, Mil temperature with Mil-Std 883 screening, \$63. Small quantity delivery is from stock. Burr-Brown Research Corp., International Airport Industrial Park, Tucson, AZ 85706. Phone (602)294-1431. 171

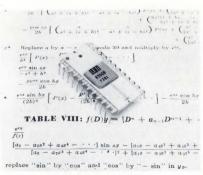
standard 4000A CMOS logic series has been expanded to include a decade counter/7-segment decoder with display enable (SCL 4026A, \$4.71 in 1k quantities), a decade counter/7-segment decoder with ripple blanking (SCL 4033A, \$4.65 in 1k quantities) and a quad clocked latch (SCL

4042A, \$2.70 in 1k quantities). In the CMOS SCL 4400A series, a pair of new devices are offered. A decade counter/7-segment decoder with display enable and bipolar outputs (SCL 4426A) and a decade counter/7-segment decoder with ripple blanking and bipolar outputs (SCL 4433A) will be available at a price of \$5.65 each in 1k quantities. A CMOS 256-bit RAM organized 64 words × 4 bits is the new addition to the company's CMOS LSI product line. SCL 5555D costs \$19.40 in 1k quantities. Solid State Scientific, 1405 Locust St., Philadelphia, PA 19102. Phone(215)735-3535.

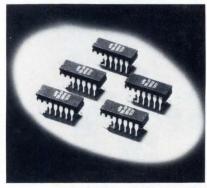


DUAL ZERO-CROSSING DETECTOR IS VERSATILE INTERFACE CIRCUIT. The Model 363 can also be used as a high-stability one-shot, a bidirectional one-shot, a frequency doubler, a stable low-frequency oscillator, a linear amplifier and a frequency-to-voltage converter. The input amplifier is referenced to 0V and employs temperature compensation to ensure stable thresholds. Low-level analog waveforms may be converted to digital signals as long as they exceed ±30 mV, the input uncertainty region. Packaged in a plastic DIP, single units sell for \$2.15. Signetics Inc., 811 E. Arques Ave., Sunnyvale, CA 94086. Phone(408) 739-7700.

2k-BIT SHIFT REGISTER HAS 10-MHZ RATE. This multiplexed dynamic shift register, HDSR 2048, utilizes ion implantation to minimize gate overlap capacity and achieve its high-shifting rate. The device is structured as a dual 1024-bit shift register and is also available as a single 1024-bit (HDSR 1024), a dual 512-bit (HDSR 1025) and a guad 256-bit (HDSR 1026). Inputs can be directly driven by MOS, TTL, or DTL ICs. Both bipolar and MOS circuits can be driven to the output stages. Power dissipation is typically 300 mW at 25°C for 10 MHz operation with 50% duty cycle clocks per 1024 bits. HDSR 2048 is priced at \$36 in quantities of 100. Hughes Aircraft Co., P.O. Box 90515, Los Angeles, CA 90009. Phone(213)670-1515.



9k BIPOLAR ROM PROVIDES HIGH-DENSITY STORAGE. The MM5260/6260 is organized as 1024 × 9, offers 120 nsec access time and dissipates 50 µW/bit. It may be ordered as a 7 × 9 font character generator and is DTL/TTL compatible with 1/10 of standard TTL input load. Three enable inputs are provided to permit memory expansion. The ROM has open collector outputs and operates from 0° to +75°C (MM6260) or -55° to $+125^{\circ}C$ (MM5260). The 9k ROM is packaged in a 24-pin ceramic DIP, as is the 8k ROM announced earlier. Delivery is from distributor stock. Pricing for the MM6260 is \$65 in lots of 100. Monolithic Memories, Inc., 1165 E. Arques Ave., Sunnyvale, CA 94086. Phone(408) 739-3535.



IC FOR AUTO TACH DRIVER. The SW-781 is a special-purpose monostable multivibrator with high-input threshold. This makes possible its use in electrically noisy automotive environments; and the units extremely stable pulse output allows it to maintain accuracy over automotive temperature ranges and over wide battery voltage ranges. In a typical tachometer application, trigger pulses are tapped off the distributor using 2 discrete resistors and a capacitor. The SW-781 converts each input trigger to a pulse of closely controlled width and amplitude. These output pulses are applied to a conventional dc meter movement through a discrete resistor. Stewart-Warner Microcircuits, 730 E. Evelyn Ave., Sunnyvale, CA 94086. Phone(213)370-8551.

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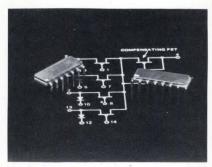
DUAL-VOLTAGE TRANSLATORS SPEED INTERFACE CHORES. The µIS7800 is rated for -55° C to $+125^{\circ}$ C; the μ IS8800 operates from 0°C to 70°C. Both units are electrically identical and offer a 31V maximum output swing with normal power dissipation of 1mW. Operation is from a standard 5V supply and the dual-channel translators are compatible with all MOS devices. Typical applications for the series include analog switching from DTL or TTL logic levels and bipolar-to-MOS shift register and memory interfacing. They are available in a 10-pin, TO-100 package. The 100 lot prices are \$9 and \$6 each. Integrated Microsystems, Inc., 16845 Hicks Rd., Los Gatos, CA 95050. Phone(408)268-2410.



RF POWER TRANSISTOR FAMILY. The 2N6080, 2N6081, 2N6082, 2N6083, and 2N6084 devices provide 4, 15, 25, 30 and 40W respectively at 12.5V, 150 to 175 MHz. Designed specifically for VHF mobile and marine transmitter applications, this series is available in MT-72 stripline packages. Solitron/Microwave also offers the same capabilities in a variety of RF packages for ultimate design flexibility. The units are priced at \$8 each in quantities of 1-99. Solitron/Microwave, Solid State Products Div., 1440 W. Indiantown Rd., Jupiter, FL 33458. Phone(305)764-8311.

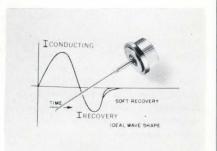
HIGH-VOLTAGE COMPLEMENTARY SILICON TRANSISTORS drive gas-discharge displays. A line of high-voltage, NPN and PNP transistors—designated DTN and DTP 203, -04, -05 and -06, in plastic TO-106 packages are intended for use as gas-discharge display anode and cathode drivers. They are now in volume production and are being supplied to calculator and

DPM manufacturers. Continuous collector current is rated at 50 mA, with a gain of 40 or greater at 10 mA. Voltage ratings are 200V for the DTN/DTP 203, to 125V for the DTN/DTP 206. Prices are 34 to 52¢ in 1-99 quantities; 17 to 25¢ in 10,000 quantities. Dionics Inc., 65 Rushmore St., Westbury, NY 11590. Phone(516)997-7474.



FET ANALOG GATES FOR \$1 PER SWITCH POINT. The 1H5009 Series feature ON-resistance of less than 150Ω . The ON-resistances are matched within as close as 5Ω for critical applications, and the units include an extra FET for temperature compensation of the feedback resistance. These gates can be driven directly from TTI or HNIL logic. IH5009 series gates are intended for shunt or summing point switching of signals up to ±15V, or series switching of signals under 200 mV. All types are available in industrial as well as military versions, and sell in 100-piece quantities for approximately \$1 per switch point. Teledyne Crystalonics, 147 Sherman St., Cambridge, MA 02140. Phone(617)491-1670.

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EPITAXIAL POWER DIODES FEATURE LOW NOISE AND FAST RECOVERY. Maximum recovery time of the S6453 Series is 200 nsec with a soft-recovery curve. Rated at 10A, with an IFSM of 150 and an I²t of 135, they are available in PIV ratings of 25, 50, 75 and 100V. The units are housed in DO21 hermetically sealed cases, 11/32 in. high × 5/8 in. diameter. Sarkes Tarzian, Inc., Semiconductor Div., 415 N. College Ave., Bloomington, IN 47401. Phone(812) ED2-1435.

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Mounting pads and lead spreaders help protect semiconductor junctions and cool devices Help eliminate top-side solder bridges Facilitate inspection, repair, and uniform mounting Nylon pads offer exclusive color coding (5 colors) to speed assembly. Also available in black DAP. Over 120 designs to choose from.

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25 WATT DC-DC REGULATED CONVERTERS

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- Short circuit protected by current limiting
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- High powered addition to the proven 9500 series

SPECIFICATIONS

- 28 VDC input, outputs available from 5 to 24 VDC in single, dual, or triple output models.
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- meet MIL-E-5400K specifications (selected)
- low profile package (4 x 4 x 1.5 inches)
- output impedance: 0.02 ohms, 0 to 10 kHz
- EMI: Input filtering included
- MTBF: Calculated per MIL-HDBK-217

Prices:

Single output, \$240.00. Dual output, \$270.00. Triple output, \$330.00

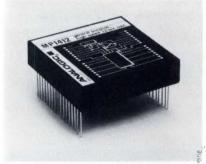
For immediate delivery call (303) 442-3837

tecnetics, Inc. Box 910 Boulder, Colorado 80302

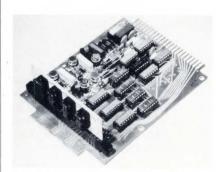


CHECK NO. 45

CIRCUITS



LOW-COST 12-BIT \$39 D/A CONVERTER Model MP1412 features settling times (1/2 LSB) of 5 µsec, 0.012% linearity and relative accuracy and a temperature coefficient of 15 ppm/°C. The price of \$39 each is in quantities of 100. The MP1412 with MSI components has an MTBF of 500,000 hours. An internal precision reference supply, ladder network and output amplifier are included. The MP1412 is packaged in a 2 × 2 × 0.4-in. MODUPAC encapsulated case and is designed to operate over the 0°C to +70°C range. Analogic Corp., Audubon Rd., Wakefield, MA 01880. Phone(617) 246-0300. 181



A/D CONVERTER Model IAD 32000-00 is a 3-1/2-digit precision unit on a standard $4\text{-}1/2 \times 6\text{-}1/2\text{-in.}$ card. The accuracy of the converter is within 0.05% of the reading ± 1 count. The display (0.6 or 0.25-in. LEDs) plugs directly into the card or can be positioned remotely using 3M Scotchflex cable. The card can be mounted in a card cage or as part of a module. The unit provides BCD, 7-segment and reset outputs, conversion-on-process, print-command, overrange and polarity signals for data processing. \$210. QCI, Inc., 2908 Scott Blvd., Santa Clara, CA 95050. Phone(418)247-2345.

LED ARRAY for continuous line and bargraph applications. The ARL-18 is a common-cathode, 8-diode array with 75-mil lead spacing and 100-mil centers between lights. Any number of arrays can be placed end-to-end to provide a continuous line of light sources. The output of each diode is 100 ft L. An 8-diode array requires only 16 mW of power per diode. It is used for indicating status on pc boards since it only

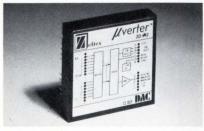
stands 0.105-in. high and directs its light parallel to the board. Available at \$5.10 (100-999) or \$3.95 (1000 units). Litronix, Inc., 1900 Homestead Rd., Cupertino, CA 95014. Phone(408)257-7910.



LOW-PROFILE POWER SUPPLY Model 108 is a regulated dc supply which utilizes high-efficiency switching techniques to provide 6 to 12V dc of adjustable output at 3A. Input is 115V ac, 50 to 70 Hz. 230V ac units are available. The unit is only 1.72 \times 3.5 \times 7.9 in. and features continuous short-circuit and overload as well as crowbar over-voltage protection, factory set at 14V. Regulation is ±1 mV for a ±10% change in input line voltage, and 2 mV max. for a zero-to-full-load change. Ripple is 2 mV p-p max. From \$99 down to \$85 at the 100-piece level. RO Associates, Inc., 3705 Haven Ave., Menlo Park, CA 94025. Phone(415)322-5321.



COMPACT VIDEO DELAY MODULES for drop-out compensation or image-enhancement applications, range in price from \$250 for 1 module to \$150 each in quantities of 1000 or more. Because the modules, which use glass delay lines, are provided complete with interfacing electronics, they fit easily into video recorder designs. Inputs and outputs are both at video frequencies. Specifications are time delay of 63.5 µsec nominal, a 3-dB bandwidth of 4.5 MHz min., a signal-to-noise ratio of 55-dB p-p/rms and differential phase of less than 2.5°. Time delays can be provided from 60 to 64 µsec ±25 nsec. Corning Glass Works, 3900 Electronics Dr., Raleigh, NC 27602. Phone (919)828-0511. 185



FAST-SETTLING 12-BIT D/A CONVERTER. Model ZD442 features a 5- μ sec settling time, 0.05% (0.002%/°C) scaling error, zero offset error and 0.01% (0.002%/°C) linearity. Input coding is offset binary or unipolar binary and the output code can be selected from 0 to +10V or \pm 5V. All digital inputs are DTL/TTL compatible. Packaged in a standard Zeltex Microverter case (patent pending), the unit includes a built-in reference. \$65. Zeltex, Inc., 1000 Chalomar Rd., Concord, CA 94518. Phone (415)686-6660.

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UNIVERSAL ACTIVE FILTER section Model μAR2000 is a universal active resonator designed for oscillator, timer, active filter and other applications. The µAR2000 operates from ± 12 to ± 15 V supplies and provides an output swing of ±10V from dc to 10 kHz. It contains a state-variable active resonator plus a fourth, uncommitted op amp. When used as a filter, the center or corner frequency is set by the addition of 2 external resistors. A third resistor determines the filter O. Packaging is in a 16-pin ceramic and metal DIP. The 100-lot price is \$12.85. Integrated Microsystems, Inc., 16845 Hicks Rd., Los Gatos, CA 95030. Phone(408)268-2410.



WIDEBAND MIC AMPLIFIER OPERATES TO 6 GHz. The AMT-6005M operates over 4 to 6 GHz. Housed in a minature $(2.3 \times 1.3 \times 0.6 \text{ in.})$ hermetically sealed, stainless-steel case, it features min. gain of 28 dB, max. gain variation of ± 1.5 dB, a max. noise figure of 10 dB and max. VSWR of 2.0:1, in and out. Power output for 1-dB gain compression is ± 10 dBm min. The unit is powered by ± 15 V dc at 150 mA. Price in small quantities is under \$4700. Avantek, Inc., 2981 Copper Rd., Santa Clara, CA 95051. Phone (408)739-6170.

FUNCTION MODULES, INC.

MODEL 104-BIN-N

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A/D CONVERTER has lowest price, smallest package and a choice of 4 models. The Model 104 family of integrating A/D converters includes converters that are complete units having their own internal reference, but can also operate from an external reference. There is also a choice of 0 to +10V or 0 to -10V inputs and binary or BCD coding. Conversion times is 20 msec for binary coded units and 8 msec for BCD coded units. Packaged in a module that is only $2 \times 2 \times 0.4$ in., each unit costs \$59 in 100 quantities. Function Modules, Inc., 2441 Campus Dr., Irvine, CA 92664. Phone (714)833-8314. 190

DIGITAL DELAY MODULES. Designed for precision DTL/TTL timing applications, these modules eliminate the need for external components. A built-in interface allows for direct plug-in without the need for additional components. Modules with total delays from 50 to 250 nsec with 5 equally spaced taps are available from stock. Series 20330 through 20332 units feature 4-nsec risetimes that are independent of delay time. They are available in 16-pin DIP-compatible units. Pulse Engineering Inc., Box 12235, San Diego, CA 92112. Phone (714)279-5900.

TRUE-rms-TO-dc-CONVERTER. This unit allows any dc meter to accurately measure true rms. It will increase the sensitivity, frequency response and accuracy of any multimeter or DVM. The unit converts any wave shape to a dc output, has an accuracy of 0.3%, a bandwidth to 500 kHz and its own input ranging amplifier of 1 mV to 150V. The converter is portable, $3 \times 4 \times 6.5$ in., and sells for \$198. UFAD Corp., Box 96, Ada, MI 49301. Phone (616)676-9000.

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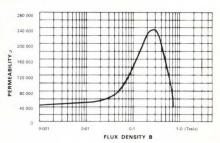
Advanced forming techniques developed by Telcon Metals, Ltd., make it possible to produce magnetic shielding with minimal degradation of metal properties during shaping and fabrication. Designated Telform, the deep drawn shield has fewer seams and conforms more closely to complex shapes than conventional shielding.

The fewer seam concept means better attenuation of spurious signals for any scientific equipment or component in a system. It prevents transients, noise and other intruding signals from penetrating to cathode ray tubes for oscilloscopes, spectrum analyzers, medical and related electronic and laboratory equipment.

Also consider the value of Telcon Metals shielding for . . .

- tubes
- reed relaysnavigational
- navigational instruments
 cables
- transformerssmall motors
- video cameras
- sensitive instruments

The following shows a typical d. c. permeability/flux density curve for Telcon 79 sheet.

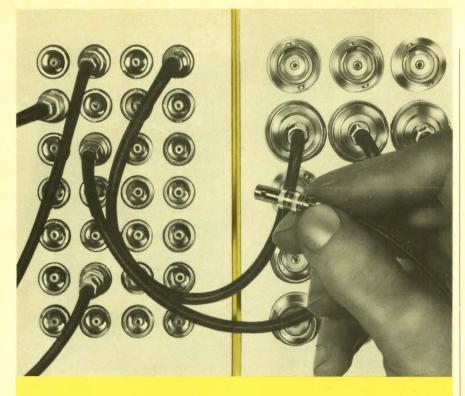


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SUCCESSIVE-APPROXIMATION A/D CON-VERTERS. Series UHM-600 converter units are available in 2-1/8 \times 4 \times 3/4-in. epoxy modules that contain internal references, clocks, conversion logic, comparators, switches and nickel-chromium thin-film ladder networks. Input voltage swings of 5, 10 and 20V can be obtained. The digital output is 8, 10 or 12 bits binary, offset binary, or BCD. The rated operating temperature range is 0°C to +70°C or -25°C to +85°C. Sprague Electric Co., Marshall St., N. Adams, MA 01247. Phone (413)664-4411. 194



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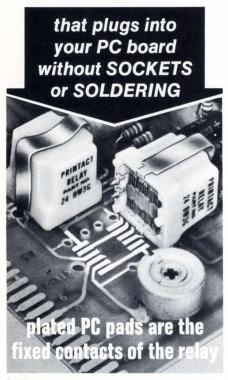
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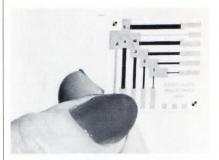
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PRINTED THIN-FILM RESISTORS. The networks are reportedly half the cost of the least-expensive thick-film resistors available. The hybrid-resistor systems are based on the use of organo-metallic compounds. At present, the range of the resistor line is limited. However, the manufacturer is exploring the low-cost marketplace in the electronic field to see if requirements advise broadening this economical line. Engelhard Industries, A Div. of Engelhard Minerals & Chemicals Corp., 231 N J Railroad Ave., Newark, N J 07105. Phone (201)242-2700.

VOLTAGE CONTROLLED AMPLIFIER Model 202 provides a 130-dB linear control range. The modular unit provides a

linear voltage-per-decibel gain range from -100 to +30 dB with excellent linearity and unit-to-unit tracking. Input noise is 6 μ V, yet peak signal may be as high as 100V. Frequency response is flat to 20 kHz and distortion products do not exceed 0.07%. The unit is packaged in a 1 \times 2 \times 1/2-in. can with 1/8-in. gold plated pc pins. \$47. DBX, Inc., 296 Newton St., Waltham, MA 02154. Phone (617)899-196

MODEL SG71 STRAIN-GAGE AMPLIFIER-POWER SUPPLY is both a signal conditioner and a power supply for strain-gage bridge circuits. Each SG71 provides buffered shortcircuit proofed dc excitation to a bridge circuit and amplifies the output to a level sufficient to operate most data-gathering systems and tape recorders. Each has 2 outputs. Output A provides ±10V dc from as low as 1 mV/V output from the bridge circuits. Output B provides ± 10 mA into 50Ω to operate oscillograph galvanometers. A 15-turn screw-driver control balances the bridge circuit. Validyne Engineering Corp., 19414 Londelius St., Northridge, CA 91324. Phone (212)886-8488. 197

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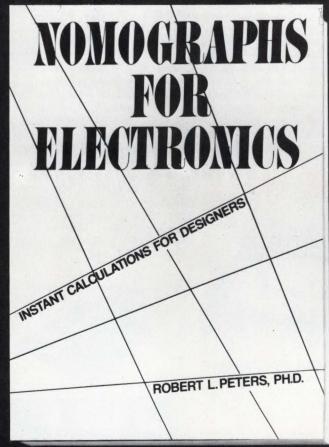
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WORD-PROCESSING UNIT PERMITS CRT EDITING. The Videotype-I text processor is designed for those who need easily edited, quickly recalled copy. As text is typed it appears on a display screen to permit editing before hard-copy production by the printer/logic console. Simultaneously, the data is recorded on a cassette tape in the console. Each cassette holds up to 30 pages of copy. "Edit," "delete," "insert" and similar command keys permit rapid editing & arrangement of text. \$16,000 or leased for \$375 per month. Lexitron Corp., 9600 De Soto Ave., Chatsworth, CA 91311.

220

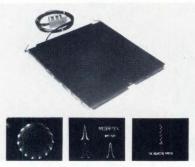


LOW-COST FUSIBLE ROM PROGRAM-MER. The PR-23A allows manual programming of the Signetics 8223 256-bit field programmable ROM in the field by unskilled personnel. To program a device, the operator places a blank F-ROM in the test socket, selects the appropriate octal word address and presses one of 8 output pushbuttons to open the selected fusible link. Program sequence is automatic and independent of operator timing to ensure uniform program conditions. A typical pattern can be programmed in about 5 minutes. Eight indicator lamps provide a continuous display of output states. A slot at the rear of the instrument holds the truth table in proper alignment with panel markings. \$199.50. Curtis Electro Devices, Inc., Box 4090, Mt. View, CA 94040. Phone (415)-964-3136. 221

CASSETTE TERMINAL OFFERS ON-LINE AND OFF-LINE CAPABILITIES. In its online mode, the 345 CMTT ties in directly with all major minicomputers. As an off-

line device it permits serial modem-to-modem communications at transfer rates of 110 and 300 baud, (10 and 30 cps). It incorporates logic for remote control (off-line) by transmission of standard ASCII control characters from serial devices. When operating in an on-line mode, the CMTT allows communication between the attached serial devices and the computer. It is supported by an extensive library of cassette-operating system software. \$3300. DICOM INDUSTRIES, 715 N. Pastoria Ave., Sunnyvale, CA 94086. Phone (408)732-1060.

FLEXIBLE DISKETTE SYSTEM SPEEDS COMPUTER DATA ENTRY. The 3740 incorporate flexible diskettes for capturing data. They resemble small phonograph records yet can hold as much information as 3000 80-column punch cards and are reusable. They are oxide coated Mylar devices with 48 tracks/in., sealed in 8-in. sq. protective plastic envelopes. With a density of 3200 bpi, each has a capacity of 5078 8-bit bytes/track and 391,006 bytes/diskette. A new drive unit spins the diskette at 360 RPM, and a read-write head accesses the diskette through a slot in the plastic envelope. Data rate is 250,000 BPS, and track-to-track access time in 50 µsec. International Business Machines Corp., 1133 Westchester Ave., White Plains, NY 10604. Phone (914)696-1900. 223



FULL-GRAPHICS CAPABILITY AVAILABLE FOR MINICOMPUTERS. The BP-721 interface converts any X-Y oscilloscope or larger X-Y display into a full-graphics display terminal. The unit, with its internal semiconductor refresh memory, enables the minicomputer user to plot points, lines, alphanumerics and real-time dynamic displays. Software is supplied for programming with simple BASIC language commands. Installation, which requires no hardware modification, is accomplished by inserting it into a vacant minicomputer slot and connecting to the X, Y and Z inputs of the oscilloscope. Interfaces for all Data General NOVA series minicomputers are available. \$1095. Megatek Corp., 1526 W. 240th St., Harbor City, CA 90710. Phone (213)530-224



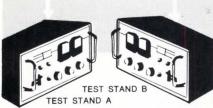
MEMORIES COMBINE MINI-DISC AND DRUM TECHNOLOGIES to provide OEM manufacturers with a large data base in a 10-in. package weighing only 25 lbs. Storage capacities range from 16k 16-bit words to 262k words. Five models are offered: 4-, 8-, 16-, 32- and 64-track versions. Each has a 4096 words/track recording density. Word transfer rate is 122 kHz. Designed for terminal applications Series 55 processes data in a bit-serial stream at a rate of over 2-million BPS. Cost/bit ratios are attractive, particularly for applications such as pointof-sale-systems and inventory control systems. \$1550 for 16k-words to \$2950 for 262k. Datum, Inc., 170 E. Liberty Ave., Anaheim, CA 92801. Phone (714)879-225

KEYBOARDS FEATURE MOS ENCODING AND BI-PAC SWITCHES. A series of TTY-33 keyboards, featuring redundant contact BI-PAC switch modules and a 40-pin ceramic MOS chip for encoding 4 mode ASCII code, and offering N-key rollover, is available for interactive computer printing, display terminals and related ASR-33 compatible applications. The encoder design reliably maintains sequential key outputs even at maximum "burst" typing speeds. MOS chip operation also requires less than 200 mA power consumption. The +5 to -12output is Tri-State or TTL compatible. Price: \$125 in 1-25 quantity; \$59 in 5000 quantity. Controls Research Corp., 2100 S. Fairview, Santa Ana, CA 92704. Phone-(714)557-7161. 226

PRINTERS INTERFACED TO SYSTEM 7.

The controller can interface 2 Centronics printers, 1 located locally at the System 7 site and 1 containing a modified RS-232 interface board located up to 1000 ft away. Under software control, data can be transmitted to either or both printers. The controller receives a parallel 8-bit input from the CPU and transmits this data in parallel to the local printer and serially to the remote printer. Baud rate for the remote printer is selectable up to 9600. \$975. Centronics Data Computer Corp., 1 Wall St., Hudson, NH 03051. Phone(603)883-0111.





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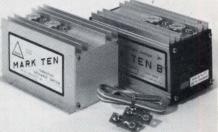
MULTIPLEXER CONTROLS SELECT ANY SEGMENT OF CHANNEL ADDRESS. Intended for use in data acquisition systems to examine a selected group of channels,

the Model GMC logic control system permits choice of the first and last channels from front panel switches. Any number of sequential channels can be selected with this system. It is useful in process control, automatic production testing and scientific data analysis systems where the A/D converter and its display can be used as a timesaving measuring device. A separate control permits selection of either continuous re-cycling, or "single-shot" starts of each scan sequence. Prices start at \$755 for a 32-channel system. Preston Scientific, Inc., 805 E. Cerritos Ave., Anaheim, CA 92805. Phone(714)776-6400.

REAL-TIME PROGRAMMERS SEQUENCE PROCESS OPERATIONS. These solid-state units control 10 to 30 operations in any sequence. They provide up to a 16.5-hr. range and have an accurate electronic clock which can select down to 1/4-sec intervals for process control. Real-time operation time intervals can be programmed directly in hr, min and sec. Time intervals can be altered by simply removing or adding pins on the front panel without affecting other time intervals. All time intervals are accurate and repeatable to ± 0.25 sec. \$2500 to \$4900. Hugle Industries Inc., 625 N. Pastoria Ave., Sunnyvale, CA 94086. Phone-(408)738-1700. 230

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I/O INTERFACE BOARD FOR NOVA MINIS. The interface board contains all necessary circuitry to connect the user's logic to the I/O bus of any Nova series minicomputer. Provision is made for multiple controllers on a single board. It can mount up to 105 14- and/or 16-pin wire-wrap sockets with allowances for mounting of 24- to 40-pin sockets. User logic is connected to the I/O logic by a set of wire-wrap posts dividing the 2 sections. The interface is packaged on a 15 × 15 in. board with 2 100-pin connectors and can be mounted directly in a single slot of the Nova computer chassis. The user has the option of up to 4 16-bit registers, a data-channel interface and counter logic for zero word count detection. \$250. MDB Systems, Inc., 981 N. Main St., Orange, CA 92667. Phone(714) 639-7238. 232



GENERAL-PURPOSE COMPUTER SYSTEM STRESSES MAINTAINABILITY. The 530, designed for both scientific and commerical applications, is priced in the minicomputer range while offering many capabilities usually available only with larger computers. These include the ability to enter and retrieve information with a minimum of computation interruption, advanced methods for interrupting normal computation to take care of high-priority items and a capability for computer trouble-shooting via long-distance telephone lines. \$21,700. Xerox Corp., Stamford, CT 06904. Phone(203)-329-8711.

HIGH-SPEED ADD-ON MEMORY AVAIL-ABLE FOR PDP-11. Computer users can add the Model SG-11 semiconductor memory system to PDP-11 computers in the field. The self powered memory unit is contained in its own enclosure and is fully hardware and software compatible. It cables directly to the Unibus connector block and operates with a cycle time of 650 to 750 nsec. It is fully wired for expansion to 28k or memory parity available as an option. The basic 4k system costs \$2200 and a full 28k memory bank \$7300. Signal Galaxies, Inc., 6955 Hayvenhurst Ave., Van Nuys, CA 91406. Phone(213)988-1570. 234



SWITCHLESS CALCULATOR FEATURES PRESSURE-SENSITIVE KEYBOARD. The Model 370 8-digit calculator adds, subtracts, divides, multiplies and performs chain and mixed calculations. Additional features include credit balance, 4 function constant, floating decimal, automatic battery life extender, storage power of 10⁻²⁰ to 10⁷⁹ and low battery indicator. It measures 5-7/8 in. × 3 in. × 3/4 in. and weighs 6-1/2 ozs. Melcor Electronics Corp., 1750 New Highway, Farmingdale, NY 11735. Phone(516)694-5570.

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10-MHz SCOPE FOR ONLY \$425. Model 5310 can be ac or dc coupled and uses digital circuitry for stable triggering to 15 MHz. Vertical ranges are from 10 mV/cm to 50V/cm in 12 calibrated ranges and accuracy is $\pm 3\%$. An uncalibrated continuouslyvariable control increases sensitivity to 5 mV/cm. Sweep ranges are from 0.5 μsec/ cm to 0.2 sec/cm in 18 calibrated steps and linearity is 1% through the full horizontal sweep. The 5310 has an 8×10 -cm display. A 2.5-kV accelerating potential and P31 phosphor produce a clear, high-contrast trace. Hickok Electrical Instrument Co., 10514 Dupont Ave., Cleveland, OH 44108. 199 Phone(215)541-8060.



TWO FREQUENCY COUNTERS IMPROVE RECEIVER TUNING ACCURACY and stability by employing digital automatic frequency control (DAFC). Designated the DRO-315 for 100-kHz-to-500-MHz coverage, these units provide a receiver tuned frequency lock to within ±1 kHz as long as power is applied. The counters have selectable presets of 8, 10, 21.4 and 60 MHz and 1 optional preset selected at the time of order. Both counters provide LED displays and each requires only 1-3/4 in.

of vertical space in a standard 19-in. rack. Watkins-Johnson Co., 6006 Executive Blvd., Rockville, MD 20852. Phone(301)881-3300. **200**



DMM/COUNTER WITH 4-1/2-DIGIT GATED FREQUENCY COUNTING to 200 MHz includes 5 ranges of dc and ac volts with 10-μV resolution, 5 ranges of dc and ac current with 10-nA resolution, 6 ranges of resistance with 10-M Ω resolution and 5 ranges of true frequency counting. The counter's wideband frequency multiplier option allows low-frequency signals to be measured virtually error-free in 1/100 of the normal time, independent of zero-crossing distortion. A 10-Hz signal can be resolved to 0.001 Hz in 10 sec. Basic accuracy for dc volts and frequency is 0.01%, 0.02% for resistance and 0.1% for ac volts. \$590. Valhalla Scientific Inc., 7707 Convoy Ct., San Diego, CA 92111. Phone(714)277-201 2732.



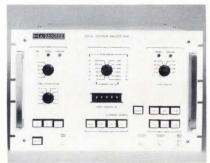
LOW-COST LAB STATION. The EU-99A lab station includes such instruments as the EU-70A 15-MHz dual-trace scope and the EU-81A 1-Hz-to-1-MHz function generator. Digital instrumentation includes the EU-801A A/D designer (a system that allows investigation of virtually any analog or digital circuit) and the additional plug-in logic circuit cards necessary to convert it to a computer logic training system. As the need arises and the budget permits, additional instrumentation and components may be added. Teaching/learning software is included and requires no prior electronics background. \$2035. Heath/Schlumberger Scientific Instruments, Benton Harbor, MI 49022. Phone(616)983-3961. 202

features true-rms, average and peak indications. Other features include a linear frequency range from 0.5 Hz to 500 kHz, sensitivities from 1 mV to 300V full scale, calibrated amplification 60 dB and ac and dc outputs. A hold function for peak measurements and a provision for a variable-meter time constant are included. Two built-in

meter time constants, interchangeable meter scales and line or battery operation are other features. B&K Instruments, Inc., 5111 W. 164th St., Cleveland, OH 44142. Phone-(216)267-4800. 203



32-MHz AUTOMATIC COUNTER features a counting range from 5 Hz to 32 MHz, a crystal controlled clock, a 5-digit long-life LED display and automatic ranging for full resolution. Decimal points are automatically positioned as is the display of units of measurement and non-significant zeros are automatically blanked. Operating from the ac line or an optional battery pack, the Model 150A has a built-in test position and an overrange indicator in the manual mode. \$475. The optional 10-hr NiCd battery pack, including charger and carrying case, costs \$200. United Systems Corp., 918 Woodley Rd., Dayton, OH 45403. Phone-(513)254-6251.



1024-LINE REAL-TIME SPECTRUM AN-ALYZER. Model DSA-2004 covers a center-frequency range of dc to 40 kHz for use in radar and sonar signal processing, acoustic spectrum-level measurement, and noise and vibration analysis. It features digital circuitry for translation, filtering, Fourier analysis, post processing and storage. The unit, which has modular construction and built-in self-test capabilities, provides resolutions selectable from 0.01 to 40 Hz and an analysis bandwidth of 10 Hz to 40 kHz. The DSA-2004, with an internal simultaneous multi-channel processing capability, provides linear and logarithmic outputs for electrographic recorders, X-Y chart recorders, CRT displays and scopes. Sanders Associates, Inc., 95 Canal St., Nashua, NH 03060. Phone(603)885-5875. 205



Dc RESISTANCE BRIDGE Model 1666 will measure both 500 $\mu\Omega$ and 7 \times 10 $^{9}\Omega$ to an accuracy of better than 1%, and less extreme resistance values to an accuracy of better than 0.02%. Four ±0.02% bridge circuits and 6-digit resolution give it a total measurement range from 1 $\mu\Omega$ (10⁻⁶ Ω) to 1 teraohm ($10^{12}\Omega$) It easily measures both open (leakage) and closed (contact) resistance of relays and switches, plus forward and reverse diode resistances. It also measures transformer winding resistance, insulation conductance and resistance thermometers, and dielectrics. Comparisons between similar resistances can be made to a resolution of 2 parts per million. \$950. General Radio, 300 Baker Ave., Concord, MA 01742. Phone(617)369-4400.

20-MHz FUNCTION GENERATOR Model 420 features fequency modulation with a range of better than 200:1, a 10V p-p output into 50Ω through an 80-dB attenuator (20V p-p open circuit), a ±10V dc offset, trigger and gate modes. The generator includes sine, square and triangular wave outputs with sine wave distortion under 0.5% to 200 kHz and square wave aberration of 5%. The generator's frequency range is from 2 Hz to 200 MHz in 7 decades at $\pm 2\%$ -ofreading accuracy (to 2 MHz) and ±10% to 20 MHz. \$785. Systron-Donner Corp., Datapulse Div., 10150 W. Jefferson Blvd. Culver City, CA 90230. Phone(213)836-6100.

FUNCTION GENERATOR Model 5700 provides a balanced 600Ω output over 0.002 Hz to 2 MHz. This low-cost instrument (only \$295) provides sine, square and triangle waveforms with a frequency accuracy of $\pm 5\%$ of reading for the entire 1000:1 tuning range of the dial. The output is controlled by a 2-position dB step attenuator and amplitude vernier, providing both a 50Ω single ended and a 600Ω balanced output. The outputs provide 15V p-p and 30V p-p, respectively. A 1V auxiliary output is also supplied. Krohn-Hite Corp., 580 Massachusetts Ave., Cambridge, MA 02139. Phone(617)491-3211.



CHECK NO. 60

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Heat Sinks

Corporation of America.

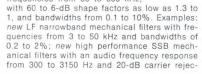
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CHECK NO. 62



NOW! A REAR-PROJECTION DISPLAY FOR UNDER \$6

IEE introduces the Series 1100 Readout, the first Rear-Projection display under \$6. Series 1100 costs far less than equivalent Rear-Projection models, yet packs all the similar features. We're talking of a .6" character displaying bright, crisp messages, numerals, symbols or colors, easily read from 20 feet. The

total plug-in package (12 positions per readout) offers quick front panel removal for lamp and film servicing. Series 1100 accepts 5, 14 or 28 volt lamps compatible with DTL/TTL input

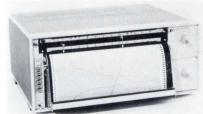
with a light output of 100 ft-L. Equally inexpensive is the mating Driver Decoder, the long life Series 7800.

The Series 1100, low cost . . . high reliability . . . from the world leader in Rear-Projection displays. Give us a call. Industrial Electronic Engineers, Inc., 7740 Lemona Ave., Van Nuys, Ca. 91405,

Telephone: (213) 787-0311. TWX 910-495-1707. Our European Office: 6707 Schifferstadt, Eichendorff-Allee 19, Germany, Phone: 06235-662.

*In quantities of 1000

EQUIPMENT



TWO OEM 1 AND 2-PEN STRIP-CHART **RECORDERS** feature more than 50 options that let the user customize these 10-in. units for specific applications at low cost. Designed to allow a user to choose spans, speeds and other options, the single-span Model 7130A/B (2-pen) and the Model 7131A/B (1-pen) units can be ordered with one of 6 available chart speeds from 6 in./ min to 1 in./hr. Options are available for adding a second speed; 4 or 8 speeds plus an external input. One of 6 input spans from 1 mV to 100V per channel can be specified. Input is single ended and floating, with 1 M Ω resistance on all spans. Price is \$1250 (7130A/B) and \$850 (7131A/B). Hewlett-Packard Co., 1501 Page Mill Rd., Palo Alto, CA 94304. Phone(415)493-1501.

209

FIELD-EFFECT LIQUID-CRYSTAL-DISPLAY

DPMs include 2-1/2-, 3-1/2- and 4-1/2digit models with prices starting as low as \$47 each (2-1/2-digit unit in 100 quantities). The TA300 Series features custom MOS/LSI ICs, and a proprietary Poly-Tek A/D conversion technique. It includes 39 different models with plug-in voltage and current range modules, bipolar operation, a shielded-transformer power supply, all-metal cases and optional fully isolated parallel BCD output. Each DPM requires 1.5W of input power. I/O signals are provided for external digital linearization, curve fitting and scale factor control and serial BCD output is standard. Tekelec, Inc., 31829 W. La Tienda Dr., Westlake Village, CA 91361. Phone(213)889-2834. 210

THREE-IN-ONE VOM/TRANSISTOR TEST-

ER Model HM-310 combines all the functions of a VOM with the added capability of measuring capacitor and transistor parameters. The self contained unit features $100\Omega/V$, an impedance taut-band movement, a 3-in. mirrored scale and burn-out protection. Measurement capabilities are: ac and dc voltage, resistance, ac and dc current, dB, capacitance, and transistor alpha, beta and leakage current. $2-1/2 \times 5-1/6 \times 6-1/2$ in. \$59.95. HM Electronics, Inc., 10975 San Diego Mission Rd., San Diego, CA 92108. Phone(714)280-6050.

211



INKLESS-WRITING SERVO RECORDER.

The Speed Servo II 10-in. strip-chart recorder uses thermal-sensitive chart paper and an electrically heated stylus. This provides advantages of convenience use in applications with excessive oscillation of the writing element, very slow-changing input signals, slow chart speeds and continuous, unattended long-term recording. The user gets a long stylus life (8700hr min.), the ability to tilt the recorder to any position and no limitation of recorder response. The recorder can be converted easily to throw-away ink-pen cartridges. Warm-up time is a nominal 10 sec. Esterline Angus, A unit of Esterline Corp., Box 24000, Indianapolis, IN 46224. Phone(317)244-7611.

REGULATED LAB POWER SUPPLIES. With only 100 μ V of ripple, the PSR series of dc power supplies also feature load regulation of 0.01%. Other features offered are overload protection, an accurate D'Arsonval meter calibrated for voltage and current, and current-limiting burnout protection. Two models are available: Model PSR 12-25 whose output is 0 to 25V dc at 500 mA and Model PSR 12-50 with an output of 0 to 50V dc at 250 mA. Both operate from 105 to 125V ac, 50/60 Hz. Dimensions are 5-1/4 in. high \times 3-1/2 in. wide \times 9-1/8 in. deep. EPSCO, Inc., 920 Westwood Ave., Addison, IL 60101. Phone(312)543-0410.

213

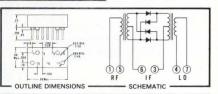
GRAPHIC-DATA DIGITIZING SYSTEM USES MAGNETIC CARDS and features digital display of the X-Y coordinate values and a stylus to digitize the points. Data from graphics, patterns, layouts, X-ray negatives, photographs, templates and drawings is recorded on a magnetic stripe on standard tab cards. Storage capacity up to 26,000 bits combines the data density of magnetic tape with the convenience of cards. Visual information may be typed, stapled or written on the cards and filed for future use. Price of a complete system is \$6995. AM economy model with only Teletype and RS-232 outputs costs \$3600. Elcomp General, 1937 Kilmer Dr., Placentia, CA. Phone-(714)540-4412.

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MODEL	LO & RF FREQUENCY IF	MIN. ISOLATION LO TO RF	MIN. ISOLATION LO TO IF	MAX. CONVERSION LOSS (SSB NF)	QUANTITY 500
750	.002-30MHz DC-30MHz	50db: .002-20MHz 40db: 20-30MHz	45db: .002-20MHz 35db: 20-30MHz	6db: .010-20MHz 8db: .002-30MHz	\$13.20
751	.01-100MHz DC-100MHz	50db: .01-10MHz 40db: 10-50MHz 35db: 50-100MHz	45db: .01-10MHz 35db: 10-50MHz 30db: 50-100MHz	6.5db: .1-50MHz 8db: .01-100MHz	\$12.00
753	.4-400MHz DC-400MHz	35db: .4-100MHz 25db: 100-400MHz	30db: .4-100MHz 20db: 100-400MHz	7db: 5-100MHz 10db: .4-400MHz	\$ 9.10
755	.05-200MHz DC-200MHz	50db: .05-10MHz 40db: 10-100MHz 35db: 100-200MHz	40db: .05-10MHz 35db: 10-100MHz 25db: 100-200MHz	6.5db: .2-100MHz 8db: .05-200MHz	\$12.00
757	.25-300MHz DC-200MHz	50db: .25-10MHz 40db: 10-100MHz 35db: 100-300MHz	45db: .25-10MHz 30db: 10-100MHz 25db: 100-300MHz	6.5db: .5-150MHz 8db: .25-300MHz	\$12.00
759	1-500MHz DC-500MHz	50db: 1-50MHz 40db: 50-200MHz 35db: 200-500MHz	45db: 1-50MHz 35db: 50-200MHz 25db: 200-500MHz	6.5db: 5-200MHz 9db: 1-500MHz	\$12.00

All specifications apply in a 50 ohm system with LO input level of +7dbm Maximum Peak input power: 50mw — Maximum Peak input current: 50ma — Storage temperature: -65 to 125°C — Operational Temperature: -54 to 100°C.



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EQUIPMENT



TDM-MODEM TEST SET Model 1310 is designed to test and analyze all digital datacommunication systems which may contain low-speed asynchronous or high-speed synchronous modems, and time-division or frequency division multiplexers. Part of the Range Rider line, the Model 1310 generates and analyzes pseudo-random data sequences in the form of start-stop asynchronous characters. 5-, 6-, 7-, or 8-level data characters with odd, even or no parity and 1, 1.4 or 2 stop bits may be selected for analysis. It calculates and directly displays bit, character or block error rates and mark-to-space transitions. Three pseudorandom patterns, alternate, mark, space, R, Y and asterisk, and U sequences are also generated and analyzed for errors. \$2150. International Data Sciences, Inc., 100 Nashua St., Providence, RI 02904. Phone(401)-274-5100. 215



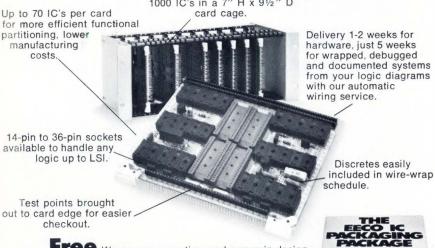
AUTOMATIC DIGITAL-CIRCUIT TESTER Model 721A provides verification of truth tables and dc parametric specifications of digital circuits having up to 16 pins. Operation is accomplished by simply inserting a device program card and selecting the appropriate limits for the family being tested. Model 721A may be readily used as a classification tool, an incoming-inspection tool, a high-speed wafer test tool, or an automatic digital test tool used in conjuction with a circuit handler. It can run a typical device test sequence in approximately 20 msec performing thousands of combined functional and parametric tests to an accuracy of ±1.5%. \$4950. Computest Corp., 3 Computer Dr., Cherry Hill, N J 08002. Phone(609)424-2400. 216



3D DISPLAY AND CONTROL FOR REAL-TIME SPECTRUM ANALYZER. Model SAI-503 generates and controls a 3D display of spectral data derived from the Honeywell/Saicor real-time spectrum analyzer. It interfaces with standard storage displays and scopes. The 3D real-time display spectrum amplitude vs frequency vs time - is obtained by displacing successive frequency scans vertically and horizontally to form an isometric pattern. The SAI-503 operates in 4 modes: continuous, single, triggered and 2D. Other features include simple controls for X, Y, and Z-axis positioning and scaling, a threshold control that eliminates background noise and automatic or manual display erase. \$900 when purchased with Saicor real-time analyzer. Signal Analysis Operation, TID, Honeywell, Inc., 595 Old Willets Path, Hauppauge, NY 11787. Phone(516)234-5700. 217

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FLOATING DIFFERENTIAL AMPLIFIER FEATURES 1-μV FULL SCALE. The X-MOD 706 features input isolation of 10 Gigaohms (1010) and 20 switch-selectable input ranges from 1 μ V to 1V full scale. Full scale output is ±10V at 10 mA and is short-circuit proof. A center-scale null meter permits rapid system calibration and adjustment. Any of 3 bandwidths - 0.1, 1 and 10 Hz can be selected by a front-panel switch. Long-term stability is 0.1% for 6 months, and temperature drift is only 0.1 μ V/°C. CMR is 120 dB from dc to 60 Hz. \$695. Preston Scientific, Inc., 805 E. Cerritos Ave., Anaheim, CA 92805. Phone(714)776-6400. 218

LSI MEMORY TEST SYSTEM. Spartan 770 "Bit Rider" for testing semiconductor memories features a modular design that allows each customer to customize his Spartan to meet individual requirements. It includes 32 memory channels, 15 drivers and a highspeed, 10A resolution comparator. The unit offers 1-nsec timing resolution with 1-ppm clock stability. A microprogrammable RAM allows for test pattern storage and pattern generation at 5 MHz to test efficiently both recursive and random logic circuits. The Spartan offers a diagnostic panel for device/ program trouble shooting. \$59,770. Western Digital Corp., 19242 Red Hill Ave., Newport Beach, CA 92663. Phone(714)-557-3550.

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COMPONENTS/MATERIALS



VARIABLE CAPACITORS HAVE BEAM LEADS. The Series 9401 trimmers feature Os of >10,000 at 100 MHz and lead configurations perfect for stripline, hybrid circuit and pc mounting. They are uniquely suited as replacements for many chip capacitors and provide a means of easily and reliably trimming without cut-and-try adjustment techniques using abrasives. Applications include impedance matching and trimming solid-state circuits, balancing semiconductors and microwave components, VHF-UHF coupling, as well as equalizing fixed capacitors. The series includes 5 models having capacitance ranges from 0.2 to 4.0 pF with additional models available up to 50.0 pF. All models have test voltages of 500V dc. 95¢ each, in volume. Johanson Mfg. Corp., 400 Rockaway Valley Rd., Boonton, N J 07005. Phone(201)334-236



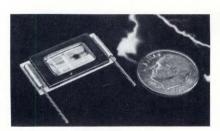
TEMPERATURE COMPENSATED UNIT COMPARES WITH OVEN OSCILLATORS. Model K1098A crystal oscillator has none of the inherent drawbacks of oven oscillators such as warm-up time or on/off power surges for heating. The elimination of the oven and its thermostat eliminates the majority of reliability problems. Stability is $\pm 1 \times 10^{-7}$ over the temperature range of 0°C to 55°C. It is well suited for application in frequency counters and in laboratory instruments such as synthesizers and signal generators. Motorola Communications and Electronics, Inc., 1301 E. Algonquin Rd., Schaumburg, IL 60172. Phone(312)358-7900. 237

INDUCTOR REPLACES DEVICES, 10
TIMES LARGER. The magnetically shielded,
transfer molded chip inductor measures

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LONG-LIFE, WOUND-TYPE FLAT MO-TOR. Thickness is about 1/3 to 1/4 of conventional motors. Previously, flat motors used a printed rotor - the coil being printed on a resin disc to form the rotor. Conventional printing techniques limited the number of flat motor windings, and made the motor unsuitable for high-voltage operation. This is an improved version of the commutator motor. Motor life is 2 to 3 times longer than conventional commutator motors and efficiency is about 20% higher. It's capacity can be increased to 800W, and it can be operated on household current by using a diode. Matsushita Electric Corp. of America, Pan Am Bldg., 200 Park Ave., New York, NY 10017. Phone(212)973-239 4980.



MICRO ARRESTER IS FIRST TO USE CERMET CONSTRUCTION. It protects high-frequency circuits from damage by high-voltage pulses caused by transients and lightning. It is designed to edge mount in a minimum of board area. A sealed-glass

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LEDs PROVIDE FULL-FLOOD VIEWING.

The RL-4403 and RL-4440 contain a gallium-arsenide-phosphide LED in a newly designed red-diffusive molded package. The viewing area extends 0.140 in. beyond the face of the mounting clip allowing wide angle viewing. The radiating area is 0.2 in. in diameter and presents an ideal visual display for indicating functions on instruments and control panels. For easy installation, the leads are 0.025 sq for wire-wrapping, soldering or pc board mountings. In addition, these devices operate on only 1.7V at 20 mA. The RL-4403 is rated at 0.8 MCD min. at 20 mA, and 1.2 MCD typical. The RL-4440 is a lower brightness, lower-cost version of the RL-4403. The RL-4403, 65¢; RL-4440, 49¢. Litronix, Inc., 19000 Homestead Rd., Cupertino, CA 95014. Phone (408)257-7910. 241



HIGH-PERFORMANCE HEAT SINKS COST ONLY A PENNY. The Penny Pincher heat sink duplicates the most effective heat transfer methods now in use, such as fin tubing. It uses any number of stackable fins, depending on the heat dissipation needed and is made of anodized aluminum for corrosion protection and high thermal conductivity. Available in type TO-18 and TO-5 with a 1-sq. in. radiation surface per fin, they can be used with silicone grease compound to further reduce the thermal impedance between the semiconductor case and the heat sink. Carbidex Corp., 1 Carbidex Rd., Southgate, MI 48195. Phone(313) 287-8600. 242



CHIP INDUCTOR. The MAGNA-CHIP measures 0.080 × 0.080 in. with a maximum thickness of 0.080 in., dependent upon value. This chip inductor is supplied with palladium-gold terminations usable with a wide assortment of attachment and bonding techniques. The inductance range is 0.2 to 5 μ H, with a current rating of up to 1A. Q is a minimum of 20, dependent upon the inductance value. Typical T.C. is 150 ppm/°C over temperature range of -55°C to +125°C. Series resistance is typically 0.2 to 0.3Ω for values less than 0.5 μ H and up to 1Ω at the 5 μ H level. Standard tolerance is 20%. \$2.50 to \$5 in 1000piece quantities. San Fernando Electric Mfg. Co., 1509-35 First St., P.O. Box 351, San Fernando, CA 91341. Phone(213)365-243

THERMISTOR IS INDIRECTLY HEATED.

The K365 unit features a combination heating element and thermistor bead enclosed in a glass bulb and is unaffected by changes in ambient conditions. When power is applied directly to the thermistor bead, the magnitude of temperature change is based on a temperature increase of 1°C per each 0.015 mW (nominal). Applied power of 0.04 mW (nominal) to the heater element will cause the element to indirectly heat the thermistor bead, resulting in a temperature increase of 1°C and changing its resistance on a typical heater power vs. bead resistance curve from $50,000\Omega$ to $15,000\Omega$. Fenwal Electronics, 63 Fountain St., Framingham, MA 01701. Phone(617)872-8841.

FREQUENCY OF UNIVERSAL ACTIVE FILTER EXTENDED. Utilization of multiloop negative feedback allows simultaneous highpass, lowpass and bandpass transfer functions. Independent tuning of gain, center frequency and Q is accomplished with the addition of external resistors. Qs as high as 1000 can be obtained at frequencies below 100 kHz. The FS-30 is in a 14-pin DIP configuration measuring 1.5×0.5 in. Operating temperature range is 0°C to 70°C, and power consumption is 156 to 225 mW at ±15 to ±20V. Kinetic Technology, Inc., 3393 De La Cruz Blvd., Santa Clara, CA 95050. Phone(408)296-9305. 245

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New Green Glow Lamp!

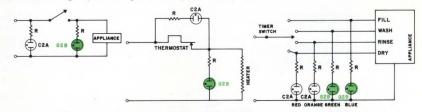


Finally, a broad spectrum bright green glow lamp from General Electric, that gives you greater design flexibility than ever before. It emits green and blue light with suitable color filters. It is called G2B.

What's more, the G2B is directly interchangeable electrically and physically with our high-brightness C2A red/orange/yellow glow lamp.

So you can use the G2B alone for 120 volt green indicator service. Or together with the C2A to emphasize multiple functions with color. For example: for safe/unsafe functions, dual state indications and to show multiple operations in up to 5 colors.

And remember. Both the G2B and C2A save you money because of their low cost, small size and rugged construction.



New Sub-Miniature Wedge Base Lamp.



If space for indicator lights is your problem, this new GE T-1% size all-glass wedge-base lamp is your solution. It measures less than ¼" in diameter.

The filament is always positioned

in the same relation to the base. It won't freeze in the socket, which virtually ends corrosion problems. And like its big brother — the T-3¼ wedge base lamp — it features a simplified socket design.

Three Potent Infrared Solid State Lamps (LEDS).



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The increased energy concentrated in a narrow 20° cone allows you to use less sensitive detectors. Or to operate the lamps at lower current. Or to space lamps and detectors

farther apart.

All are excellent matches for GE photodetectors and can be used in many photoelectric applications. They're also particularly useful in applications demanding an infrared source capable of withstanding severe shock and vibration.

To get free technical information on any or all of these lamps, just write: General Electric Company, Miniature Lamp Products Department, Inquiry Bureau, Nela Park, Cleveland, Ohio 44112.



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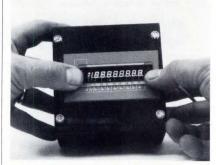
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COMPONENTS/MATERIALS



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MINI-MOUNT USED FOR BREADBOARD-ING LSI PACKAGES. Without drilling holes, you can assemble your circuit prototype on any flat surface. Pressure-sensitive adhesive on the back of the LSI-42 MINI-MOUNT holds it in place while interconnecting wires are installed. The mounts can be moved or exchanged as the circuit develops, yet will remain firmly in place in the finished assembly. This breadboarding system provides a fast and flexible method of assembling high-performance electronic circuits-including analog, digital and RF circuits from dc to the GHz region, without restraining you to a fixed-grid pattern. They come in different configurations to fit 14and 16-pin DIPs, 6- to 12-pin TO-5 style cans, transistors, inductors, resistors and other components. Cristiansen Radio, Inc., 3034 Nestall, Laguna Beach, CA 92651. Phone(714)497-1506. 247

YOKE MINIMIZES CIGARING ON ALPHANUMERIC DISPLAYS. The LY916 position-write yoke eliminates the need for electrostatic deflection plates. It is a high-speed high-Q ferrite-core deflection yoke featuring fast position and write and is excellent for resonant circuits. It has 0.25% perpendicularity and 0.1% maximum residual magnetism. The LY916 is for 90° deflection angle on a 1-7/16-in. CRT neck

diameter. (Also available for 52° and 70° deflection angle.) CELCO/Constantine Engineering Labs. Co., 70 Constantine Dr., Mahwah, N J 07430. Phone(714)621-2662.

THUMB SIZED XENON LAMP DELIVERS 200K cp. An arc lamp that measures only 2 cu in. is the reading light for optical character reading machines for computers. It provides an exceptionally consistent white light, ideally compatible with existing inks and papers formulated for use with OCR machines. It is the first arc-type light source in which the electrodes, reflector and lens are sealed into a single, pressurized unit. The lamp's efficiency is 3 to 4 times that of previous high-intensity arc lights, but the unit is only a tenth as large. The X6207 is a 150W unit whose light is virtually identical to sunlight. It can be operated in any position. EIMAC Div. of Varian Assoc., 301 Industrial Way, San Carlos, CA 94070. Phone(415)592-1221. 249

RADIAL LEADED MONOLITHIC CAPACI-TORS ARE EPOXY COATED. CLOVER CAPS are electrically and physically interchangeable with all popular leaded capacitors. They are available in 6 physical sizes with capacitance ranging from 2.2 pF to 4.7 mF. Fifty and 100V configurations are standard in 3 ceramic formulations. The Ultra-Stable series insures complete stability of capacitance with variations in temperature, frequency and voltage and also where low loss and/or high Q are necessary. The Semi-Stable series is designated for applications where moderate variations in capacitance may be tolerated. The General Purpose series is used in circuits where wider variations in capacitance are acceptable. Varadyne Capacitor Div., 2110 Broadway, Santa Monica, CA 90404. Phone (213)829-277 2984

FREQUENCY REJECTION FILTER. The filter has the characteristics of the Twin "T" R-C filter and is most suitable for applications where the pass frequencies lie outside of the 0.4 to 2.5 fn range. It produces optimum attenuation at 25°C (±10°C) with the rms input voltage less than 1.4V and dc input voltage less than 10V (dc). The filter has a minimum of 38 dBV attenuation when tested at 25°C, 0.7 Vrms input at the rejection frequency. Source and load impedances are 600Ω max. and $100 \text{ k}\Omega$ min., respectively. Price (in 100-500 quantities) is \$8.35. Dale Electronics, Inc., Dept. 860, P.O. Box 609, Columbus, NE 68601. Phone (402)564-3131. 254

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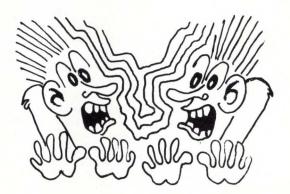
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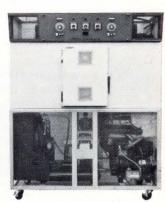
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LITERATURE



TELEPHONE-TYPE RELAYS. This 60-pg. catalog is a complete guide to applications and selection of telephone-type relays. The wide variety of relays include unitized subminiature, miniature, small and medium size relays. Also contained in this catalog are special features, accessories, enclosures, complete specifications, photos, dimensional line drawings, relay definitions, standard contact forms and ordering information. Magnecraft Electric Co., 5575 N. Lynch Ave., Chicago, IL 60630.



A/D AND D/A CONVERSION DEVICES. A comprehensive 3-ring binder catalog contains detailed electrical and mechanical information on a line of data-conversion modules subsystems and systems. Products described are A/D and D/A converters, sample-and-hold modules, digital panel meters, dc power supplies and data acquisition systems. Datel Systems, Inc., 1020 Turnpike St., Canton, MA 02021. 268



4-1/2-DIGIT DMM. Performance characte istics, design features and complete specifications of the Data Precision Model 245, 4-1/2-digit, low-cost, portable digital multimeter (DMM) are presented in an 8-pg. brochure. Model 245 is a \$245 portable instrument that packs the performance of expensive laboratory instruments into a pocket sized unit. Data Precision Corp., Audubon Rd., Wakefield, MA 01880. 276

BULLETIN DETAILS EXPANDABILITY IN CMOS LOGIC GATES. The bulletin outlines SSSI method of employing added buffer stages and is complete with diagrams on the configurations possible. Also carried are tables on dynamic and output drive characteristics as well as circuit capabilities and overall performance characteristics. Those interested in obtaining the details on CMOS EXPANDABLE GATES should address their requests, specifying Bulletin AN-102, to Solid State Scientific, Inc., Montgomeryville, PA 19102.

MEASUREMENT AND CONTROL HAND-BOOK. The 288-pg. book begins with an introduction to measurements and describes in detail elementary transducer elements through design considerations of complete measurement systems. It is written in a manner which permits the information to be readily understood by a wide range of readers. Mathematical discussions are minimized in favor of description, illustrations and other graphic presentations. Free to transducer and controls instrumentation designers. \$7.95 to others. Schaevitz Engineering, P.O. Box 505, Camden, N J 258 08101.

page "Solid-State Display and Optoelectronics Designer's Catalog" contains data sheets for all of HP's line of opto devices and solid-state displays. The four sections of the catalog contain detailed operating characteristics of photodetectors, isolators, LED lamps and LED displays. Each section has a selection guide high-lighting characteristics of each device important to the designer. Hewlett-Packard, Inquiries Manager, 1501 Page Mill Rd., Palo Alto, CA 94304.

ANALOG PANEL METERS. This 6-pg. brochure describes API's line of analog panel meters. Shown are Series 7000 panel meters which feature advanced styling with a 3-way choice of mounting (front, bezel or lens) and slide-out scales. Taut-band movement and 1% tracking are standard in most popular dc ranges. Prices and dimensions are listed. LFE Corp., 1601 Trapelo Rd., Waltham, MA 02154.

MINIATURE NEON LAMPS. The 127-pg. book, *Miniature Neon Lamps: Elements of Electronic Versatility*, gives detailed information on aspects of construction and applications as indicators, in voltage regulation and reference, and as electronic circuitry components. A handy reference to the characteristics of most available neon lamps also is included. Glowlite Corp., Sub. of El-Tronics, Inc., Pauls Valley, OK 73075

SEMICONDUCTOR FUSE HANDBOOK. It deals with fast-acting fuses, fuse characteristics, coordinating fuses with semiconductors and other applications. Copies of the 100-pg. handbook, HB-50, are available by writing on company letterhead to International Rectifier Corporation, Semiconductor Division, 233 Kansas Street, El Segundo, CA 90245.

PHASE AND AMPLITUDE RESPONSE OF A VARIABLE ELECTRONIC TIMER is the title of a 16-pg. application note that describes a simple, general method for determination of phase and amplitude response of high-pass, low-pass and bandpass filters for 4-pole Butterworth and Bessel filters. Tables and normalized plots of phase and amplitude response of high-pass, low-pass and commonly used bandpass settings are provided. A complete set of data including cutoff frequencies, center frequency, bandwidth, noise bandwidth and filter gain is tabulated for any bandpass setting. Ithaca, Inc., 735 W. Clinton St., Ithaca, NY 14850. 266

CATALOG LISTS DATA FOR SNAP ACTION SWITCHES. Included in the catalog are complete product listings, engineering drawings, specifications, operating characteristics, and technical data on general purpose panel-mount pushbutton, low-torque, subminiature, open type, and standard gold "crosspoint" contact switches. A handy switch selector-locator gives instant access to the entire line, and makes finding the right snap-action switch for any application just a matter of seconds. Cherry Electrical Products Corp., 3600 Sunset Ave., Waukegan, IL 60085.

THE 1973 INSTRUMENT CATALOG. Instrumentation covered in the 232-pg. catalog includes power, sweep, noise, AM/FM synthesized and programmable signal generator; precision terminations; fixed-variable and programmable attenuators; wave analysers and selective microvoltmeters; field-strength meters; spectrum analyzers; wideband microvoltmeters and power meters; impedance and reflection measuring equipment; R, L, C and Q meters; diode, transistor and IC testers; TV measuring equipment; and sound-level meters. Rohde & Schwarz Sales Co. (USA), Inc., 111 Lexington Ave., Passaic, N J 07055.

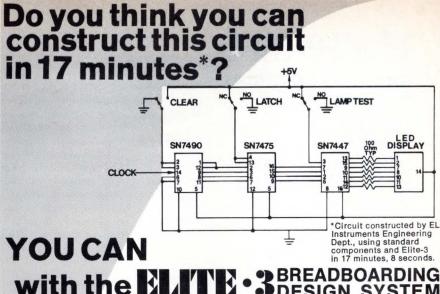
HANDBOOK SPEEDS SOLUTIONS TO **DESIGN PROBLEMS.** Nomographs offer a way to get fast, accurate answers to design problems, and it is a rare engineer indeed who hasn't wished he could reach into his file to get just the nomograph he needs. Filling this wish is the just published book Nomographs for Electronics: Instant Calculations for Designers by R. L. Peters, Ph.D. (Cahners Books, Boston, 320 pp., \$27.50). The author has assembled a collection of nomographs covering areas from basic electrical circuitry to supporting systems components such as motors and generators into an easy-to-use desk top directory. Cahners Books, Div. of Cahners Publishing Co., Inc., 89 Franklin St., Boston, MA 02110. 260

VOLTAGE VARIABLE CAPACITOR GUIDE AIDS SELECTION. A 4-page selection guide, with design selection parameters for over 350 voltage variable capacitor diodes presents the important design parameters of capacitance-Q-PIV-and device type. On the fourth page of the literature, another important design tool a nomograph, is included. By using this nomograph, the design engineer can quickly determine the capacitance ratio between two operating voltages V, and V, and/or the capacitance at V2 when the capacitance at V, is known. Codi Semiconductor Div. of Computer Diode Corp., Pollitt Dr. South, Fair Lawn, NJ 07401. 272

WAVEFORM GENERATORS are described in an 8-pg. catalog. The F200 Series are said to provide the most versatile and comprehensive features available. Complete specifications are provided as well as howto-do-it information for generating a variety of waveforms AILTECH, 19535 E. Walnut Dr., City of Industry, CA 91748. 259

MICROCIRCUIT DESIGN HANDBOOK.

This 24-pg. publication features comprehensive guidelines for converting discrete component circuits into thick-film microcircuits. It offers complete sections on microcircuit applications, designing by plan, production and product reliability. Among design considerations discussed are definitions of current performance and packaging requirements, and evaluations of component power dissipation, density and compatibility. Helipot Div., Beckman Instruments, Inc., P.O. Box 11866, Santa Ana, CA 92711. 267



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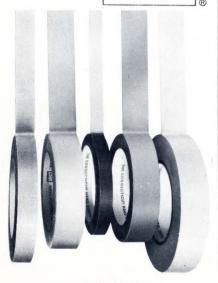
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LITERATURE

TAPE TRANSPORT INTERFACING. A 90-pg. OEM users manual provides the system designer with a comprehensive description of designing an interface for any of Pertec's synchronous digital magnetic-tape transports. The manual covers motion control, PE writing and reading, NRZI writing and reading, read-only and multiple transport configurations as well as a description of NRZI and PE tape formats. \$2.75. Pertec Corp., Dept 735, 10880 Wilshire Blvd., Los Angeles, CA 90024. **265**

LED PRODUCT SELECTOR GUIDE. The 72-pg. guide is divided into nine product categories—Light Sources; Ultra-Miniature Indicators; 0.625-in. Readouts and 0.205-in. Readouts; 0.300-in. Readouts; Decoder/Drivers; Switches and Opto-Isolators. All the units described work with or contain LEDs. For each product category, complete specifications, curves, applications and mounting details are given. Dialight Corp., 60 Stewart Ave., Brooklyn, NY 11237.

RELAY CATALOG describes 740 items. This 32-page, 2-color catalog completely describes stock relays for custom applications. Includes photos, dimensional drawings, specifications, prices and ordering information. Many new items have been added to this year's edition which includes general purpose, power, reeds, coaxial, telephone type, time delay, solid state, and mercury displacement relays. Magnecraft Electric Co., 5575 N. Lynch Ave., Chicago, IL 60630.

SUB-MINI/SOLID STATE LAMPS Cataloges include the Miniature Lamp (3-6253) in which the bulb sizes are given in in. and metric equivalents. This is the first time for any GE lamp cataloge. Others include the Sealed Beam Lamp (3-6251); the Sub-Miniature Lamp (3-6252); Solid State Lamps (3-6254) and an Index (3-6255) which lists all lamps and the specific cataloge in which they appear. Please identify cataloge number. Inquiry Bureau, General Electric, Nela Park, Cleveland, Ohio 44112.

ELECTRONIC INSTRUMENTATION CAT-

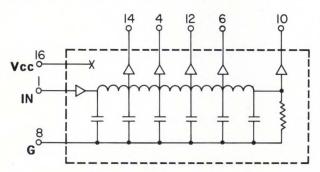
ALOG. This 52-pg. catalog provides detailed descriptions and specifications for the complete Heath/Schlumberger line of design and lab instrumentation. Included is a series of vhf counters that can provide capability to 600 MHz for as little as \$795, and a complete line of oscilloscopes, generators, power supplies and digital voltmeters. Digital instrumentation includes the lowest cost patchable minicomputer interface system on the market, complete digital systems for design, research and teaching, individual modules and a wide variety of plug-in circuit cards for functions in both the analog and digital domains. Heath/Schlumberger Scientific Instruments, Benton Harbor, MI 49002.

APP NOTE DETAILS OPERATION OF COUNTER TIME-BASE IC. The versatile low-power divider chain contained in the MK 5009 replaces eight TTL decade dividers, as well as other logic. Note describes these useful applications for the MK 5009 P. universal time-base circuit: A low-power frequency counter; wide range pulse generator; thumbwheel programmed timer. Also included is descriptive information about the 5009's on-chip oscillator circuit. Along with frequency counters, the 5009 is useful in a variety of other instrument, timer and clock applications. Mostek Corp., 1215 W. Crosby Rd., Carrollton, TX 75006.

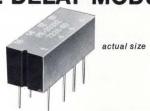
EPOXY B REPORT. This 16-pg. booklet describes the properties and test results of a new semiconductor IC package material. The report includes background information on the development of Epoxy B and the results of various studies conducted on devices made with Epoxy B. The test data is based on thermal intermittent studies, operations life tests, moisture resistance tests including humidity at high temperature and salt atmosphere, and other considerations. National Semiconductor Corp., 2900 Semiconductor Dr., Santa Clara, CA 95051. **271**

LINEAR SHORT-FORM CATALOG is a guide to all of Philbrick's latest developments in analog technology. Specially featured is Philbrick's 1421, 1422, 1424, 1425, 1426, 1428 and 1429 Series of economy FET microcircuit op amps; Model 1324 fast-settling monolithic op amp; the 1028, 1029, 1030, 1702 and 1703 discrete op amps; and the 1423 and 1427 microcircuit units. A section of nonlinear function modules and low-cost modular power supplies completes the product descriptions. Teledyne Philbrick, Allied Dr., at Rte. 128, Dedham, MA 02026.

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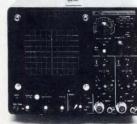
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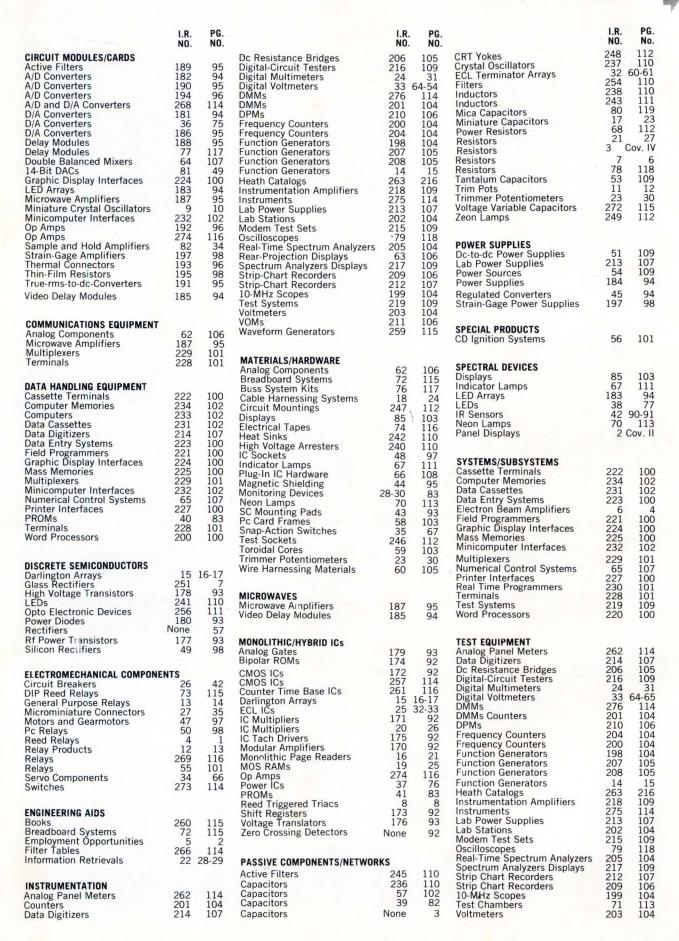
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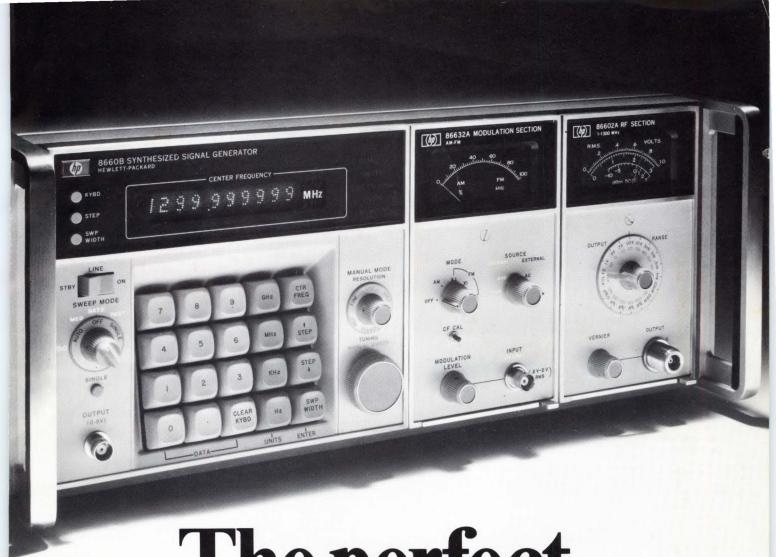
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