

LSIs for LCDs

Meeting the Many Needs of Wide-ranging Applications : SHARP's LSIs for LCDs

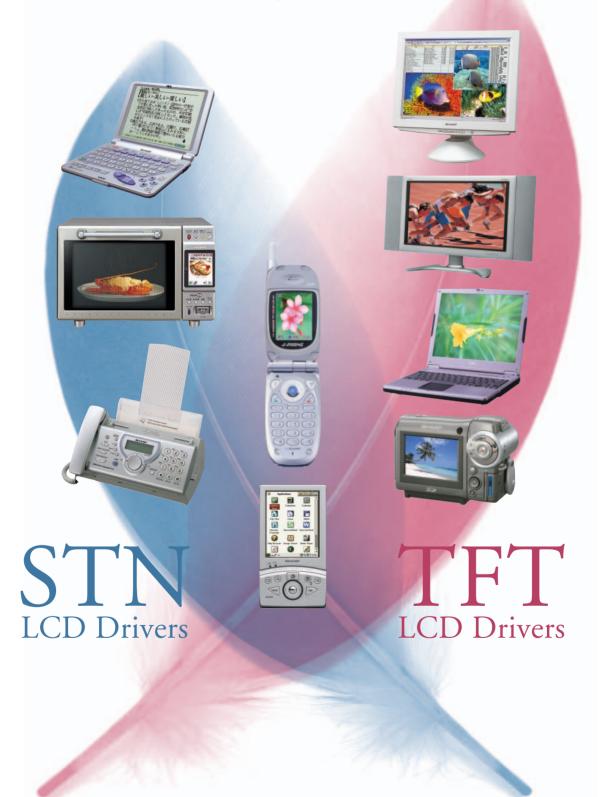
The performance of LCD panels has been enhanced by the expansion of LCD use,

and improvements are needed in their peripherals.

SHARP contributes to the design of highly functional applications through LSIs for LCDs,

such as LCD drivers, LCD controllers, video interface ICs, gray-scale ICs,

power supply ICs, etc. that satisfy needs across a wide range of applications.



LSIs for LCDs

Fundamental Technology of LCD Drivers that Supports LCD Panels

Technology of TFT LCD drivers TFT Technology of STN LCD drivers STN



LCD Panels

Smaller and Thinner Outline

High-density mounting • Several types of TCP and SOF (System On Film) • COG Twin LCD drive single-chip driver SIM

Color Display

Multi level gray scale : 256-level gray scale (16 780 thousand colors) IIII Multi color : 65 536 colors IIII

Lower Power Consumption

Low voltage/ Low power consumption

- 1.8 V low voltage drive TFT
- Wide variety of power save mode
- Prechargeless output
 IFT
- Partial drive mode
 STN

Higher Quality

Shadowingless drive ST Lower output deviation TT Dot inversion drive TT

Lower EMI

Decrease in frequency
 • Dual-edge 1-port input
 • Single-edge 2-port input
 TFT RSDS™ input TFT

Larger Panel/ Higher Definition

> Highter output pin counts : 480 outputs III Higher speed : 85 MHz III (equivalent to 160 MHz)

Fundamental Technology of LCD Drivers

IFT

RSDS^{™*1} input

contributes to lower EMI*² of LCDs, lower power consumption and higherdensity mounting.

*1 RSDS™ : Reduced Swing Differential Signaling RSDS is a trademark of National Semiconductor Corporation. *2 EMI : Electro-Magnetic Interference

Partial drive mode STN

contributes to low power consumption of LCDs by partially displaying a cellular phone's LCD at standby.

Twin LCD drive single-chip driver

can operate as a single-chip LCD driver with fully built-in functions required for LCDs, such as LCD controller, power supply circuit and display RAM. In addition, to share the use of an LCD driver and a backlight, twin LCDs (main LCD and sub LCD) allow to reduce the size and weight of the products.

Shadowingless drive

can create high quality LCDs by suppressing the generation of uneven brightness along with characters and patterns.

Lower output deviation

can suppress uneven output voltage thanks to SHARP's unique off-set cancel function when displaying on the LCDs and by making the brightness of each pixel uniform.

Dot inversion drive

can create high-quality display LCDs by decreasing shadowing and flicker of adjacent dots on the LCDs.

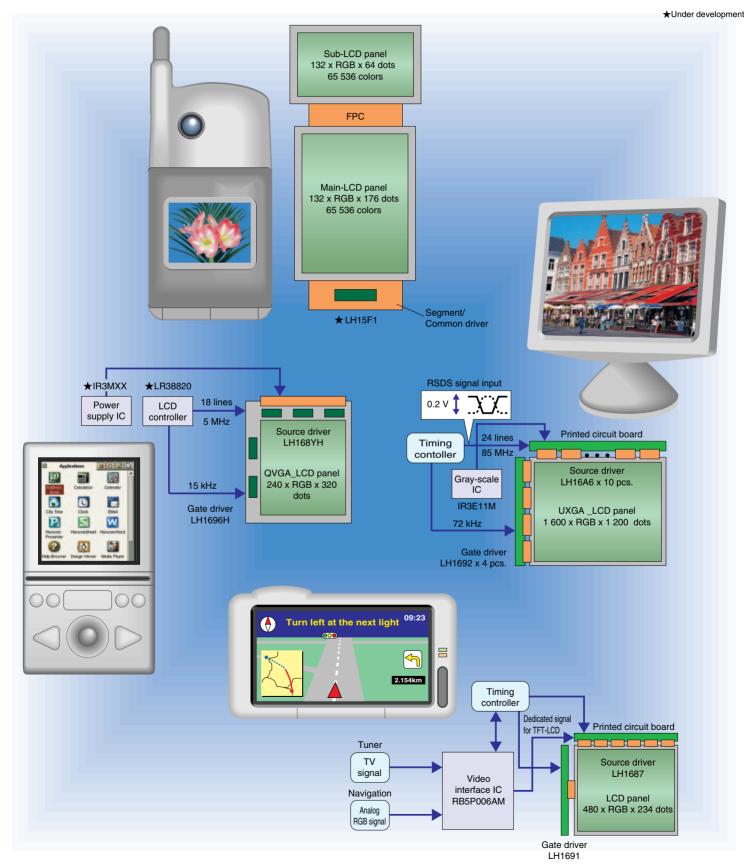
Dual-edge 1-port input and single-edge 2-port input

are made possible decreasing in frequency. At dual-edge 1-port input, the data are sampled at both the rising edge and the falling edge of clocks. At singleedge 2-port input, the data are sampled as 2-pixel data simultaneously at the rising edge or the falling edge of clocks.

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SHARP's LSIs for LCDs Providing a System Solution

SHARP's LSIs for LCDs can provide a system solution in combination with an LCD driver, LCD controller, LCD video interface IC, gray-scale IC and power supply IC, depending on use. SHARP also has contributed to creating smaller sizes and multi-functional products by integrating LCD peripheral LSIs in a stacked package.



Lineup of LCD Drivers For TFT LCDs

Drive function	Drive technology	Model No.	Gray scale	No. of LCD drive outputs	Display voltage (V)MAX.	Reference page	
		LH168K		309/312/ 321/324	12		
		LH168M*1 LH168T LH168S*2	64 levels (6-bit)	384	13	4, 7, 8 3, 4, 7, 8 3, 4, 7, 8 3, 4, 9, 10 4, 7, 8 4, 9, 10 3, 4, 9, 10 3, 4, 9, 10 3, 4, 9, 10 3, 4, 9, 10 4, 7, 8	
	Dot Inversion Drive		256 levels (8-bit)	LH16A3*1		13.5	4, 7, 8
[]		LH168R ★LH16A5*1		384	13 15		
Source Drive		LH168V ★LH16A6*1	(8-Dit)	480	13 15	3, 4, 7, 8	
	Line Inversion Drive	LH168YH LH168P LH16A1	64 levels (6-bit)	240 300/309 384	5.5		
		LH1684 LH1687	Analog	240			
Gate Drive		LH1696H LH1691	_	164 240	- 33		
		LH1694 LH1692		256 300	42	4, 7, 8 3, 4, 7, 8	

*1 Low EMI using RSDS™ EMI : <u>Electro-Magnetic Interference</u> RSDS™ : <u>Beduced Swing Differential Signaling</u>. RSDS is a trademark of National Semiconductor Corporation. *2 LH168S is a pin assignment variation of LH168T.

For STN LCDs (For Medium/Small Panels)

Туре	Drive function		Model No.	No. of LCD drive outputs Segment/Common	Duty ratio	Display voltage (V)MAX.	Supply voltage (V)	Reference page
[]			★LH15H1	288/66	1/10, 1/18, 1/26, 1/34, 1/42, 1/50, 1/58, 1/66	+13.2	1.8 to 3.3	4, 5, 6, 12, 13, 14
For Color Graphics (With a built-in) display RAM)	Segment and Common		LH15A1	384/82	1/17, 1/32, 1/47, 1/62, 1/77, 1/82	+15	1.7 to 3.3	4, 5, 6, 13, 14
			LH15B1*1	396/88	to 1/88 (Selectable per 1 line)	+18	- 1.7 10 3.3	4, 6
			LH15D1*1 ★LH15E1 ★LH15F1	396/176 396/176 + 64	to 1/176 (Selectable per 1 line)	+19	1.8 to 3.3	4, 5, 6 3, 4, 5, 6
			LH155K	128/64	1/16, 1/32, 1/48, 1/64			0, 4, 3, 0
For Graphics (With a built-in) display RAM)	Segment and Common		LH155P	134/66	1/10, 1/18, 1/26, 1/34, 1/42, 1/50, 1/58, 1/66	+13.2	1.8 to 3.3	4, 5, 6
			LH155R	128/93	1/41, 1/93	+16.5		
			LH155T	128/109	1/41, 1/109	+10.5		
*1 Without display RAM and	LCD controller	L	LH155G	248/68	1/67, 1/68	+14	1.8 to 5.5	

For STN LCDs (For Large/Medium Panels)

Drive technology	Drive function	Model No.	No. of LCD drive outputs	Duty ratio	Display voltage (V)MAX.	Reference page
	Segment	LH155E*3	160	1/100, 1/120, 1/128, 1/200, 1/240, 1/256	+5.5	4, 9, 10
	(5 V drive)	LH1583		to 1/240	+0.0	-, 0, 10
New Drive Technology*1	_	LH1580 LH1581	240	to 1/480		
reamonegy		 LH153D	120/160	1/160		
	Common	LH1537	200/240	1/200, 1/240	+45	4, 9, 10
		LH1538	120/128	to 1/480	+80	
		 LH1542	80	to 1/240	+30	
	Segment	LH1549	160	to 1/480	+42	4, 9, 10
		LH1548	240	10 1/400	772	
Conventional Drive	Common	LH1532	100	to 1/240	+30	4, 9, 10
Technology*2	Common	LH1530	120	to 1/480	+42	4, 9, 10
		 LH1565	100	to 1/240	+30	
	Segment	 LH1560		+42	4, 9, 10	
	or common	LH1562	240 to 1/480		+42	

*1 New drive technology : A drive technology which drives LCDs with low voltage of 5 V on segment side, and drives LCDs with high voltage on common side. Driving with low voltage on segment side enables LCDs to reduce power consumption and shadowing.
 *2 Conventional drive technology : A drive technology which drives LCDs with high voltage on both segment and common sides.
 *3 With a built-in display RAM

For Cellular Phones *Under development For Cellular Phones Number of ★LH15F1 display colors Segment/Common 396 x (176 + 64) outputs Display voltage : 19 V Supply voltage : 1.8 to 3.3 V ★LH15H1 LH15A1 Segment/Common Segment/Common Twin LCD drive 288 x 66 outputs 384 x 82 outputs Display voltage : 13.2 V Display voltage : 15 V Supply voltage : 1.8 to 3.3 V Supply voltage : 1.7 to 3.3 V ★LH15E1 Segment/Common ++396 x 176 outputs 65 536 Display voltage : 19 V Supply voltage : 1.8 to 3.3 V Single-chip Larger capacity LR38840 LR38840/44 LCD controller LCD controller Color Larger capacity Enhanced display capability Graphic ★LH15H1 LH15A1 type For Segment/Common Segment/Common with a STN 256 288 x 66 outputs 384 x 82 outputs built-in **LCDs** Display voltage : 13.2 V Display voltage : 15 V Larger display Supply voltage : 1.8 to 3.3 V Supply voltage : 1.7 to 3.3 V capacity RAM LH155G Segment/Common 248 x 68 outputs Display voltage : 14 V Supply voltage : 1.8 to 5.5 V LH155R LH155T Segment/Common Segment/Common 128 x 93 outputs 128 x 109 outputs Larger Display voltage : 16.5 V Supply voltage : 1.8 to 3.3 V Display voltage : 16.5 V Supply voltage : 1.8 to 3.3 V capacity B/W Larger

capacity

LH155P

Segment/Common

134 x 66 outputs

Display voltage : 13.2 V

Supply voltage : 1.8 to 3.3 V

: LCD controller

Partial drive

LH155K

Segment/Common

128 x 64 outputs

Display voltage : 13.2 V

Supply voltage : 1.8 to 3.3 V

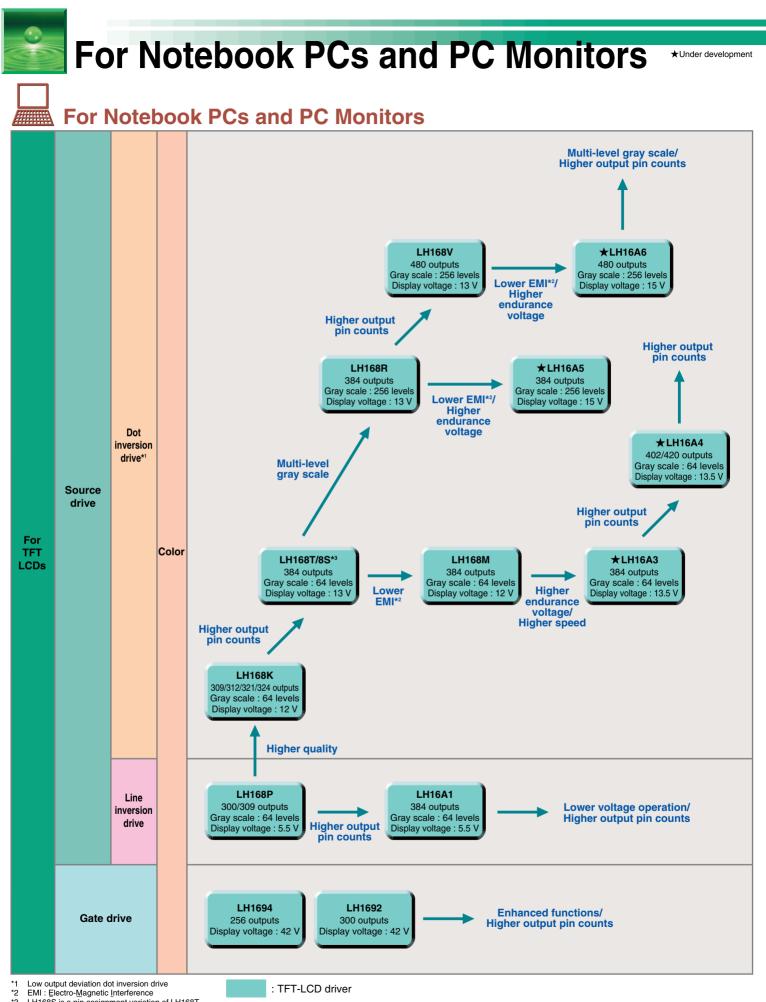
: STN-LCD driver

STN-LCD Drivers for Cellular Phones

Тур	е	Drive function	Model No.	No. of LCD of Segment	drive outputs Common	Duty ratio	Display voltage (V) MAX.	Data input	Clock frequency (MHz) MAX.	Supply voltage (V)	Package
	256		★LH15H1	288	66	1/10, 1/18, 1/26, 1/34, 1/42, 1/50, 1/58, 1/66	+13.2	8/16-bit	4 (at 3 V)	1.8 to 3.3	SOF
For color	colors		LH15A1	384	82	1/17, 1/32, 1/47, 1/62, 1/77, 1/82	+15	parallel/serial	(at 3 V)		
graphics			LH15B1*1		88	to 1/88 (Selectable per 1 line)	+18	[Display data] 12-bit parallel		1.7 to 3.3	TCP/SOF
built-in display RAM	65 536		LH15D1*1	396	176		10	[Command data] Serial			
	colors	- · ·	★LH15E1	0.50	170	to 1/176 (Selectable per 1 line)	+19	8/16-bit	4	1.8 to 3.3	SOF
		Segment and Common	★LH15F1		176 + 64		113	parallel/serial	(at 3 V)	18to 33	
		Common	LH155K	128	64	1/16, 1/32, 1/48, 1/64	+13.2		2 (at 3 V)		
For gra	nhics		LH155P	134	66	1/10, 1/18, 1/26, 1/34, 1/42, 1/50, 1/58, 1/66	13.2		3.3 (at 3 V) 1.8 to 3.3		
With a b display	uilt-in \		LH155R	128	93	1/41, 1/93	+16.5	8-bit parallel/serial	4.5	1.0 10 0.0	TCP
			LH155T	.20	109	1/41, 1/109	. 10.5	(at		(at 3 V)	
			LH155G	248	68	1/67, 1/68	+14		3 (at 5 V)	1.8 to 5.5	

*1 Without display RAM and LCD controller

TCP : Tape Carrier Package SOF : System On Film



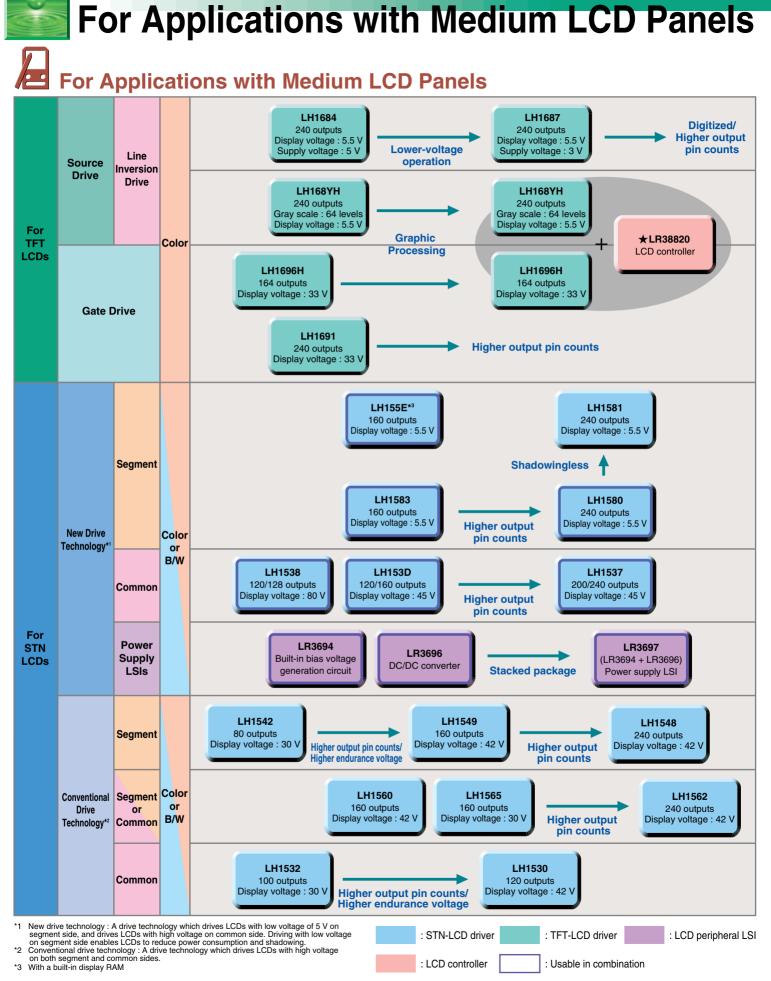
*3 LH168S is a pin assignment variation of LH168T.

TFT-LCD Drivers for Notebook PCs and PC Monitors

Drive function	Model No.	Gray scale	No of LCD drive outputs	Display voltage (V) MAX.	Clock frequency (MHz) MAX.	Supply voltage (V)	Function	Package										
	LH168K		309/312/ 321/324	12	55		Data input port selectable : 1 port/2 ports* ² , built-in reference voltage generation circuit, R-DAC system											
	LH168T			13	65	2.7 to 3.6	2-port data input, built-in reference voltage generation circuit,											
	LH168S*3	64 levels	384	15	05		R-DAC system											
	LH168M	(6-bit)	(6-bit)	(6-bit)	(6-bit)	(6-bit)	364	12	68	3.0 to 3.6								
Source drive	★LH16A3			13.5	85		Low EMI* ⁴ using RSDS ^{TM+5} , built-in reference voltage generation circuit, R-DAC system											
(Dot inversion drive*1)	★LH16A4		402/420	15.5	65	2.7 to 3.6												
	LH168R	384		13	65	2.7 10 3.0	2-port data input, built-in reference voltage generation circuit, R-DAC system											
	★LH16A5	256 levels (8-bit)	256 levels (8-bit)										304	15	85		Low EMI*₄ using RSDS™₅, built-in reference voltage generation circuit, R-DAC system	TCP/SOF
	LH168V										480	13	65	2.5 to 3.6	Clock single-edge (2-port input) or clock dual-edge (1-port input) selectable (built-in data sampling switching function), built-in reference voltage generation circuit, R-DAC system]		
	★LH16A6		400	15	85	2.7 to 3.6	Low EMI*4 using RSDSTM*5, built-in reference voltage generation circuit, R-DAC system											
Source drive	LH168P	64-levels	300/309	5.5	55	3.0 to 5.5	Built-in reference voltage generation circuit, R-DAC system											
(Line inversion drive)	LH16A1	(6-bit)	384	5.5	57	2.7 to 3.6	2-port data input, built-in reference voltage generation circuit, R-DAC system											
	LH1694		LH1694 LH1692						256	256 42		2.7 to 3.6	Output signal masking function, usable with both positive/negative power supplies, enables chain connection					
	LH1692				300	42	0.1	3.0 to 5.5	5.5 1-pulse (normal) or 2-pulse (continuous/jumping) scanning selectable usable with both positive/negative power supplies									

Low output deviation dot inversion drive
 1-port data input at 309/321 outputs, 2-port data input at 312/324 outputs.
 LH168S is a pin assignment variation of LH168T.
 EMI : Electro-Magnetic Interference
 RSDS™ : Reduced Swing Differential Signaling RSDS is a trademark of National Semiconductor Corporation.

TCP : Tape Carrier Package SOF : System On Film



TFT-LCD Drivers for Applications with Medium LCD Panels

Drive function	Model No.	Gray scale	No of LCD drive outputs	Display voltage (V) MAX.	Clock frequency (MHz) MAX.	Supply voltage (V)	Function	Package	Applications
	LH168YH	64 levels (6-bit)			35 2.3 to 5.5 Built-in reference voltage generation circuit, polarity inversion of input data				Personal digital assistants
Source drive (Line inversion drive)	LH1684	Analog	240	5.5	10	4.5 to 5.5	Three-point simultaneous or normal sampling selectable (Sampling frequency : 20 MHz)	TCP	Automobile
	LH1687	Analog			12.5	3.0 to 5.5	Three-point simultaneous or normal sampling selectable (Sampling frequency : 25 MHz), power saving function, 3 V drive (MIN.), prechargeless output	TCP/COG	navigation systems
Gata driva	LH1696H 164		164	33	0.1	2.3 to 5.5	1-pulse scanning, all "Low" mode	COG	Personal digital assistants
Gate drive	LH1691		240	33	0.1	3.0 to 5.5	1-pulse (normal) or 2-pulse (continuous/jumping) scanning selectable, usable with both positive/negative power supplies	TCP/COG	Automobile navigation systems

TCP : Tape Carrier Package COG : Chip On Glass

STN-LCD Drivers for Applications with Medium LCD Panels

Drive technology	Drive function	Model No.	No. of LCD drive outputs	Duty ratio	Display voltage (V) MAX.	Data input	Clock frequency (MHz) MAX.	Supply voltage (V)	Package	Applications
		LH155E*3	160	1/100, 1/120, 1/128, 1/200, 1/240, 1/256		8-bit parallel/serial	2 (at 3 V)	2.4 to 3.3		
	Segment	LH1583	100	to 1/240	+ 5.5	4/8-bit parallel	12 (at 2.4 V)/ 20 (at 5 V)	2.4 to 5.5		
	Segment	LH1580	240	240 to 1/480	+ 5.5	8/12-bit parallel	30 (at 2.5 V)/	2.5 to 5.5		
New drive technology*1		LH1581	240	10 1/400			55 (at 5 V)	2.0 10 0.0		
		LH153D	120/160	1/160	+ 45		3 (at 2.4 V)/ 4 (at 5 V)			
	Common	LH1537	200/240	1/200, 1/240	+ 43	_				
		LH1538	120/128	to 1/480	+ 80		3 (at 2.5 V)/ 4 (at 5 V)	_	TCP	Personal digital assistants
		LH1542	80	to 1/240	+ 30	4-bit parallel	8			
	Segment	LH1549	160	to 1/480	+ 42	4/8-bit parallel	12 (at 2.5 V)/ 20 (at 5 V)	_		
		LH1548	240	10 1/400	+ 42	8/12-bit parallel	12 (at 2.5 V)/ 25 (at 5 V)			
Conventional drive	Common	LH1532	100	to 1/240	+ 30	_	4	2.5 to 5.5		
technology*2		LH1530	120	to 1/480	+ 42		3 (at 2.5 V)/ 4 (at 5 V)			
	Comment	LH1565	160	to 1/240	+ 30		[Segment mode] 8 [Common mode] 4			
	Segment or Common	LH1560	100	to 1/480	+ 42	4/8-bit parallel (at segment drive)				
		LH1562	240	10 1/400			[Segment mode] 12 (at 2.5 V)/20 (at 5 V) [Common mode] 4			

*1 New drive technology : A drive technology which drives LCDs with low voltage of 5 V on segment side, and drives LCDs with high voltage on common side. Driving with low voltage on segment side enables LCDs to reduce power consumption and shadowing.
 *2 Conventional drive technology : A drive technology which drives LCDs with high voltage on both segment and common sides.
 *2 With a built is drive technology.

*3 With a built-in display RAM

Power Supply LSIs for Medium STN-LCD Panels

Model No.	Description		Supply voltage (V)	Package
LR3694	Bias voltage generation circuit for LCD drive, electronic volume control circuit	For LH155E,	2.4 to 3.3 (V_{DD}, V_P), 14.4 to 19.8 (V_{H6}), -16.5 to $-12.0~(V_{L6})$	P-TQFP048-0707
LR3696	DC/DC converter for LCD drive power supply	LH1583, LH1580, LH153D,	2.4 to 3.3 (Vpp, Vp)	P-QFP032-0707
LR3697	DC/DC converter for LCD drive power supply, bias voltage generation circuit for LCD drive, electronic volume control circuit	LH1537, LH1538	2.4 10 5.3 (VID, VP)	P-QFP072-1010

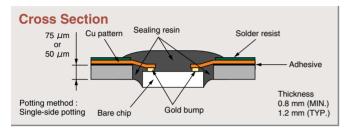
TCP : Tape Carrier Package

Package Technologies for LCD Drivers

Improvements in SHARP's original packaging technology, as well as in design and wafer process technologies, make possible smaller, thinner and more highly-functional LCD drivers. SHARP can provide two types of packages, TCP and SOF, which can be selected depending on customer needs.

TCP (Tape Carrier Package)

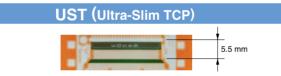
This is a package which can easily achieve higher pin counts, finer pin pitches and reduced dimensions. It allows flexible pattern design of outer lead shapes and pitches, to accept different pin connections. Depending on use, SHARP's TCPs are available in a variety of tape patterns, coatings, and other options. We can customize our TCPs to accommodate our customers' needs.



Features

- Both face-up or face-down chip mounting are available.
- Alignment can be easily determined on user side by adopting overhang pattern.
- Capable of incorporating passive components.

TCP Varieties

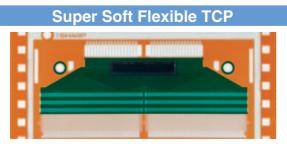


The UST combines ultra-slim chip technology and TCP technology to achieve an external TCP size of 5.5 mm.

High-Pin-Count Fine-Pitch TCP



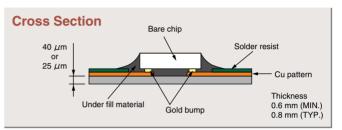
Using fine-pitch technology, a 35 mm-wide tape, 480output (pad pitch 50 μ m; outer lead pitch 55 μ m), highpin-count and fine-pitch TCP has been developed.



The enhancement of our own TCP assembly technology and tape materials has resulted in the development of a super-soft flexible TCP, a product that has improved bendability and three times as much bending strength as existing products.

SOF(System On Film)

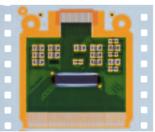
This package enables mounting of various components including bare chips, such as LCD drivers and LCD controllers, and peripheral circuit components onto film. It contributes to the realization of higher-level functions in applications. This package is mainly used in small LCD panels and contributes to making smaller and thinner portable equipment.

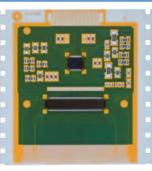


Features

- Capable of mounting various components including bare chips, such as LCD drivers and LCD controllers, and peripheral circuit components.
- Contributes to the design of thin and compact products thanks to highly flexible and thin film package which can be bent freely, such as by angle-bending.
- Suitable for finer pin pitches due to absence of flying lead.
- Flexible pattern design can be achieved by designing patterns on a film under the chip.

Single chip SOF





Multi chip SOF



SHARP offers SOF (System On Film) modules with LCD drivers mounted on film. As multiple chip mounting is possible, they enable mounting of various components including bare chips, such as LCD drivers and LCD controllers, LCD panels, and peripheral circuit components. Therefore, they can help create system modules for a range of applications, and make possible higher-level functions in applications. We can provide a standard product, LR0G918A, suitable for sub LCD display of a cellular phone, a toy camera, etc.

Features

Multiple chip mounting

Enables mounting of various components including bare chips, such as LCD drivers and LCD controllers, LCD panels, and peripheral circuit components, and thereby contributes to the realization of higher-level functions in applications.

Fully custom-designed package

Highly flexible and thin film package

By using highly flexible and thin film, SOF modules contribute to the design of thin and compact products. They can also easily achieve finer pin pitches and higher output pin counts.

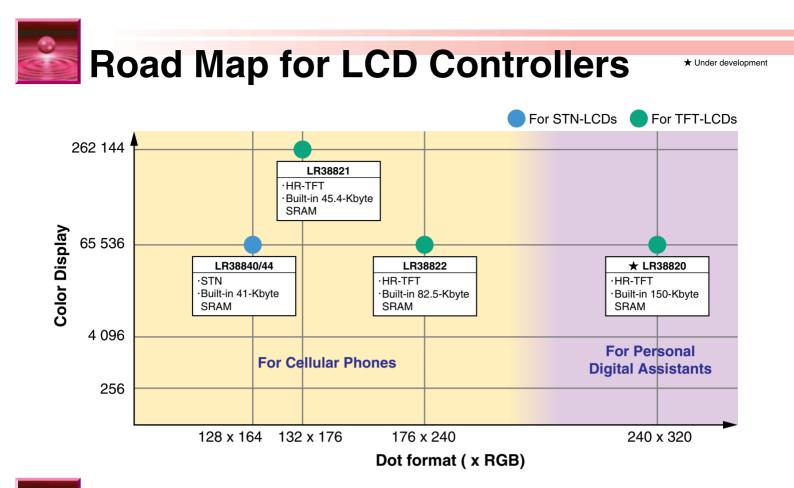
LCD Module

Model No.	★LR0G918A
Module structure	 LCD panel + SOF (with an LCD driver) Built-in LCD drive power supply circuit and oscillator
LCD panel	Color STN, transflective
Dot format H x V (dot)	72 x RGB x 64
Active area W x H (mm)	17.268 x 15.345
Dot size W x H (mm)	0.068 x 0.24
LCD driver	★LH15H1
Duty ratio	1/10, 1/18, 1/26, 1/34, 1/42, 1/50, 1/58, 1/66
LCD drive power supply	Built-in booster circuit (x 2, x 3, x 4)
Outline dimensions W x H x D (mm)	25 x 44.34 x approx. 1.47*1
MPU Interface	8/16-bit parallel (68-family/80-family)
Supply voltage (V)	$V_{\text{DD}} = V_{\text{EE}} = 3.0$ (recommended)
Operating temp. (°C)	—20 to +70
Storage temp. (°C)	—30 to +80

*1 W : Not including the burr of plastic mold resin. H : Indicates the size before SOF bending. D : Indicates the thickness of LCD panel. Module without LCD panel is also available.







Features of LCD Controllers

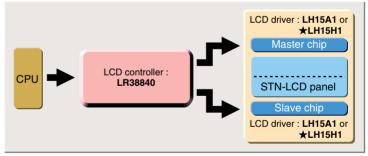
★ Under development

STN-LCD Controllers for Cellular Phones (LR38840/LR38844)

- Provides 65 536-color display
- Display color selectable : 256/4 096/ 65 536 colors
- Low power consumption
- CPU interface : 68-family or 80-family selectable
- Bus width : 8-bit or 16-bit selectable
- For LH15A1 or ★ LH15H1 (LR38840) For LH15A1 (LR38844)
- LCD size : up to 20 992 (128 x 164) dots

TFT-LCD Controllers for Cellular Phones (LR38821/LR38822)

- Provides 262 144-color display (LR38821)
 Provides 65 536-color display (LR38822)
- Low power consumption
- CPU interface : 80-family (LR38821) 68-family and 80-family selectable (LR38822)
- Bus width : 8-bit or 16-bit selectable
- LCD size : up to 23 232 (132 x 176) dots (LR38821) up to 42 240 (176 x 240) dots (LR38822)

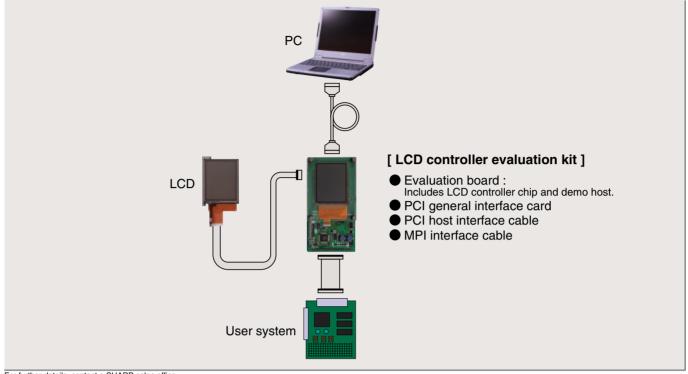


TFT-LCD Controller for Personal Digital Assistants (*LR38820)

- Built-in simple graphic processing functions, such as copying, mirror imaging, painting out and rotation, which places a minimal load on the main CPU.
- Provides 65 536-color display
- Low power consumption
- CPU interface : 80-family
- Bus width : 16-bit
- LCD size : up to 76 800 (240 x 320) dots

LCD Controller Evaluation Tools

SHARP can provide whole-system evaluation boards that work through linkage to user systems.



For further details, contact a SHARP sales office

Specifications for LCD Controllers

+ Under development

STN-LCD Controllers for Cellular Phones

Model No.	Display area (Dot) MAX.	Display color [MAX.]	Function/Feature		CPU interface	Frame memory size			ion*1 (mW) TYP. 65 536-color display	
LR38840			 Provides 65 536-color display Display colors selectable : 256/4 096/65 536 colors Power saving function, reducing the power consumption in standby mode Built-in CPU interface, LCD interface, clock generator, display memory 	For LH15A1, ★LH15H1	68-family/				7	
LR38844	128 x RGB x 164	65 536	 High-speed host access Higher performance model Provides 65 536-color display Display colors selectable : 256/4 096/65 536 colors Power saving function, reducing the power consumption in standby mode Built-in CPU interface, LCD interface, clcok generator, display memory 	For LH15A1		41 Kbytes	2.5	1.5	6	T-TFBGA081-0808

*1 When connected to 8-bit bus at 5.4 MHz.

FT-LCD Controllers for Cellular Phones

Model No.	Display area (Dot) MAX.	Display color [MAX.]	Function	CPU interface	Frame memory size		age (V) TYP.	Power consumption (mW) TYP.	Package
LR38821	132 x RGB x 176	262 144	Built-in CPU interface, LCD interface, timing generator,	80-family (8/16-bit)	45.4 Kbytes	2.5		3	T-TFBGA112-1010
LR38822	176 x RGB x 240	65 536	clock generator, display memory	68-family/ 80-family (8/16-bit)	82.5 Kbytes	-	3.3	5	1-1FBGA112-1010

TFT-LCD Controller for Personal Digital Assistants

	Model No.	Display area (Dot) MAX.	Display color [MAX.]	Function	CPU interface	Frame memory size		ige (V) TYP. I/O	Power consumption (mW) TYP.	Package
,	LR38820	240 x RGB x 320	65 536	 Simple graphic processing function*1 Built-in CPU interface, LCD interface, timing generator, clock generator, display memory 	80-family (16-bit)	150 Kbytes	2.5	3.3	25	P-LQFP120-1414/ T-TFBGA112-1010

*1 It enables simple graphic processing, such as copying, mirror imaging, painting out and rotation, which places a minimal load on the main CPU.



Gray-scale ICs for TFT-LCDs

Model No.	Panel type	Function	No. of output circuits	Output current (mA) MAX.	Common output current (mA) MAX.	Supply voltage (V) TYP.	Package
IR3E201N/ IR3E204N	Small panels	Gray-scale voltage generator for LCD drivers,	10	±1	±1	5	P-MFP018
IR3E3XX	Line inversion drive	built-in dividing résistors	5	±1	±1	5	P-SSOP012-0225
IR3E08M1	·Medium/Large panels ·Line inversion drive		9	±10	±10	5	
IR3E11M	 Large panels Up to 20-type panels 	Gray-scale voltage generator for LCD drivers	10	±15	±150	10.5	P-TQFP048-0707
★IR3E12M	•SXGA/UXGA •Dot inversion drive		18	±15	±150	10.5	

Features

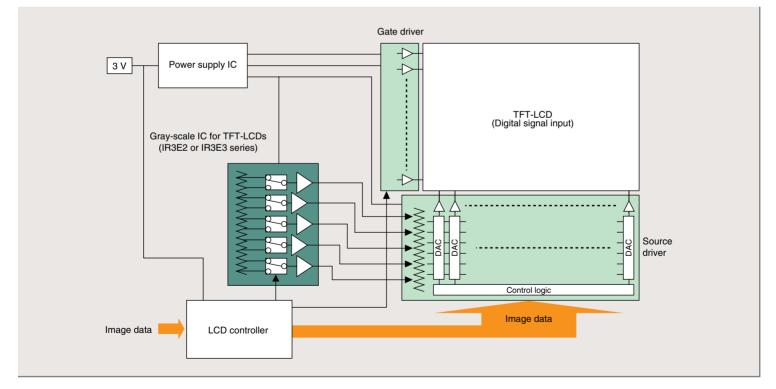
For small TFT-LCD panels : IR3E2 series and IR3E3 series

- ·Ideal for mobile equipment thanks to low-power-consumption design.
- Integrating regulator and resistance array dramatically reduces the number of external components.
- •The setting of output voltage can be customized, depending on the LCD characteristics.

• For large TFT-LCD panels with dot inversion drive : IR3E11M and \star IR3E12M

- •Available for up to 20-type SXGA or UXGA panels with large common output current (±150 mA [MAX.]).
- •External volume and gray-scale adjustment are not required by integrated high-definition regulator.
- •The number of LCD components can be decreased.

System Configration Example : Small TFT-LCDs (IR3E2 series and IR3E3 series)



Power Supply IC for TFT-LCDs

Model No.	Supply voltage(V)	C	Dutput voltage (V)TYP.			Deelvage		
		VDD	Vout1	Vout2	VDD	Vout1	Vout2	Package
★IR3MXX	2.7 to 3.6	5.1	15.3	-10.2	0 to -10	0 to -0.2	0 to 0.2	P-VQFN020-0404

Video Interface ICs for TFT-LCDs

	Input signal			Color	LCD panel			Serial	Supply voltage	Power			
Model No.	Composite video	Y/C	Y/color difference	Analog RGB	decode	<u>+</u> power source	+power source	Low voltage source	Digital input	data control	Voltage (V)	consumption (mW) TYP.	nption Package TYP.
IR3Y18A		0			NTSC/PAL	0	0				4.5/12 or 4.5/-7.5	130	P-QFP048-0707
IR3Y26A/A1				0	-			0			5/7.5	140	P-QFP048-1010/ P-QFP048-0707
IR3Y29AM/BM	0	0		0	NTSC/PAL			0			5/7.5	190	
IR3Y31M	0	0		0	NTSC/PAL	0	0				4.5/12 or 4.5/-7.5	160	
IR3Y34M			0	0	_		0				- 3/12	88	P-QFP048-0707
IR3Y35M			0	0	_		0					91	
IR3Y37M			0	0	-			0			3/6.5	95/77* ²	
IR3Y37AM			0	0	-			0				106/88*2	
RB5P0010M				0	-	0	0			0	3/12 or 3/4.5/-7.5	92	
RB5P0020M			0	0	-			0			3/5	70/57*2	
RB5P0050M			0	0	-			0		0		95/80*2	
RB5P0060M		0		0	NTSC/PAL			0		0	3/5/13	5/13 120 P-QFP048-10	D OED048 1010
RB5P006AM	0	0		0	NTSC/PAL			0		0			1-QFF040-1010
★RB5P0070M*1	0	0		0	NTSC/PAL				0	0	3/7	330	P-QFP072-1010

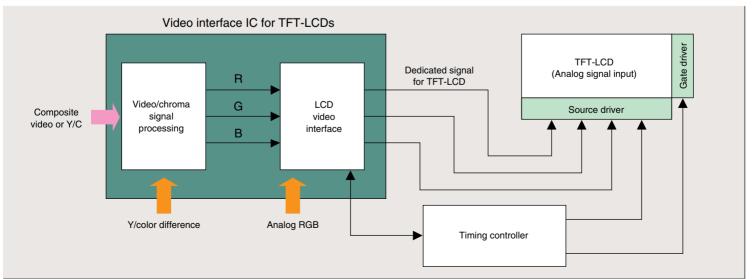
O : Available

*1 For digital signal input panels
*2 At analog RGB input

Features

- Suitable for battery driven products thanks to low power consumption.
- Wide lineup variety, from amorphous silicon TFT-LCDs to poly-silicon TFT-LCDs.
- Natural gray-scale display and wide range of brightness adjustment are realized by adopting original SHARP brightness
- adjustment circuit where common amplitude and gamma correction point react to each other.
- Can be unified with a timing controller IC by adopting a stacked package and can adopt QFN packages to contribute to making smaller products.

System Configuration Example : Analog Signal Input TFT-LCDs



Classification of Video Interface ICs for TFT-LCDs

Applications		Compact LCD TVs/ Automobile navigation systems	Digital still cameras/Camcorders/ Automobile navigation systems	Digital still cameras/Camcorders		
	Input Signal	Composite Video & Y/C	Analog RGB	Y/Color Difference		
Source	For 12 V (or +5 V/-8 V) LCD panels*1	IR3Y18A IR3Y	IR3Y	/34M /35M		
supply voltage	For 5V LCD panels	RB5P	RB5P			

*1 IR3Y34M and IR3Y35M are applied to only LCD panels of 12 V source supply voltage.
 *2 For digital signal input panels



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